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Featured Technology — Microstrip Design Basics

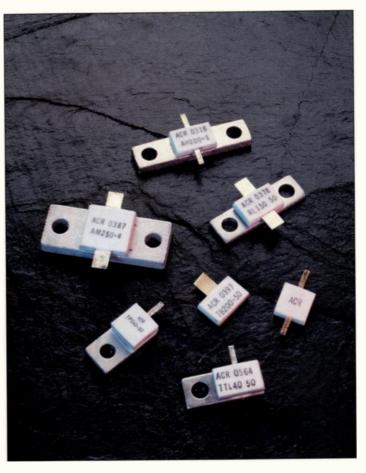
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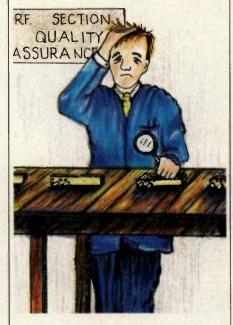


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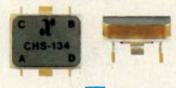
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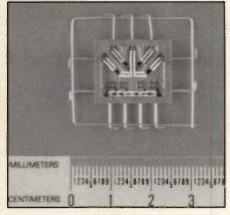


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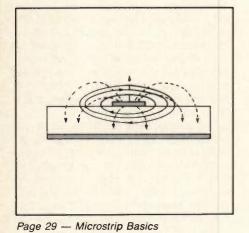
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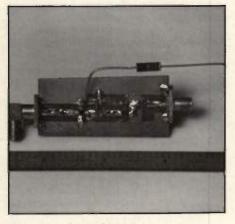


June 1988



Page 19 - GaAs MMIC Switches





Page 64 — MMIC Multipliers

Cover Story

19 GaAs MMIC Switches for High Performance Applications

GaAs technology has reached the level of maturity where standard products are available to the RF engineer, particularly in the area of switches. Tachonics Corporation's line of switch modules, described in this article, includes models that handle up to 5 watts. — Raymond S. Pengelly, Amin Ezzedine and Barak Maoz

Featured Technology Section

29 Principles of Microstrip Design

This article takes the reader on a step-by-step review of the basic design equations for microstrip circuits. Examples are provided to demonstrate the calculation of characteristic impedance, electrical length, reactance, cross-coupling, and other essential microstripline design parameters.

38 Microstrip Analysis and Design for Various Substrate Materials

A computer program, based on equations developed by Sobol and Hammerstad, is presented to design and analyze microstrip transmission lines. The program is written in BASIC to allow its use on different personal computers.

- D.R. Hertling and R.K. Feeney

43 CAD Amplifier Matching with Microstrip Lines

Microstrip design is applied to amplifier circuits in this article. The author reviews stub and transmission line matching, and provides a BASIC program which allows an engineer to try and choose from a wide variety of L-network matching networks.— Stanley Novak

New Products at MTT-S 1988

59 Highlights of new products to be unveiled in New York.

64 MMIC Active Multipliers

Simplicity and stability are attributes of MMIC amplifiers which can be used to advantage in VHF/UHF multiplier circuits. This note describes the performance of a 220 MHz to 440 MHz doubler circuit. — Jerry Hinshaw

69 Designer's Notebook — Unequal Power Division With a Lumped Element Divider

The author demonstrates how Wilkinson type lumped-element dividers can be used to divide the input power into output ports with unequal power ratios.

- David Burgess

55 RFI/EMC Corner — Fundamentals of Bod/Black System Engineering

B Red/Black System Engineering

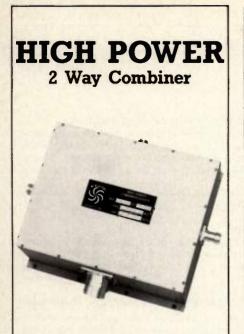
TEMPEST requirements are often unfamiliar or midunderstood by engineers. The basic requirements of engineering for Red (secure) and Black (unclassified) data are explained in this article. — Mike Brooks

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In Search of Human Engineering

rf editorial



By Gary A. Breed Editor

A while back, my parents flew to Colo-rado from Illinois to pay a visit. Upon arrival, they got into their rental car, turned the key, and were greeted by a blaring radio, fans going full blast, and no idea how to shut them off! You see, this was one of those new models with a master computer system and a touch-screen video display - the highest of high tech. Yet, it wasn't even possible to start the car and drive away without a lesson from the attendant.

This scenario isn't limited to a few fancy cars, either. Think about it: How many pieces of test equipment in your lab reguired a thorough analysis of the instruction manual (or a call to the factory) before you could make a routine measurement? How many hours did it take to become friendly with your favorite "user-friendly" computer program?

It is extremely unproductive when the complexity of an engineer's tools makes it difficult to perform his or her design tasks. It is even more disconcerting that a manager, who doesn't use the equipment on a daily basis, might not be able to independently evaluate an engineer's design procedures and test results! What is needed is attention to the operators' needs, with the same priority that is given to features and performance.

We need to get through this bottleneck of complexity. The power of computers and intelligent instruments has to be accessible to the users.

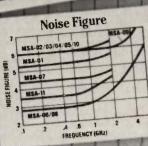
OK, so I've overstated the problem. There are plenty of straightforward instruments and programs that are uncomplicated in the way they operate. However, the clear trend is toward greater capability as microprocessors and memory become truly universal, and as personal computers become more powerful. This increased capability cannot be accessed by the normal front panel, where each control has a specific function. It also means that the user cannot simply "read" the front panel to determine the machine's operation.

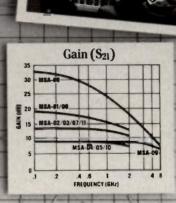
Fortunately, there are some beginning efforts under way to improve the situation. Software may incorporate mice, windows, and HELP keys, and can prompt the user for the data it needs. Some of these ideas are being incorporated into test and production equipment, via CRT or other means of text and graphic display. It is obvious that these are early efforts, with many different arrangements of "soft keys," windows, pre-programmed setups, keypads and rotary dials.

Despite my concern, I am certain that this bottleneck will be overcome. Over and over through history, new technology has taken some time to become accessible to everyone who can benefit from it. For RF engineers to benefit from the computing power in software and test equipment, it has to become more accessible, and soon.

Jan Freed

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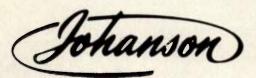
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Editor

Gary A. Breed Technical Editor Mark Gomez

Consulting Editors Andy Przedpelski Robert J. Zavrel, Jr.

National Sales Manager Kate Walsh

Advertising: West Coast District Manager Mary Bandfield 1341 Ocean Ave., Ste. 58 Santa Monica, CA 90401 (213) 458-6683

Midwestern States Kate Walsh Main Office

Eastern Sales Manager Joseph Palmer 36 Belmont Rd. S.W. 3 West Harwich, MA 02671 (617) 394-2311

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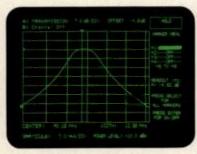
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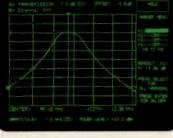
et the drift.

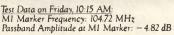
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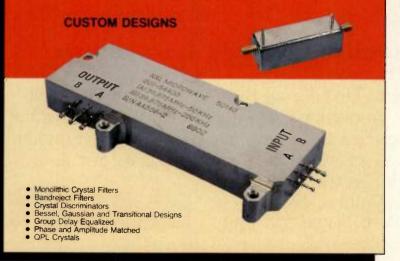
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A PPDF Correction Editor:

The article, "A Programmable Pulse Driven Formatter", featured in the April 1988 issue, has an error I would like to correct. Figure 3, Return to One (R1), a PPDF mode, is not correct. Figure 3 is an exact duplication of Figure 2, and should be corrected to look like the oscilloscope photograph on page 30, Figure 8.

I would like to take this opportunity to say I think that your magazine is an excellent source of new and exciting ideas in the RF engineering field.

Norbert Rehm

Regency Electronics, Inc. Indianapolis, IN

Editor:

The article "A Programmable Pulse Driven Formatter" appearing in the April 1988 Featured Technology section indeed had an error that I would like to correct. The waveforms shown on page 26 should have been as shown below.

Figure 2 was incorrectly called Figure 21, and Figure 3 duplicated the RZ waveforms from Figure 2 instead of the correct R1 waveforms.

The article looked great and we con-

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tinue to receive compliments on it from our customers. Thanks for all your help!

Karl C. Zabel Harris Microwave Semiconductor Milpitas, CA

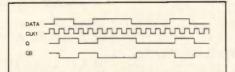


Figure 1, Typical NRZ data output waveforms.

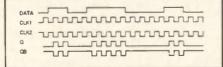


Figure 2. Typical RZ data output waveforms.

Fi	gu	re 3	. Typic	al R1	data	output
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- Principles of RF Circuit Design: Theory and Application July 6-8, 1988, Santa Clara, CA July 25-27, 1988, College Park, MD
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Information: Eva Koltai, Besser Associates, Inc., 3975 East Bayshore Road, Palo Alto, CA 94303; Tel: (415) 969-3400

UCLA Extension

Microwave/Millimeter-Wave Monolithic Integrated Circuits July 18-20, 1988, Los Angeles, CA

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Information: Sandra Scoredos, EEsof, Inc., 5795 Lindero Canyon Road, Westlake Village, CA 91362. Tel: (818) 991-7530

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Georgia Institute of Technology

Microwave Antenna Measurements July 25-29, 1988, Atlanta, GA

Techniques of Radar Reflectivity Measurement August 2-5, 1988, Atlanta, GA

Information: Deidre Mercer, Education Extension Services, Georgia Institute of Technology, Atlanta, GA 30332-0385. Tel: (404) 894-2547.

Southeastern Center for Electrical Engineering Education

Antennas: Principles, Design, and Measurements August 2-5, 1988, San Diego, CA

Information: Ann Beekman, SCEEE, 1101 Massachusetts Ave., St. Cloud, FL 32796. Tel: (305) 892-6146

R & B Enterprises

The R & B EMI Training Institute August 8-19, 1988, Philadelphia, PA

Information: Greg Gore, R & B Enterprises, 20 Clipper Road, West Conshohocken, PA 19428. Tel: (215) 825-1684

Design & Evaluation, Inc.

The Worst Case Circuit Analysis Training Seminar September 12-14, 1988, Boston, MA October 17-19, 1988, San Francisco, CA

Information: Design & Evaluation, Inc., 1000 White Horse Road, Suite 304, Voorhees, NJ 08043. Tel: (609) 770-0800

University Consortium for Continuing Education Modern Microwave Techniques September 26-29, 1988, Washington, DC

Information: University Consortium for Continuing Education, 16161 Ventura Boulevard, M/S C-752, Encino, CA 91436. Tel: (818) 995-6335



New Microwave Laboratory at NJIT

Two microwave companies have made contributions towards the creation of a microwave laboratory and curriculum at New Jersey Institute of Technology. The program is intended to increase the number of degreed electrical engineers with microwave background for the state's more than 60 microwave companies.

Compact Software, Inc. of Paterson and Communication Techniques, Inc. of Whippany each contributed \$10,000 to support the laboratory which will be part of NJIT's Center for Microwave and Lightwave Engineering. The contributions will be used to provide microwave equipment and supplies for the laboratory. Other contribu-

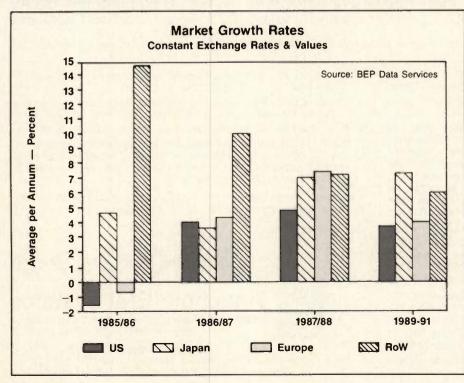
World Electronics Market to Reach \$492 Billion in 1988

The World Electronics Market is forecast to reach \$492 billion (U.S.) in 1988 according to a report published by BEP Data Services. This represents real growth, excluding inflation and exchange rate fluctuations, at a rate of 6 percent over 1987. The forecast calls for a growth of 4.9 percent in the United States which is higher than the 4.1 percent recorded in 1987. However, the consumer electronics products market is forecast to drop by around 1 percent. The rate of growth for the U.S. electronic market as a whole will reduce to under 4 percent per annum over tions of both cash and equipment have been promised from more than a dozen microwave equipment manufacturers throughout the New York metropolitan area.

The incentive for establishing the microwave laboratory stems from the decreasing number of microwave engineers produced every year by universities throughout the United States. According to Gerald Whitman, director of the Center for Microwave and Lightwave Engineering at NJIT, the number of graduates has not kept pace with the growth of the microwave industry, or the technological development.

the 1989 to 1991 period.

The report also indicates that Japan is projected to recover in 1988 with a real growth of 7.1 percent. The follows a poor year, by Japanese standards, in 1987 where growth was 3.7 percent due to the dampening effects of the strong yen on the industry's exports. The 14 other significant electronics markets outside Europe which BEP terms the "Rest of the World" consists mainly of Asia-Pacific, Brazil and South Africa. This market grew significantly in 1986 and 1987 with high exports of equipment from Asia-Pacific. However, the slowing market in the U.S.A. for consumer goods will adversely affect exports



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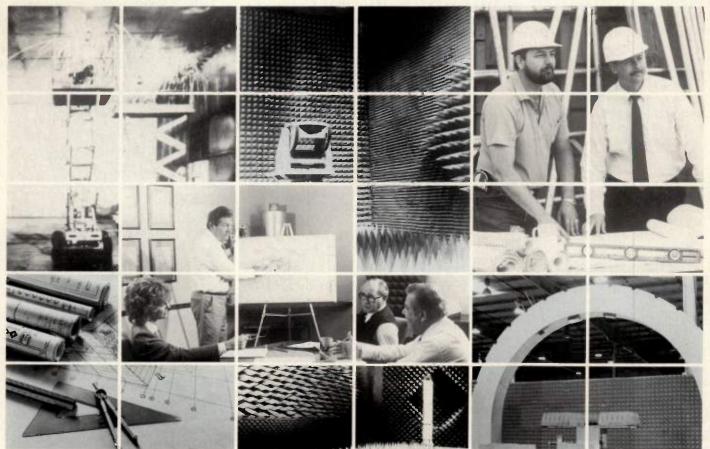
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for the Far East and the rate of market growth in these countries will fall to 7 percent in 1988 and down to 6 percent per annum in the subsequent three years.

The Yearbook of World Electronics Data 1988 costs \$975 and can be obtained from BEP Data Services, PO. Box 28, Luton LU2 OBL, England.

W-J Receives \$9M Order

Watkins-Johnson Company has received an order worth over \$9 million for the delivery of additional subsystem equipment for the Royal Australian Navy's new construction submarine program. It includes a significant amount of Australian industry involvement. The order was made under an existing contract with Rockwell Ship Systems Australia, Pty. Limited.

Zenith Wins Cable Terminal Contract

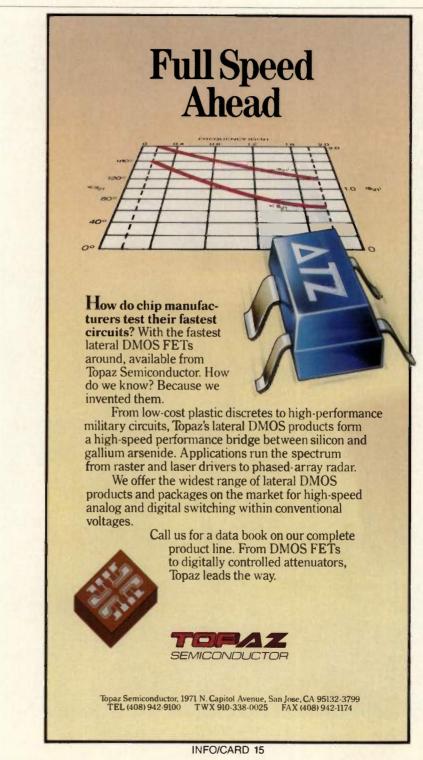
Zenith Electronics Corporation will be producing new cable terminals to provide customers with access to a wide variety of interactive services. This cable and telecommunications system is being launched in Canada and the United States. Under the agreement (valued at approximately \$14 million Canadian), Zenith will manufacture an initial order of 55,000 video terminals for Les Enterprises Videoway Ltee. The terminals will allow access to videotex data bases, teletext, electronic messaging, closed captioning and downloading of computer software via cable in addition to basic cable, pay-TV, pay-per-view and interactive television services. Zenith expects to begin shipment by the end of 1988.

MSC Expands Foundry Capabilities

Microwave Semiconductor Corporation has supplemented its small signal gallium arsenide foundry service with large signal modelling and added two new high power processes. The new 300 mA/mm higher power process is added to the existing 200 mA/mm small signal medium power process to allow design engineers to extend the range of applications for GaAs MMICs to higher power levels of operation with predictable results.

TriQuint and HP to Integrate MMIC Foundry Library

TriQuint Semiconductor announced that it has signed an agreement to incorporate its MMIC library of foundry design models into Hewlett-Packard Company's microwave CAE design system. The HP 85150A microwave design system is a software package that provides schematic capture, simulation, chip layout, and overall project documentation. The Tri-Quint MMIC Library models are algorithmic expressions that represents the electrical behavior of MMIC elements over a wide range of bias and operating frequency. By providing access to these models within a CAE system, a designer can simulate the operation of a MMIC to be built in the TriQuint foundry before investing in fabrication, packaging and evaluation of the actual device. In the agreement, TriQuint is responsible for maintaining and further developing the library. The current library works on with the HP microwave design system software to represent MMIC elements built in TriQuint's HA 0.5 micron microwave and 1A general purpose IC processes. The LINMOD 3.2 library is priced at \$995 and will be available in July from TriQuint. Telephone (503) 644-3535.





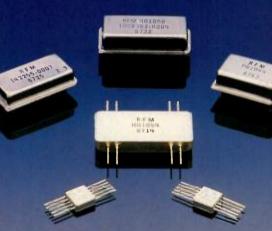
TIW Awarded Contract

TIW Systems, Inc., of Sunnyvale, Calif., has been awarded a contract to upgrade the tracking and antenna control system at the matura earth station by the Trinidad and Tabago External Telecommunications Company Limited (TEXTEL). TIW will replace the existing tracking and antenna control system with a microprocessor based satellite tracking drive system.

Lockheed Awards Contract to W-J

Lockheed Aeronautical Systems Group of Ontario, Calif., has awarded a \$5.7 million contract to Watkins-Johnson Company. The contract calls for W-J to supply products and technology to enhance existing wideband receiving systems with the addition of a direction-finding capability. Work on the contract will be conducted at W-J's San Jose facility.

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Noise Com Moves to Larger Facility

Noise Com, a supplier of noise sources, automated noise generating equipment, digitally tuned VCOs and synthesizers, has moved to a larger facility at E. 64 Midland Avenue, Paramus, NJ 07652. Their phone number is (201) 261-8797.

Rantec Opens New Manufacturing Plant

Rantec Anechoic/EMI Systems Group has opened a 94,000 square foot plant in Durant, Okla. The plant is equipped with absorber manufacturing equipment including two semi-automated impregnators and a computer controlled saw.

Leader Announces New Facility

Leader Instruments Corporation has announced the completion of a 10,000 square foot facility in Cypress, Calif. The new building houses sales, service, and warehousing.

ERA Launches Standards Program

The Electronic Representatives Association (ERA) announced the launch of Commitment to Performance, a program to articulate the professional standards that customers and principals should expect from manufacturers representatives in the electronics industry. A brochure that spells out the eight performance standards that a principal can expect from a manufacturers' representative, plus seven standards that customers can expect is available.

Rockwell Opens European Design Center

Rockwell has opened the Semiconductor Products Division of Rockwell International's European remote design center in Sophia Antipolis, France. Initial activities for the design center include hardware and software enhancements of the division's data and image, telephony, ISDN and local area network products to meet customer and European PTT specifications. Computer design equipment located in the facility will be linked directly to the division's research and development center in Newport Beach, Calif. The center is located at Les Taissounieres -B.1, Route des Doulines, Sophia Antipolis, 06560 Valbonne, France.

Penstock Opens New Sales Offices

Penstock has opened three new sales offices in Arizona, Colorado and Indiana. The new locations will cover southern Nevada and Arizona from the Tempe, Ariz., office; Colorado and Utah for the Englewood, Colo., office and Indiana and Ohio from the Indianapolis, Ind., office.

rf cover story

GaAs Switch Modules For High Performance Applications

By Raymond S. Pengelly, Amin Ezzeddine and Barak Maoz Tachonics Corporation

GaAs MMIC technology and circuit design has matured to a point where a number of products are now available commercially. Until recently these products have been single bare die or packaged single devices. Subsystem research and development centers have been building multi-chip modules for some time (ref. 1 and 2 for example), but there have been very few examples of such items being commercially available. This article describes a number of GaAs MMIC switches and the assembly of these parts into multi-chip modules built specifically as cost-effective products.

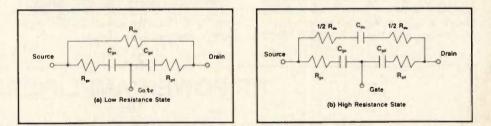
standard 1 micron in-house fabrica-Ation technology is used to manufacture the MMICs described. The active devices, FETs and diodes, are produced using optimized selective implantation of Si resulting in planar circuits. Resistors are produced using either n or n+ implants or TaN thin-films. Capacitors are produced using Si₃N₄ dielectric which is also used for FET and other component passivation. Crossovers of second-level over first-level metallization employ airbridges. Following thinning, through-GaAs vias are etched if necessary, the 3 inch wafers being back metallized, DC tested and sawed.

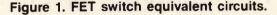
MESFETs are generally used as replacements for PIN diodes in MMICs because the technology required to produce high performance planar PIN diodes is less well understood and more difficult than that required for FETs. It is, therefore, also easier to integrate other functions on the same die, reducing fabrication complexity and increasing yields. Depletion mode FETs (normally "on") are used where the FET is pinched-off to produce its high resistance state. The low resistance achieved in the "on" state of the FET is determined by a number of technological factors including channel resistance, ohmic contact resistances and parasitic resistances between drain and source.

The GaAs FET switch requires very little current and low voltage to control it, typically -5 to -8 volts (i.e., 2 volts or so beyond pinch-off) at a few tens of microamps. The other major electrical advantage of the FET switch is its low video breakthrough from the control port (gate) to the RF line (drain and/or source). This results in the device having very low switching harmonics in the RF band. For example, a typical MESFET switch operating at 10 MHz p.r.f. with 5 nsec. rise and fall-times will have a spectral noise contribution of -60 dBm at 200 MHz without any video filtering. The switch has also intrinsically very fast switching speed, typically less than 2 nsec. The small size and relative ease of use of MMIC switches allows the replacement of PIN diode circuits at frequencies up to 20 GHz in such applications as switched VCOs, channelized receivers and phase shifters. The advantages and disadvantages of FETs versus PIN diodes used as switches are summarized in Table 1.

The MESFET used as a switch is basically a voltage controlled resistor

whose resistance value is determined by the voltage applied to the gate of the device. No intentional DC voltage needs to be applied to the drain or source of the FET as in an amplifier. Figure 1 shows a simple equivalent circuit of the FET switch in its low and high impedance states. When the FET is in its "on" state the insertion loss of the switch is mainly due to the drain-to-source resistance, R_{ds}. A typical value for R_{ds} is 3 ohms for a 1 mm gate width FET having 1 μm gate length. In the "off" state the isolation is mainly determined by the corresponding R_{ds} value only at low frequencies (typically 100K ohm) and, as frequency increases by the parasitic drain-to-source capacitance $(C_{ds} + \frac{1}{2}[C_{gs} + C_{gd}]$, where the gate is assumed to be RF opencircuit). Typical values for this parasitic capacitance are 0.4 pF, leading to a reactance of 50 ohms at 8 GHz. In order to provide isolation at high frequencies either this capacitance has to be reson-





Characteristic	FETs	PINs
Insertion Loss	Can be low	Usually low
Isolation	Excellent	Good
Current consumption	Very low - typ. 50 uA	Medium - typ. 20 mA
Operation to DC	Very good	Not possible
Video breakthrough	Very low without	Only low with
	precautions	precautions
Power handling	Up to 10 watts CW	100 watts but at
		lower speed
Speed	Very fast - 1 nsec.	Fast - 10 nsec.
Phillippe and the set		

Table 1. FETs versus PIN diodes.

ated with inductance leading to relatively narrow-band operation or incorporated into a filter/travelling-wave structure to provide wider bandwidths.

The high-isolation broadband switches designed by Tachonics employ both series and shunt FETs, but without using series or shunt tuning elements. The effective source-drain capacitance of the shunt FETs is incorporated into a capacitively loaded transmission line (Figure 2). The addition of series FETs to each branch of the SPDT switch improves the low frequency isolation when they are biased to their high resistance states, while the shunt FETs biased to their low resistance states provide isolation at the higher frequencies. Many of these MMIC switches also contain switched 50 ohm loads at each throw such that the devices maintain a matched condition in both "on" and "off" states. These loads can optionally not be bonded to ground providing a switch with "open circuit" reflective VSWRs in the "off" state.

Product Examples

The TCSW-0400 SPDT switch operates over DC to 6 GHz and has been manufactured in volume production. This switch has an insertion loss of 0.8 dB at 4 GHz with a corresponding isolation of >35 dB and a 1 dB input compression point of >30 dBm. This switch has been used to produce a number of low frequency custom designs such as transfer switches and digital attenuators.

The main electrical parameters of the complete family of switch devices are shown in Table 2. Figure 3 shows the circuit diagram of the TCSW-0600 SPDT

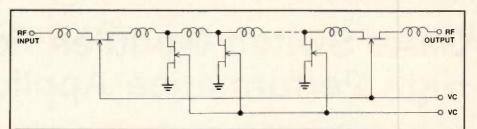


Figure 2. Switch using FETs in artificial transmission line.

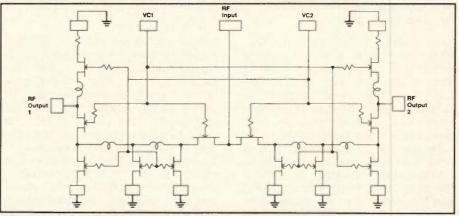


Figure 3. Circuit design of TCSW-0600 absorptive switch.

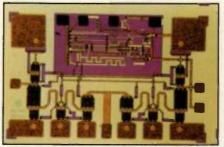


Figure 4. Photograph of TCSW-1600 SPDT switch with driver.

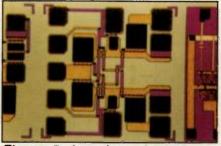


Figure 5. Low loss, high power SPDT switch (TCSW-1300).

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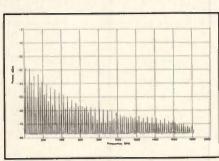


Figure 6. Switching noise spectra of TCSW-0500.

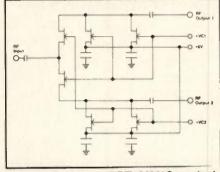


Figure 7. GaAs FET MMIC switch operated from positive voltages.

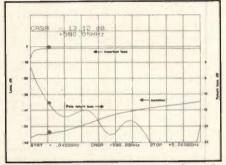


Figure 8. RF performance of positive supply configuration.

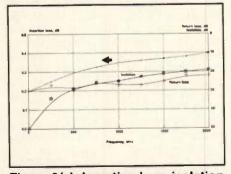


Figure 9(a). Insertion loss, isolation and return loss of TCSW-1301 SPDT switch.

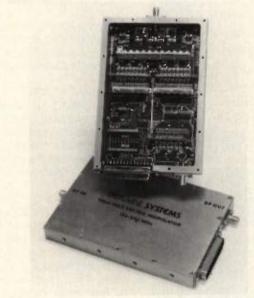
switch (3). The switch can be configured to be either absorptive or reflective allowing it to be used in multithrow switch applications. The TCSW-0500 and 0600 SPST and SPDT switches have been

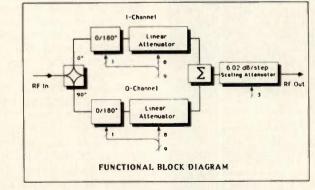
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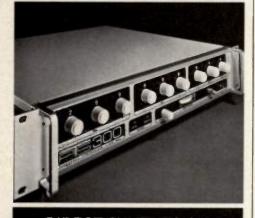
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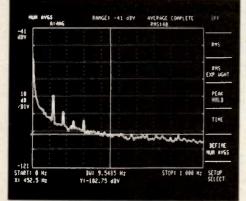
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used in a range of high isolation, fast switch modules operating up to 8 GHz. Options with GaAs on-board TTL drivers have also been dveloped. The TCSW-1600 SPDT switch with driver is shown in Figure 4. Derivatives of the TCSW-0500 through 0800 switch family are the TCSW-5300 and 5400 devices, fully integrated SP3T and SP4T die operating over 8 GHz bandwidths. These latter switches are basically combinations of SPST switches. All were designed for both single and multi-die packaging and driver interfacing.

Like PIN diode switches, MMIC counterparts can use both series and shunt FETs. In this case, drivers with complementary control voltages are required to drive the series and shunt FETs in the circuit. A number of drivers exist, such as the Teledyne TSC CMOS 420-430 series, Harris CMOS HI 5051, Maxim IH 5043 and Impellimax GX 101B2 circuits. In addition to using such external drivers, Tachonics has also developed two integrated driven switches (the TCSW-1500 and 1600, SPST and SPDT), which incorporate 1 volt threshold depletion mode gates on the same die as the RF switches.

Features of MMIC Switches

GaAs MMIC switches using MESFETs have a number of features which can be optimized during design. These features include power handling, switching speed and video breakthrough, all areas where similar attention has also been given to PIN diodes.

Power Handling. The power handling capability of MESFET switches is dependent on a number of device variables such as saturated drain current (normal FET definition), pinch-off voltage, gate-todrain and gate-to-source breakdown voltages. These parameters are influenced by four major process dependent variables: gate recess depth and width, total gate width, implantation profiles and gateto-drain and gate-to-source separations. In addition it is possible to enhance RF power handling by changing the working characteristic impedance, and by using a number of FETs in novel arrangements. The TCSW-0400 and TCSW-0600 switches, for example, have 1 dB insertion loss compression input powers of 1 and 2 watts CW, respectively. As frequency is decreased the devices are not able to handle so much CW power, particularly as the frequency goes below $1/\pi R_g (C_{gs} + C_{gd} + 2C_{ds})$, where R_g is the resistance used in series with the gate of each FET. For example, a 1µm gate width FET having a 2k ohm gate resistor, the frequency at which RF power handling starts to degrade is approximately 250 MHz.

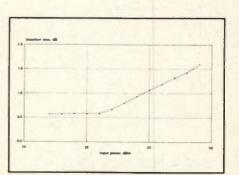


Figure 9(b). Compression characteristics of TCSW-1301 at 3 GHz.

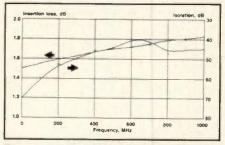


Figure 10. Performance of transfer switch module.

This knee frequency can be decreased by either increasing the gate width (at the expense of switching speed and bandwidth) or increasing the value of R_a .

or increasing the value of R_g. Because switch power handling depends also on the instantaneous RF voltage across gate-to-drain and gate-tosource it is usual that FETs will handle RF power until either the gate diode is forward-biased during the positive halfcycle or reaches breakdown in the negative half-cycle. Thus, a careful compromise has to be made with the quiescent gate voltage that is applied in the "on" or "off" states (depending on whether the FET is in series or shunt). For a FET with a >16 volt (gate-tosource/drain) breakdown voltage, a control voltage of -8 volts will assure that the source/drain is able to handle approximately 8.7 volts or 16 volts when the gate is at -8 or 0 volts, respectively. In the simplest case of a series/shunt FET combination the switch is then able to handle approximately 162/100 watts or 2.5 watts.

Tachonics has developed a 5 watt CW SPDT switch having a loss of <0.4 dB up to 2 GHz. In order to provide such power handling the switch incorporates FETs whose parameters have been optimized simultaneously by a combination of small and large signal analyses (Figure 5).

Switching Speed and Video Breakthrough. The MESFET is intrinsically a fast switch where the channel is depleted of electrons, following the time constant

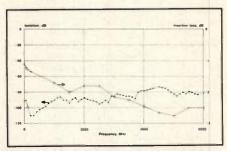


Figure 11. Isolation and insertion loss of very high isolation switch.

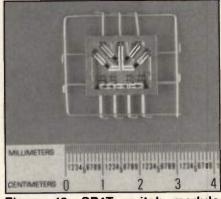


Figure 12. SP4T switch module with driver.

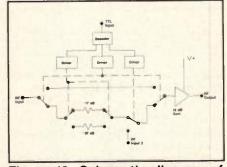


Figure 13. Schematic diagram of programmable gain module.

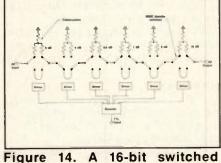


Figure 14. A 16-bit switched attenuator.

determined by the driving impedance of the gate source/drain and the total gate circuit capacitance. For a 1μ m gate width FET the switching speed is approximately 0.5 nsec assuming a 2K ohm resistor in series with the gate electrode. For example, the TCSW-0600 switch consists of 5 devices per throw having a combined switching time of approximately 1.5 nsec. It is not only important to provide GaAs MMIC switches with inherently fast switching speed but also to ensure that the time constants to the FETs in the switch are equal such that video breakthrough is minimized. Such design action also ensures that the switching noise (spectral content of the video breakthrough) is minimized, a particularly important feature in maximizing dynamic range at low frequencies.

An example of the switching noise spectra of a TCSW-0500 SPST switch driven at a 15 MHz PRF (50 percent duty cycle) with complimentary control pulses from a fast TTL driver having 5 nsec rise and fall times is shown in Figure 6. No special precautions were taken in reducing the video breakthrough as would be the case for a PIN diode counterpart.,

Driving MMIC Switches from Positive Voltages. Many users need to use positive control voltages, usually for one or two reasons: either only positive supply rails are available, or they are more familiar with using positive voltages from their PIN diode experiences.

Figure 7 shows a scheme where the TCSW-0400 SPDT switch is controlled with standard TTL logic levels. The shunt FETs are raised off ground using 120 pF chip capacitors while the RF switch elements use 30 pF chip blocking capacitors. The sources of the shunt FETs are raised to a positive voltage equal to the absolute value of the control voltage (e.g., +5 or 8 volts). The RF performance of such a switch arrangement is shown in Figure 8. The switch operates well from 250 MHz to its upper design limit, but operation to DC is not possible.

Applications

High Power Switches. Commercially available MMIC switches have typically had 1 dB compression points in the 1/2 to 1 watt CW region. The switch shown in Figure 5 was specifically designed for higher power applications. Gate widths were chosen to handle the required peak RF currents, where the structure of the FETs was optimized for high breakdown voltage. The switch was also designed for low loss in the DC to 3 GHz frequency band, enabling its application in L- and Sband active array radar transmit/receive applications. The two throws of the switch can be bonded differently allowing high power switching on the transmit throw and high isolation (lower power) on the receive

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throw. Figure 9(a) shows the small-signal performance of the switch in the receive and transmit throws indicating an insertion loss for the die itself of 0.35 dB at L band. In the transmit throw the power handling is indicated by Figure 9(b) where a 1 dB compression point of 5 watts CW is shown.

Switch Modules. A range of switch modules has been built using combinations of TCSW-0400 through 0800 switch die and various drivers (CMOS, TTL, ECL) in a variety of packages. For low frequencies (<2 GHz) requiring medium isolations (>30 dB) low cost glass-to-metal seal flatpacks can be employed. Figure 10 shows the main results of a transfer switch used at radar IFs up to 1 GHz, driven by two CMOS drivers in a 14 lead flatpack. Insertion loss was <1.6 dB with a minimum isolation of 53 dB at 200 MHz. Total switching time including driver delay time was 70 nsec. The complete unit measures only 0.375 inches square by 0.11 inches deep.

Isolations in excess of 70 dB or so require special attention to be paid to both the RF and control circuits. Figure 11 shows the insertion loss and isolation of a unit which contains three packaged switches integrated onto a single microstrip carrier. In order to retain high isolation the three switches are not only housed in separated compartments but special attention was also paid to minimizing RF leakage through the control ports via the driver circuitry.

As discussed previously, SPST switches are often used to produce multithrow units. Figure 12 shows an example of a matched SP4T using 4 TCSW-0700 SPST switch die for operation to 8 GHz. This same unit can also be used as an SP3T switch where all the ports are matched even in the "all-off" state. In this unit four CMOS drivers are used with a single -12 volt rail. The module size is 0.625 inches square by 0.13 inches deep.

Table 3 summarizes some of the performance features of Tachonics MMIC switch modules.

Programmable Gain. MMIC switches and amplifiers have also been combined with chip attenuators to produce programmable gain modules. Figure 13 shows the schematic of such a unit operating over the 500 MHz to 4 GHz frequency range. In this particular unit, five TCSW-0400 SPDT die were combined with a single TCWL-0100 50 MHz to 4 GHz amplifier die allowing +10, -10 and -30 dB gains to be chosen from port 1 to port 3 or the full gain of 10 dB from port 2 to port 3. Return loss is better than 13 dB on both inputs and the output under all gain conditions.

Model	Frequency Range,	Insertion Loss, dB	Isolation dB	Maxi	mum	1 dB Input Power
Number	GHz	@ f1 & f2	@ f1 & f2	On	Off	dBm
0400	DC-6	0.6/1.0	70/30	<1.5	Refl.	30
0401	DC-5	0.8/1.5	70/27	<1.5	Refl.	30
0402	DC-6	0.8/1.4	65/27	<1.7	Refl.	30
0700	DC-18	0.8/1.7	55/34	<1.6	<2.0	31
0800	DC-18	1.1/2.2	63/34	<1.9	<2.5	31
0502	DC-8	0.7/1.4	63/45	<1.5	<1.5	31
0602	DC-8	1.0/1.8	66/40	<1.5	<1.5	31
0720	DC-18	1.1/3.2	52/35	<2	<2	31
0820	DC-18	1.5/3.0	60/30	<1.9	<3.0	31
1301	DC-3	0.20/0.45	70/40	<1.2	<1.4	37
1607 (note)	DC-8	1.0/1.8	66/40	<1.5	<1.5	31

Notes

f1 is 45 MHz for 04xx and 1301.

f2 is 3 GHz for 1301.

f2 is 6 GHz for 0400, 5 GHz for 0401 and 6 GHz for 0402.

f1 is 1 GHz for 0502, 0602 and 1607; 8 GHz for 07XX and 08XX.

f2 is 8 GHz for 05XX, 0602 and 1607; 18 GHz for 07XX and 08XX.

TCSW-1607 is a SPDT switch with integrated GaAs TTL driver.

Table 2. Summary of performance of Tachonics GaAs MMIC switches.

Product No.	TCSW-0140	TCSW-0240	TCSW-1141	TCSW-1241	TCSW-14XX
TYPE	SPST Non-reflective Undriven	SPDT Non-reflective Undriven	SPST Non-reflective Driven	SPDT Non-reflective Driven	SP4T Non-reflective Driven
Isolation	70 dB min.	70 dB min.	70 dB min.	70 dB min.	50 dB min.
Insertion Loss	2 dB max.	2 dB max.	2 dB max.	2 dB max.	2.5 dB max.
Rise/Fall Time	2 nS max.	2 nS max.	8 nS max. 5 nS typ.	8 nS max. 5 nS typ.	100 nS max.
Rep. Rate	200 MHz max.	200 MHz max.	15 MHz max.	15 MHz max.	4 MHz max.
Package Style	TC40 Drop In	TC40 Drop In	TC41 Drop In	TC41 Drop In	
Prototypes Ready By	Available	Available	Available	Available	6/30/88

Table 3. Performance characteristics of Tachonics switch modules.

Switched Attenuators. Figure 14 shows the circuit schematic for a 4 bit switched attenuator covering DC to 3 GHz employing eight TCSW-1300 SPDT die together with four thin-film chip T-attenuators providing 1 through 15 dB attenuation. The phase of each attenuator bit is held constant by equalizing the electrical length between the "straight-through" path and the attenuator pads. Using the TCSW-1300 low-loss SPDT die results in an insertion loss per bit of as low as 0.7 dB, leading to a 3.0 dB insertion loss for the complete four-bit attenuator.

Conclusions

This article has described the operation and application of a number of GaAs MMIC FET-based switch die and modules. Information on the characteristics and control of such devices has shown that such switches have now reached a relatively mature status. MMIC FET switches have a number of advantages over PIN diodes, particularly in respect to low video breakthrough, conveniently high isolations per chip and the ability to be intrinsically matched while operating with very low drive currents.

The authors would like to thank a number of their colleagues who have made significant contributions to the work described above. They include: D. Brant, J. Faguet, R. Makkay, and the wafer fabrication, assembly and test teams.

For more information on the GaAs MMIC switch modules described in this article, circle INFO/CARD #190.

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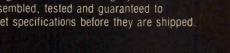
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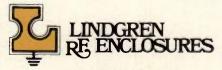
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rf featured technology

Principles of Microstrip Design

By Alam Tam American Microwave Technology

Microstrip is becoming a more important and popular transmission media for use in the RF and microwave industry, based upon its electrical, cost, size, manufacturability and reproducibility performance. All RF, microwave and high speed digital circuit designers should become familiar with microstrip design techniques to better their job skills. This microstrip design tutorial will enhance your understanding of the advantages, electrical characteristics and physical properties through practical applications, specific equations, useful tables and design examples.

Microstrip is a transmission line which carries the RF signal from one point to another with a unique characteristic impedance. The characteristic impedance is determined by the selected w/h ratio, plus the board or substrate material used. The physical cross-sectional construction of a microstrip above the dielectric medium is shown in Figure 1. The high frequency energy will produce a skin effect on the microstrip conductor. The current density around the conductor is then concentrated on a sheet where a skin thickness deep on the surface will be exposed to the electric field.

The transverse electric (TE) mode defines that a transmission have magnetic field components in the direction of energy flow yet the electric field is everywhere transverse. On the other hand, transverse magnetic (TM) mode defines that the transmission have the electric field components in the direction of energy flow yet the magnetic field is everywhere transverse. They are referred to as "higher order" modes for microwave transmission.

As a practical recommendation, the physical separation between the metallic cover and the actual microstrips on the substrate should be at least 10 times the substrate thickness. To avoid the higher waveguide mode excitation within the enclosure, the physical width of the housing in which the microstrip are situated, should not be more than one half of the free space wavelength at the operating frequency.

Microstrip design has proven to be a very mature technology. It provides wide operating bandwidth, good miniaturization, tremendous weight reduction, satisfactory thermal characteristics, reasonable RF power handling, ease of component integration and mass production. It is being widely used in many military and spaceborne programs. Historically, the reliability performance of microstrip designs has been successful, as predicted.

Review of Fundamental Transmission Line Equations

Since the electric and magnetic field lines and the ground plane are not entirely contained in the substrate as shown in Figure 2, the propagating mode along the microstrip is not purely transverse electromagnetic (TEM) but quasi-TEM. Assuming it is quasi-TEM, the phase velocity in the microstrip is given by:

$$V_{\rm p} = C/\sqrt{\epsilon_{\rm eff}} \tag{1}$$

The wavelength in the microstrip line is given by:

$$\lambda_{g} = V_{p}/f \tag{2a}$$

Where,
$$\lambda_g = \lambda_o / \sqrt{\epsilon_{eff}} = C / f \sqrt{\epsilon_{eff}}$$
 (2b)

 λ_{q} = microstrip wavelength

 $\lambda_o =$ electrical wavelength in the free air

RF Design

The characteristic impedance of the transmission line is also given by:

 $Z_0 = 1 / V_p(C)$, where $C = 3 \times 10^8$ m/s (3)

Determination of Characteristic Impedance (Zo) The Wheeler and Schneider formulas shall be considered

because they provide accuracy better than 2 percent. For w/h < 1,

01 101

$$Zo = \frac{60}{\sqrt{\epsilon_{eff}}} \quad In (8h/W_e + .25W_e/h), where:$$
(4a)

$$\in_{\text{eff}} = \frac{\in_{r} + 1}{2} + \frac{\in_{r} - 1}{2} \left[(1 + 12h/W_{e})^{-0.5} + 0.04(1 - W_{e}/h)^{2} \right]$$
(4b)

For w/h > 1,

$$Zo = \frac{377}{\sqrt{\epsilon_{\text{eff}}} \left[W_{\text{e}}/h + 1.393 + 0.667 \ln(W_{\text{e}}/h + 1.444)\right]}$$
(5a)

$$W_e = W + \frac{t}{\pi} (\ln \frac{2h_e}{t} + 1)$$
 Effective microstrip width (5b)

 $h_e = h - (2t)$ Effective thickness of the substrate (5c)

$$\in_{\text{eff}} = \frac{\in_{\text{r}} + 1}{2} + \frac{\in_{\text{r}} - 1}{2} [(1 + 12h/W_{\text{e}})^{-0.5}] \text{Effective dielectric constant}$$
(5d)

Example #1: What is the characteristic impedance of an 80 mil wide microstripline on a 1/32" thick Teflon fiberglass board with a dielectric constant of 2.55?

$$\epsilon_{\text{eff}} = 1.78 + 0.775 \ (0.425) = 2.1$$

$$W_e = 80 + 2 = 82 \text{ mil}$$

$$h_e = 31 - 2(1.4) = 28$$
 mil

@t = 1.4 mil thickness for 1 oz. copper clad material

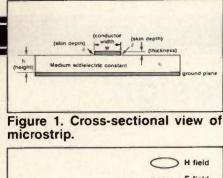
We/h=82/31=2.65, which is greater than 1 in this case:

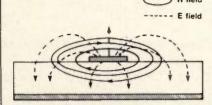
Thus, Zo =
$$\frac{377}{\sqrt{2.1} [82/31+1.393+0.667 \text{ In } (82/31+1.444)]}$$
$$=\frac{377}{7.22} = 52 \text{ Ohms}$$

An alternate solution can be achieved by using the graphical design approach, referring to Figure 3. Draw a vertical line starting at w/h = 2.65 axis until it intercepts with the e = 2.55 curve. At the same point, make a line in parallel with the w/h axis until it reaches the vertical Zo axis. Therefore, the new characteristic impedance can be easily found and the result should be fairly close to that calculated in Example #1.

Electrical length of an inductive microstrip is determined by:

$$I = \frac{L \times V_{p}}{Z_{0}}$$
(6)





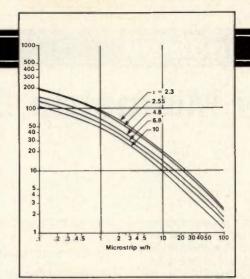
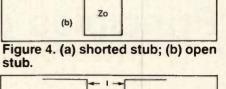


Figure 3. Characteristic impedance vs. w/h ratio.



Zo

(a)

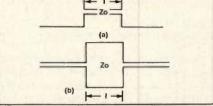


Figure 5. Electrical length (I) and Zo relationships: (a) inductor; (b) capacitor.

Propagation Delay of a Microstrip

The propagation delay normally has nothing to do with the characteristic impedance of a microstrip, and is described by:

$$t_{pd} = \sqrt{0.475 \epsilon_r + 0.67} \text{ nsec/ft.}$$
 (10)

Example #4: What is the propagation delay of a foot long microstrip on a G-10 fiberglass epoxy board which has a dielectric constant of 4.8?

$$t_{pd} = \sqrt{0.475} \in + 0.67 \text{ nsec/ft.} \times 1 \text{ ft.} = \sqrt{0.475(4.8)} + 0.67$$

= 1.75 nanoseconds.

Physical and Electrical Properties

1. Conductor and Dielectric Losses — There are two major dissipative losses in any microstrip design. Dielectric loss normally is inversely proportional to the sheet resistivity (R_s) of the substrate. For a well-defined characteristic impedance, the conductor loss decreases inversely with the substrate thickness and increases with the square root of the frequency. For Conductor Loss:

$$a_c = 8.68 \text{ R}_s / \text{Zo} \times \text{w} @\text{w/h} > 1, \text{ dB/cm}$$
 (11a)

$$\alpha_{\rm c} = 23.4 \sqrt{f({\rm GHz}) \times \rho} \, {\rm dB/in} \tag{11b}$$

 α_{c} = attenuation coefficient

 ϱ = conductor resistivity in Ohm/cm, assuming that there is no sheet roughness on substrate

For dielectric loss:

$$\alpha_{d} = 2.56f(GHz) \left[\frac{(\in_{eff}(f)-1)}{\sqrt{\in_{eff}(f)}} \right] \quad (1.2^{1/3.4})(tan\delta) \text{ dB/in}$$
(12)

 $tan \delta = loss tangent of a dielectric material$

$$\mathsf{R}_{\mathsf{s}} = \sqrt{\pi \mathsf{f}_{(\mathsf{HZ})} \mu \varrho} \tag{13}$$

R_s = surface skin resistivity in Ohm per unit square

 σ = conductor conductivity in mho/meter = 1 / ρ

 μ = permeability of the free space (4x π x10⁻⁷ Henry/meter)

Surface roughness will increase the conductor loss. Conductor losses usually greatly exceed dielectric losses for most microstrip lines, especially on alumina or sapphire substrates. However, semiconductor substrates with either silicon or gallium arsenide material result in much higher dielectric losses than conductor losses.

Example #2: What is the electrical length of a 5 mil wide microstrip on an Alumina substrate with inductance of 15 nH?

First of all, the given information is listed as below:

 $E_r = 9.8$; h = 25 mil w/t = .75 mil

Using Equations 5b, 5c and 5d, W_e = 6 mil, h_e = 23.5 mil and ε_{eff} = 6.0 can be calculated.

For w/h < 1, Zo =
$$\frac{60}{\sqrt{\epsilon_{eff}}}$$
 ln (8h/W_e+.25 W_e/h) = $\frac{60}{\sqrt{6.0}}$ ×
ln (8 × 25/6 + .25 × 6/25)=86 ohms
therefore, I = $\frac{L \times V_p}{Zo} = \frac{15 \times 10^9 \times 0.41 \times 3 \times 10^{10}}{86}$

Open and Shorted Circuit Reactances

= 2.15 cm or 0.85 in.

Usually, the open and short circuit types of microstriplines are called stubs and they are often used as DC feeding elements and distributed elements for microwave active and passive circuit designs. The shorted stub behaves like an inductor and it normally has high characteristic impedance. On the other hand, the open stub behaves like a capacitor and it has very low characteristic impedance. Practically speaking, the electrical lengths of the shorted and open stubs are directly related to the effective wavelengths at the specific operating frequency.

$$Z_{sc} = X_L = +j Zo \tan\theta$$
 (ohm) (7a)
It becomes an inductive reactance.
 $Z_{oc} = X_C = -j Zo \cot\theta$ (Ohm) (8a)

It becomes capacitance reactance.

 $\theta = \tan^{-1} \left(X_L / Z_0 \right) \tag{7b}$

 $\theta = \cot^{-1} \left(X_{\rm C} / {\rm Zo} \right) \tag{8b}$

$$\theta = 2\pi i / \lambda_{\alpha}$$

$$I = \Theta \lambda_0 / 360^\circ \sqrt{\epsilon_{\text{eff}}}$$
(9b)

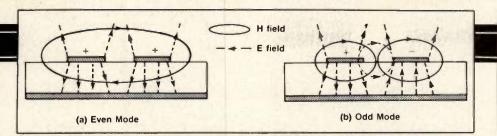
Example #3: What will be the electrical length of a microstrip if its reactance equals the characteristic impedance?

Since $\theta = \tan^{-1}(X_L/Z_0) = \tan^{-1}(1) = 45^\circ \text{ or } \lambda_q / 8.$

Likewise,
$$\theta = \operatorname{Cot}^{-1}(X_c/\operatorname{Zo}) = \operatorname{Cot}^{-1}(1) = 45^\circ \text{ or } \lambda_o / 8.$$

Then, $X_L < 90$ Ohms, $X_c > 30$ Ohms are suggested for practical circuit application.

(9a)



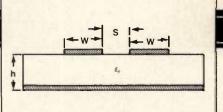


Figure 6. The electric and magnetic field distribution for the (a) even mode (b) odd mode wave excitation in the coupled microstrip lines.

Example #6: What are the conductor and dielectric losses of a 21 mil wide 50 ohm microstrip on a Duroid 6010 board operating at 10 GHz assuming no sheet roughness?

Given:
$$\varrho = 2x10^{-6}$$
 ohm/cm, $\in_{eff}(10 \text{ GHz}) = 7.0$, $\tan \delta = 12x10^{-4}$ in;
 $\alpha_{c} = 23.4 \sqrt{f(\text{GHz})x \ \varrho} = 23.4 \sqrt{10(2x10^{-6})} = 0.105 \text{ dB/in.}$
 $\alpha_{d} = 2.56f(\text{GHz}) \left[\frac{(\in_{eff}(f)-1)}{\sqrt{\in_{eff}(f)}} \right] (1.2^{t/3.4})(\tan \delta)$
 $= 2.56(10) \left[\frac{(7.03 - 1)}{\sqrt{7.03}} \right] (1.2)^{2.94}(12x10^{-4}) = 0.12 \text{ dB/in.}$

 $\alpha_{\rm T} = \alpha_{\rm c} + \alpha_{\rm d} = 0.105 + 0.12 = 0.23$ dB/in.

2. Radiation Loss — Microstrip is actually an asymmetric transmission line structure and it is often used in unshielded or poorly shielded circuits where the radiation energy is free either to propagate away or to induce current flow. This will result in power loss. Discontinuities such as an open end, step change, bend or corner will radiate to some extent. For lower dielectric constant substrates, radiation loss is significant at higher characteristic impedance levels. For higher dielectric constant substrates, radiation becomes significant until low characteristic impedance levels are reached. Therefore, special care by the circuit designer to reduce radiation from microstrip circuits is highly recommended. Radiation resistance of open circuited microstrip is given by:

$$\mathsf{R}_{\mathsf{r}} = 240\pi^{2} \left(\frac{\mathsf{h}}{\lambda_{\mathsf{o}}}\right)^{2} \left\{ \left(\frac{\in_{\mathsf{eff}}+1}{\in_{\mathsf{eff}}}\right) - \left[\frac{(\in_{\mathsf{eff}}-1)}{2\in_{\mathsf{eff}}\sqrt{\in_{\mathsf{eff}}}} \ln\left(\frac{\sqrt{\in_{\mathsf{eff}}+1}}{\sqrt{\in_{\mathsf{eff}}-1}}\right)\right] \right\}$$
(14)

Example #7: What will be the resistive radiation loss of a 50 ohm microstrip on a Duroid 6010 board operating at 10 GHz?

$$\begin{split} & \text{Given: } \textbf{E}_{\text{eff}} = 6.8, \ h = 0.0635 \ \text{cm}; \ \lambda_{\text{o}} = 3 \ \text{cm}. \\ & \text{R}_{\text{r}} = 240\pi^2 \left(\frac{h}{\lambda_{\text{o}}}\right)^2 \left\{ \left(\frac{\textbf{E}_{\text{eff}} + 1}{\textbf{E}_{\text{eff}}}\right) - \left[\frac{(\textbf{E}_{\text{eff}} - 1)}{2\textbf{E}_{\text{eff}}\sqrt{\textbf{E}_{\text{eff}}}} \ln\left(\frac{\sqrt{\textbf{E}_{\text{eff}}} + 1}{\sqrt{\textbf{E}_{\text{eff}}} - 1}\right)\right] \right\} \\ & = 240\pi^2 \left(\frac{.0635}{3}\right)^2 \left\{ \left(\frac{6.8 + 1}{6.8}\right) - \left[\frac{(6.8 - 1)}{2(6.8)\sqrt{6.8}} \ln\left(\frac{\sqrt{6.8} + 1}{\sqrt{6.8} - 1}\right)\right] \right\} \\ & = 1.58 \ \text{dB}. \end{split}$$

3. Surface Wave Propagating Loss — Surface Wave energy, trapped underneath the surface of the substrate dielectric medium, will be propagated away in the form of a range of TE and radial TM modes. As operating frequency increases, the surface wave conductance becomes an important loss element.

4. Parasitic Coupling — This primarily includes parallel microstrip resonator or line coupling, which will cause signal cross-talk and fringing capacitance. The electric and magnetic field line distribution of a quasi-TEM coupled microstrip is shown in Figure 6 and a cross-sectional view of a typical coupled microstrip is shown in Figure 7. The most important electrical characteristics depend on the coupling factor, odd mode and even mode characteristic impedances. The physical spacing between the parallel coupled microstrips can then be determined.

In order to improve the crosstalk or isolation in high speed digital systems, the coupled microstrips should have high characteristic impedances and reasonable separation, within space limitation on the board. The mutual inductance and capacitance between two lines are used to determine the crosstalk coefficient. Forward crosstalk (K_b) is normally smaller than the backward crosstalk on microstrip except for very long lines (>5 feet). Forward crosstalk does not exist at all microstrips since they are made with a homogenous medium so that the inductively and capacitively induced currents cancel. The backward crosstalk coefficient for various types of microstrip line on a G-10 fiberglass epoxy board is given by:

$$K_{b} = (1/4\pi t_{pd}) \times (L_{m}/ZO + C_{m} \times ZO)$$

 $L_m = inductive coupling$

 C_m = capacitive coupling

For high speed digital board designs, keep in mind that it is advantageous to have smooth, rounded line edges, constant line widths and minimum lead length for the terminating resistors.

Example #8: What is the backward crosstalk constant between two microstrips with 80 mil spacing on a 60 mil thick fiberglass epoxy G-10 board?

Using the graphical design approach by referring to Figure 8, draw a vertical line starting at s=50 mil Line Spacing Axis until it intercepts with h=59 mil curve. At the same point, make a horizontal line to the Kb axis. Therefore, the backward crosstalk constant or coefficient is found to be 0.06 as shown. the other line (passive line) would have a coupled signal of 6 percent of the amplitude from the active line in a direction opposite to that of the driving signal. The reflection coefficient in this case will be twice as much, or 12 percent.

$$C = 20 \log \left| \frac{Zoe - Zoo}{Zoe + Zoo} \right| dB$$
(15)

$$Zo^2 = Zoe \times Zoo$$
 (16)

$$Zoe = Zo \sqrt{\frac{1 + 10^{20}}{1 - 10^{20}}}$$
(17a)

$$Zoo = Zo \sqrt{\frac{1 - 10^{c/20}}{1 + 10^{c/20}}}$$
(17b)

C = Coupling factor

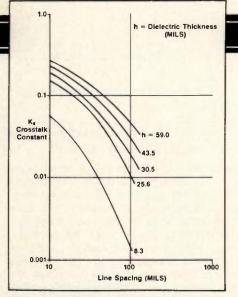
Zoe = Even mode characteristic impedance

Zoo = Odd mode characteristic impedance

 $\lambda_{ge} = 300 \times Zoe / f(GHz) \times Zo1e$ (18a)

- $\lambda_{go} = 300 \times Zoo / f(GHz) \times Zo1o.$ (18b)
- λ_{ge} = Even mode wavelength
- $\lambda_{go} = Odd mode wavelength$

Zoe is due to the microstrips being at the same potential and carrying equal currents in the same direction. Zoo is due to the microstrip being at equal but opposite potential and carrying equal current in opposite direction.



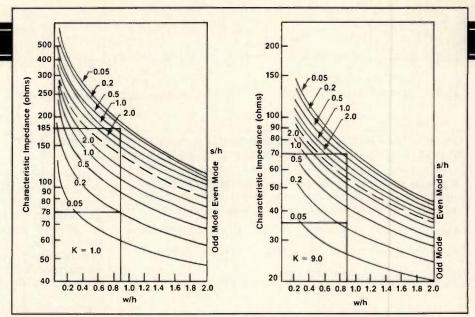


Figure 8. Backward crosstalk on G-10 board.

Figure 9. The Bryant and Weiss curves for (a) e = 1 and (b) e = 9.

Example #9: What should be the width, spacing and length for a 10 dB coupler design using all 50 ohm microstrip on a 40 mil thick substrate @ 4 GHz?

$Zoe = Zo \sqrt{\frac{1+10^{c/20}}{1-10^{c/20}}} = 50 \sqrt{\frac{1+10^{10/20}}{1-10^{10/20}}}$	= 69.5 ohm
$\frac{200}{1-10^{0/20}} = \frac{30}{1-10^{10/20}}$	- 09.9 0mm
$Z_{00} = Z_0 \sqrt{\frac{1 - 10^{c/20}}{2}} = 50 \sqrt{\frac{1 - 10^{10/20}}{2}}$	00
$200 = 20 \sqrt{\frac{1}{1 + 10^{c/20}}} = 50 \sqrt{\frac{1}{1 + 10^{10/20}}}$	= 36 ohm

 $Zo^2 = Zoe \times Zoo = 69.5 \times 36 = 50$ ohm (To doublecheck the Zo and it agrees)

Using the Bryant and Weiss curve in Figure 9b, draw two horizontal lines, one for Zoe and one for Zoo until these two lines intercept with the same s/h ratio. In this case, the s/h ratio is found to be 0.25, then the w/h ratio of 0.87 can be easily located graphically. Secondly, move on to the figure 9a using similar approach, the Zo1e and Zo1o will be equal to 185 ohm and 75 ohm respectively.

 $\begin{array}{l} \lambda_{ge} = 300 \times Zoe \; / \; f(GHz) \times Zo1e = 28 \; mm \; or \; 1.11 \; in. \\ \lambda_{go} = 300 \times Zoo \; / f(GHz) \times Zo1o = 36 \; mm \; or \; 1.42 \; in. \end{array}$

The spacing = $s/h \times h = 0.25 \times 40$ mil = 10 mil

The width = $w/h \times h = 0.87 \times 40$ mil = 35 mil

Since most of the coupler designs use a quarter wavelength approach, the length should be the average of the even and odd mode wavelength divided by 4. The answer for that will be 8 mm or 0.32 in.

5. Dispersion Effect — At higher frequencies, the effective dielectric constant and the characteristic impedance of a microstrip line begins to change and makes the conductor dispersive. In simple words, high impedance transmission lines on thinner substrates are less dispersive. The effect of dispersion on characteristic impedance can be generally neglected. Dispersive effects raise the effective dielectric constant slightly as frequency is increased.

Getsinger's expression for Dispersion is:

$$\epsilon_{\text{eff}}(f) = \epsilon_{\text{r}} - \frac{\epsilon_{\text{r}} - \epsilon_{\text{eff}}}{1 + G(f / f_{\text{o}})^2}$$
(19a)

$$f_{p} = Zo/(8\pi h_{e})$$
(19b)
G = 0.6 + 0.009Zo (19c)

and, f is in GHz and h = Substrate thickness (cm). f_p = the cutoff frequency of next higher order propagation mode.

For
$$f_p >> f_s$$

$$\in_{\text{eff}}(f) = \in_{\text{eff}}$$

Example #10: What is the effective dielectric constant of a 50 ohm microstrip on a 25 mil thick Duroid 6010 board operating at 10 GHz and the $\in_{eff}(0) = 6.80$ and $h_e = 0.056$ cm?

By considering the dispersive effect because of the operating frequency,

$$f_p = Zo/(8\pi h_e) = 50 / 8 \pi \times (0.056 \text{ cm}) = 35.6 \text{ GHz}$$

G = 0.6 + 0.009Zo = 0.6 + 0.009(50) = 1.05

$$\epsilon_{\text{eff}}(f) = \epsilon_{\text{r}} - \frac{\epsilon_{\text{r}} - \epsilon_{\text{eff}}}{1 + G(f / f_{\text{p}})^2} = 10.2 - \left[\frac{10.2 - 6.8}{1 + 1.05(10/35.6)^2}\right] = 7.06$$

An alternate solution uses the graphical design approach, referring to Figure 10. The same answer as calculated in Example #9 can be found.

6. Discontinuities — Any distributed circuits whether in waveguide, coaxial lines, or other types of propagation structure must inherently contain discontinuities. Although such discontinuities give very small amount of capacitances and inductances, the reactances of these become extremely important at higher microwave frequencies (above 10 GHz). Discontinuity effects and radiation loss are directly related. There are eight categories under discontinuities which are tabulated and shown in Table 1.

7. Higher Order Modes Limitation — The maximum frequency of operation in a microstrip line is limited by the excitation of spurious modes in the form of surface waves and transverse resonances. Surface waves are TM and TE modes which propagate across a dielectric substrate with the ground plane at the bottom. The frequency at which the extensive coupling will occur between the quasi-TEM microtripline mode and the lowestorder surface wave TM mode is defined as:

$$F_{t} (TEM1) = \frac{C}{2\pi h} \sqrt{\frac{2}{\epsilon_{r}-1}} \quad \tan^{-1}(\epsilon_{r})$$
 (20a)

if \in_r is greater than 10,

$$f_t$$
 (TEM) = $\frac{10.6}{h\sqrt{\epsilon_r}}$ (GHz), where h is in cm (20b)

June 1988

(19d)

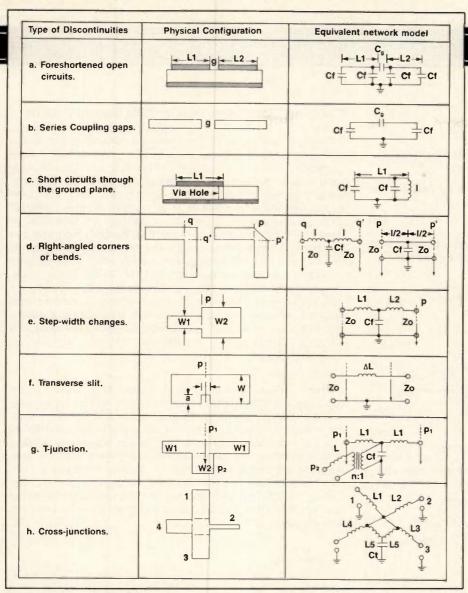


Table 1. Discontinuities.

The cutoff frequency decreases when either the substrate thickness or dielectric constant is increased. The lowest order TE surface wave mode has a lower cutoff frequency.

$$f_c(TE) = \frac{7.5}{h\sqrt{\epsilon_r - 1}}$$
 (GHz) for wide microstripline (21)

In summary, the first order TE mode may be excited at a frequency of about $0.71f_1$ (TEM1) and f_t (TEM2) is about $1.5f_1$ (TEM1);

$$f_t(TEM2) = \frac{C}{2\pi h} \sqrt{\frac{2}{c_r - 1}} \frac{3\pi}{4}$$
 (22)

Thus, the lowest order TM surface wave defines the upper limit of operating frequency for microstriplines.

For sufficiently wide microstriplines, a TE mode may exist which also couples strongly to the microstripline mode. At the cutoff frequency, the equivalent circuit is a resonant transmission line with length (w+2d) where d accounts for fringing capacitance and is of the order of 0.2h.

w+2d =
$$\frac{\lambda_{\text{TE}}}{2\sqrt{\epsilon_{\text{r}}}}$$
 (23a)

$$f_{t}(TE) = \frac{30}{\sqrt{E}(2w + 8b)}$$
 (23b)

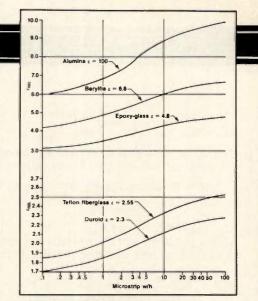


Figure 10. Effective dielectric constant vs. w/h ratio.

e1, > 10	t,(TEM1)	f _c (TE)	f,(TEM2)	f,(TE)
h = 25 mil	52.3 GHz	39 GHz	83 GHz	53 GHz

Table 2. Cutoff frequencies for higher-order modes.

Material	Relative Dielectric Constant	Loss Tangent (* 10 ⁻⁴ in)	Coefficient Of Expansion (× 10 ⁻⁶ /*C)	Thermal Conductivity (W/M *C)	Thickness	Max Usable Frequency (GHz)
99.5% Alumina	9.8	1	6.0	25	15-30	8-16
99.5% BeO	6.7	3	5.7	250	10-30	10-20
RT/Duroid 5870	2.3	5	5-20	26	5-30	12-25
RT/Duroid 6006*	8.0	12	20	.4	10-30	10-20
RT/Duroid 6010*	10.5	12	20	4	15-30	8-18
Glass Epoxy G-10	5.2	250	10-15	3	8-30	2-4
Polyimide	3.4	75	54	5	7.30	4-10

Table 3. Common substrate characteristics.

$$_{t}(TE) = \frac{10.75}{h\sqrt{\epsilon_{r}}} \quad (GHz) @ w=h$$
(23c)

Thus, this frequency is approximately same as f_t(TEM1).

f

8. Quality Factor (Q) — Although the microstrip line would appear to be an excellent choice for high Q resonant circuits, it is also limited by the radiation losses for low dielectric constant substrates, plus the surface wave coupling, depending on the characteristic impedance of the microstrip line. Notice that the thinner substrates allow higher frequencies to be used due to the surface wave modes, causing serious degradation of the Q factor, especially at the lower operating frequencies. However, a Q-factor of a couple of hundred above X band is still feasible on a standard 25 mil thick alumina substrate.

The rule of thumb for microstrip design: Q is inversely proportional to the thickness of the substrate.

$$\alpha_{c} = \frac{8.68 \text{ R}_{s}}{2\pi \text{Zo h}} [1 - (w/4h)^{2}]\{1 + h/w + h/w\pi[\ln (2h/t) - t/h]\}$$

dB/unit length (24a)

 $Q_c = 4,780 \text{ h} \sqrt{f(GHz)}$, h is in cm, for a copper conductor.(24b) With the consideration of the dispersion effect, the unloaded Q (Q_u) can be expressed:

$$\frac{1}{Q_{u}} = \frac{1}{Q_{c}} + \frac{1}{Q_{d}} + \frac{1}{Q_{r}} = \frac{\lambda_{o}(\alpha_{c} + \alpha_{d} + \alpha_{r})}{8.686\pi\sqrt{\varepsilon_{eff}}}$$
(25)

The Q_c , Q_d , Q_r are the quality factors corresponding to conductor, dielectric and radiation losses respectively.

Finally, the circuit Quality factor Q_o can be defined as:

$$\frac{1}{Q_{o}} = \frac{1}{Q_{c}} + \frac{1}{Q_{d}} = \frac{\lambda_{o}(\alpha_{c} + \alpha_{d})}{\pi\sqrt{\epsilon_{eff}}}$$
(26)

This is due to the fact that the radiation losses are higher than the conductor and dielectric losses at higher frequencies.

$$\alpha_{d} = 27.3 \left(\frac{\epsilon_{eff} - 1}{\epsilon_{r} - 1}\right) \left(\frac{\epsilon_{r}}{\epsilon_{eff}}\right) \left(\frac{\tan \delta}{\lambda_{g}}\right) \quad dB/\text{unit length}$$
(27)

$$Q_{d} = \frac{27.3}{\alpha_{d}}$$
(28)

$$Q_{r} = \frac{Zo(f)}{480\pi \left(\frac{h}{\lambda_{o}}\right)^{2} \left\{ \left(\frac{\varepsilon_{eff(f)}+1}{\varepsilon_{eff(f)}}\right) - \left[\frac{(\varepsilon_{eff(f)}-1)^{2}}{2(\varepsilon_{eff(f)})^{3/2}} \ln\left(\frac{\sqrt{\varepsilon_{eff(f)}}+1}{\sqrt{\varepsilon_{eff(f)}}-1}\right)\right] \right\}}$$
(29a)
$$Zo(f) = Zo \left(\varepsilon_{eff(f)} / \varepsilon_{eff(f)}\right)^{1/2}$$
(29b)

Example #11: What is the circuit Q_c and Q_u of a 50 ohm copper microstrip on a Duroid 6010 board with loss tangent of 12 × 10⁻⁶ in. and no sheet roughness at 10 GHz? Given: $\in_r = 10.2$, $\in_{eff(0)} = 6.8$, $\in_{eff(f)} = 7.06$, h=0.0635 cm. $\lambda_{a} = \lambda_{o} / \sqrt{\epsilon_{eff}} = C / f \sqrt{\epsilon_{eff}} = 3 \times 10^{10} / 10 \times 10^{9} \times \sqrt{6.8} = 0.46$ in. $Q_c = 4,780 \text{ h}\sqrt{f(GHz)} = 4780(0.0635 \text{ cm})\sqrt{10} = 959.$ $\alpha_{d} = 27.3 \qquad \left(\frac{\in_{eff}-1}{\in_{r}-1}\right) \left(\frac{\in_{r}}{\in_{eff}}\right) \left(\frac{\tan \, \delta}{\lambda_{g}}\right)$ $= 27.3 \left(\frac{6.8-1}{10.2-1}\right) \left(\frac{10.2}{6.8}\right) \left(\frac{12 \times 10^{-6} \text{ in.}}{0.46 \text{ in.}}\right) = 0.067 \text{ dB/}\lambda \text{g}.$ $Q_d = 27.3 / \alpha_d = 27.3 / 0.067 = 406$ $\frac{1}{Q_{o}} = \frac{1}{Q_{c}} + \frac{1}{Q_{d}} = \frac{1}{959} + \frac{1}{406}$ Q. = 285. $Z_{0}(f) = Z_{0} (\in_{eff(0)} / \in_{eff(f)})^{1/2} = 50 (6.8 / 7.06)^{1/2} = 49 \text{ ohm}$ $Q_{r} = \frac{Zo(f)}{480\pi \left(\frac{h}{\lambda_{o}}\right)^{2} \left\{ \left(\frac{\in_{eff(f)}+1}{\in_{eff(f)}}\right) - \left[\frac{(\in_{eff(f)}-1)^{2}}{(\in_{eff(f)})^{3/2}} \ln\left(\frac{\sqrt{\in_{eff(f)}+1}}{\sqrt{\in_{eff(f)}-1}}\right) \right] \right\}}$ $\left(\frac{0.0635}{3}\right)^{2} \left\{ \left(\frac{7.06+1}{7.06}\right) - \left[\frac{(7.06-1)^{2}}{2(7.06)^{3/2}} \ln\left(\frac{\sqrt{7.06}+1}{\sqrt{7.06}-1}\right)\right] \right\}$ 480π 0.676 (0.368) = 197 $\frac{1}{Q_{_{II}}} = \frac{1}{Q_{_{C}}} + \frac{1}{Q_{_{I}}} + \frac{1}{Q_{_{I}}} = \frac{1}{959} + \frac{1}{406} + \frac{1}{197}$ $Q_{\rm u} = 116.5$

Computer-Aided Design Approach Considerations

With the rapid software and computer hardware development, RF and microwave circuit designers can take advantage of the most popular CAD software, such as SUPER COMPACT® from Compact Software and Touchstone™ from EEsof. They not only save time but also improve the design accuracy. The CAD software provides powerful circuit optimization and fine tune capabilities with a high degree of accuracy for the designers and it can be operated in any personal computers (i.e. IBM/XT/AT or compatible) for convenience. In addition, the CAD programs can generate artworks and associated documentation for any active and passive microstrip design, especially when the operating frequency reaches greater than 10 GHz; There are so many variables to consider for the microstrip design that even a senior circuit designer will have a rough time if he wants to do it manually. Due to its lower cost, user-friendliness, technical performance and flexibility, the RF and microwave CAD system will eventually become a practical and cost-effective long-term solution for microstrip design.

Conclusion

There are quite a number of figures and tables readily available, providing important information for effective microstrip designs. Specifically, Table 3 can enhance your working knowledge about the electrical and physical characteristics of different types of substrates before the hardware subassembly process is implemented. This article also includes many practical suggestions besides the illustrative microstrip design examples which will benefit the new microstrip designers in particular. Finally, the list of technical papers and references should assist any microstrip designer who is interested in advanced reading and research.

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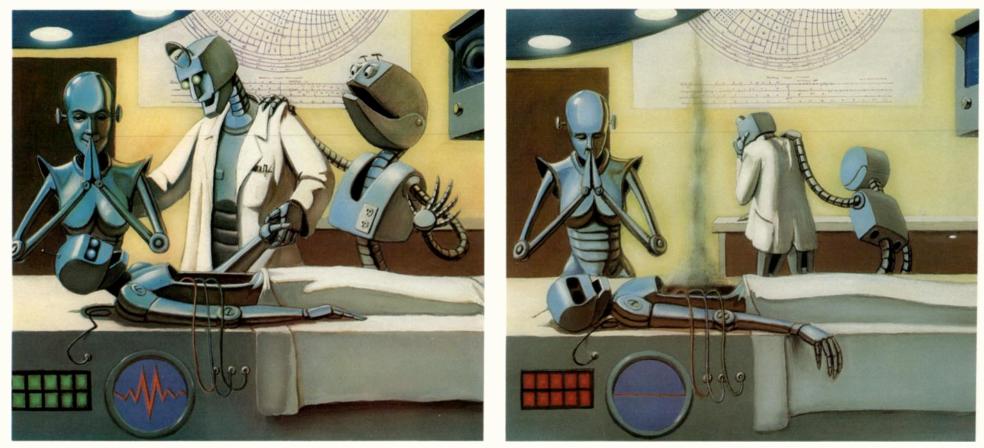
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About the Author

Alam Tam is system engineering manager at American Microwave Technology, Inc., 1127 S. Placentia Ave., Fullerton, CA 92631. He can be reached by telephone at (714) 680-4936.

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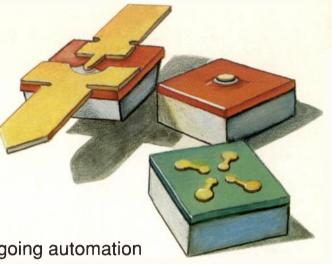
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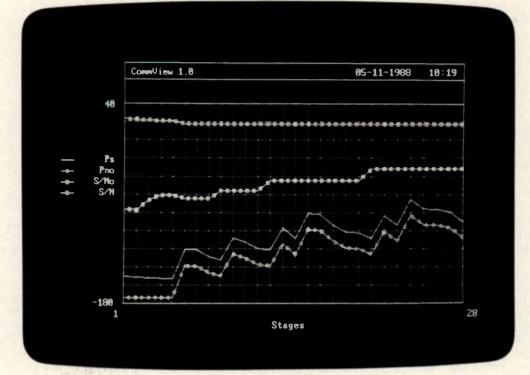
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Microstrip Analysis and Design for Various Substrate Materials

By D.R. Hertling and R.K. Feeney Georgia Institute of Technology

As the frequency of operation of an RF circuit increases to several hundred MHz or beyond, it becomes increasingly difficult to realize lumped inductors and capacitors. Fortunately, transmission line structures can be made to duplicate the function of lumped elements and also to generate additional circuit elements that are not possible with inductors or capacitors. A strip conductor separated from a ground plane by a dielectric material (a double-sided printed circuit board) forms a microstrip transmission line system. Even when not intentionally used as circuit elements, microstrip transmission lines are always present whenever doublesided printed circuit board is employed. Even digital designers should be aware of the transmission line properties of traces on double-sided printed circuit board. This article presents a computer program in BASIC which provides information needed to design or analyze microstrip transmission lines.

he cross section of a typical microstrip transmission line together with applicable dimensions is shown in Figure 1. The analysis of wave propagation on this structure is not as simple as for coaxial cables or open-wire transmission lines. The microstrip transmission line is dispersive and cannot be described by a simple TEM mode. However, because of its practical importance, the microstrip transmission line has been extensively analyzed. Initial work was done by Wheeler(1) using conformal analysis to develop a set of descriptive equations. The equations so obtained are not very convenient for general engineering use and, consequently, considerable effort has been devoted to obtaining semiempirical equations that are sufficiently accurate for routine engineering analysis and design. Several of these semiempirical equations claim accuracy of 1 percent or better which is good enough for most design applications. Since several sets of equations exist, the choice of which to use is often a matter of preference of the designer. The program presented in this paper gives the user the choice of two sets of equations. One set was developed by Sobol (2) and the other set by Hammerstad (3). The authors have found both sets to be quite satisfactory. There is very good agreement between two sets of equations and for users who prefer a different set of equations, a subroutine using the preferred set can be inserted in place of a given subroutine or added as an additional subroutine.

When the program is executed, it prompts the user to choose one of five commands. The first command prints a table of characteristic impedances and velocity factors for specified board properties and a user-specified range of stripwidths. The second command is for analysis; it determines the characteristic impedance and normalized length of a microstrip line of arbitrary dimensions. The third command is used to design microstrip lines of specified characteristic impedance and normalized length. It calculates the required line width to give a desired characteristic impedance and the physical length in mils of microstrip lines at a specified frequency. Both the synthesis and analysis commands have sub-menus which prompt the user for appropriate inputs. The fourth command allows changing board parameters and the choice of equations. Finally, the last command ends the program.

Examples of Program Use

Two examples that make use of the program are presented. The first example prints a summary for 60 mil thick, twoounce copper, Tefton-glass circuit board. The second example determines the dimensions of a 2.5 GHz Butterworth filter made on gold-alumina substrate.

Example One — For this example, the program will be used to create a summary sheet for 60-mil, two ounce, Teflon-glass circuit board material. On an 8½ by 11 sheet, approximately 50 line widths can be summarized. A listing from 20 mils to 1 inch in steps of 20 mils covers most design applications and gives, at a

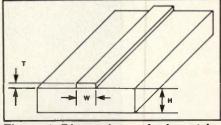


Figure 1. Dimensions of microstrip transmission line.

glance, a good idea of the variation in characteristic impedance and velocity factor over this range of microstrip widths. For finer resolution, the user can simply request smaller steps over a particular range of widths.

Upon execution of the program, the user is prompted to enter the material type, dielectric thickness, metal thickness, and relative dielectric constant. Length and width dimensions used in this program are in mils. These material parameters are retained in memory and need not be entered again during execution of the program. The user should enter TEFLON-GLASS, 60, 2.7, and 2.55 respectively. The computer then displays:

Enter 1 for lengths in degrees even though this will not actually be used in Example 1. Next,

ENTER — 1 FOR SOBOL'S OR — 2 FOR HAMMERSTAD'S EQUATIONS

Enter 1 for Sobol's equations; the program then displays the following menu: plays the following menu:

 PRINT A SUMMARY SHEET OF ZO AND VF FOR A RANGE OF WIDTHS
 ANALYZE (PHYSICAL TO ELEC-TRICAL PARAMETERS)
 DESIGN (ELECTRICAL TO PHY-SICAL PARAMETERS)
 CHANGE INITIAL PARAMETERS
 EXIT THE PROGRAM

	900 IF F1=1 THEN EL-3608PL/LAMBDA ELSE EL-PL/LAMBDA
NICROSTRIP DATA PROGRAM	PIO PRINT
SO REM # D. R. MERTLING AND R. K. FEENEY 10/30/87	920 PRINT "1000000000000000000000000000000000000
0 P1-4+ATM(1)	940 IF F1=1 THEN BOTO 970
0 C=3E+10/2.54	950 PRINT USING "FOR A LINE WEBER, MILS (DWG "+PL)
0 PRINT "\$	900 PRINT USING "THE ELECTRICAL LENGTH IS BE AND "ALS.EL.GOTO PRO
IO PRINT	
00 PRINT	960 PRINT USING "THE ELECTRICAL LENGTH 15 ****** *+L*IEL
10 INPUT "ENTER - DIELECTRIC THICKNESS (MILS)M	1000 6010 870
20 PRINT	1010 REM 20 AND VE FOR A GIVEN WIDTH
JO INPUT "ENTER - METAL THICKNESS (MILS) ",T	1020 INPUT "ENTER DESIRED ZO ".ZO
AO PRINT	1030 PRINT
SO INPUT "ENTER - RELATIVE DIELECTRIC CONSTANT ",ER	1040 INPUT "ENTER THE FREQUENCY IN MHZ ",F
00 PRINT	1050 GOSUB 1420 1060 PRINT
70 IMPUT "ENTER 1 - LENGTHS IN DEGREES : 2 - LENGTHS IN MAVELENGTHS ",F1 80 IF F1=1 THEN L+*"DEGREES" ELSE L+*"WAVELENGTHS"	1070 PRINT "####################################
NO PRINT	1080 PRINT
TO INFUT "ENTER - 1 FOR SOBOL'S OR - 2 FOR HOMMERSTAD'S SOLATIONS - SOM	1090 PRINT "HICROSTRIP DATA FOR "+TYPES+" USING "+FOS+" FOLIATIONS"
10 IF EQN = 1 THEN EDS-"SOBOL'S" ELSE ED-"HAMMERSTAD'S"	1100 PRINT
20 PRINT	1110 LAMBDA+VF=C/F/1000
30 PRINT "####################################	1120 PRINT USING "FOR ZO - HARR OHMS THE WIDTH - APPR. MILE" 120, W
40 PRINT	1130 PRINT
50 PRINT "1 - PRINT A SUMMARY SHEET OF 20 AND VF FDR A RANGE OF WIDTHS"	1140 PRINT USING "AT MORE.ON MHZ & WAVELENSTH IS MOORD.W MILS";F,LAMBDA
O PRINT "2 - ANALYZE (PHYRICAL TO ELECTRICAL RARAMETERS)=	TTO PRINT
70 PRINT "3 - DESIGN (ELECTRICAL TO PHYSICAL PARAMETERS)" 80 PRINT "4 - CHANGE INITIAL PARAMETERS"	1160 PRINT "ESSESSESSESSESSESSESSESSESSESSESSESSESS
TO PRINT "9 - CHANGE INITIAL PARAMETERS"	1180 PRINT "1 - RETIRN TO MATH MEMI"
OO PRINT	1190 PRINT "2 - CALCULATE PHYSICAL LINE LENGTHS"
10 PRINT "####################################	1200 PRINT
20 PRINT	1210 PRINT "BARRARARARARARARARARARARARARARARARARARA
30 INPUT "ENTER COMMAND ", CHD	1220 PRINT
40 PRINT	1230 INPUT "ENTER COMMAND ", CMD1
00 ON CHD GOSUB 370,610,1010,70,1710	1240 IF CMDI+1 THEN RETURN
00 GOTO 220 70 REM SUMMARY SHEET OF ZO AND VF	12BO PRINT
AD PRINT	
90 INPUT "ENTER - WIDTH INCREMENT (MILS) ",WINC	1270 PRINT
CO PRINT	1280 PRINT "ELECTRICAL LENGTHS ARE IN "+L.
10 INPUT "ENTER - NUMBER OF CALCULATIONS ". MCALC	1300 INPUT "ENTER DEBIRED ELECTRICAL LENGTH (O TO QUIT) ",EL
20 PRINT	1310 IF EL=O THEN RETURN
30 LPRINTILPRINT	1320 IF F1=1 THEN LENGTH-LAMBDAJEL/360 FLSE LENGTH-LAMBDAJEL
40 LPRINT "MICROSTRIP DATA FOR "+TYPE6+" USING "+E06+" EQUATIONS"	1330 PRINT
SO LPRINT	1340 IF F1=1 THEN GOTO 1370
60 LPRINT USING "DIELECTRIC THICKNESS (MILS) ##8.08"1M 70 LPRINT	1350 PRINT USING "FOR A LINE ****. ** "+LS*" LONG";EL; 1360 PRINT USING " THE LENGTH IS *****. * MIL5";LENGTH:GOTO 1390
BO LPRINT USING "METAL THICKNESS (MILS)	1370 PRINT USING "FOR A LINE *****. **LS** LONG":ELI
PO LPRINT	1380 PRINT USING " THE LENGTH IS MILE"ILENGTH
00 LPRINT USING "DIELECTRIC CONSTANT	1390 PRINT
10 LPRINTILPRINT	1400 PRINT
20 LPRINT "WIDTH (MILB) IMPEDANCE VELOCITY FACTOR" 20 LPRINT	1410 5070 1300
	1420 REM NEWTON-RAPHSON ROUTINE
50 FDR I=1 TO NCALC	1430 X1=10:DX=.001 1440 W=X1:DN EQN GDSUB 1490.1590:Y1=R0-Z0
	1450 W=X1+DX10N EGN GDSUB 1490,1590;Y2=R0-Z0
ON EON GOSUB 1490,1390	1460 X2=X1-DX+V1/(Y2-V1)
SO LPRINT USING " seas assume a sustained of the season of	1470 IF AB5(X2-X1) (=.01 THEN RETURN
90 NEXT I	1480 11=12:50TD 1440
DO RETURN	1490 REM MICROSTRIP SUBROUTINE (SDBOL)
10 REM 20 AND VE FOR A GIVEN WIDTH	1300 IF 4/H 4,05 DR W/H >20 THEN GOTD 1720
20 INPUT "ENTER THE WIDTH (MILS) ",W	1910 WEFF=#+T/PI (LOG(2#H/T)+1)
30 DN EON GOSUB 1490,1590	1520 A=377/SOR(ER) = M/WEFF
40 PRINT	1530 R0=A/(1+1.735+ER^(-7.240001E-02)+(WEFF/H)^(83b))
50 PRINT "\$	1540 IF (WEFF/N) (= .6 THEN GOTO 1570
TO PRINT "MICROSTRIP DATA FOR "+TYPES+" USING "+EDS+" EDUATIONS"	1550 VF=1/(SOR(1+.63*(ER-1)*(WEFF/H)^.1255)) 1560 GOTO 1580
NO PRINT "DIGROSTRIP DATA FOR "+TYPES+" USING "+EOS+" EDUATIONS"	1570 VF=1/(SOR(1+.6*(ER-1)*(WEFF/H)^.0297))
PO PRINT USING "FOR W - PERSE, P HILE ZO	1560 RETURN
DO PRINT	1990 REM MICROSTRIP SUBROUTINE (HAMMERSTAD)
10 PMINT "####################################	1000 IF W/H 4.05 DR W/H 220 THEN GOTO 1720
20 PRINT	1610 IF W/M > .16 THEN WEFF HH T/PIE(LOG(28H/T)+1)+BOTO 1630
30 PRINT "1 - RETURN TO MAIN HENU"	3020 WEFF=W+T/PI#(LO3(4#PI#W/T)+1)
40 PRINT "2 - CALCULATE ELECTRICAL LINE LENGTHS"	1630 1F W/H >1 THEN GOTD 1670
50 PRINT "\$12253386256625623623623623623624445386866666665665562586666666666666666666	1640 EEF=(ER+1)/2+(ER-1)/2+((1/SDR(1+12+H/WEFF))+.04+(1-WEFF/H)^2) 1630 VF=1/SDR(EEF)
70 PRINT	1600 RO=00/SUR(EEF)=LDG(B+H/WEFF+WEFF/H/4);RETURN
SO INPUT "ENTER COMMAND ". CHD2	1670 EEF=(ER+1)/2+(ER-1)/2+(1/(SDA(1+12*H/WEFF)))
TO IF CHD2=1 THEN RETURN	1680 VF=1/SOR(EEF)
DO PRINT	1490 R0=377/SQR(EEF)/[WEFF/H+1.393+2/3+LOB(WEFF/H+1.444))
10 INPUT "ENTER THE FREQUENCY (MHZ) ",F	1700 RETURN
20 PRINT	1710 5TOP
30 PRINT "\$	1720 RET ERROR TRAP SUBROUTINE
NO PRINT "ELECTRICAL LENGTHS ARE IN "+LS	1730 PRINT: PRINT "###ERROR CONDITION###" 1740 PRINT: PRINT "W/M RATIO DUT DF RANGE"
DU PRIMI "ELECTRICAL LENGTHS ARE IN "+LS	1730 PRINTERRINT "W/W RATID DUT DE RANGE" 1730 PRINTERRINT USING "W/W = ###.################################
TO INPUT "ENTER PHYSICAL LENGTH (MILS) - (O TO QUITI ".PL	1760 PRINT PRINT "EQUATIONS ACCURATE FOR .05 < W/M < 20"
30 IF PL=0 THEN RETURN	1770 6010 220
P0 LAMBDA=VF1C/F/1000	1780 END

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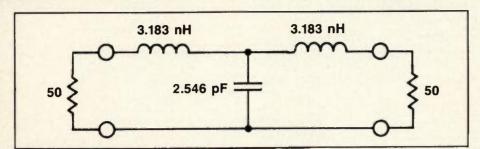
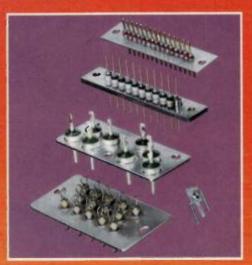


Figure 2. Lumped-element prototype network.



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P.O. Box 37144, Tucson, AZ 85740-7144 Phone:602-744-0400 Telex:(RCA)299-640 Fax:602-744-6155 For this example choose command 1. The program prompts for the width increment in mils and the number of desired calculations. Enter 20 and 50 respectively as prompted. This specifies calculations from 20 mils to 1 inch in 20 mil increments. The computer prints the output shown in Table 2 and when finished, the above menu is again displayed. If desired, the user can again choose command number 1 and print a summary with a different width increment and number of points without reentering the board parameters.

Example Two - Figure 2 is a lumped element prototype of a 50-ohm*Butterworth low-pass filter with a cutoff frequency of 2.5 GHz. The microstrip equivalent of this filter is shown in Figure 3. This filter is to be constructed on a goldalumina substrate with a dielectric thickness of 25 mils, metal thickness of 0.1 mils, and relative dielectric constant of 9.9. Since a different material is being used choose command 4 to change initial parameters. When prompted, enter GOLD-ALUMINA, 25, 0.1, and 9.9 respectively. For this example, specify normalized line lengths in degrees and choose Sobol's equations. After these initial parameters have been chosen, the main menu is displayed. In order to design the required microstrip sections, use command 3 from the main menu. First design 50-ohm lines which will be used as line stretchers on both the input and output sides of the filter. When the computer prompts for the desired ZO and the design frequency in MHz, enter 50 and 2500 respectively. The program uses a Newton-Raphson routine to iteratively solve for the line width. This routine typically takes 5 to 10 iterations and, depending on computer speed, can take several seconds. Finally, the program calculates the wavelength in inches. For the above input, the computer displays the following:

MICROSTRIP DATA FOR GOLD-ALU-MINA USING SOBOL'S EQUATIONS

FOR ZO = 50.00 OHMS THE WIDTH = 23.3 MILS

AT 2500.00 MHZ A WAVELENGTH IS 1844.0 MILS

The computer then displays the design sub-menu shown below.

1 — RETURN TO MAIN MENU 2 — CALCULATE PHYSICAL LINE LENGTHS (MILS)

June 1988

Since the physical line lengths of the 50-ohm sections do not matter, choose command 1 to return to the main menu. The user can now select command 1, 2, or 3 without reentering the board parameters. To design the following microstrip lines, again choose command 3. Then to design the 78 ohm lines at 2500 MHz, enter 78 and 2500. The computer displays the following:

MICROSTRIP DATA FOR GOLD-ALU-MINA USING SOBOL'S EQUATIONS

FOR ZO = 78 OHMS THE WIDTH = 7.7 MILS

AT 2500 MHZ A WAVELENGTH IS 1903.3 MILS

When the design sub-menu is displayed choose command 2 in order to design physical lengths. When prompted, enter the 52.2 degree electrical length. The physical length is then displayed.

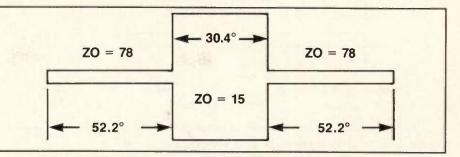
FOR A LINE 52.2 DEGREES LONG THE PHYSICAL LENGTH IS 276.0 MILS

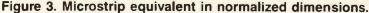
To return to the main menu, enter a line length of zero. Next, to design the 15 ohm line select the design command from the main menu again, and enter 15 and 2500 for ZO and the frequency. By selecting command 2 in the design sub-menu, the user can find the electrical length of the 30.4 degree line. This line is 150.2 mils wide and 140.8 mils long. The dimensions of the completed filter are shown in Figure 4.

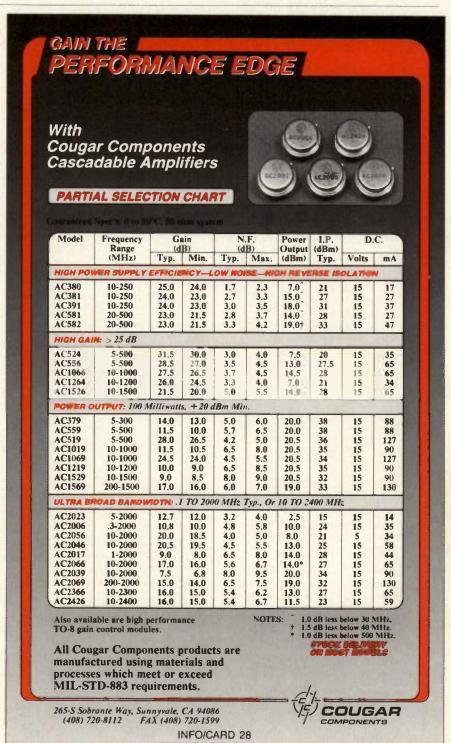
Comparison of Microstrip Equations

Some users may wish to remove the option of selecting equations and use one of the given or a user-defined subroutine. This can be easily done by removing the prompt at line 200 and substituting a statement setting the variable EQN to 1 or 2 (or 3 if a new subroutine is added). If a subroutine is added, its line number must be added to the ON EQN GOSUB statement in line 570. The program will then always use the prescribed set of equations.

Both given sets of equations are accurate for width to dielectric thickness ratios (W/H) between 0.05 and 20. If a W/H ratio out of this range is specified by the user or results from the Newton-Raphson routine, an error trap alerts the user. W/H ratios out of the allowable range are usually caused by specifying transmission lines of either very large or very small characteristic impedances. An error mes-







RF Design

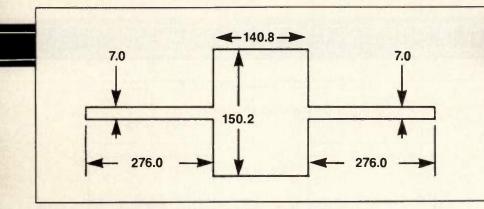


Figure 4. Microstrip equivalent with dimensions in mils.

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Aydin Vector Division - POB 328, Newtown, PA 18940-0328 Tel 215-968-4271, TWX 510-667-2320, FAX 215-968-3214 INFO/CARD 29 sage is printed on the screen followed by the W/H ratio and the allowable range. The program then returns to the main menu and, since the initial data is retained, the user can select a command without reentering the substrate parameters.

Conclusions

Microstrip transmission lines are very important in modern RF design. Presented here is a simple, easy-to-use program that does both design and analysis of microstrip elements. The program may easily be changed to accommodate any desired microstrip impedance function. To receive a copy of the program on an IBM formatted diskette, send \$5.00 to the authors to cover shipping and handling.

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About the Authors

Robert K. Feeney, Ph.D., is professor, and David R. Hertling, Ph.D., is associate professor at the School of Electrical Engineering, Georgia Institute of Technology, Atlanta, Ga. 30332. They can be reached at the above address or by telephone at (404) 894-2932 for Dr. Hertling and (404) 894-2924 for Dr. Feeney.

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CAD Amplifier Matching With Microstrip Lines

By Dr. Stanley Novak Polytechnic University

In lumped element matching circuits, the simplest network with two elements is an L-matching network. This network offers a maximum of four solutions. In contrast, equivalent matching networks realized with two transmission lines (also in L- configuration) provides virtually an infinite number of solutions, particularly if the designer is not limited to one transmission line impedance. The variety of solutions includes a combination of open/ shorted stubs at the input and output, or line-stub networks at the input combined with quarter-wave transformers and stubs at the output, or vice versa. To reduce the number of circuit options, computer analysis with one limitation on the line-stub match and one on transformer-stub match should be introduced. For practical purposes, only stub lengths below 90 and 180 degrees are considered.

The computer program presented here allows the designer to choose from a variety of solutions. The input parameters

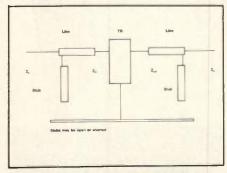


Figure 1a. Line-stub matching.

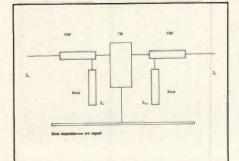


Figure 1b. Transformer-stub matching.

for the program are input and output impedances of a transistor (represented by a two-port network), frequency and a characteristic impedance of a connected (main) transmission line. The program operates as a self-contained unit, and was developed as an extension to the program for amplifier design published in reference (1). It operates on the CBM 64 as well as the HP 9000 series computer. Note that the variables defined for the CBM computer are not tolerated in HP BASIC, and therefore the non-dimensioned variables are changed to double variables (i.e. R to RR, I to II etc.).

Matching with Stubs and Lines

This is a viable alternative to lumped element matching networks. A combination of both methods is sometimes used, usually by replacing capacitive stubs by capacitors. Transmission line matching is particularly attractive at frequencies when the magnitudes of lumped elements become too small for reliable and easy realization. The equivalent of a lumped element matching network with two pure reactances is a transmission line network with two lengths of lossless lines. The series lines represent series reactances and shunt lines (stub) represent parallel reactances. In the first case the series transmission line is connected to the input terminals of the transistor.

At the point where the real part of the input admittance of the line (with selected characteristic impedance) is equal to the admittance of the connected (main) line, another line is added in parallel (stub) to cancel the susceptance to complete the matching. The susceptance may be real-

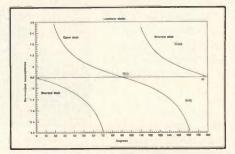


Figure 2. Electrical length versus susceptance.

ized by open or shorted stub, which in general can have any value of characteristic impedance. However, in this case the computer limits the choice to the same value as the series line. A similar approach is used at the transistor output.

In the second case, the designer has to determine the input admittance of the transistor and add directly to the input terminal length of the open or shorted line

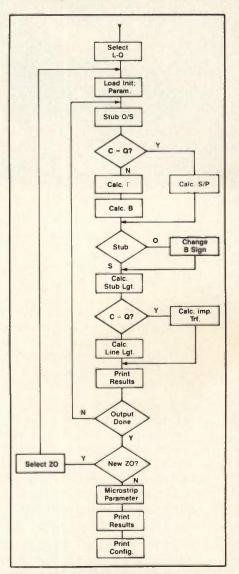


Figure 3. Flowchart for the microstrip match program.

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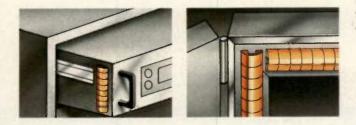
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	LC-10°LA						
	RETURN						
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				EL CONST			
				NESS IMM			
	1 M-3E+10						
	GOSUB 3						
	PRINT						
		RAMETE	RS FOR	MICROST	RIPJ	LINES (0".F. 1003000,"MHZ CENTER FREQUENCY	ŧ
1690	PRINT*SL	JESTRAT	ETHICK	NESS=",H;	1MM	MT .	
				EL CONST			
1710	PRINT'LI	NEWIDTH	FOR" Z	"OHMS-"	INT	T(W*E) E(TMM)'	
						"LG"ELE, TMMI"	
						(2)*LG*E) E.TMMT	
3740	IF CS+'O'	THEN 3T	80				
3750	PRINT B	LENGH	T-INGIN	T(T(3)*LC	182	ETMNT	
1760	PRINT BS	LENGI	TOUT-	INTIT(4)	LGI	"D ETMMI"	
3170	GOTO 38-	10					
0476	ZO- INTC	V(8))					
3790	QOSU'B 3	520					
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4100							

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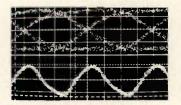
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UPG7048-25	4:1 Multiplexer	800 ps	2.5 GHz		
UPG705B-15	1:4 DeMultiplexer	800 ps	1.5 GHz		
UPG705B-20	1:4 DeMultiplexer	800 ps	2.0 GHz		
UPG705B-25	1:4 DeMultiplexer	800 ps	2.5 GHz		
UPG706B-1	High Speed Flip Flop	400 ps	4.0 Gbps		
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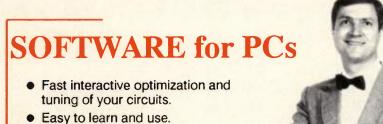


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CEL, 3260 Jay Street, Santa Clara, CA 95054; (408) 988-3500 🗆 Los Angeles, CA (213) 645-0985 🗆 Bellevue, WA (206) 455-1101 🗆 Scottsdale, AZ (602) 945-1381 or 941-3927 Richardson, TX (214) 437-5487 🗆 Burr Ridge, IL (312) 655-0089 🗆 Cockeysville, MD (301) 667-1310 🗆 Peabody, MA (617) 535-2885 💷 Hackensack, NJ (201) 487-1155 or 487-1160 Palm Bay, FL (305) 727-8045 🗆 Norcross, GA (404) 446-7300 🖨 Nepean, Ontario, Canada (613) 726-0626 🗆 Europe, NEC Electronics GmbH 0211/650301 (stub). This cancels the reactive part of the admittance and uses a series line quarter-wave transformer to convert the remaining resistive part to the characteristic impedance of the connecting transmission line (which is 50 ohms in most cases). This is repeated at the output and both cases are illustrated in Figure 1.

The only limitation on matching line impedances is the substrate material, which gives the practical boundaries on the realizable width of the lines. The exception is the quarterwave transformer whose impedance is given by the real part of the transistor input/output impedances and impedance of connected transmission line.

In the case of stubs, use of either the open or shorted configuration is determined by circuit requirements such as biasing. When circuit size is a consideration, the selection of stub configuration is usually critical since one method may have a shorter stub over another. Note



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INFO/CARD 34 Come see us at the MTT-S Show, Booth #1216. that adding half wavelengths to the calculated value will change an open stub to a shorted stub and vice versa.

Calculations of Stub Impedances

For short sections of lines, the designer can assume that the losses could be neglected and the results from lossless analysis of transmission lines can be used. In this case, operating with parallel stub elements, the admittance of the open and shorted stub is:

$$Y_{os} = j Y_o \tan\theta \tag{1}$$

$$Y_{ss} = -j Y_{o} \cot\theta$$
 (2)

Note that the resulting values are pure susceptances, and the only difference between tan and -cot functions is 90 degrees or $\pi/2$ radians. This is reflected in equation 3;

$$\tan (90 + \theta) = -\cot\theta \tag{3}$$

If open capacitive stubs shorter than 90 degrees with a positive sign for susceptance are desired and a negative value for θ is obtained, the designer has to add 90 degrees to obtain the desired solution. In



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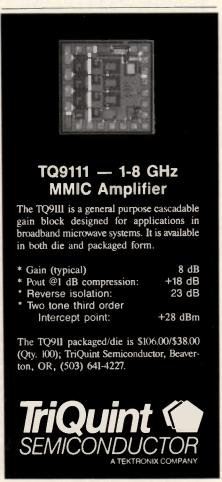
the case of shorted (inductive) stubs, shorter than 90 degrees with negative susceptance value, 90 degrees may have to be added to θ if necessary. This is done automatically in the program.

The relationship between the functions for open and shorted stubs is shown in Figure 2. From Figure 2 note that useful areas for realization of stubs is below 60 degrees of electrical length at the frequency of design, (or below $\pi/3$ radians or .167 lambda free space). Impedance above this value rises too rapidly for reliable realization. This limit should be taken as a guidance when determining which computed value is suitable for realization of practical matching networks.

The angle θ in the equations actually represents the following relationship:

$\theta = \beta I = 2\pi i \lambda$ (rad)	(4)
or	
$\theta = \beta I = 360 I / \lambda$ (degrees)	(4a)

From this, for known susceptances the required length I can be calculated using (1) and (2) respectively. Considering that for shorted stubs (<90 degrees) the suscep-



tance is negative, from (2);

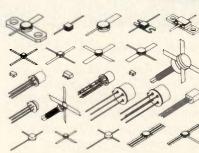
$Y_{ss} = -jB_{ss} = -j Y_{o}cot\theta$	(5)
or	
$\tan\theta = Y_o/B_{ss}$	(6)
from which,	
$\beta I_{ss} = \theta = \tan^{-1} (Y_o/B_{ss})$	(7)
Note that equation 7 gives the value of	f al
Note that equation 7 gives the value of	л рі

in degrees or radians depending on the conversion used. For open stubs shorter than 90 degrees the value of susceptance needs to be positive and,

$$Y_{os} = jB_{os} = jY_{o} \tan\theta$$
 (8)
or
 $\tan\theta = (B_{os}/Y_{o})$ (9)

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RF Design

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$$\beta I_{os} = \tan^{-1} (B_{os}/Y_o)$$

From equation 7 the length of the shorted stub becomes;

$$I_{ss} = \lambda/2\pi \tan^{-1}(Y_o/B_{ss})$$
(11)

and from equation 10 for open stub;

$$I_{os} = \lambda/2\pi \tan^{-1(B_{os}/Y_o)}$$
(12)

The wavelength can be obtained from frequency of design by the following;

$$\lambda = 3.10^8/f \tag{13}$$

which when substituted into equations 11

$$I_m = I \sqrt{\varepsilon_{\text{eff}}}$$
 (14)

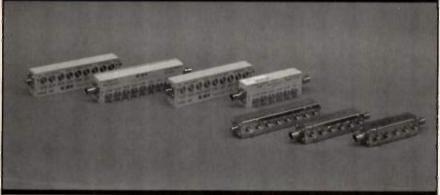
This concludes the stub design.

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4457	75Ω	DC-1000MHz	0-127dB	1dB	
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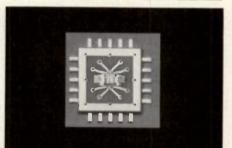
admittance equivalent circuit. The required matching susceptance may be capacitive or inductive, and can be realized by open or shorted stub. For example, to realize inductive susceptance (Figure 2), open stub lengths in the 90 $< \theta <$ 180 degrees range are used and compared with inductive shorted stubs in the $0 < \theta < 90$ degrees region which provide for the same match. In the case of capacitive susceptance the situation is reversed. Therefore, matching stubs longer than 90 degrees are sometimes necessary. Fortunately, due to symmetrical character of the tan θ and $-\cot\theta$ functions similar solutions are obtained by trigonometry manipulation.

Note that capacitive stubs are sometimes preferred to lumped capacitance, in the form of chip capacitor or by some tunable version. The required value is then determined from the susceptance as;

$C = B/(2\pi f)$

Microstrip Lengths and Width Calculations

When the evaluation of free space lengths and impedances of the lines is completed, the next step is the selection of dielectric substrate material for the final realization of the circuit. After the material is selected, the lengths and widths of the



TQ9141 — 1-8 GHz MMIC Active Power Divider

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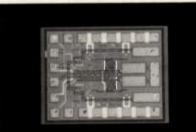


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lines and stubs may be evaluated. This is done using Wheeler's [2] equations, assuming for simplification zero thickness of the conductive layer. Then from the chosen material parameters and known lengths and impedances of the lines, each line is evaluated for the required length and width. Width is determined by characteristic impedance of the line and applicable effective dielectric constant.

Using substrates that have a permitivity range of two to ten, the line impedance has to be maintained between 20 and 150 ohms since lines below this range are too wide and lines above this range are too narrow to be reliably etched on the substrate. When designing with quarter wave transformers, impedance generally exceeds this range. In such cases a solution is to calculate intermediate impedance as a square of the input and output impedances, and use two transformers in series.

The bandwidth of amplifiers designed by using only two matching elements is usually restricted to about 10 percent. In case where this is unsatisfactory it is possible to use analysis programs with optimization facilities that extend bandwidth. Obviously this is achieved by reduction of the overall gain.



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RF Design

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Program Logistics

The program is based on a short review of basic principles used in matching circuits with transmission lines. Further information is detailed in references 3,4 and 5. Processing of entry data is shown in Figure 3.

After loading the input and output impedances and operating frequency of the transistor, the program designs a matching network. After the operator selects the type of network desired (line and stub or transformer and stub), the program finds the data for input network, selects the stub for output network, and displays the results. Calculations are done for the stan dard 50 ohm environment. However, the operator is offered a choice of impedance values for matching lines. The operator can also repeat calculations for the same impedance while changing requirements for stubs. If a new impedance is selected, the calculations are repeated with a new selection of stubs. After a satisfactory solution is obtained, the user may exit by refusing a new value of Zo. The program is then redirected to the final evaluation of widths and lengths of lines in the selected substrate environment.

The final configuration of the circuit is

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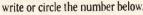
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Table 1. MRF 571 at 1000 MHz with 50 ohm lines.

displayed together with the selected stub elements. In cases where quarterwave transformer and stub matching is selected, the values of transformers impedances and line parameters are shown together with linewidth of the mainline microstrip (50 ohm). Examples are illustrated in Tables 1 and 2.

Note that in some cases the match of two transmission line networks is not realizable. When this happens, the computer displays a message indicating that for chosen characteristic impedance Z_o , a solution is not available.

Conclusion

The program presented above offers significant time savings for the design of narrow band amplifier matching circuits. When used on computers with graphic facilities, simple adaptation provides for graphic display of all available combinations for match within a chosen range of line impedances.

References

1. S. Novak, "Computer Enhanced Amplifier Design," *RF Design*, Feb. 87, pp. 97-109.

2. H.A. Wheeler, "Transmission Line Properties of a Strip on a Dielectric Sheet on a Plane," *IEEE Trans. on MTT*, Aug. 77, pp. 631-647.

3. T.T. Ha, Solid State Microwave Amplifier Design, Chap. 3-4, Wiley 1981.

4. R.S. Carson, *High Frequency Amplifiers*, Wiley 1975.

5. G.D. Vendelin, *Design of Amplifiers* and Oscillators by the S-Parameter Method, Chap. 1, 2, 3, Wiley 1982.

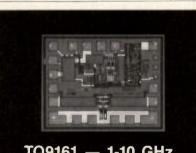


Table 2. MRF 571 using 20 ohm lines and 100 ohm stubs.

6. S. Novak, "Amplifier Matching with Transmission Lines," 1987 International Symposium/Brazil.

About the Author

Dr. Stanley Novak is industry professor, director EE Laboratories at Polytechnic University, 333 Jay Street, Brooklyn, NY 11201. He can be reached by telephone at (718) 260-3476.



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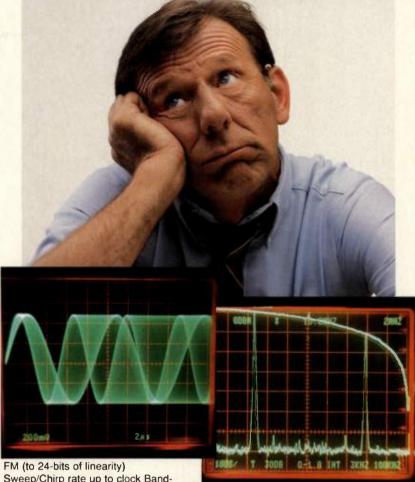
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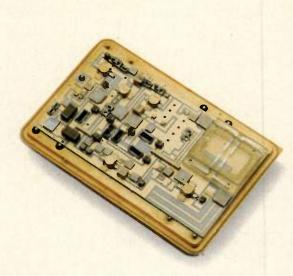
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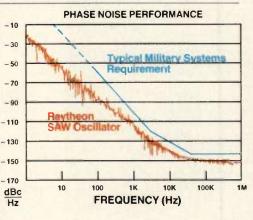
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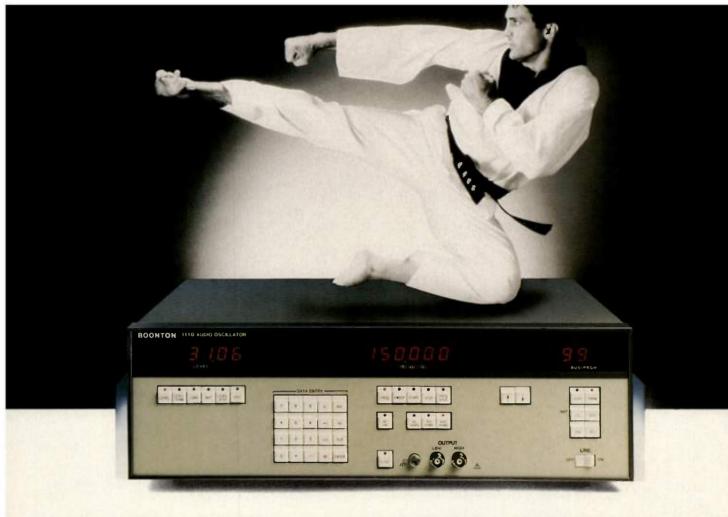
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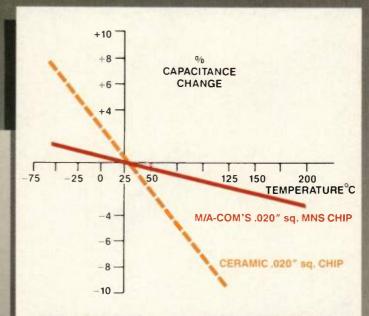
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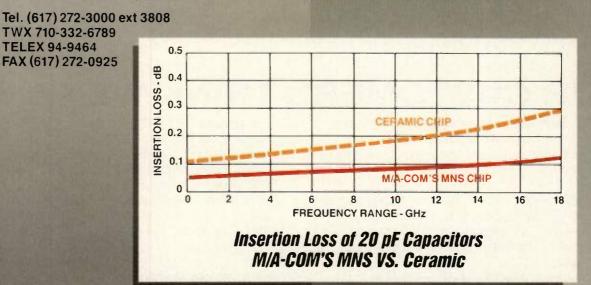
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INFO/CARD 49 RF Design New Products at MTT-S 1988

New York Show Spotlights RF and Microwave Offerings

Toggle Switch Attenuators Kay Elemetrics

A line of toggle switch attenuators utilizing surface mount technology will be introduced. The devices feature a 3 watt power rating with a frequency range of up to 3 GHz. These attenuators are useful for test calibration. Kay Elemetrics Corp., Pine Brook, NJ. INFO/CARD #206.

Tuned Filters and Multicouplers K&L Microwave

K&L will be showing a line of digitally and manually tuned filters and multicouplers including a 225-400 MHz frequency agile hopping multiplexer. The filters can be tuned in two modes; the normal mode and active mode. The normal mode directs the filters to frequencies previously selected through a frequency management system utilizing digitally controlled stepping motors. In the active mode, individual filters hop in a 12 MHz band about the frequency selected from the normal mode. The multicoupler handles 30 watts average with peak specification of 120 watts or 50 watts CW. K&L Microwave, Inc., Salisbury, MD. Please circle INFO/CARD #205.

GaAs Switch Daico Industries

The P/N DS0602 is a low current, broadband, GaAs FET SPDT switch with a frequency range of 5 to 2000 MHz and VSWR of 1.5:1. The device draws 150 microamps and switches in 26 ns. From 5 to 20 MHz, the isolation is 55 dB. Other features include internal 50 ohm termination and 4 ns transition time. Daico Industries, Inc., Compton, CA. Please circle INFO/CARD #204.

Cascadable Amplifiers Cougar Components

Models AC380 and AC580 are thin film cascadable amplifiers that operate from



10 to 250 MHz and 10 to 500 MHz, respectively. Typical gain is 25 dB on the 380 and 23 dB on the 580. Noise figure ranges from 1.7 dB to 2.2 dB depending on the model. Operating current at 15 V is 17 mA on the 380 and 18 mA on the 580. The amplifiers are packaged in 4-pin TO-8 packages. Cougar Components, Sunnyvale, CA. INFO/CARD #203.

Tuning Sticks

American Technical Ceramics

ATC unveils a tuning stick — a device that makes on-board capacitor value selections. It consists of an ATC 100 Series Superchip® radial wire leaded capacitor labeled with its specific value and attached to a non-conductive holder. The values range from 0.1 pF to 1000 pF and price is \$49.95 per kit. American Technical Ceramics, Huntington Station, NY. INFO/CARD #202.

MMIC Design Workstation EEsof

The MMIC Workstation[™] is a broadbased computer-aided tool that is designed for use with Apollo and Sun platforms. The workstation allows users to



perform schematic capture, linear and non-linear simulation, full-custom layout, and design verification. **EEsof**, **Inc.**, **Westlake Village**, **CA. INFO/CARD #201.**

Digital Receiver System Steinbrecher

ACCUVERTER™ is a digital receiver system that converts RF signals into digital words while preserving more than 96 dB of available spurious-free dynamic range without the use of AGC. It serves as an interface between RF signals and digital signal processing. Steinbrecher Corp., Woburn, MA. INFO/CARD #200.

Wideband Power Divider Merrimac

Merrimac Industries unveils the PDM-27-10G power divider with a frequency range of 1.5 to 18 GHz. It measures $1.79^{\circ} \times 1.0^{\circ} \times 0.4^{\circ}$ and costs \$550 in small quantities.

The Tacan coupler system is a high power sub-system designed to monitor the forward and reflected power of a Tacan radar transmitted signal. Two detected outputs for each direction are provided. The system measures 15,000 watts peak over the 960 to 1215 MHz range. Coupling is within 0.2 dB and directivity is at least 20 dB over the whole band. Merrimac Industries, Inc., West Caldwell, NJ. INFO/CARD #199.



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FLL171ME	2.3	32.5	12.5
FLL351ME	2.3	35.5	11.5
FLL50ME	2.3	36.0	10.0
FLL100ME	2.3	39.0	8.0
FLL200IB-1	1.5	42.5	13.0
FLL200IB-2	2.3	42.5	11.0

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PBW Discriminator Aydin Vector

The Aydin Vector MMD-91 airborne discriminator is used to convert FM multiplexed subcarrier signals to the original channel information. It features circuits for the input bandpass filter, pulse averaging



detection circuit and output lowpass filter. Specifications include \pm 7.5 percent deviation for the IRIG PBW signals and stability of \pm 1.0 percent. Aydin Vector Division, Newtown, PA. INFO/CARD #198.

Triax Connectors AMP Inc.

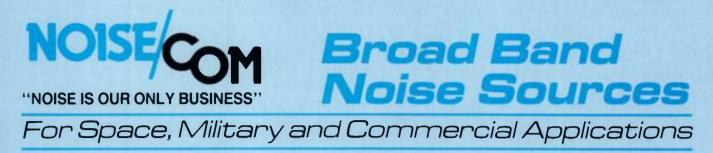
These miniature and subminiature triax connectors offer termination to both cable and printed circuit boards. They use crimp technology for easy assembly, reduced rejects and lower applied costs. Performance is rated to 500 MHz with good EMI/RFI shielding characteristics. AMP Inc., Harrisburg, PA. INFO/CARD #197.

Instrumentation Amplifier Hewlett-Packard

HP introduces a 100 kHz to 3 GHz instrumentation amplifier with 25 dB gain and 20 dBm output power. Output power is adjustable over a +2 dBm to +20 dBm range. With built-in internal leveling, output flatness is held to \pm 1.5 dB over the full band. The HP 8347A is priced at \$3,950.



The HP 5371A is an instrument designed to analyze and characterize timevarying signals to 500 MHz. It allows users to understand the dynamics of frequency, phase, time interval and jitter of complex signals. Hewlett-Packard Company, Palo Alto, CA. INFO/CARD #196.



DC-50 GHz

Chips and Diodes Glass Ceramic Beam-Lead Hermetically Sealed MIL-STD-202 Audio VHF UHF RF MW MM	<section-header><text><text></text></text></section-header>	Broad Band Precision, Calibrated Coaxial SMA, N, TNC Output Connectors
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TYPICAL STANDARD MODELSNC 501up to 500 MHzNC 502up to 1 GHzNC 503up to 2 GHzNC 504up to 3 GHzNC 505up to 4 GHzNC 506up to 5 GHzALL ARE IN STOCK	TYPICAL STANDARD MODELSNC 1101Aup to 20 kHzNC 1107Aup to 100 MHzNC 1108Aup to 500 MHzNC 1109Aup to 1 GHzNC 1110Aup to 1.5 GHzOther frequency rangesand output levels availableMOST ARE IN STOCK	NOISE IS OUR ONLY BUSINESS" NOISE IS OUR ONLY BUSINESS" NOISE COM, INC. E. 64 Midland Ave. Paramus, NJ 07652 PHONE (201) 261-8797 FAX (201) 261-8339 TWX 910-380-8198

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NC 5200 Series	DARD MODELS up to 50 GHz 15.5 dB ENR, noise figure meter compatible up to 50 GHz 21-25 dB ENR,	NC 6101 NC 6107 NC 6108 NC 6109 NC 6110 NC 6111 NC 6218 Other standard MOST ARI	up to 20 kHz up to 100 MHz up to 500 MHz up to 1 GHz up to 1.5 GHz up to 2 GHz up to 18 GHz models available E IN STOCK	DC COUPLED AMPLIFIED MODULES 1 volt output into 50 ohms DC-100 kHz Low offset voltage Compact DC-4 MHz
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exceeds 60 dB. Phase noise at 1 kHz is -70 dBc/Hz. Communications Techniques, Inc., Whippany, NJ. Please circle INFO/CARD #193.

SAW Oscillators Andersen Laboratories

Andersen unveils a line of SAW oscillators that operate up to 2.4 GHz. The frequency determining elements are fabricated on quartz substrates for optimum stability. The specifications include a carrier frequency range of 1.2 GHz to 2.4 GHz, output power of 13 dBm ±2 dB, spurious outputs at -60 dBc max. and SSB phase noise of -90 dBc/Hz max. at 1 kHz offset. Andersen Laboratories, Inc., Bloomfield, CT. INFO/CARD #191.

GaAs Chip Set Anadigics

This GaAs chip set is intended for applications in excess of 2 Gbit/sec and consists of the ALD30010 laser driver, the ATA30010 transimpedance amplifier, and the ACP10010 0.5 ns decision circuit. Anadigics, Warren, NJ. Please circle INFO/CARD #195.

Automated Tuner Maury Microwave

A 3.5 mm automated tuner covering the 4 to 26.5 GHz range will be introduced for use with the Maury automated tuner system (ATS). The tuner can be controlled via the GPIB ATS controller. Maury Microwave Corp., Cucamonga, CA. Please circle INFO/CARD #194.

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rf design feature

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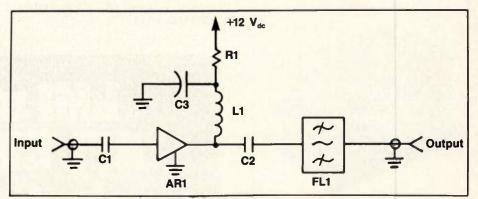
By Jerry Hinshaw

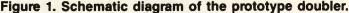
This article describes a practical, inexpensive and unconditionally stable frequency multiplier design suitable for a wide range of VHF through low microwave applications. A single silicon MMIC stage provides conversion gain as a doubler. Several stages may be cascaded to provide a binary multiplication factor, with the output signal at a useful level. Good spectral purity and fair tuning range are additional positive features of this approach.

he idea which led to the construction of this circuit was the discovery that the second harmonic output of a moderately driven MMIC amplifier is at or above the input level. This suggested that the device can be used as a unity gain doubler. The MMIC devices are unconditionally stable, so an output filter cannot drive the MMIC into oscillation. There are no tuned idlers to restrict bandwidth, so the multiplier has the same bandwidth as the output filter, up to the theoretical 50 percent. (Maximum bandwidth of a singleoutput multiplier is 1/N, where N is the multiplication ratio. At greater bandwidths, other harmonics will appear at the output.)

By contrast, earlier methods of harmonic multiplication have some shortcomings. Instability, tuning difficulties, temperature problems, cost, losses, and complexity are some of the difficulties faced by designers of transistor or diode multipliers. MMIC stages, with their stability and broadband gain, minimize or eliminate these problems. The only real shortcoming of today's MMIC blocks is that their resistive biasing and feedback schemes result in moderate power consumption. Except for battery-powered equipment, the power requirements are not too restrictive. To fully appreciate the appeal of this MMIC design, let us review several other methods of frequency multiplication.

One comparable method uses step recovery diodes (SRDs). These have been common for a quarter-century, and for many years were one of the best, if not only, solid-state methods of producing moderate power at microwave frequencies. In an SRD multiplier, energy is stored, usually in a small inductor, then "dumped" into a resonant output circuit





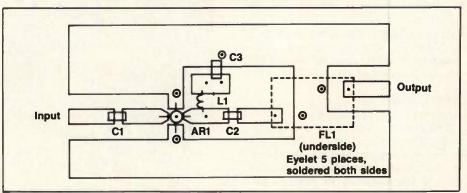


Figure 2. Component layout sketch.

Device	Quantity	Cost
Amplifier MMIC	1	0.99
Helical Filter	1	3.10
Chip Capacitors	3	0.36
Resistor	1	0.11
Inductor	1	0.27 (optional)
Circuit Board	-	0.40 (at \$1 per in ²)
TOTAL	NUMBER OF THE OWNER	\$5.23

Table 1. Cost estimates for double circuit, in 100 quantity.

in a small fraction of an RF cycle by the fast-switching diode. The rapid transition from open to conduction creates an output rich in harmonics. An output filter selects one of the harmonics and rejects the others.

An SRD multiplier has few parts, and can have fairly low losses, but never gain. Disadvantages include a specialized, and therefore expensive, diode. Matching the input and output can be tricky, especially in applications where moderate bandwidth and operation over a wide temperature range are required. The output spectrum "breaks up" into undesired harmonics as the port impedance of the diode shifts with changes in RF drive and temperature. In short, tuning and stabilizing an SRD multiplier can be a major headache.

A second method of frequency multiplication in the lower microwave region is the operation of a transistor stage as a nonlinear amplifier with the input tuned to the fundamental and the output matched at the second (or higher) harmonic. The transistor is usually biased as a Class C amplifier, so the input signal drives the device through the highly nonlinear region of the device's I-V junction response near the origin. This produces an output rich in harmonics. The transistor provides gain, and the harmonic power level can be as great or greater than the fundamental drive power, as with the MMIC.

However, a single discrete transistor stage will often be unstable, especially with the reactive terminations needed to filter the undesired harmonics. As with the SRD, a transistor multiplier can produce oscillatory spurious outputs at certain drive levels, especially as temperature changes shift the operating point and alter the junction characteristics. The need for tuned input and output networks adds to the size and cost of such multipliers, and they have never been especially popular.

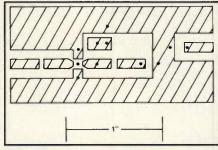


Figure 3. 1:1 artwork master for test board.

Compared to these two methods, the MMIC doubler design has some real advantages. The MMIC gain block is very small, and its stable characteristics mean that a production circuit can give repeatable performance with minimal adjustment. the circuit will operate reliably over the full operating temperature range with only small variations in output power and without spurious generation. The variation in output power versus temperature seems even smaller for MMIC stages with high drive levels, probably because the saturation characteristics vary less than the small-signal gain as temperature changes.

Circuit Description

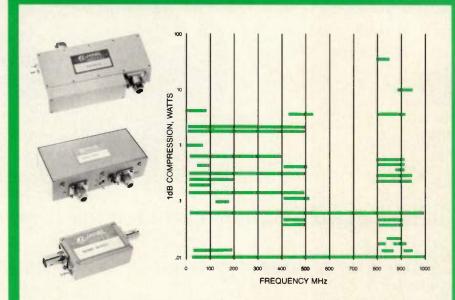
To test this MMIC design idea, I built a simple VHF multiplier stage. This circuit used a readily available helical resonator filter at the output to select the desired second harmonic. The result was a lowcost, small doubler stage which performed as expected. This test circuit is representative of typical applications in the VHF and UHF range.

The basic multiplier has one active component, a filter, three capacitors and one resistor, as shown in Figure 1. An input signal applied to the MMIC amplifier produces an output at the input frequency as well as the second harmonic. At the output, a filter tuned to twice the input frequency passes the second harmonic energy and reflects the fundamental (x1) signal back into the MMIC, where it may contribute to more distortion. (The x2 out-

put is, after all, really just desired distortion.)

The output frequency of the test circuit was chosen to utilize a 440 MHz bandpass filter that was on hand. This filter, manufactured by Toko, is small and inexpensive, but has very good skirt selectivity and only about 2 dB passband loss. It seemed a good candidate for combining with a MMIC. Thus, the test circuit doubles a 220 MHz input to a 440 MHz

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V _{IN} Low/High 0/-5	V
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Dimensions 0.180" × 0.180" × 0.052	7"

GaAs MMIC SPDT Switch Model SW-219

Frequency Range	DC to 3 GHz
Insertion Loss	0.7 dB max
Isolation	40 dB min
Switching Speed	2 ns typ
Control Voltages	
V _{IN} Low/High	0/-5V
Input Power for 1 dB Comp	ression 25 dBm typ
Dimensions0.	$180'' \times 0.180'' \times 0.057''$

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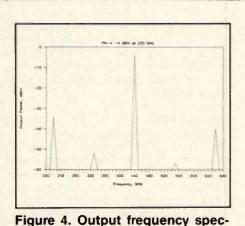
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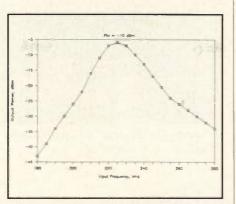


Figure 5. Output bandwidth plot.

trum plot.

output. No attempt was made to reduce the size of this prototype, but the total area occupied by the components is small. The filter used is small, but even smaller filters using ceramic-loaded resonators are available.

The multiplier was hastily built on a hand-cut board. A simple layout (Figures 2 and 3) was drawn at 2:1 scale, reduced to 1:1 and used to guide a hobby knife blade in scoring the copper on one side of a 1/16 inch glass-epoxy PC board. After scoring, the copper is easily peeled away to leave isolated microstripline traces. The other side of the board is left undisturbed as a ground plane. Several eyelets couple the top and bottom ground planes together in areas needing a good ground, such as near the MMIC leads and the filter connections. Details of the construction are painfully visible in the photographs, Figures 6 and 7.

Test Results

The prototype doubler tests were simple and pleasing. The MMIC bias was normal, about 4.5 volts at the output lead and 17 milliamperes current. A test signal at

220 MHz immediately produced a strong output at 440 MHz, with undesired harmonics well attenuated. These data are presented in Figure 4, which shows the output frequency spectrum for a constant power 220 MHz input. The 220 MHz fundamental is over 30 dB below the second harmonic output, with other products lower yet. The other low level products shown are due to leakage from a 110 MHz crystal oscillator that was part of the test setup. No tuning of the filter was necessary, as the factory preset adjustments put it on frequency.

The output power varies with input power, but above -5 dBm drive level there is little change. This indicates that the saturation level for harmonic output is fairly constant, a desirable characteristic for easier cascading of several stages.

The doubler circuit has a fairly narrow bandwidth, as shown in Figure 5. This is as expected from the sharp helical resonator filter alone, with no significant bandwidth shrinkage that might be attributed to the MMIC stage. No instabilities were observed as the input frequency was swept. For a narrow-band application, this



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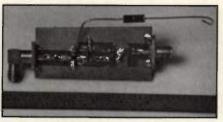


Figure 6. Photo of prototype doubler — topside.

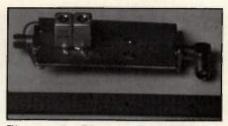


Figure 7. Photo of prototype doubler — underside.

Innovation

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Applications

There are many potential applications for this MMIC doubler design. In general, it can be used whenever low cost, small size, stability and moderate bandwidth are required, and where the moderate power supply drain is not a problem.

As an example, VHF and UHF local oscillator (LO) chains for mobile radios can be built with this technique, using cascaded doublers. Narrowband stages are ideal for single-frequency LOs, since they maximize the spectral purity of the final output. For a cascade of several doublers, the input drive power should be relatively strong, so the output level of each stage is saturated.

For multiple frequency use, a lowfrequency synthesizer could drive a cascade of wider-band doublers. At present, low- cost synthesizer components work up to about 1 GHz. Above this, MMIC doublers could easily increase the range to 2 to 4 GHz. For example, a synthesizer covering 732.5-737.5 MHz can be doubled to 1465-1475 MHz. This signal can then be used to downconvert the 1535-1545 MHz mobile satellite band to a fixed 70 MHz IF for demodulation. The lower synthesis frequency can be achieved at lower cost and lower power consumption than it would directly at L-band. Phase noise at the output of the doubler will, of course, be increased by the familiar 20 log N (dB), where N is the multiplication ratio. However, this degradation would probably be seen in a practical synthesizer, as well. For narrowband voice and data applications, the MMIC design will certainly suffice.

The range of potential uses for silicon MMIC multipliers seems wide, and should remain so even as GaAs devices become more available. The cost of silicon MMICs will doubtless remain below that of GaAs for some time. The simplicity of this design, and its ease of production, is an improvement over earlier frequency multiplication schemes.

About the Author

Jerry Hinshaw is a senior staff engineer at Bendix Communications Division, 1300 E. Joppa Road, Towson, MD 21204. This article is his 1987 RF Design Awards contest entry.

rf designer's notebook

Unequal Power Division with a Lumped Element Divider

By David H. Burgess Honeywell, Inc.

The Wilkinson power divider is widely known and many variations are used in microwave systems today (1). Its most useful characteristics are good impedance matching at each port and high isolation between output ports. This device, usually implemented using transmission lines, can also be realized with lumped elements, making it useful for VHF applications (2). For some applications, an unequal power split between the two output ports is desirable. Such a design is described for hybrid Wilkinson power dividers by Parad and Moynihan (3). This paper discusses the design of a lumped element version of an unequal split power divider.

The design equations for the Wilkinson power divider are derived in terms of the power split ratio, k, the characteristic impedance, Z_o , and the center frequency, ω . The proposed circuit is shown in Figure 1. A sample design is verified by computer simulation using TouchstoneTM and the measured response of a prototype circuit is presented.

By forming the equivalent circuit shown in Figure 2, the circuit can be analyzed by employing the method of even and odd mode analysis outlined by Reed and Wheeler (4). To achieve a power splitting ratio of

$$K = \frac{Power \text{ out Port 2}}{Power \text{ out Port 3}} \ge 1$$

the impedance level from port 1 to port 3 is constrained to be

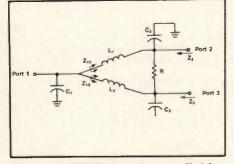


Figure 1. Wilkinson power divider.

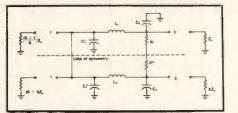


Figure 2. Equivalent circuit.

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k times the level from port 1 to port 2. This results in

$$Z_{12} = \frac{1 + K}{K} Z_{o}$$

$$Z_{13} = KZ_{12} = (1 + K) Z_{o}$$

$$Z_{2} = Z_{o}$$

$$Z_{3} = KZ_{o}$$

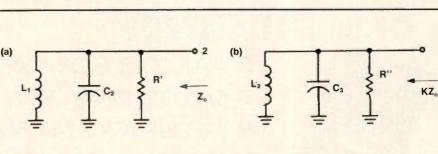
where Z_o is the input impedance looking into port 1. Z_{12} and Z_{13} were calculated from the requirements that Z_{12} in parallel with Z_{13} equals Z_o and $Z_{13} = kZ_{12}$. Note also that the output impedance of port 3 is no longer Z_o and must be transformed back if desired.

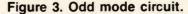
For the odd mode, equal voltages of opposite phase are applied to ports 2 and 3 resulting in a virtual ground along the line of symmetry. From this, the simplified circuit of Figure 3 can be drawn. At the design center frequency, ω_0 , each port should be matched.

So from figure 3a $R' = Z_0$

$$\omega_0 L_1 = \frac{1}{\omega_0 C_2}$$

(2)





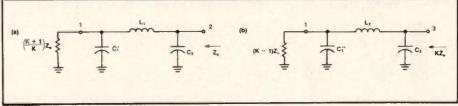
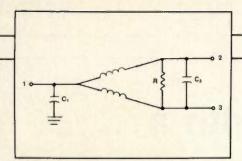
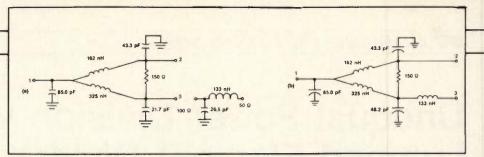
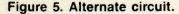


Figure 4. Even mode circuit.







and from Figure 3b

Figure 6. The final circuit.

(3)

Similarly from Figure 4b

$$\omega_{o}L_{2} = \frac{1}{\omega_{o}C_{3}} \tag{4}$$

Also from Figure 2 R = R' + R''

and substituting (1) and (3) $R = Z_o + KZ_o$

or

$$R = (K + 1) Z_{o}$$

 $R'' = KZ_{o}$

In the even mode, voltages of equal amplitude and phase are applied to ports 2 and 3. This results in an open circuit along the line of symmetry and the simplified equivalent circuits are given in Figure 4. Again, the ports should be matched at ω_{o} .

From Figure 4a

$$Y_{o} = j\omega_{o}C_{2} + \frac{\begin{pmatrix} K \\ \overline{K+1} & Y_{o} + j\omega_{o}C_{1} \end{pmatrix} \frac{1}{j\omega_{o}L_{1}}}{\begin{pmatrix} K \\ \overline{K+1} & Y_{o} + j\omega_{o}C_{1} \end{pmatrix} + \frac{1}{j\omega_{o}L_{1}}}$$

where
$$Y_o = \frac{1}{Z_o}$$

which after sufficient manipulation yields

$$\omega_{o}L_{1} = \frac{1}{\omega_{o}C_{1}} = \sqrt{\frac{K+1}{K}} Z_{o}$$

$$Y_{o}/K = j\omega_{o}C_{3} + \left(\frac{\frac{1}{K+1} + Y_{o} + j\omega_{o}C_{1}}{\frac{1}{K+1} + Y_{o} + j\omega_{o}C_{1}}\right) + \frac{1}{\frac{1}{j\omega_{o}L_{2}}}$$

which yields $\omega_{o}L_{2} = \frac{1}{\omega_{o}C_{1}} = \sqrt{K(K+1)}Z_{o}$ (6)

Using equations (1) through (6), each component can now be solved for in terms of k, ω_0 , and Z₀. The resulting design equa-

$$R = (K + 1)Z_{o}$$

tions are summarized below.

Touchston	FOWDIV	Ver(1.30 .clt 01		0:11:50	0008-1768	- 1000
FREQ-MHZ	DB(S11) PD	DBES22J PD	DBLS351 PD	DBES213 PD	DUES313 PD	DHE SS:
30,0000	-15.510	-23.240	-9.91	-1.961	-5.111	-9.5
35.0000	-14.459	-21.980	-10.94*	-1.877	-5.142	-10.90
40.0000	-15.607	-20.902	-12.383	~1.828	-5.101	-12.73
45.0000	-17.147	-20.907	-14.42	-1.800	-5.01	-15.08
50.0000	-19.531	~22.381	-17.486	-1.783	-4.907	-18.41
55.0000	-24.244	-26.676	-22.993	-1.769	-4.91	-24 -28
60.0000	- 69.657	-67.799	-57.694	-1.761	-4.7/1	-70.75
65.0000	-21.364	-23.694	~21.361	-1,781	-4.837	-24.08
70.0000	-14.005	-16.500	-14.452	-1.889	-5.094	-18.12
75.0000	-9.405	-12.053	-10.091	-2-177	-5.646	-15.24
80.0000	-6.234	-8.957	-7.006	-2.742	-6.587	-13.63
85.0000	-4.044	-6.798	-4.85	-3.625	-7.938	-12.98
70,0000	-2.638	-5.327	-3. 7	-4.770	- 9.630	-17.04

Figure 7. Touchstone[™] results.



(5)

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(7)

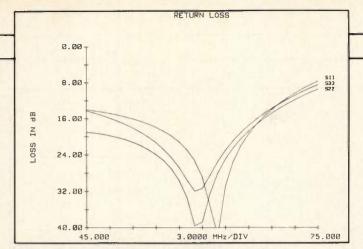


Figure 8. Return loss versus frequency.

$$C_1 = \sqrt{\frac{K+1}{K} \frac{1}{\omega_0 Z_0}}$$
(8)

$$L_1 = \sqrt{\frac{K+1}{K} \frac{Z_0}{\omega_0}}$$
(9)

$$L_2 = \sqrt{K(K+1)} Z_0 / \omega_0 \tag{10}$$

$$C_2 = \sqrt{\frac{K}{K+1}} \frac{1}{\omega_0 Z_0}$$
(11)

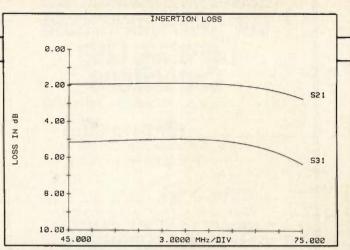
$$C_3 = \frac{1}{\omega_o \sqrt{K(K+1)}Z_o}$$
(12)

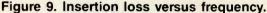
An alternate circuit is shown in Figure 5. A similar analysis results in the design equations given below.

 $R = (K + 1)Z_{o}$

$$C_1 = \frac{1}{\omega_0 \sqrt{K} Z_0}$$

$$L_1 = \frac{Z_0}{\omega_0 \sqrt{K}}$$





$$L_{2} = \frac{\sqrt{K} Z_{o}}{\omega_{o}}$$
$$C_{2} = \frac{\sqrt{K}}{\omega_{o}(K+1)Z_{o}}$$

Computer Simulation

A power divider was designed using equations (7) through (12) with k = 2 and $f_0 = 60$ MHz. Additionally, a 100 ohm to 50 ohm "L" matching network was added to port 3. By choosing a matching network with a shunt capacitor followed by a series inductor, the shunt capacitor can be combined with C3, thereby requiring the addition of only one component to match port 3 to 50 ohms. The calculated circuit values are displayed in Figure 6.

At 60 MHz all three ports should be closely matched to 50 ohms, and ports 2 and 3 should be well isolated. The calculated values for S21 and S31 are -1.76 dB and -4.77 dB, respectively. The circuit was simulated on Touchstone and the printout is listed in Figure 7. Considering that only three significant figures were retained for the component values, the results are essentially exact, which they should be if the design equations are valid. The fact that this power divider is constructed of low pass type structures is clearly seen in the frequency response displayed by the computer simulation. Note that the high end performance degrades much more rapidly than the low end.



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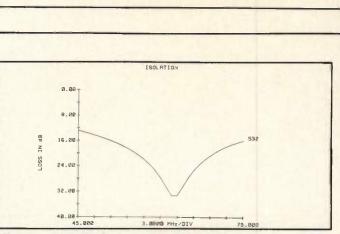


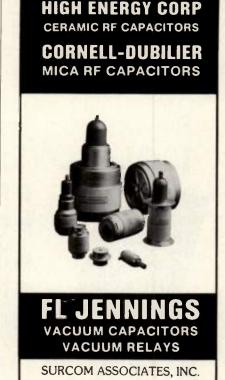
Figure 10. Isolation versus frequency.

Test Results

A prototype power divider was constructed using standard capacitor and resistor values and hand-wound toroidal inductors. The prototype circuit was measured on a network analyzer. Figure 8 shows the return loss at each port to be in excess of 14 dB (VSWR of 1.5:1) up to 69 MHz. The measured transmission loss at 50 MHz from port 1 to port 2 was 1.9 dB and the loss from port 1 to port 3 was 4.9 dB. This is in close agreement with the results of the computer simulation which did not take into account component losses due to finite Q's. Note the 3 dB difference, or 2 to 1 ratio, between S₂₁ and S₃₁. Finally, the isolation between ports 2 and 3, shown in Figure 10, was greater than 20 dB from 55 to 69 MHz, giving the device a useful bandwidth greater than 20 percent.

Numb	W (2)	Impedance Ohms (Power W)	Frequency	BNC	UNIT	PRICE (4)	EFFECTIVE	9-15-86	PC
			and a	ONC	INC		SRA	UMF	PC
AT SOL	tionuators	1 to 20 dB	DC-1 SGHz	14 00	20.00				
AT 51	-1	50 (5W)	DC 1 5GHz	11.00	15.00	20 00	18 00	-	12.00
AT-52		50 (1W)	DC-1 5GHz	14 50	20 50	20 50	19.50	-	12.00
AT-53		50 (25W)	DC-3 DGHz	14.00	17 00	-	15.00	_	-
AT 54		50 (25W)	DC-4 2GHz	-	-	-	18.00		-
AT 55		50 (25W)	DC-4 2GHz	-	-	-	14.40 11	0 Pa 2	-
AT 78 c	AT SO	76 or 93 (5W)	DC 1 5GHz (750MHz)	11 50	20 00	20 00	18 00	-	-
Detecto	H. Miser, Z	ero Bias Schottky							
CD-51,	75	50, 75	01 4 2GHz	54 00	-	-	54.00	-	
DM 151		50	01-4 2GHz	-	-	-	64 QD	-	-
Resistiv	e impedar	ce Transformera Mi	NIMUM Loss Rada						
RT-50/1	15	50 to 75	DC 1 5GHz	10.50	19.50	19 50	17 50		
RT \$0/1	3	50 to 93	DC 1 0GHz	13 00	19.50	19 80	17.50	-	_
CT-SO (50 (59/)							
CT 51	•,	50 (5W)	DC 4 2GHz DC 4 2GHz	11 80	15.00	15.00	17 50	-	-
CT-52		50 (1W)	DC 2 5GHz	9 90	12 00	10.50	9 50	15.50	-
CT-53/		50 (5W)	DC 4 2GHz	5 60(1	10.00	10.00	13:00		-
CT-54		50 (2W)	DC 2 DGHz	14 00	15 00	15.00	17.50	_	-
CT-75 CT 93		75 (25W)	DC 2 5GHz	10 50	15 00	15.00	13.00	15.50	-
C1.93		93 (25W)	DC 2 5GHz	13 00	15 00	-	15 00	15 50	-
Manage									
MT-51	cned Term	50 1 05 1 to 3	1, Open Circuit, Short Cl	rcutt					
MT 75		50	DC 3 0GHz DC 1 0GHz	45 50	45 80	45 50	45.50		-
				-	-	45 50	-	-	-
	ru Termina	tions shunt resistor							
FT 50 FT 78		50	DC 1 0GHz	10 50	19 50	18.50	17.50	-	-
FT 90		75 93	DC-500MHz DC-150MHz	10 50	18 50	19.50	17 50	-	-
			DC 150MHz	13 00	10 50	19 50	17 50	-	-
Orectio	mel Couple								
DC 500		50	250-500MHz	60 00	-	84 00		-	-
Resistiv	· Decoupie	ar, series resistor or i	Capacitive Coupler, series	capacitor					
RD or C	C-1000	1000 (1000PF)	DC-1 5GHz	12 00	18.00	18.00	17.00	-	-
Adapter									
CA-80 (N to SMA)	50	DC-4 2GHz	13.00	13.00	13.00	13.00		
Inductio	n Decoupt	ers, series inductor		10.00		10.00	13.00		_
LD-815	e ceccapi	0 17uH	DC 500MHz	12 00	18 00	18.00			
LD-6R8		6.8uH	DC 55MHz	12 00	18 00	18 00	17 00	-	-
fired At		ets. 3, 8 10 and 20				*****	17 00	-	-
AT 50-8	ET (3)	50	DC 1 5GHz	60 00	64 00				
AT-51-8		50	DC 1 SGHz	48 00	64 00	84 00 84 00	76.00	-	-
		siere. 2 and 4 output			~~~~	~~~~	00.00	-	-
TC-125-	2	50 Solution and 4 output	1.5-125MHz	54 00		67 00			
TC 125-	4	50	1 5-125MHz	67 00	-	87.00	67.00 61.50	-	-
		riders 3, 4 and 9 por				00.00	01 30	-	-
AC 3 50	- ower Di	50 3.4 and 9 por	DC 2 OGHz	84 00	84.00		64.00		
RC-4 50		50	DC-500MHz	64 00	84.00	-	64.00	-	-
RC 6-50		50	DC-500MHz	-	-	-	84.50	-	-
RC 3-75	4-75	78	DC 500MHz	64 00	84.00	-	64.00		-
Double (Selenced N	lixors							
DBM-10	00	50	8-1000MHz	61.00	-	71.00	61.00	-	34 00
08M-50	OPC	50	2-500MHz	-	-	-	-	-	34 00
RF Fuse	1/8 Amo	and \$/16 Amp							
FL 50		50	DC-1 SGHz	12 00	18 00	45.50	17.00		
FL 75		75	OC 1 5GHz	12 00	18.00		17 00	-	-
MOTE .	Catton	remeters fully to the							
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Summary

Equations which allow the design of lumped element power dividers with arbitrary power splitting ratios while preserving matched conditions at each port and high isolation between the output ports have been presented.

These equations were verified by computer simulation and were used to build a prototype circuit that performed in close agreement with theory.

The author wishes to thank Dennis Francis for his assistance in constructing and testing the prototype circuit.

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About the Author

David H. Burgess is senior project engineer at Honeywell, Inc., Sperry Commercial Flight Systems Division, P.O. Box 52029, Phoenix, AZ 85072.

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SA5-200-512 SA5-200-513 SA5-200-530	200 1800 MHz 1000 - 18000 MHz 150 530 MHz		SAS-200 560 SAS-200 561	per MIL-STD-461 pir MIL-STD-461	
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Fundamentals of Red/Black System Engineering

Basic Information on TEMPEST Requirements

By Michael L. Brooks Litton Data Systems

For a secure communications program, the task of system engineering includes describing the overall TEMPEST engineering requirements. Within the context of this task is the requirement to define the Red/Black system engineering methodology. This article provides the fundamental concepts for determining this methodology in order to illustrate the necessity of a front-end Red/Black engineering definition for a TEMPEST system design.

To achieve a successful Red/Black-engineering design, system engineers must have the necessary background to enable them to define Red/Black engineering tasks for a secure communications, or TEMPEST, program. Unfortunately, Red/Black engineering is overlooked by many engineers due to their lack of understanding of the applicable TEMPEST requirements and the consequent tasks required in determining the Red/Black system engineering methodology. This aspect of system engineering is important for national security and mission success of military programs. This implies the necessity of increasing the understanding of system engineers (and technical managers) in the Red/Black design process.

The basic units of a secure communications, or TEMPEST, system are the Black and Red equipment types. The Black equipment are those which process data not containing any classified (national security sensitive) information, whereas Red equipment processes data with classified content. By definition, a TEMPEST system will include both Black and Red equipment types. (However, one possible exception is a satellite system which may be defined totally as Red, with only its space environment defined as Black.)

By definition, the transfer of Red data between Red and Black units is not permissible. However, Black and Red units cannot be connected directly, because some type of interface is required between these equipment to prevent the inadvertent transfer of classified information. The Red/Black interface contains the necessary circuitry (such as filters and isolation devices) and the required mechanical design techniques (such as shielding and low-impedance bonds) to prevent this illegal transfer of data. The reference provides a complete discussion of the Red/Black interface and related terms.

This Red/Black interface is separate from the Red and Black units (assuming that these units are existing designs, which were not originally engineered with Red/Black constraints, and are not modifiable), and serve a single purpose of achieving directional Black-to-Red information transfer. The exception to this single-purpose function are encryption devices, which serve a dual role as Red/ Black interfaces. First, these devices encrypt the Red signals and convert them into Black signals, and also de-encrypt incoming Black (encrypted-Red) signals and convert these back into the plain-text Red format. Second, the encryption device provides the required Red/Black separation and isolation needed to prevent the coupling of the plain-text Red signals onto the encrypted-Red or other Black lines.

Table 1 is an interconnection matrix indicating the different possibilities for the transfer of Red and Black data within a system. Note that while Red-to-Red and

From:	To:	Red Unit	Black Unit
Red Data @ Red Unit		ок	No
Black Data @ Red Unit		ок	OK,?
Black Data @ Black Unit		OK,?	ок

Table 1. Red/black interconnection matrix.

Black-to-Black signal transfers are acceptable, the transfer of Black data between Black and Red units may be accomplished only via a Red/Black interface ("OK,?"). In some cases, Red-to-Red signal interconnection may not be permitted if these distinct Red signals have been given different classification levels. Of course, the transfer of Red data to a Black unit is not permitted.

Ground Rules

Table 2 lists the ground rules for Red/ Black system engineering. The system engineer must be able and willing to pay the price to ensure a satisfactory and complete TEMPEST system design for the secure communications program. Included in this price for a successful design is the requirement for the system engineer to acquire a thorough understanding of the program, TEMPEST requirements and their implications to the Red/Black engineering methodology. Then, an understanding of the functional operation of each equipment unit or subsystem in the system must be obtained in order for the system engineer to differentiate between the Red and Black signals and equipment types.

Armed with this background in system operation, Red signal flow patterns and equipment designations, the system engineer is then able to determine if the conceptual or preliminary system design is adequate for providing a sufficient number of Red/Black interfaces. If a sufficient number of these interfaces is not provided in the system, then it is imperative that the system engineer work with the program office personnel and electical and mechanical design engineers to ensure that an adequate number of Red/Black interfaces is included in the design.

It is necessary for the system engineer to enlist the assistant of a program TEMPEST engineer to educate these individuals, as well as himself or herself, in Plessey GaAs FETs. Drop-in NE710 replacements for companies with severe wait problems.

Most people tend to think of the NE710 as the standard FET of the industry, particularly when it comes to broadband military amplifiers. But what many fail to realize is that oftentimes you have to wait for your parts. That's why it's crucial that you have a second source for gallium arsenide FETs. And that's precisely where Plessey comes in. We designed our P35-1140s to be drop-in replacements for NE710s. You'll find that Plessey FETs give you improved handling and higher yield. So, call today. And lose wait now.



Plessey, Plessey Three-Five Group and the Plessey symbol are registered trademarks of the Plessey Company plc. TEMPEST terminology and in the requirements for Red/Black interfaces and their electrical/mechanical design constraints. The requirements for these interfaces must be sufficiently detailed for the electrical and mechanical engineers, so that they will be capable of implementing the correct design methodology at each different interface.

After the Red/Black engineering methodology is established for the system, the system engineer must be willing to maintain a constant interaction with these engineers to ensure that the TEMPEST system requirements are completely understood. Finally, the system engineer must coordinate all aspects of the system design with the program TEMPEST engineer to avoid conflicting design methodologies, and utilize him or her as a technical resource to verify the accuracy and completeness of the TEMPEST system design.

Conclusion

The system engineer has an important role in the TEMPEST design process for a secure communications system. Not on-

- 1. Understand the TEMPEST system requirements.
- 2. Analyze the system architecture for Red and Black signal flow.
- 3. Differentiate between the Red and Black equipment types.
- 4. Determine if a sufficient number of Red/Black interfaces are provided in the system.
- 5. Clarify the Red/Black interface design constraints to the equipment electrical and mechanical engineers.
- 6. Follow-up the system engineering design tasks with interactions with all engineers to ensure a satisfactory design.
- Coordinate the system design approach with the program TEMPEST engineer to avoid conflicting design methodologies and to verify the accuracy and completeness of the design.

Table 2. Red/black system engineering groundrules.



ly must he or she obtain a good understanding of the Red/Black design requirements and their implications to the system design, but must also be willing to assist the program TEMPEST engineer in educating those involved in the design process to ensure that all engineers and managers appreciate these implications and the methods of their implementation. Complete and sufficient TEMPEST system designs that will satisfy the program requirements are imperative to ensure mission success and the maintenance of national security.

References

1. Michael L. Brooks, "Understanding TEMPEST Requirements — Part 1," *RF Design*, June 1986, p. 69.

2. Michael L. Brooks, "Understanding TEMPEST Requirements — Part 2," *RF Design*, August 1986, p. 32.

About the Author

Mike Brooks is a systems EMC/ TEMPEST engineer with Litton Data Systems, 8000 Woodley Avenue, Van Nuys, CA 91409. He may be contacted at (818) 904-2448.

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Rogers Introduces a PTFE Composite

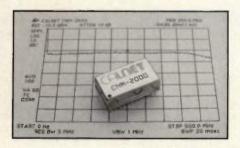
RT/duroid[®] 5880 glass microfiber reinforced PTFE composite is designed for stripline and microstrip circuit applications. It contains glass reinforcing microfibers which are randomly oriented to maximize benefits of fiber reinforcement in final circuit applications. Normally supplied as a laminate with electrodeposited copper, the material composites can be clad with rolled copper foil if required. Cladding with aluminum, copper or brass plate is also available.

This laminate has a thickness of 0.0035" and is useful in applications requiring a low loss laminate. This includes antenna covers, innerlayers in three-layer couplers, and suspended substrates such as those in high-Q filters.

The dielectric constant is 2.20 ± 0.02 at 10 GHz and dissipation factor is 0.0009at 10 GHz. Other thicknesses available range from 0.005" to 0.375". Rogers Corporation, Chandler, AZ. Please circle INFO/CARD #188.

Wideband Noise Generator

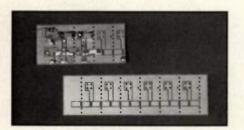
Calnet introduces the Model CNM-2000 noise generator module that produces true white Gaussian noise from 10 kHz to

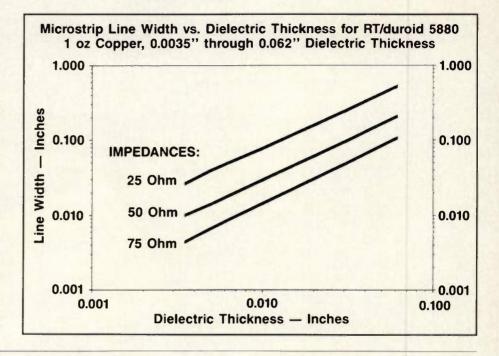


500 MHz into a 50 ohm load. It uses shot and thermal noise as its source. In single quantities, the unit is priced at \$149. Calnet Electronics Inc., Kanata, Ontario. Canada. INFO/CARD #186.

RF/Microwave Prototyping Boards

RF Prototype Systems introduces prototype boards for evaluation and verification purposes. The boards are double sided

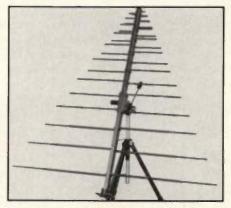




and have plated thru holes. The LPF1 is a general filter that accommodates filters with up to seven sections including lowpass, highpass and narrow bandpass. The GBI, another board, is a gain block layout that provides a 50 ohm interconnection and DC bias for MMIC amplifiers. For evaluating TO-8 voltage controlled oscillators and amplifiers, the TO81 board with holes for SMA connectors and power supply decoupling components is available. In single quantities, the price ranges from \$4.50 to \$9.75 depending on the board. **RF Prototype Systems, San Diego, CA. INFO/CARD #185.**

Log-Periodic Antenna

Model AT1100 log-periodic antenna provides power gain over an isotropic of 6.5 dB (min). Gain flatness is 1.0 dB while



VSWR is 1.8:1 (max) and bandwidth is 100 to 1000 MHz. The antenna is 68 inches from front to back and costs \$2,800. Amplifier Research, Souderton, PA. Please circle INFO/CARD #184.

Voltage Controlled Oscillator

This TO-8 oscillator is available for frequencies from 100 MHz to 6000 MHz. Wideband (octave range) and narrowband (±10 percent of center frequency) units with an optional buffer stage are available.



Frequency variation over the -55° C to $+85^{\circ}$ C is typically ± 0.075 percent for the narrowband and ± 0.15 percent for the wideband units. Micropac Industries, Inc., Garland, TX. INFO/CARD #183.

Tracking Generators

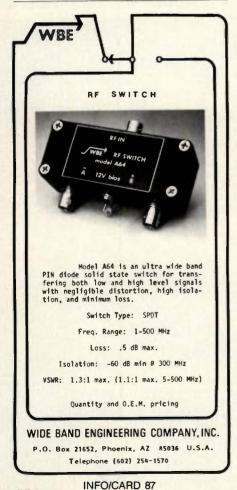
Avcom introduces the MTG-8566A microwave tracking generator which is designed to be utilized with the HP 8566A. The MTG-8566A has a phase locked cavity oscillator referenced to an ovenized crystal oscillator. IF feedthrough at 2050 MHz or 3621.4 MHz is under -65 dBm with a tracked signal response of ±1.8 dB. A 100 dB attenuator is optional. Prices for the MTG series begin at \$1,435. Avcom, Richmond, VA. Please circle INFO/CARD #182.

Logarithmic Amplifier

The AD9521 is a 7 to 250 MHz, 4.7 dB noise figure logarithmic amplifier. Voltage gain range for this amplifier is flat to 1.2 dB from 30 to 160 MHz. Typical applications include radar signal receivers, electronic countermeasures, sonar equipment and miniaturized log strips. Analog Devices, Norwood, MA. INFO/CARD #181.

TV Receiver Chip

LM1822 is a video IF amplifier/PLL detector system which contains all the functions needed for video IF signal processing for TV receivers and cable converter equipment. It incorporates a five stage gain controlled IF amplifier, a phase



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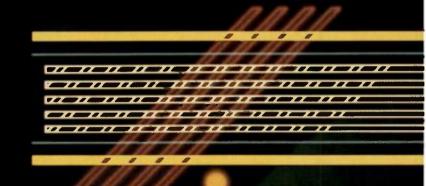
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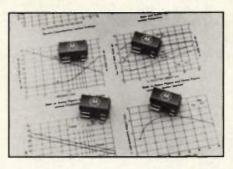
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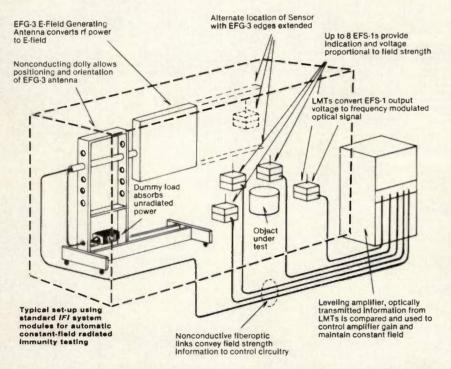
locked loop synchronous detector with white spot noise inversion, an AFC detector and gated AGC. The device accepts IF input signals from SAW filters. In quantities of 100, the IC is priced at \$2.95 each. GE Solid State, Somerville, NJ. Please circle INFO/CARD #180.

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lead surface mount SOT-143 package. The current gain bandwidth is 5.5 GHz and collector base capacitance is 0.7 pF while collector-emitter breakdown voltage is 15 volts (min). The 1 GHz noise figure is specified at 1.8 dB. Motorola, Inc., Phoenix, AZ. INFO/CARD #178.

Tunable Notch Filters

Microwave Filter Company unveils the 6367 Series of tunable notch filters for 30 to 900 MHz which cover an approximate 2:1 frequency range with an adjustable 3 dB bandwidth. Units are available with 50 ohm BNC connectors or 75 ohm F connectors. The price ranges from \$139 to \$179 depending on the frequency range required. Microwave Filter Company, Inc., East Syracuse, NY. Please circle INFO/CARD #179.

Numerically Controlled Oscillator

Stanford Telecommunications introduces the Model STEL-1172B CMOS numerically controlled oscillator. The device generates digitized sine and cosine functions and can be used in conjunction with a D/A converter in analog signal generation and digital signal processing applications. It outputs a phase continuous signal



which can be continuously switched between frequencies in a microsecond. Other features include a 50 MHz clock frequency, 32-bit frequency resolution, -55 dB spur levels, 8-bit sine and cosine outputs or 12-bit phase output and a microprocessor bus interface. The STEL-1172A is packaged in a 40-pin DIP and costs \$225 when purchased in quantities above 100. Stanford Telecommunications, Inc., Santa Clara, CA. INFO/CARD #177.

Dual-Channel Millivoltmeter

Model URV5 is a dual-channel millivoltmeter which measures high impedance RF voltages from 20 kHz to 1 GHz, thruline coaxial voltages from 9 kHz to 2 GHz, power levels from 1 MHz to 18 GHz (50 or 75 ohm), and DC voltages up to 400 V. The instrument has probes and sensors which are automatically sensed for the



proper display of results. Each channel can be displayed separately, as the ratio of one channel to the other, or referred to any reference. A separate analog display allows interpretation of measurement trends. The cost is \$3960 less probes and sensors. **Rohde & Schwarz, Inc., Lan**ham, MD. INFO/CARD #176.

Calibrated Attenuator Set

RLC Electronics unveils a precision type N calibrated attenuator set. Model



A-18-C-N consists of four (3, 6, 10 and 20 dB) fixed attenuators designed to meet the requirements of MIL-A-3933. The average power rating is 2 watts with 500 watts peak. The complete set is available for \$360. RLC Electronics, Inc., Mt. Kisco, NY. INFO/CARD #175.

Hi-Rel Preamplifiers

Model MLA24M is a hi-rel preamplifier that covers from 5 MHz to 1000 MHz with a 3 dB noise figure, linear power output



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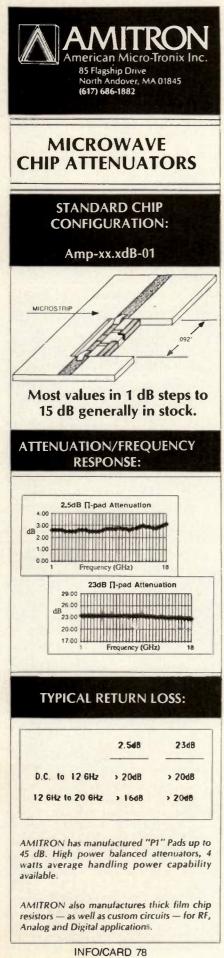
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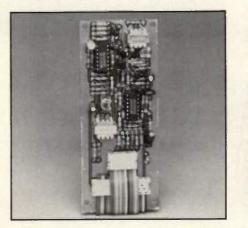


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of 18 dBm and a gain of 20 dB. The device features an aluminum case with female SMA connectors and two mounting holes. Lightning, static and reverse polarity protection is included. Wi-Comm Electronics, Inc., Masssena, NY. Please circle INFO/CARD #174.

Audio Interface Assembly

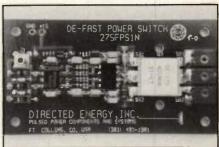
Neulink has developed an interface assembly to process audio information for either speech or data. The data unit can



transmit or receive 700 to 2400 baud manchester coded data, while the speech unit is optimized for passing speech signals. The units are available with Neulink's 450 to 470 MHz or 928 to 960 MHz RF links and interface through a standard 15-pin "D" style connector. Celltronics, San Diego, CA. INFO/CARD #173.

Switch Module

FPS1N is a switch module designed for switch mode, pulsed and low frequency RF applications. It produces a voltage slew rate of 200 kV/us and a current slew rate of 10,000 A/us. The switch is designed for DC to 1 MHz power conversion, as a driver for image intensifiers, Impatt



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diodes, laser diodes, acoustic transducers, TWT and planar triode grid drivers, and as a lab pulse generator. Directed Energy, Inc., Fort Collins, CO. INFO/CARD #172.

Variable Active Attenuator

Janel Labs introduces two variable attenuators using PIN diode circuitry with power capability to +23 dBm and maximum VSWR of 2.0:1. Models AT882 and AT883 provide attenuation of 2.0 dB to 30 dB over 5 to 500 MHz and 2.7 dB to 20 dB over 500 to 1000 MHz. In single quantities, the attenuators are \$62. Janel Laboratories, Inc., Corvallis, OR. Please circle INFO/CARD #170.

RF Vector Voltmeter

The HP 8508A is an RF vector voltmeter useful for RF voltage and phase measurements. The standard version comes with two high impedance probes and operates from 100 kHz to 1 GHz while the optional version has two 50 ohm type N inputs with a 300 kHz to 2 GHz range. With either input channel, the instrument can directly read voltage or power. By connecting the second input elsewhere in the



circuit under test, gain or loss and phase difference between the two nodes can be measured. Signals as low as 10 microvolts (-87 dBm) can be measured and dynamic range is 90 dB. The basic voltmeter costs \$5,500. Hewlett-Packard Company, Palo Alto, CA. Please circle INFO/CARD #171.

Notch Filter

Model 6165 from Microwave Filter Company is a notch filter that eliminates broadband transmitter sideband interference to VHF receivers at the same site. The notch frequency is 47.005 with a minimum depth

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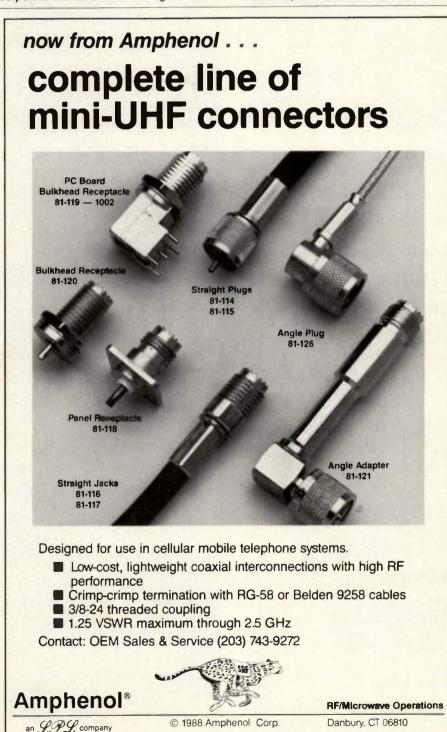
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rf products Continued

of 112 dB and insertion loss of 0.5 dB (max) at 49.595 MHz. At this frequency, power handling is 10 kW CW while impedance is 50 ohms. Price is \$8,680. Microwave Filter Company, East Syracuse, NY. INFO/CARD #170.

8-Bit A/D Has 180 MHz Bandwidth

A monolithic 8-bit flash analog to digital converter that provides typical word rates of 100 MHz with TTL compatible digital outputs is available from Analog Devices. The AD9012 has a minimum encoding rate of 75 MHz, while its 180 MHz bandwidth allows sampling of signals beyond the Nyquist rate. The input capacitance is 16 pF, signal to noise ratio is greater than 46 dB, and harmonic suppression is 54 dB from DC to 1.23 MHz. Applications for this converter include radar, digital oscilloscopes, digital radio, and electronic warfare systems. Pricing begins at \$70 in 100-piece quantities. Analog Devices, Norwood, MA. INFO/CARD #169.



Two-Way Power Divider

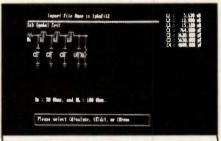
PDM-02S-4G is a stripline power divider covering multi-octaves. The amplitude balance is 0.2 dB max., phase balance is 2° and insertion loss is 0.9 dB. Output port isolation is 17 dB over the 0.5 to 7.5 GHz frequency range. In small quantities, the power divider is \$995. Merrimac Industries, Inc., West Caldwell, NJ. Please circle INFO/CARD #168.

Coax Stripper

This coaxial cable stripping machine strips coaxial, fiber-optic, semi-rigid, triaxial and concentric multiple pair. It accepts cable ranging in size from 0.040" to 0.450" o.d. Price is \$1495. Western Electronic Products Co., San Francisco, CA. INFO/CARD #167.

SPST RF Switch

SWTF1101 is a miniaturized thick film switch packaged in a 8-pin header. The frequency range is 50 MHz to 1 GHz, VSWR is less than 1.5:1, and isolation is greater than 40 dB. When measured between 50 percent logic input to 90 percent or 10 percent RF output, switching speed is less than 100 ns. KDI Electronics, Whippany, NJ. INFO/CARD #166.



Example from RF Notes 4, Network Analysis

RF Notes 4 (NETWORK ANALYSIS) is the latest addition to the very popular Etron design assistant programs for the IBM PC. These programs can save time and money in analog design problems, are fully menu driven, error trapped, and user selectable color or monochrome.

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June 1988

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Video Shift Registers

NSC unveils the DP8515V-350 and DP8516V-350 video shift registers that feature a parallel load rate of 30 MHz and maximum shift rate of 350 MHz. Both devices are parallel-in, serial-out 16-bit units that can act as two 8-bit shift registers through a tap at the eighth bit. They operate from a single ±5 V power supply and feature four 16-bit words of FIFO buffer, essential for high-speed multiple board graphics systems. In 100-unit guan-

tities, the price is \$33 each. National Semiconductor Corp., Santa Clara, CA. INFO/CARD #165.

Analog to Digital Conversion System

Plessey Semiconductors announces the availability of a single chip analog to digital conversion system. Designated the SP94308, the device offers an 8-bit resolution at an 18 MHz sampling rate, with a minimum analog bandwidth of 6 MHz

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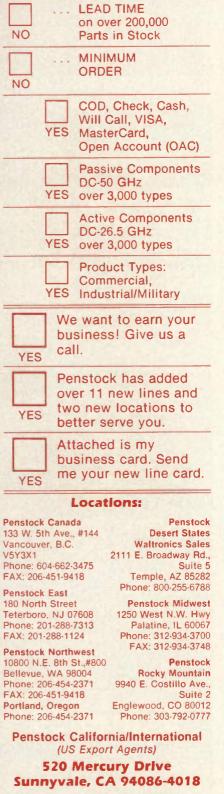
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without preceding sample and hold circuit. When conversion of luma or full composite video signals is required, the device can digitize both PAL and NTSC standards. It is packaged in 28-pin plastic DIPs and costs \$12.24 each in quantities of 1,000. Plessey Semiconductors North America, Irvine, CA. INFO/CARD #164.

Time Interval Counter

The SR620 is a reciprocal interpolating counter-time with a 4 ps single shot resolution for time intervals and 11 digits of resolution for frequency. It can measure time interval, period, phase, pulse width, risetime, falltime, and frequency to 1.3 GHz. Options include a high stability ovenized crystal time base and PC control software. Price for the SR620 is \$3,850. Stanford Research Systems, Inc., Sunnyvale, CA. INFO/CARD #163.

High Speed Op Amps

CLC205 and CLC206 are high speed op amps that feature -3 dB bandwidths of 170 MHz and 180 MHz, respectively. The 205 has harmonic distortion of -57dB (20 MHz), slew rate of 2400 V/us, and settling time of 22 ns to 0.1 percent. The 206 features -3 dB large signal bandwidth of 70 MHz (20 V_{p-p}), harmonic distortion of -59 dB (20 MHz), 3400 V/us slew rate and settling time to 0.1 percent of 19 ns. Cost ranges from \$72 to \$177 in single quantities depending on package purchased. Comlinear Corp., Fort Collins, CO. INFO/CARD #191.

Remote Transfer Switch

RLC introduces the Model SR-TC-R-D-40 remote transfer switch that operates from DC to 40 GHz. VSWR ranges from 1.3 to 2.0, insertion loss goes from 0.25 dB to 1.0 dB and isolation goes from 45 dB to 70 dB depending on frequency range. Prices start at \$525 in unit quantities. RLC Electronics, Inc., Mt. Kisco, NY. INFO/CARD #190.

PBW Discriminator

The MMD-91 airborne discriminator is used to convert FM multiplexed subcarrier signals to the original channel information. Specifications include \pm 7.5 percent deviation for IRIG PBW signals and stability of \pm 1.0 percent for full scale output. Aydin Vector Division, Newtown, PA. INFO/CARD #189.





Receiver Simulation Program

CommView 1.0 allows receiver cascades to be created and simulated while including the effects of excess broadband and discrete noise at each stage. Comm-View also accepts broadband and CW jammer inputs and calculates the associated performance degradation. It allows the user to sweep the input signal level and provides AGC voltage, S/No, S/N, E_b/N_o, E_b/N and bit error rate versus input level. Signal power, noise density, S/No, and S/N are provided stage by stage. E_b/N_o, E_b/N and bit error rate can be determined for AM, FM, FSK, PSK, and QAM. Webb Laboratories, North Lake, WI. INFO/CARD #169.

Circuit Simulation Software

ECA-2 from Tatum Labs is now available for the Apple Macintosh. This analog circuit simulator features AC, DC, transient, Fourier, temperature, Worst-case, and Monte-Carlo analyses. The software has the capability to plot graphics as the points are being computed in real time. This package is priced at \$675 and a free demo disk is available. Tatum Labs, Inc., Ann Arbor, MI. INFO/CARD #209.

Active Filter Design Package

PMSS Active Filter Design Tools 2.0 features on screen design and component selection with the results shown in Bode plots and comparisons of specific components to ideal situations. Highpass, lowpasss, bandpass, Butterworth, Chebyshev, Bessel, and elliptic filters are supported in the 1 to 50 kHz range. Seven different slope options are available in the Chebyshev mode and filters with up to six poles can be designed. This IBM compatible software is available for \$55. Power Mountain Software Systems, Cora, Wyoming. INFO/CARD #208.

Communications and System Simulator

TESS[™] is a general purpose communications and system simulator for the IBM PC and compatibles. The simulator handles systems that contain filters, mixers and VCOs as well as digital logic. A library of over fifty devices is featured. With MODGEN[™], a companion package, engineers can modify or add models with a few lines of code. TESS is \$495 and MODGEN is \$245. Tesoft, Roswell, GA. INFO/CARD #207.



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rf literature

Brochure Details Test Instruments

The listings in the brochure include specifications on oscilloscopes, adapters and calibrators for oscilloscopes, IC testers, digital testers, probes, multimeters, function generators, pulse generators, multifunction counters, power supplies, video testers, stereo generators, and components testers. A section on accessories is also included together with summary comparison charts and selected product applications. **B&K-Precision**, Chicago, IL. INFO/CARD #219.

Low Power MOS Catalog

Siliconix has published a low power MOS catalog detailing its line of Nchannel enhancement mode, P-channel enhancement mode and N-channel depletion mode devices. The parameters listed are product summary, absolute maximum ratings, thermal resistance

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A UNITED PARCEL SERVICE COMPANY An Equal Opportunity Employer. ratings, electrical characteristics, and source-drain diode ratings and characteristics. Siliconix, Inc., Santa Clara, CA. INFO/CARD #220.

Dielectric Resonator Catalog and Guide

A Designer's Guide to Microwave Dielectric Ceramics contains a detailed design and application note with illustrations and references as well as five new dielectric products. The products range from dielectric materials in various configurations to tuning screws, bonding adhesives and a T.C. tuning kit. A selection of characteristics and specifications together with performance examples are featured. Trans-Tech, Inc., subsidiary of Alpha Industries, Inc., Woburn, MA. INFO/CARD #218.

Analog IC Catalog on Disk

Precision Monolithics has introduced a data disk catalog covering its line of analog signal conditioning and data conversion ICs. The *Precision Decision* catalog is user friendly, menu driven and can be run on IBM PC compatible machines. It can be used as a design tool for pinpointing the optimum device for specific applications. The selection process is based on a parametric spec search. The resulting list includes data such as package options and pricing. Competitive cross-referencing is also accessible. **Precision Monolithics, Inc., Santa Clara, CA. INFO/CARD #217.**

Signal Conditioning Filter Catalog

Volume 31 of the Active and Passive Signal Conditioning Filter Catalog is available from TTE. The filters listed include both passive and active in various configurations such as Butterworth, Chebyshev, Subminiature, TTE, anti-aliasing and various notch forms. Filter specification worksheets together with a glossary of terms, filter test procedure, and cross reference numbers are included. TTE, Inc., West Los Angeles, CA. Please circle INFO/CARD #216.

Application Note on Temperature Rise Calculations for Circuit Boards

A report from Rogers Corp. provides formulas to estimate temperature rise of conductor traces built in microstrip or stripline on RT/duroid. It reviews simplifying assumptions which should be taken into account when doing temperature rise estimations. A calculation for current heating in a bias line and another for RF resistive heating in a microstrip transmission line is provided. Rogers Corporation, Chandler, AZ. INFO/CARD #215.

Coaxial Connector Catalog

Applied Engineering Products is offering its 1988 Subminiature Coaxial Connectors and Cable Assemblies catalog for military and OEM use. It depicts the company's product line together with dimensions, specifications, cross referencing, assembly tooling, assembly instruction and plating options. Applied Engineering Products, New Haven, CT. Please circle INFO/CARD #214.

Microwave Counters Brochure

The brochure describes the 2440 Series of microwave counters form Marconi. The specifications together with features and available accessories are listed. The frequencies supported start at 0.1 Hz and extend to 26.5 GHz depending on the specific model. Marconi Instruments, Allendale, NJ. INFO/CARD #213.

Short Form Catalog Lists Military Connectors

U.S. Components is offering a shortform catalog that lists standard PC connectors, special design PC connectors, standard rack and panel connectors, and special rack and panel connectors. An introduction to the various connectors is featured. U.S. Components, Inc., Bohemia, NY. INFO/CARD #212.

Engineering Reference Guide

This wall chart showcases an engineering reference guide for high precision data acquisition. A data converter's resolution is presented in terms of LSB weight, parts per million, percent of full scale range and a voltage reading. The chart also details Datel's high speed and precision component product line. Products shown include D/A converters, A/D converters, sampleand-hold devices and operational amplifiers. Brief descriptions of available technical and product literature are also provided. Datel, Inc., Mansfield, MA. INFO/CARD #211.

Medium Power Amplifiers Featured in Brochure

A brochure describing low noise, medium power, DC to microwave amplifiers is available from Trontech. Specific types featured include silicon and GaAs FET low noise amplifiers, medium and high power RF and microwave amplifiers, ultra-wide band low noise and medium power amplifiers. Frequency response, gain flatness, noise figure, output power, saturated output, gain compression, power requirement, input power and case style specifications are given where appropriate. Trontech, Inc., Neptune, NJ. INFO/CARD #168.



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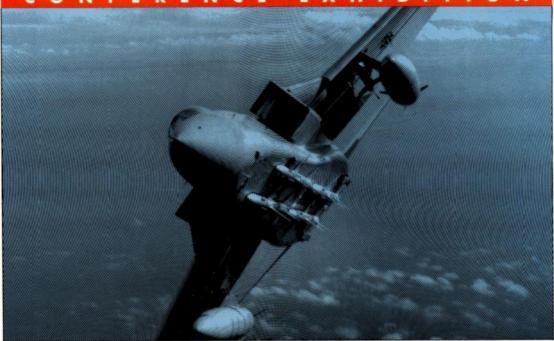
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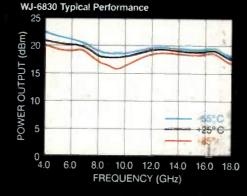
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