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ON READER-SERVICE CARD CIRCLE 222

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Model	Automatic Fundamental Nulling	High-Pass Filter	AM Detector	Gear Reduction Tuning	Price
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ON READER-SERVICE CARD CIRCLE 3



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News



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Process causes flashes of color to appear on black-and-white TV screens. Page 38



'Chirping' (pulse compression) betters radar range and resolution but keeps peak power down. Page 23

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Laser 'suicide' study taps heat buildup as cause of destruction. Page 42
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News Scope

Congress seeks general radiation safety standard

Public concern over the potential X-ray hazard of color-TV sets appears to have died down—at least for the time being. But the reverberations may affect more electronic products than just TV sets.

The Senate Commerce Committee recently concluded hearings on a bill to "protect the public health from radiation emissions from electronic products" (Senate Resolution Bill 2067). Hearings are expected to resume in January. In the meantime, a public health subcommittee of the House Commerce Committee is holding hearings on a house version of the same bill.

The clamor arose earlier this year with the disclosure that some 100,-000 General Electric sets had left the assembly line without adequate shielding. The Government warned of potential danger to televiewers who might sit too close to the sets for too long a time. Most of the sets were recalled by the manufacturer, and the defect in them—a misaligned shunt regulator tube was corrected. But about 1500 of the X-ray-emitted sets have still not been located.

The news caused such other major color-TV manufacturers as Magnavox, Philco, RCA and Zenith to make elaborate checks of their own sets. And it set in motion a parade of witnesses—health, industrial and



Congress looks beyond television radiation controversy.

technical—before the House subcommittee.

One witness, James G. Terrill, Jr., director of the National Center for Radiological Health in the Dept. of Health, Education and Welfare, noted that service technicians might convert a seemingly harmless color-TV set into a possible X-ray hazard by turning the voltages up far beyond the levels recommended in manufacturers' specifications.

But another witness, Lauriston S. Taylor, president of the National Council on Radiation Protection and Measurements, a nongovernmental expert organization founded 38 years ago, scoffed at the notion that a widespread X-ray peril existed in the TV industry. Exposures as high as 1000 roentgens, or 2 million times the TV safety standard, would not even produce skin irritation under ordinary TV use, he said.

"TV equipment generally," Taylor testified, "is made to reduce radiation to an unimportant level, certainly for the general public and usually for the serviceman."

The Senate hearings appeared to signal a broader Government approach to the investigation of all forms of potentially hazardous radiation, from microwave frequencies to X-rays. In the view of a spokesman for the Electronic Industries Association, radiation safety standards may eventually be set not only for medical and dental Xray equipment—but for such equipment as microwave ovens, ultrasonic diathermy machines, ultraviolet sunlamps and so forth.

This broader approach to radiation safety has been hinted at by the National Center for Radiological Health. In a speech in Montreal, Canada, on Aug. 18 to the International Conference on Guidelines to Radiological Health, Terrill said:

"In many of the developed nations . . . electronic products, some of them with a high potential for hazardous radiation emission, are being manufactured in increasing numbers and sophistication and for an ever widening variety of uses. Generally speaking, circumstances for the control of radiation hazards are more favorable in industry than in the home.

"Partially for this reason, all of us with radiological-health responsibilities must be especially concerned about the advent of electronic home appliances, among other new electronic products, not to mention color-television sets already in use in households throughout both our countries."

IR-scanning satellite proposed by Honeywell

Honeywell Inc. is looking for backing for an infrared-scanning orbital-spacecraft series to investigate the Earth's limb, the haze that emcompasses this planet. Promotion for the estimated \$10 million program began with a series of briefings before NASA officials, members of Congress and the press in Washington, D. C.

The firm's Systems and Research Div. pointed up the success of the two Scanner highaltitude probes launched last year by Langley Research Center and the conclusions of a recently completed feasibility study for Langley. The scanner vehicles were rocketed to 400-mile-altitudes to obtain the first high-resolution horizonradiance profiles in the 14-16micron range. The 15-month \$700,-000 feasibility study rcommended development of an unmanned. fully automated 725-pound Orbital Scanner to study and accurately plot variations in the apparent vs the true horizon. To improve accuracy, an artificial horizon would be used, based on the 15-micron carbon dioxide band, 50 to 70 miles above the Earth.

With a complete definition of the horizon, said Honeywell study manager Larry G. Larson, the problem of determining true local vertical will be almost overcome. This could yield greatly improved space navigation and attitude determination with less complex and costly onboard in-

News Scope_{continued}

strumentation. A minimum improvement in horizon-sensing to an accuracy of 0.81° would be the aim of the program, Larson pointed out.

Time-sharing systems found to be inadequate

A single large-computer center with many terminals will not necessarily suffice for an industrial user, according to Thomas O'Sullivan, manager of the Data Systems Section of Raytheon's Space and Information Systems Div., Sudbury, Mass.

Speaking at a recent Association for Computing Machinery conference in Washington, D.C., O'Sullivan based his conclusions on Raytheon's experience with time-sharing systems. The company initially used just one terminal linked to a remote computer service. After only two years, requirements had grown to 500 terminal hours a month, which involved 12 terminals and, unexpectedly, nine different centers.

The reasons for using several different centers ranged from the need for larger data files to crowding at one college-based center, where periodic pile-ups of student assignments reduced the time available to Raytheon.

Recently, Raytheon attempted to evaluate the services it employed from an economic and operational standpoint. The conclusions, strangely enough, were that the better the command language and diagnostics, the less efficient the time-sharing system. According to O'Sullivan, the easier the system is to use, the longer the program takes to be executed.

The most economic way to run a time-sharing system, he said, would be to charge only for the elements of service used—for example, for central-processor time and telephone lines.

Looking to the future, O'Sullivan argued that higher-speed output terminals and channels capable of transmitting larger volumes of data (more than 1200 bits per second) would be necessary. He also criticized current typewriter terminals as too noisy and inflexible.

Honeywell to market French digital voltmeter

Honeywell Inc.'s Test Instruments Div. will sell a French-built threedigit voltmeter in late October.

The portable ac-dc volt-ohm-amp meter, which was introduced onto the European market earlier this year, is built by Schneider Radio Television, S. A., Ivry-sur-Siene, France. It is expected to sell in the U.S. for about \$500.

C. M. Perkins, vice president of the division in Denver, said the test instrument is capable of covering 23 different measurement ranges for a variety of testing requirements.

He described the accuracy as $\pm 5\%$ for dc voltage and $\pm 1\%$ for ac voltage ranges. The seven-pound instrument operates either off ac line outlets or rechargeable nickle-cadmium batteries.

2500 computer projects indexed by Government

How much money is NASA spendon all its projects in the computer sciences? How many of these projects are being carried out by Company A or Company Z?

Answers to questions like these are now available in an index of almost 2500 Government-sponsored R&D projects in the computer sciences. The index is available for study in the form of computer tape printout at the National Bureau of Standards Center for Computer Sciences and Technology, Gaithersburg, Md.

Microwave unit set up by Texas Instruments

With an eye toward worldwide markets, Texas Instruments, Inc., has set up a new microwave prodocts branch in its Semiconductor Components Div.

The branch, which will consolidate a number of diversified microwave activities in the division, will develop, manufacture and sell "a full range of silicon, germanium and gallium arsenide microwave semiconductor products." Included, the company has announced, will be amplifier and oscillator transistors; switching, varactor and noise diodes; and Schottky-barrier mixer diodes.

The developmental efforts will focus on bulk-effect devices, beam-lead switching and varactor diodes, and integrated circuits and modules.

Command system aids firing of nuclear devices

Lawrence Radiation Laboratory at Livermore, Calif., reports final testing has been completed on a telemetry command and control system for firing nuclear devices.

The system is being developed under the Project Plowshare program which is exploring peaceful uses for nuclear explosions.

The control system is described by a Livermore spokesman as a simpler, safer and more efficient method of checking out and sending commands over the complex circuits that are needed to fire multiple nuclear devices.

All power and control signals as well as return monitoring signals to a remote firing site are sent over a single coaxial cable.

Scientists have proposed using multiple underground nuclear explosions for the purpose of digging new harbors or canals at considerably lower cost than by conventional techniques. In fact there are reports that plans for building a new Middle East canal parallel to Suez but completely through Israeli territory are now under active consideration.

Tape producers spurred by advent of Crolyn

Dupont's announcement of its new line of Crolyn (chromium dioxide) recording tape appears to have gotten its competitors worried. Faced with the prospect of competition with a superior recording tape, other manufacturers are trying either to join Dupont or to come up with a tape of comparable or higher quality.

Sony is reported to have taken a license for the manufacture of Crolyn tape. Minnesota Mining and Manufacturing (3M) Company is said to be making an all-out effort to develop techniques that bypass Dupont's patents.





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TOMORROW'S THINKING IN TODAY'S PRODUCTS

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The sound of holography is ultra

Quartz transducers, even hi-fi tweeters, resolve three-dimensional objects onto film

Richard N. Einhorn News Editor

Holography, up to now dependent on the coherent light of the laser, has been found to work with sound waves far beyond the range of human hearing.

Scientists in at least four laboratories have been illuminating their subjects with ultrasonic beams and forming holograms that can be reconstructed with the aid of lasers. The resolution is that of the sound waves—much coarser than that of the light waves. But ultrasonic energy has penetrating power, and acoustic holograms have been taken of the interior of an object.

The obvious application of ultrasonic holograms is in medical diagnosis.¹ A bodily organ can be imaged without risk by means of ultrasound. Laser holography, however, is something else again. Dr. Robert J. Collier, a Bell Telephone Laboratories scientist, was once asked by a physician to record an optical hologram inside a patient's body. But, as Collier wryly commented, "The doctor did not realize that current holographic techniques would require rigor mortis to set in before a successful hologram could be obtained."

The phenomenon that makes ultrasonic holography feasible is the analog of laser holography: interference of an illuminating beam and a reference beam, and reconstruction of the wavefront.

At Bendix Research Laboratories, Southfield, Mich., a technique that closely resembles the offset reference beam approach of Emmett N. Leith and Juris Upatnieks² is being investigated. R. K. Mueller and N. K. Sheridon³ of Bendix say that holography may be extended to scalar acoustic wave fields, because "the principles underlying holography are commonly described by treating the electromagnetic field in a scalar approximation."

In this scheme, advantage is taken of the interference produced at the interface between air and water by ultrasonic waves. Two ultrasonic transducers immersed in a water bath (see Fig. 1) are excited by a single rf generator, so that their outputs are coherent with respect to each other. They may be considered as point sources beaming out at the surface. Where the beams intersect (between points 1 and 2 in Fig. 1), the surface is deformed by the radiation pressure.

When an object (such as a metal plate with a design punched out) is fixed in the path of one of the coherent acoustic beams, the radiation pressure over the surface is constant. The pressure pattern opposes the restoring forces of gravity and surface tension to form a stationary ripple pattern. The deformed water surface represents a recording of the intensity of the impinging sound waves; the amplitude of the ripple pattern is proportional to the acoustic intensity at the surface. Thus, the ripple pattern has all the characteristics of a hologram.

To reconstruct the sound hologram optically, a laser positioned above and to the right of point 2 illuminates the ripple pattern. Its beam is phase-modulated by the surface pattern. A telescope focused at infinity and aimed at the hologram collects the light reflected from the surface. Someone peering through the eyepiece would be able to see the object as though he were looking down into the water, except that it would appear to be lighted from below instead of from above. The image's orientation is the same as when the object was exposed to



Acoustic hologram of array of aluminum panels is formed by detecting ultrasonic waves that reflect from their surfaces. Raster scanning causes the horizontal bands,

while the sound waves produce the vertical fringes. The photographic reconstruction is on the right. The length of the array is 84 cm along the long diagonal.

NEWS

(ultrasound, continued)

the ultrasonic beam. If the telescope field of view covered only part of the ripple pattern, there would still be an image of the submerged object, as with ordinary holograms.

There is one great advantage to this type of hologram: the acoustically illuminated object can be viewed in real time.

The image quality of the holograms so far produced is not very high, Mueller and Sheridon report. They think that the poor quality is due to the deviation from sphericity of the acoustic reference beam. Furthermore, the optically reconstructed image is demagnified roughly by the ratio of the sound wavelength to the light wavelength, and optical magnification has to be used to yield useful images. Since the resolution in the acoustic image is limited by the acoustic wavelength, however, demagnification should not reduce resolution.

An alternative approach, retaining the essential features of the offset-reference beam approach but dispensing with the acoustic reference wave, has been developed by Gail A. Massey⁴ of Sylvania Electronic Systems, Mountain View, Calif.

Massey points out that acoustic transducers, including loudspeakers and microphones, are for all practical purposes linear devices. Their voltage or current output therefore represents the acoustic field that they sample. Systems which operate on the transducer output voltages can thus be used to record the hologram.

It's done in air, too

Massey used a James B. Lansing model 075-105 loudspeaker fed by a 25-kHz oscillator as his illuminating transducer and a Turner model 141-11 microphone as his scanning transducer (see Fig. 2). Energy reflecting off the subject (an array of aluminum plates) is detected linearly by the microphone, which is translated over the hologram X-Y plane, located 5 meters from the aluminum plates. This plane is intentionally tilted so that the surface is offset 25° with respect to the plane of the object. Massey says this is equivalent to the spatial frequency translation provided by the offset reference in the Leith-Upatnieks scheme.

As the microphone scans, its output is filtered, amplified and combined with the direct output of the oscillator that feeds the loudspeaker. The sum voltage is the electronic analog of the hologram's optical field. This voltage is applied to a light driver to vary the intensity of a tungsten lamp attached to the scanning microphone. The light output is recorded by a camera that is set for a time exposure equal to the scanning interval. Since the output of the lamp is linear with respect to the electrical input, the film in the camera is exposed to intensity distributions similar to those obtained in laser holography, but with much lower spatial frequencies.

Asked whether the losses in air relative to water were not too great, he said, "A hi-fi tweeter puts out ample sound for our purposes. Even 20 or 30 dB of mismatch is not serious because enough power can be reflected. Our basic problem is pickup (of the reflection)."

As for the resolution afforded by the 25-kHz frequency, Massey said, "We scan over a raster that is 100 cm by 100 cm. We're working with a 1-cm wavelength, and our resolution is 2 degrees; the theoretical resolution is equal to twice the



1. Laser illumination of water surface permits optical reconstruction of submerged object. The air-water interface is the hologram. An rf generator excites both crystals to produce coherent ultrasonic beams.



2. Hi-fi tweeter beams 25-kHz acoustic energy at array of reflectors in air. A scanning microphone detects reflected sound waves and translates them over the hologram plane, which is scanned by a light beam.

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(ultrasound, continued)

wavelength divided by the aperture (the width of the hologram), or 20 milliradians."

He explained that with the very large scan and small angle he uses, the blurring is low, since the resolution is roughly linear with the width of the hologram. Increasing the angle of scanning decreases the resolution.

Elimination of the acoustic reference is claimed to offer several advantages:

In principle it permits the synthesis of an apparent reference wave with any desired phase or amplitude characteristics.

• It obviates the need for a second sound source in the medium. Massey says this is convenient for work in solids, where the object space might be inaccessible.

Massey mentioned medical diagnosis as the most likely application for ultrasonic holograms. "For medical uses, you are concerned with the physical resolution rather than the angular resolution," he said. "The shorter the sonic wavelength, the smaller the object you can image."

He said that his experiments were intended to demonstrate feasibility, and that if he were operating in a water medium he would use 5 MHz. In his present work he is scaling up the water experiment.

Miners could use holograms

He mentioned other applications as well, particularly underground exploration. "This could be very useful in mapping faults or locating ore thousands of feet down, using a long wavelength to penetrate scattering layers."

Still another technique was announced by Preston and Kreuzer⁵ of the Perkin-Elmer Corp., Norwalk, Conn. A transducer, excited by a 5-MHz signal generator, coherently illuminates a small hole in an opaque object in a water bath. An X-Y scanner positions a tiny receiving transducer over the surface of the water, so that it scans a pattern similar to a TV raster. This maps the sound field. The output of the receiving transducer, which resonates at 5 MHz, is electronically summed with the direct output of the signal generator, as in Massey's setup. The resultant output is rectified, filtered and used to modulate the light of a tungsten bulb that is fastened to the transducer. The modulated light pulses are 2 to 10 ms in duration. A camera then makes a time exposure of the scanning light, and the transparency produced from the film constitutes the hologram.

To reconstruct the image, Preston and Kreuzer illuminate the ultrasonic hologram with a laser beam. Using a lens system that diffracts the light, they photograph the object. They have formed threedimensional holograms of machine screws and even of a small hole inside an aluminum block.

Like Sheridon and Mueller, they report slight degradation of the image by spherical aberration. This is caused by recording the hologram at one wavelength and reconstructing it at another.

A fourth technique, which most closely resembles that of Preston and Kreuzer, but with simpler apparatus, was reported by Dr. Frederick L. Thurstone of the Bowman Gay School of Medicine, Winston-Salem, N. C.⁶

Thus far all the work in this budding field has been experimental, and no products are in sight. Preston points out that the scanning techniques, including his own, are too slow for diagnosis.

"A patient can't hold his breath for 20 minutes," he says.

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PULSE RESPONSE

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NEWS

Phased-array power increased by 100

A technique for the design of solid-state phased arrays that could handle power densities 100 times greater than those of conventional arrays has been reported by scientists of RCA's Missile and Surface Radar Div., Moorestown, N. J.

For the first time thousands of low-impedance solid-state power sources, each driving a separate element, have been successfully paralleled, Dr. Arthur S. Robinson, technical director, says. Robinson claims that the degree of isolation between adjacent elements prevents chain-reaction failures, a common liability of paralleled power devices.

"We expect this development to be of major importance in the development of high-power, highefficiency solid-state phased arrays," he says.

Dr. Willard T. Patton, one of the inventors of the new device, explained that by end-loading the radiating dipoles he has achieved nearly uniform current distribution in each element. He says previous investigators erroneously assumed that current is distributed sinusoidally along the radiating elements, and that the reactance becomes prohibitively large.

Patton states that RCA has achieved "an almost completely resistive driving-point impedance."

His experimental array consists of four rows of 25 dipoles each—a total of 100 elements (1/40 wavelength at 410 MHz) in the area normally occupied by a single halfwave radiating element. This array is actually a "wavefront amplifier" with back-to-back receiving and transmitting elements connected by stripline amplifier circuits.



100 phased-array elements occupy same space as one conventional unit.

New 'chirping' schemes sharpen radar returns

Compact signal-processing components improve performance without an increase in peak power

Neil Sclater

East Coast Editor

"Chirping"—a technique for pulse compression—is becoming a highly popular way to improve the resolution and accuracy of radars without increasing their peak power.

Recent improvements in solidstate electronics and dispersive delay lines are making pulse chirping more attractive than when it was introduced over 10 years ago. The new signal-processing systems are more reliable and compact and cost less than earlier lumped-constant, matched-filter delay line systems.

Chirping refers to a pulse-powermodulation scheme that forms long frequency-modulated output pulses that are analogous to the high pitched squeaks of a bat. The technique is used to avoid the conflict in ordinary pulsed radars between the requirements for long range and simultaneous high resolution. It does with long pulses what ordinary radars do with extremely short pulses.

By avoiding the necessity for using short pulses to transmit large amounts of peak power, fm chirping is especially valuable when the transmitter power is limited or there is a danger of waveguide breakdown. The large frequency content, or bandwidth, of a narrow pulse accounts for its high resolution capability. Frequency modulation provides the equivalent bandwidth of a narrow pulse with a wider pulse. As a consequence, the chirped system may have range and resolution equal to an ordinary pulse system operating at far higher peak power. The receiver is designed to compress the wide fm pulse back to a narrower one.

Engineers in two leading companies in the field agree that the future for pulse-compression systems in radar is bright, but they differ in their methods of system design.

Andersen Laboratories, Inc., of Bloomfield, Conn., advocates the use of acoustic dispersive delay lines in systems for compact, reliable, lowcost performance. The Hazeltine Corp., of Little Neck, N. Y., favors printed-circuit, quarter-wave shorted lines for the same reasons. Both agree that the lumped constantmatched filters are difficult to use in airborne or lightweight radars.

An Andersen Laboratories' vice president, Stanley Rittenburg, referring to experimental progress, says:

"We have demonstrated that we can achieve radar resolution on the order of two feet with our equipment. This means that the radar op-



Pulse compression is the key to increased range and resolution in radars.

erator can distinguish targets that are this close together thousands of yards away. A very narrow fournanosecond pulse would be required to do this in a normal pulsed radar system using 250 times more output peak power."

Rittenburg adds that this performance has been achieved with an fm chirp pulse only one microsecond long and with the use of miniaturized components.

40-dB noise improvement shown

The experimental system uses simple small filters to reduce the sidelobes, or unwanted signals, that accompany the desired return signal as a result of recompression. These Taylor weighting filters permit 40-dB suppression—an improvement of 10 dB over Andersen's present production systems.

The company is building compact chirp subsystems for improving a Navy destroyer distant warning search radar, the SPS-40. This radar formerly used large matched filters that occupied several standard equipment racks to do the same processing. The complete subsystem for this radar is packaged in a case that weighs 5 pounds and occupies about 0.1 cubic foot (see photo).

Rittenburg says that there are more than 100 SPS-40 radars in the Navy fleet at present and that 40 new ones will be added under existing contracts.

Navy interest is high because of the possibility that "chirp" radars can pick out submarine periscopes, of obvious advantage in anti submarine warfare.

Andersen Laboratories also has supplied pulse chirp circuits for the Air Force's AN/FPS-85 phased-array space-detection and tracking radar system.

Other Andersen equipment is being built for radars on missilerange tracking ships. A miniature prototype system has also been developed for Texas Instruments' solid-state airborne phased-array radar, MERA.

David D. Holmes, head of the radar research laboratory at Hazel-

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('chirping', continued)

tine says that a cascaded series of miniature quarter-wave shorted lines are competitive with acoustic delay lines if compactness, light weight and low cost are essential.

He notes, however, that Hazeltine has experimented with all techniques but finds the lumped-constant, bridged-tee network to be of most value in the work it is doing in ground-based, long-range surveillance and instrumentation radars. The instrumention radar is used to determine the re-entry radar signatures of ballistic missiles.

Holmes says that the new lumped-constant filters are smaller and far more reliable than the first units made years ago at Bell Telephone Laboratories and in Europe. They have characteristics not yet attainable with the acoustic delay lines.

Hazeltine pulse-compression sys-

tems will be used on the Air Force Ramsite radar, being built by the Ballistic Systems Div. at White Sands, N. M. Other radars using its systems are the Advanced Research Project Agency's (Altair), Lincoln Laboratory's (Alcor), North Atlantic Treaty Organization (Nadge) and an English experimental radar.

Rittenburg predicts increased use expansion compression systems in such applications as airborne and manpack radars, because acoustic delay lines have made these applications practical. Earlier matched filter systems included bulky transversal equalizers that required continuous adjustment and maintenance to obtain "clean" returned pulses following recompression.

Commercial uses seen

There are numerous commercial applications for these systems, he said. In addition to their use in all kinds of radars, they can be used in



'Chirp' radar waveforms, compared with ordinary pulse waveforms: (a) Representations of transmitted pulses (the chirp pulse is frequency-modulated by dispersive delay). (b) Spectrum of waveforms, showing relative bandwidth differences for equal pulse lengths. (c) Returned signals after mixing, and after reversal in the case of the chirp. (d) Envelopes after detection, and after recompression in the case of the chirp. Resolution is related directly to bandwidth. Pulse compression is the ratio of time, or T, in (c) to T in (d). digital communications systems and microwave spectrum analyzers. They lend themselves to systems with solid-state transmitters where high peak power is difficult to obtain, Rittenburg adds.

The key element in a pulse expansion-compression system is the dispersive delay line, Rittenburg and Holmes say. This line of lumpedconstant filter or acoustic-converts incident signals to fm signals and stretches them in time. Radar energy modulated with the output of this delay line will return to the receiver in a similar stretched form. It is then processed with another dispersive delay line that recompresses the signal into a narrow pulse. This pulse furnishes more information than could be obtained by demodulating conventional pulses of much higher power. The returned pulse is "cleaned up"that is, the unwanted sidelobes can be reduced, by the use of weighting filters and phase equalizers or the bulkier transversal equalizers.

Richard McLean, an Andersen engineer, explains that his company pioneered in acoustic dispersive delay lines for pulse compression. He says the company makes many types of the lines, depending on system requirements. One, a metallic strip line of steel or aluminum, reorganizes incident signals on the basis of the acoustic velocities of their frequency components. RF signals to be processed are first converted to acoustic signals by a transducer, before they are introduced into the thin strips of metal that act as waveguides for the acoustic energy.

After the signal is stretched and reformed into an fm signal, it is reconverted to rf by terminal transducers. But, McLean says, they are limited in their ability to handle wide frequency bands.

Newer techniques use quartz and sapphire as the acoustic delay medium. Incident rf energy is transformed to acoustic energy by solidstate cadmium sulfide transducers before it enters a wedge-shaped delay line. An expanded fm signal is obtained by tapping transducers set in a grating-like array along the hypotenuse of the wedge.

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Spot Noise Figure (Neutralized), NF (max.)	4.0 dB	400 MHz	TRANSCON	V ₀₀ = +15V V ₀₀ =0
Spot Noise Figure, NF (max.) (Neutralized)	2.0 dB	100 MHz	-	
Power Gain, G _P , (min.) (Neutralized)	10.0 dB	400 MHz	0.1	10 100 1000 FREQUENCY MHz



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('chirping', continued)

and output transducers arrayed along adjacent perpendicular surfaces of a wedge. The geometry of the transmission paths between transducer elements accounts for dispersion in the lines, McLean says.

In addition to improved matched filters, Holmes says that Hazeltine is using what he terms an rf strip line. It is a cascade of quarterwave-coupled transmission lines embedded in a dielectric. Ground planes are attached to both sides.

Hazeltine says it has achieved center frequencies of from about 200 to 2000 MHz with bandwidths of 100 to 1000 MHz with its quarterwave strip lines.

2-ns resolution predicted

Andersen Laboratories has developed dispersive delay lines that cover center frequencies from 1 to 500 MHz, and it is working on one at 1250 MHz. Bandwidths over this range vary from 1 to 250 MHz.

McLean reports that efforts to develop sapphire as a delay medium and to adapt cadmium sulfide as a transducer element has led to these advances. He predicts that with these materials and improved electronic components, resolution in the 2-nanosecond range will be practical within a year.

Both Holmes and Rittenburg agree that the design of an expansion-compression, signal-processing scheme calls for the solution of impedance-matching and other design problems. The performance of the radar system, they say, depends on the ability of the designer to minimize all phase and amplitude errors.

Andersen Laboratories and Hazeltine are extending the frequency and bandwidth capabilities of dispersive delay lines, because these will give improved radar resolution in future systems.

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Adding the 'chirp' to transmitter-receiver

Pulse-compression, or "chirp," signal processing affects both the transmitter and receiver of a radar system, as this block diagram shows. Six basic component blocks (shaded) are necessary to improve the radar's performance.

A timer triggers a pulse from the impulse generator. This pulse is a signal whose spectrum is flat —typically 3 cycles at the receiver's intermediate frequency.

After amplification the pulse is applied to the dispersive delay line, which expands the signal by dispersing its frequency components linearly in time. The resultant expanded "up chirp" linear fm signal is amplified for modulation of the transmitter.

The transmitted signal will

have the same waveform as the modulated signal. When this energy strikes the target, it returns to the receiver also as a "up chirp." After mixing with the local oscillator and being amplified, the signal appears at the spectrum inverter. This circuit reverses the direction of the chirp. After the signal is amplified, it goes to a dispersive delay line with the same characteristics as the pulse stretcher.

The output of the delay line is an unweighted, compressed pulse. It is amplified, processed by a weighting filter and made available for display.

The dispersive delay line may be lumped-constant filters, acoustic and rf strip lines or crystal diffraction gratings.





Compact, lightweight pulse-compression subsystems are being installed on Navy SPS-40 search radar. This Andersen Laboratories system uses a steel acoustic strip line to obtain linear fm dispersion. Components include (1) temperature-controlled delay line, (2) amplifiers, (3) bandpass filters, (4) weighting filter (5) gate circuit, and (6) impulse generator and input switch.

ELECTRONIC. DESIGN 19, September 13, 1967

Designed Sealed for the second second

Helipot's New Model 77P Cermet Trimming Potentiometer

Here's the new Model 77P, the first low-cost, general purpose trimmer with a sealed housing and cermet resistance element! DESIGNED to wider performance parameters than any other adjustment potentiometer in its price range. It is directly interchangeable with competitive Models 3067 and 3068–SEALED to permit p.c. board solvent cleaning and potting without trimmer contamination or failure–DELIVERED from local stock at the low list price of \$1.95. In large quantities, Model 77P sells for as little as \$1.10. • Compare Model 77P specifications with those of unsealed trimmers, then call your local Helipot representative for an evaluation sample.

	Helitrim Model 77P	Model 3067 Wirewound	Model 3068 Carbon
Resistance Range, ohms	10 - 2 meg	50 - 20K	20K - 1 meg
Resolution	Essentially Infinite	1.7 (100) to 0.3 (20K)	Essentially Infinite
Sealing	Yes	No	No
Power Rating, watts	0.75	0.5	0.2
Maximum Operating Temp. °C	105	85	85

Beckman

INSTRUMENTS, INC.

HELIPOT DIVISION FULLERTON, CALIFORNIA • 92634



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ON READER-SERVICE CARD CIRCLE 17





NO OTHER PHOTO TRANSISTOR CAN MATCH IT

- . . . IN LOW PRICE
- . . . IN HIGH SENSITIVITY



New L14B photo transistor shown actual size

Save with them now. They're priced as low as 97c in lots of 100-999.

Housed in a clear epoxy encapsulant, GE L14B photo transistors can sense the presence (or absence) of either visible or infra-red light. They're more sensitive than competitive devices, too, with 75% light response at $\pm 40^{\circ}$. Light current, typically, is 7 milliamps at 10 volts with irradiance at 5 milliwatts per square centimeter. And a base lead is also provided to control the sensitivity of the device, even though only the collector and emitter leads are needed for many applications.

Typical applications include machine controls, counters, coin machines, burglar alarms, solid state relays and frictionless potentiometers ... also position controls, pyrometers, light meters, cameras, day-night controls and limit switches.

Circle Number 811.

STILL ANOTHER NEW GE ECONOMY IC: THE PA223 OPERATIONAL AMPLIFIER

Go ahead and compare these features with those of any other *low cost* operational amplifier IC:

Input impedance2.5 megohms	
Offset current 20 nanoamps	
Offset voltage 4 millivolts	
Voltage gain	

The PA223 IC is fabricated on a single monolithic silicon substrate and housed in a 14-lead dual-in-line economy plastic package. By virtue of its darlington amplifier input configuration, it requires very little input current. PA223's also provide a current limiting feature for short circuit protection. Output is Class B. Circle Number 812. New high in solid state load control: up to 4150 Watts with GE 15-AMP TRIACS



SC51 15-amp press-fit type Triac (shown actual size)

That's 50% more power than you can now control with 10-amp Triacs, and 150% more than you can control with 6-amp units. Yet the new 15-amp models are housed in the same size packages as your lower-rated GE Triacs.

SC50 and SC51 devices are particularly useful for temperature control in residential and commercial



SC50 15-amp stud-mounted Triac (shown actual size)

heating applications. Or you can use them for static switching in motor control, power conversion applications, and as overvoltage protective devices. 200-, 400-, and 500-volt ratings are ready for delivery now for use on 120-, 240- and 277-volt lines. 50- and 100-volt ratings are also available.

Circle Number 813.

Complete 3600-watt Temperature Control Circuit using the SC51 Triac and General Electric PA424 zerovoltage switch IC. (Circle Reader's Service Card Number 814 if you'd also like more information on the PA424.)



These are just three more examples of GE's total electronic capability. For more information, call your GE engineer/salesman or distributor. Or write to Section 220-60, General Electric Company, Schenectady, N.Y. In Canada: Canadian General Electric, 189 Dufferin St., Toronto, Ont. Export: Electronic Components Sales, IGE Export Division, 159 Madison Ave., New York, N.Y., U.S.A.



Senate to scrutinize space budget



Washington Report CHARLES D. LAFOND WASHINGTON BUREAU

Senate to reexamine NASA budget

Passage of the \$4.58-billion Fiscal 1968 NASA appropriation bill by the House was only a prelude to further Senate machinations against the bill. At the time of writing, no date had been set by the upper chamber's appropriations subcommittee to restudy the bill, but whenever it is, there will be a fight over it. Subcommittee chairman Sen. Warren G. Magnuson (D-Wash.) is expected at least to strive to hold the budget to the level passed by the House. Others, probably led by Sen. William Proxmire (D-Wis.), will seek deeper cuts in the already emasculated space funds. It might be recalled that only last month the two houses agreed on a \$4.87 billion NASA budget ceiling.

While the requested \$516.6 million slash in funds has not seriously hurt the Apollo program per se, many major programs planned for 1970 have been set back by the more than 10 per cent cut. Drastic surgery has been applied to Voyager (unmanned exploration of Mars and Venus in preparation for manned missions), the Apollo Applications Program, and nuclearrocket development. The worst loss to be suffered will be the breakup of industry and NASA project teams, organizations not easy to replace.

Military appropriation bill passed

Despite arguments over defense policy during three days of heated debate, the Senate passed a whopping \$70.1 billion Dept. of Defense appropriation bill. For the electronics industry, prime interest centers on the \$22 billion allocated for procurement and the \$7.1 billion for research, development, test, and evaluation. Market experts expect about \$9.8 billion of the former and \$3 to \$4 billion of the RDT&E money to end up in the industry's coffers.

One of the severer cutbacks, affecting avionics manufacturers, was the Senate's refusal to give a limited production go-ahead to the Navy version of the F-111 variable-wing fighterbomber. Only six of 20 aircraft requested were authorized.

Meanwhile, pressure is mounting in Congress for some positive action on the Nike-X antiballistic-missile program. In its report to the President, the Armed Forces Committee urged an immediate start on a program to deploy the system. The Committee pointed out that over \$300 million of the \$730 million authorized for Nike-X is tagged for initial deployment. It also reminded the President that \$153 million appropriated for the system last year remains intact.

Vast arrays proposed for space listening

The development of a 36-dish radio telescope, the expansion of a new 130-ft-dia antenna facility into an eight-reflector array, and a major modification of a 1000-ft-dia antenna at Arecibo, P. R., have been recommended if the U.S. is to stay competitive with Soviet astronomy. An expert advisory panel, established by the National Science Foundation to evaluate proposed large radio-astronomy facilities for foundation grants, selected these projects as most likely to advance the U.S. radio-astronomy effort.

Continuation of a three-year-old design study by the National Radio Astronomy Observatory was urged. The project involves use of thirtysix 85-ft reflectors arranged in a Y formation, each leg measuring 13 miles in length. Estimated cost is \$52 million. The California Institute of Technology suggested the eightantenna array. The first 120-ft dish is now nearing completion at a site in Owens Valley, Calif. Seven more would be added to form a Tshape, the east-west leg extending 9000 ft and the north-south leg 16,000 ft. Total cost would be \$15 million.

Cornell University, designer of the Arecibo spherical antenna, has proposed replacing the

Washington Report CONTINUED

existing wire-mesh surface with a nearly solid reflector to improve receiving sensitivity greatly and to permit operation to the 10-cm wavelength. Estimated cost is \$3 million.

Airlines seek millions for traffic control

The Air Transport Association has asked Congress for an additional \$101.5 million for the Federal Aviation Administration to begin a five-year airport-improvement program. Association president Stuart G. Tipton cited inadequacies at high-density U.S. terminals and gross deficiencies at 292 of the 526 airports served by scheduled carriers. Testifying before the Senate Appropriations Subcommittee, Tipton urged that all these terminals should at least be provided with a control tower, radar service, and an instrument landing system.

The Association's recommendations for Fiscal 1968, involve purchase above all of electronic instrumentation, including 75 surveillance radars (\$35 million), 85 bright-tube radar displays (\$1.5 million), 150 instrument landing systems (\$15 million), 100 visual-approach slope indicators (\$1.5 million), 15 beacon alphanumerics (\$10 million), and 50 system backups for auxiliary power, radar and communications (\$3 million). Tipton also requested an added \$10 million for research and development.

For the future, Tipton proposed the acquisition of 232 towers and 360 instrument landing systems. By 1972, he declared, the en-route air traffic control automation program and the installation of alphanumeric radar displays at all terminals should be completed. Tipton urged the FAA to press development of lower-cost versions of existing instrumentation and to consider the cost savings possible through quantity procurements.

IBM space scientists look to the Seventies

A computer-controlled, self-adapting unmanned space laboratory in Earth orbit could achieve with a single payload what might otherwise require a whole series of missions, according to IBM scientists. Conceptual studies performed by the firm's Federal Systems Div. support this belief, a company spokesman said. Such spacecraft could be developed for use by the mid-1970s, he added.

Called a Spacecraft Computer-Managed Laboratory, the system would employ programmable logic for control. Its design would permit the automatic recombination of both logic and laboratory elements as dictated by in-flight findings. The functions of calibration, failure detection, simple assay sequencing, and instrumentation control could all be governed by the on-board computer. Experiment control would also be fairly adaptive—if the results of one experiment showed the following experiment to be impractical, then it would be bypassed and the next experiment after that begun.

More down-to-earth applications of the design could be geared to civil problems as a spin-off during development of the necessary technology. These might include specialized systems for a Food and Drug Administration drug-testing laboratory, pharmaceutical research or clinical laboratories.

Here-and-now not neglected

Meanwhile, despite recent conflicting reports in the trade press, Washington insiders assert that IBM Federal Systems Div. has been selected by the Navy to perform avionics integration for the new A-7D and E aircraft. Negotiations are underway to complete a firm contract and an announcement of the award is expected to be made soon.

Biosatellite success hangs on electronics

A second attempt to orbit and recover biological experiments was scheduled to begin on Sept. 7 at Cape Kennedy, Fla. Much of the success or failure of Biosatellite B will depend on its onboard electronic subsystems during three days of Earth orbit, reentry and final parachute (or sea) recovery. Electrical failure in the retrorocket-firing circuits prevented recovery of the first spacecraft launched last Dec. 14. To ensure proper ignition this time, NASA officials disclosed that an intensive system study had "resulted in addition of a duplicate firing circuit, and improved checkout circuits and procedures for all de-orbit systems."

A principal goal in the experiments is to determine the effects of radiation and zero gravity on biological specimens. To assist in maintaining minimum induced gravity, the orbital package will employ a stabilization system to control rotational forces to less than 10^{-5} g for 95% of the time. Rotation of the craft must be held to less than 0.05 rpm.

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ELECTRONIC DESIGN 19, September 13, 1967

DIVISION

new generation

the TALYSTEP - the TALYSURF

precisely determines thickness and evaluates surface of THIN FILM DEPOSITS recorded at 500x to 1,000,000x



the TALYSTEP

Measures and records thickness of evaporated and other thin film deposits. Step heights are recorded at eight magnifications, from 5000X to 1,000,000X; a variable stylus pressure of 0 to 30 milligrams prevents damage to even soft deposits. Stylus tips of 0.0001", 0.0005", 0.003" are interchangeable. Three traverse speeds are available --- a viewing microscope assists in selecting area of specimen to be measured. Step heights below 25 angstroms can be measured and permanently recorded electronically on the inkless rectilinear teledeltos graph. Compensation for small surface curvature is provided.

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ing surface characteristics and

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components that require accu-

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rate evaluation.

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Evaluates variation in surface finish of substrates and deposits; and thickness of deposits. Eight magnification values range from 500X to 100,000X. Traceable calibration masters are furnished with each instrument.

With one traverse the low pressure fine stylus electronically traces actual surface profile—recording every irregularity on an inkless rectilinear graph. The Talysurf record is your guarantee of surface finish precision—on hard or soft material, flat or curved shape.

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- The Rank Taylor Hobson Talystep and Talysurf are recognized

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125-kG supercooled magnet weighs 40 lb

A 125,000-gauss superconducting magnet, described as "smaller than a one-gallon can," has been delivered to the Physics Department of Rutgers University, New Brunswick, N. J., by the Radio Corp. of America's Industrial Tube Div., Harrison, N. J.

According to Walter F. Lawrence, manager for superconductive products, the device is the first commercially available magnet with such a powerful field.

The device was made with three superconductive ribbons of niobium-tin vaporized onto a flexible, stainless-steel substrate. Since the three ribbons are electrically compatible, only one power supply is required, Mr. Lawrence said.

He added: "Because each of the three ribbons is specifically designed for . . . a particular field region of the magnet, a substantial savings in superconductive material cost is realized by employing the correct combination of the three."

The magnet fits into a case with an outside diameter of 6-1/8 inches and an over-all length of 8-1/2 inches. Total weight is approximately 40 pounds. The current required to produce a full rated field of 125 kG is 82 amp $\pm 5\%$.



Curved solar panels on board the recently launched experimental communications satellite, LES-5, permit all panel surfaces to be evenly exposed to solar radiation during rotation cycle of the spinstabilized vehicle. It was constructed by Electro-Optical Systems in Pasadena, Calif.

New µ-PAC Prices

Up to 46% reduction on 75% of the more than 50 I/C μ -PAC modules — including lower prices for flip-flops, gates, amplifiers,

I/O modules, power supplies, and hardware. New quantity discounts are also available. Write for detailed price list.



CIRCLE READER SERVICE NUMBER 91


INTRODUCTION

Honeywell Computer Control Division μ -**PACS** are a broad line of general purpose 5 mc monolithic integrated circuit modules designed for digital systems applications. μ -**PACS** retain the straightforward logic design and flexibility of Honeywell's long established discrete building block lines, while incorporating the greater reliability, size, and price advantages of integrated circuits.

Special design features of the line include —

- High packaging density
- Wide selection of module types
- Noise protection in excess of 1 volt
- DC coupled circuitry throughout
- Low power consumption

This summary brochure is intended to serve as an introduction for the logic designer currently engaged in the selection of a line of digital logic modules. It contains technical data on the μ -PAC line and highlights Honeywell's capabilities as a leading supplier of digital logic modules. Whether your design requirements involve a one-time logic interface or volume systems assembly, you'll find μ -PACS the ideal answer to your hardware problems. A reply card has been included to assist you in obtaining complete µ-PAC specifications.

EXPERIENCE — In the past 14 years, Honeywell has delivered over a million digital logic modules. PACS have become the standard for quality and value in major industrial, research, and defense organizations throughout the world. PACS have helped make Honeywell a leading supplier of digital computers, core memory systems and special memory test systems. This broad experience has, in turn, contributed to the user-oriented design of μ -**PACS**. **SERVICE** — Honeywell maintains world-wide sales and service offices staffed by highly qualified applications engineers to provide technical assistance and design support. μ -PACS are fully documented by comprehensive instruction manuals, application notes, wiring and assembly notes, schematic parts lists and logic design aids.

BREADTH OF LINE - Only one supplier is necessary when you design with μ -PACS — a complete line including interface modules, memory modules, and analog modules as well as the widest, most versatile selection of standard logic functions. All necessary mounting hardware, power supplies, and special wiring and design accessories are available at minimum cost. Honevwell maintains a special μ -PAC design and fabrication capability to aid the systems designer in solving custom problems on any given system design. The standard μ -PAC product line is continually expanding to provide new approaches in solving systems design problems. In addition, custom solderless-wrap wiring capabilities are offered to assist customers with volume systems fabrication.

WARRANTY— All μ -**PAC** modules are covered by a full one-year warranty. A repair service is available at our main plant for modules damaged due to misuse or misapplication.



CAPABILITIES



Honeywell's leadership as a major supplier of I/C digital logic modules can be attributed to two major factors. First, nearly 14 years' experi-ence in the design and manufacture of an extensive range of electrically, mechanically, and logically complete building block circuit lines. Second, its ability to meet major technological challenges - from digital use of the vacuum tube to transistors and, most recently, integrated cir-cuits. To continue our leadership, Honeywell spent more than two years' in research and development of I/C design, fabrication and modular product application techniques. The result — in 1965 — was μ -PAC, the industry's first line of general purpose I/C digital logic modules.

With μ -**PACS**, Honeywell offers a broad range of capabilities to meet a variety of logic circuit design and packaging problems. A unique combination of extensive facilities, modern equipment, and talented engineering and manufacturing personnel assure Honeywell a leading role in digital technology. These same capabilities and facilities represent a continuing commitment to market demands — pose significant advantages to be considered in your make-or-buy evaluations.





RELIABILITY

Reliability is a total approach at Honeywell. µ-PAC reliability is first, the result of active research and development programs. The vastly more reliable monolithic I/C approach in logic module design was perfected after a dozen years' experience in a variety of logic design approaches. Conservative specifications with worst-case, end-of-life techniques were selected as basic design parameters. Rigid manufacturing procedures with proven assembly and fabrication techniques have been formulated to achieve design reliability specifications.

100% static and dynamic electrical test are performed on all circuits against tight standards for speed, driving capability and noise immunity using automatic, custom-designed test equipment to speed up testing and eliminate human error. Extensive quality control programs which include operating life, storage, shock vibration, centrifuge, solder, heat, temperature cycle, thermal shock, moisture resistance, salt atmosphere and lead fatigue are used to monitor μ -**PAC** manufacturing operations.

Reliable implementation of μ -PACS — without costly user re-design — is a key factor in the overall reliability of a μ -PAC system. Comprehensive documentation consisting of instruction manuals, reliability manuals, wiring and assembly notes, complete engineering specifications, schematic drawings, application notes, and logic design aids are provided.







u-PAC SPECIFICATIONS

ELECTRICAL & ENVIRONMENTAL

DC to 5 mc*

Circuit Characteristics

Frequency I/C Logic Type Logic ONE

Logic ZERO Noise Rejection

Ambient Operating Temp. Power Supply Voltage

NAND Gate

Fan In Fan Out Stray Capacitance** Circuit Delay (measured at + 1.5 volts, averaged over 2 stages)

J-K Flip-Flop

Inputs: DC Set Input DC Reset Input Clock Control Fan Out Stray Capacitance** Circuit Delay (measured at 1.5 volts) Clocked

DC Set/Reset

Power Amplifier

Output Drive Capability Stray Capacitance** Circuit Delay (measured at +1.5 24 nanoseconds, typical volts, averaged over 2 stages)

Diode Transistor Logic +2.5 volts to +6.3 volts (or an open circuit at the input) 0 volt to +1.1 volts, maximum 1.35 volts, typical 1.05 volts, minimum 0° C to $+55^{\circ}$ C +5.1 volts to +6.3 volts (-6 volts also available for some auxiliary non-logic circuits.)

12 8 40 picofarads 24 nanoseconds, typical 30 nanoseconds, maximum

Loading 2/3 unit load 2/3 unit load 1 unit load 1 unit load 8 40 picofarads

45 nanoseconds, typical 60 nanoseconds, maximum 45 nanoseconds, typical 80 nanoseconds, maximum

25 loads 250 picofarads 30 nanoseconds, maximum

Etched type. 34 pin, gold plated, mating to solderless-wrap or taper

.24 inch glass impregnated epoxy

Factory pre-wired in all µ-BLOCS

14 lead, .250 x .125 x .065 inch flat packs soldered to etched wiring. Up to 22 flat packs on a single card.

2.9 x 2.7 x .24 inches

MECHANICAL

pin

Physical Size Connector

Board Material I/C Assembly

Power Distribution

*At a 5 mc clock rate there is enough usable logic time in one clock cycle to preset and propagate through the clocked flip-flop, and pass through 3 series NAND gates.

**Specified at maximum delay times. Additional stray capacitance affects only circuit delay times. See µ-PAC Manual for additional details.

 μ -PACS are static asynchronous digital logic circuits. Diode transistor logic (DTL) is employed for its noise rejection, speed and expandable input capabilities. Circuits meet all design requirements of a 5 megacycle line, featuring input gate expansion, output cascading, high fan-out, high noise thresholds, and low propagation delays.

Performance specifications are conservative - based on "worst case" stack-up of tolerances and will usually exceed the specifications listed. The basic logic unit, the NAND gate, performs a NAND function for positive logic and a NOR function for negative logic. Inputs are generally expandable by addition of diode clusters available on selected gate modules.

Most µ-PAC flip-flop modules utilize a single, versatile flip-flop circuit. This basic circuit is a double rank J-K flip-flop. In addition, a flip-flop consisting of two cross coupled NAND circuits is used to provide an RS type flip-flop module.

The Power Amplifier PAC adds high drive capability gating to the line with the added feature of short delay time. Built-in short circuit protection (patent applied for) limits the output current when the output is short circuited.

Loading numbers are expressed in easy-to-use unit numbers, and include wide safety margins at maximum operating frequency. In addition to indicated fan-out, ample margin is included for the specified stray capacitance to permit greater freedom in PAC-to-PAC wiring. Nominal μ-PAC unit load is 1.6 milliamperes.

µ-PACS

FLIP-FLOP PACS

COUNTER BC-335 — Contains six independent flip-flops with appropriate inputs for operation as binary counters. Individual DC set and reset inputs allow presetting in all modes. A common DC reset input is shared by all circuits.

BINARY COUNTER BC-336 — Contains between 8 and 20 prewired binary counter stages. The standard stocked BC-336 contains 8 stages and is custom assembled to 20 stages as specified by the user. The PAC also contains one independent two input NAND gate.

FAST CARRY COUNTER BC-337 — Contains a prewired eight-stage counter. By utilizing a few jumper connections at the PAC terminals, the counter can be operated in either a binary or an 8421 BCD mode.

BUFFER REGISTER BR-335 — Contains six independent flip-flops for use in serial and parallel transfer applications. Independent DC set inputs are available at each flip-flop for presetting operations.

GATED FLIP-FLOP FA-335 — Contains four independent general purpose flip-flops, each with clocked and DC inputs and a common reset.

BASIC FLIP-FLOP FF-335 — Contains eight independent, low-cost DC operated flip-flops. Individual DC set and DC reset inputs are provided.

SHIFT REGISTER SR-335 — Contains between 8 and 16 prewired shift register stages. The standard stocked SR-335 contains 8 stages and is custom assembled to 16 stages as specified by the user.

UP/DOWN COUNTER UD-335 — Contains four counter stages prewired to provide the counting operation in both the Up mode and Down mode, depending upon the command provided at the control input.

UNIVERSAL FLIP-FLOP UF-335 — Contains three independent general purpose flip-flops, each with independent clocked and DC input gating and a common DC reset.

GATE PACS

MULTI-INPUT NAND DC-335 — Contains 2 six-input NAND gates with nodes and 4 three-diode clusters. The diode clusters can be tied to the gate nodes of this or other μ -**PACS** to expand the number of gate inputs.

NAND TYPE 1 DI-335 — Contains 10 two-input NAND gates. Two of the gates have disconnected collector loads which are brought out on separate terminals.

NAND TYPE 2 DL-335 — Contains 6 four-input NAND gates. Two of the gates have disconnected collector load resistors which are brought out on separate terminals.

EXPANDABLE NAND DN-335 — Contains 6 three-input NAND gates with nodes. Two of the gates have disconnected collector loads which are brought out on separate terminals.

AMPLIFIER PACS

POWER AMPLIFIER PA-335 — Contains 6 three-input high drive NAND gates, each capable of driving 25 unit loads and 250 picofarads stray capacitance.

POWER AMPLIFIER PA-336 — Contains 6 three-input high drive NAND gates, each capable of driving 25 unit loads and 250 picofarads stray capacitance.

NON-INVERTING POWER AMPLIFIER PN-335 — Contains 6 three-input high-drive AND gates, each capable of driving 25 unit loads and 250 picofarads stray capacitance.

DELAY PACS

DELAY MULTIVIBRATOR DM-335 — Contains two independent monostable (one-shot) multivibrators capable of generating assertion and negation pulses in a variety of widths. Each circuit has two NAND inputs, an enable, a range control and three discrete variable delay taps. ADJUSTABLE DELAY MULTIVIBRA-TOR DM-336 — Contains two independent monostable (one-shot) multivibrators capable of generating assertion and negation pulses in a variety of widths. Each circuit has two NAND inputs, an enable, and three discrete variable delay taps and is continuously variable in ranges.

MEMORY PACS

SERIAL MEMORY SM-330 — Is a complete storage system capable of storing up to 256 bits of serial μ -**PAC** data at a maximum rate of one million bits per second. The PAC contains two independent storage circuits which can either function separately or in series and operate by a common system clock input.

CLOCK PACS

MASTER CLOCK MC-335 — Contains a crystal controlled oscillator, a pulse shaper, and a power amplifier. The Negation pulse is available at the output of the power amplifier section. The additional power amplifier circuit is available to provide the Assertion output when tied in series with the Negation output.

MULTIVIBRATOR CLOCK MV-335 — Contains a self-starting, free running, variable frequency multivibrator, a pulse shaper section, and a power amplifier section. The Negation pulse is available at the output of the power amplifier section. The additional power amplifier circuit is available for providing an Assertion output when tied in series with the Negation output.

FUNCTIONAL GATING PACS

ADDER AP-335 — Contains 8 twoinput half-adder (exclusive OR) stages which can be connected to form full adders, parity generators, and comparators.

SELECTION GATE TYPE 1 DG-335 — Contains four independent functional gate structures. Each gate structure has 3 two-input NAND gates with separate load circuits and performs the AND-OR-INVERT function.

SELECTION GATE TYPE 2 DG-336 -

Contains two independent functional gate structures. Each gate structure has 4 three-input NAND gates with separate load circuits and performs the AND-OR-INVERT function.

EXCLUSIVE OR EO-335 — Contains five independent functional gate structures and one independent single input NAND gate. Each gate structure contains 3 two-input NAND gates and performs AND-OR and AND-OR-INVERT functions.

OCTAL/DECIMAL DECODER OD-335 — Contains a prewired binary-tooctal decoder and two additional independent NAND gates to expand the matrix for BCD-to-decimal decoding. Three additional inputs, in addition to the six binary inputs, are provided to permit the matrix to be expanded to 16, 32, or 64 outputs by connecting additional decoders.

TRANSFER GATE TG-335 — Contains four independent functional gate structures. Two of the structures have 4 two-input NAND gates, one input on each gate being common to the other four gates.

SYSTEM INPUT/OUTPUT PACS

DISPLAY DRIVER DD-330 — Is designed to drive remote projection type digital display devices from a 4-bit binary-coded-decimal (BCD) input. On external command, the BCD input data is converted to decimal and stored in the output driver.

NEGATIVE LOGIC LEVEL CONVERTER LC-335 — Contains 10 independent circuits which accept negative voltage logic signals and convert them to μ -**PAC** signals. Each circuit has 2 inputs. The N input accepts signals at ground and -4 to -15 volts and provides a μ -**PAC** output (0 volts and +6 volts). The μ -input uses a μ -**PAC** signal to control or gate the negative voltage logic signal.

LAMP DRIVER LD-330 — Contains twelve identical independent lamp

driver circuits. Each circuit is capable of switching up to 70 milliamperes of current from any positive voltage up to 20 volts at a maximum frequency of 100 kc.

HIGH DRIVE LAMP DRIVER LD-331

— Contains 8 independent lamp driver circuits. Each circuit is capable of switching 300 milliamperes of current from any positive voltage up to 35 volts at a maximum frequency of 10 kc.

NEGATIVE LOGIC LEVEL DRIVER LD-335 — Contains 8 identical independent circuits. Each circuit is capable of converting standard μ -**PAC** signals to negative voltage levels of 0 volts and a minus voltage of up to 25 volts.

RELAY RC-330 — Consists of eight microminiature reed relays which can operate independently or together, when properly connected.

SOLENOID DRIVER SD-330 — Contains three independent circuits for driving heavy resistive, capacitive or inductive loads in such applications as solenoid or relay driving. The PAC also contains an independent two-input NAND gate.

SCHMITT TRIGGER ST-335 — Contains two independent trigger circuits, each capable of converting arbitrarily shaped inputs into μ -PAC compatible outputs.

ADJUSTABLE SCHMITT TRIGGER ST-336 — Contains two independent trigger circuits, each capable of converting arbitrarily shaped inputs into μ -PAC compatible outputs.

TRANSMISSION LINE DRIVER XD-335 — Contains 6 two-input driver circuits. Each circuit is capable of driving standard 50 ohm, 75 ohm and 93 ohm coaxial cable or twisted pair cables at up to 5 mc repetition rates.

TRANSMISSION LINE DRIVER XD-336 — Contains 6 two-input driver circuits. Each circuit is capable of driving 50 feet of standard 50 ohm, 75 ohm or 93 ohm coaxial cables or twisted pair cables at up to 5 mc repetition rates.

ANALOG/DIGITAL PACS

SIX-BIT DIGITAL-TO-ANALOG CON-VERTER LN-330 — Contains a six-bit ladder network with the output buffered by a differential amplifier. The amplifier includes an offset/scaling network for bipolar or unipolar operation, and a potentiometer for zero offset adjustment.

The following 5 D-A PACS are new

FOUR-BIT D-A CONVERTER LP-330 — contains a four-bit ladder network with solid state switching. It requires an external reference voltage of — 10 volts nominal. Conversion accuracy is $\pm 0.05\%$ full scale, relative to the DC reference voltage.

SIX-BIT D-A CONVERTER LP-331 — contains a six-bit ladder network with solid state switching. It requires an external reference voltage of -10 volts nominal. Accuracy is $\pm 0.5\%$ full scale, relative to the DC reference voltage.

OPERATIONAL AMPLIFIER OA-330 is a high gain, wide band DC amplifier primarily intended for use as a D/A output buffer. The DC gain of the amplifier is typically -5×10^5 , and the gain bandwidth is 3 megacycles.

DC-DC POWER CONVERTER PC-330 — uses the +6 volt and -6 volt supply voltages to generate +24 volts and -24 volts. The PAC supplies the additional required voltages for the analog series of PACS, such as LP-330, LP-331, OA-330 and PR-330.

PRECISION REFERENCE PR-330 is a precision, shunt regulated — 10 volt supply. The PAC is intended for use with the LP-330 and LP-331 PACS which require a current sink reference regulator.

ASSEMBLY AIDS







POWER SUPPLIES

PLUG-IN — models PB-330 and PB-331, are integrally packaged units that can be mounted directly into μ -**BLOCS**. The PB-330 mounts directly in model BM-BLOCS and the PB-331 mounts into model BL-BLOCS. They supply current at both μ -**PAC** voltage levels, +6 and -6 volts, and are designed to drive all modules contained in their respective BLOCS.

Power Supply	+6 Volts DC	– 6 Volts DC	Line Current Full Load	Overall Dimensions	Weight
PB-330	2.5 A	.25 A	0.3 A @ 100 VAC	8 ³ / ₄ x 2 ³ / ₄ x 4 ¹ / ₂	8 lbs.
PB-331	10 A	1.0 A	5.0 A @ 100 VAC	8 ³ / ₄ x 5 ¹ / ₂ x 4 ¹ / ₂	17 lbs.

RACK MOUNT — model RP-330 rack-mounting power supply is a regulated power source capable of supplying current at both +6 volts and -6 volts μ -PAC voltage levels.

Overall supply voltage variations due to worst-case combinations of input line voltage, DC load regulation, dynamic load regulation, ripple and long-term drift are less than $\pm 2\%$. This is well within μ -**PAC** voltage level tolerances.

Power Supply	+6 Volts DC	- 6 Volts DC	Line Current Full Load	Overall Dimensions	Weight
RP-330	25 A	2.5 A	5.0 A @ 100 VAC	5 ¹ / ₄ x 15 x 19	60 lbs.

MOUNTING HARDWARE

 μ -**BLOCS** — Seven different μ -**BLOC** units are available for housing μ -**PACS**. All BLOCS use the same basic structure but differ in width dimension, provisions for plug-in power supply and types and number of connectors (see table).

Mounting ears are detachable and allow front or back mounting of the connector plane. Laminated copper strips insulated by mylar are used for power distribution. PAC connectors are prewired for +6 volts and ground. Height and depth dimensions are standard for all BLOCS at $12\frac{1}{4}$ " by $5\frac{1}{4}$ " respectively.

		SPE	CIFICATIO	JNS			
Model	PAC Capacity	Connector Type	Height	Depth	Width	Housing for Power Supply	Weight (Lbs.)
BM-330	24	solderless-wrap	12%	5%	51%	PB-330	8.2
BM-335	24	taper pin	12%	5%	87/16	PB-330	9.6
BM-337	36	taper pin	121/32	5%	87/16	(none)	10.4
BL-330	96	solderless-wrap	121/32	51/2	161%	PB-331	16.0
BL-331	48	taper pin	1232	51/8	161/16	PB-331	16.0
BL-332	144	solderless-wrap	1232	5%	16י%ו	(none)	18.3
BL-333	72	taper pin	1232	51⁄8	16י%ו	(попе)	18.3

MOUNTING PANELS — models PM-330 and PM-331, are used to mount the BM series μ -**BLOCS** to 19 inch RETMA relay racks. They can be fastened to either the PAC side or connector side of the BLOC. Panel space can be used to mount switches, indicator lights, meters, etc. The **PM-330** is 87/₁₆ inches across and mounts the BM-330. The **PM-331** is 51% inches across and mounts the BM-337.

TILT DRAWER UNIT — model BT-332 contains 240 μ -**PAC** slots employing solderless-wrap connectors.

SPECIFICATIONS							
Model	PAC Capacity	Connector Type	Height	Overall Depth	Panel Width	Weight (lbs.)	
BT-332	240	Solderless Wrap	5¾6	22%	19	60	

SUPPLEMENTARY PACS AND ACCESSORIES

COPPER CLAD PAC KIT AS-330 — Consists of a standard μ -**PAC** card, a separate handle, and retaining roll pins. The card portion contains the standard 34 gold plated fingers attached to 5.5 square inches of copper plate on each side of the card. This allows for custom etching of desired interconnection patterns.

BLANK PAC BP-330 — Is a standard μ -**PAC** card with etched power and ground busses originating at the appropriate connector terminals and distributed around the card's periphery. The remainder of the card space (approximately 3.5 square inches) is available for the mounting of any desired special circuits or components by use of standard lugs and point-to-point wiring.

JUMPER LEAD SET JT-330 — Contains 420 assorted lengths of taper pin jumper leads. The leads are made of plastic insulated #24 stranded wire with gold-plated AMP taper plns at each end.

SYSTEM NORMALIZER PAC SN-330 — Contains a time delay circuit which preconditions system control flip-flops to the proper state at the instant of power turn-on.

TEST POINT PAC — Facilitates the observation of waveform characteristics for various circuit positions within the system. It contains 34 test points, each of which is prewired to a connector terminal and bears the number of the appropriate terminal. The PAC is plugged into a prewired μ -PAC connector.

UNIT INDICATORS UI-110, UI-330 — Are self-contained transistorized neon indicators for displaying the

state of any μ -**PAC** flip-flop, gate, or other logic unit. The UI-110 uses a +90 volt supply and is driven by standard μ -**PAC** signals. The UI-330 is identical in performance to the UI-110 except that it can be powered from +6 volts. **SOLDERLESS-WRAP KIT WK-330** — Is designed to provide all associated equipment and material necessary to facilitate the implementation of μ -**BLOC** interwiring.

SOLDERLESS-WRAP TOOLS — The **Battery Operated Solderless-Wrap Gun** provides a simple method for interwiring μ -**BLOC** solderless-wrap connectors with the prescribed 30 gauge wire. Its nickel-cadmium battery provides sufficient power to make up to 4,000 connections without recharging.

The Manually Operated Solderless-Wrap Tool provides a simple inexpensive method of solderless wrapping 30 gauge wire to μ -BLOC solderlesswrap connectors. It is useful for small one-shot wiring tasks, for prototype checkouts, demo units, etc.

The **Taper Pin Insertion Tool** is used to insert taper pin jumper leads into taper pin connectors. The tool's spring loaded action and ease of use greatly facilitates the taper pin wiring operation.

LOGIC SYMBOL SHEETS — Logic Symbol Sheets are available for each applicable product type in the μ -**PAC** line. Use of the logic symbol sheets greatly simplifies system logic design and wiring, and effectively minimizes drafting requirements for the production of final engineering drawings. Printed on each sticker are logic symbol, pin connections and circuit identifiers. Space is provided for designating physical location in the respective μ -**BLOC**.







New µ-PAC Modules

5 D-A Modules have been added to the standard μ -PAC line to provide 4- and 6-bit coverter capability for systems designers requiring analog output control. Like all **µ-PACS**, they're backed by in-depth documentation, 14 years' logic design experience, and high reliability standards. Write for detailed specs.



CIRCLE READER SERVICE NUMBER 91

Computer display shows 3-D pictures



Computer-generated 3-D pictures are created at Brown University. Charles Strauss, the developer, views twin pictures which merge when viewed through a stereoscope.

Ingenious programing and the use of a stereoscope permit threedimensional pictures to be viewed on the face of a conventional computer display unit.

The computer technique was developed at Brown University, Providence, R.I. Brown professors say it might eliminate the need, for instance, to build physical models of complex piping systems to eliminate interference problems.

Pairs of images, differing slightly in perspective, are displayed on the one-foot-square screen. By looking at the screen through the stereoscope, the viewer sees the two images merged into one with the added dimension of depth.

The images—geometric drawings stored in the computer's memory can be enlarged, reduced or positioned with the display controls.

The program also permits creation and alteration of both pictures with an attached light pen.

Spatial relations are easier to see with this scheme than with the display of a single isometric drawing, the university says.

Brown mathematicians believe that time and money could be saved by presenting the data from architectural plans to the computer and then inspecting the 3-D results.

The project was executed by Charles Strauss during work for a doctorate in applied mathematics. He used an IBM 360 computer and 2250 display unit.

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2N4416	2.0 4.0	100 MHz 400 MHz	18 10	0.8	7500	N	JFET	TO-72	\$3.35
MFE2002	2.5	100 MHz		2.0	6500	Ρ	JFET	TO-72	4.05
MFE2001	2.0 4.0	100 MHz 400 MHz	18 10	1.0	800	N	JFET	TO-72	2.95
MFE2000	2.0 4.0	100 MHz 400 MHz	18 10	1.0	6000	N	JFET	TO-72	2.65
MPF157	4.5	200 MHz	16	0.2	2000	N	MOS	TO-92	.80
MPF158	4.5	400 MHz	10	0.2	2000	N	MOS	TO-92	1.00
MFE4001	2.5	100 Hz		2.0	4000	Ρ	JFET	TO-72	7.40
MFE4002	2.5	100 Hz		2.0	5000	Ρ	JFET	TO-72	4.30
MFE4003	2.5	100 Hz		2.0	6000	Ρ	JFET	TO-72	5.40
MFE4004	2.5	100 Hz		2.0	4000	Ρ	JFET	TO-72	7.90
MFE4005	2.5	100 Hz		2.0	5000	Ρ	JFET	TO-72	4.80
MFE4006	2.5	100 Hz		2.0	6000	P	JFET	TO-72	5.90
MPF151	2.5	1 KHz		2.0	4000	P	JFET	TO-92	.67
MPF152	2.5	1 KHz		2.0	5000	P	JFET	TO-92	.50
MPF153	2.5	1 KHz		2.0	6000	Ρ	JFET	TO-92	.67
MPF154	2.5	1 KHz		2.0	4000	Р	JFET	TO-92	1.00
MPF155	2.5	1 KHz		2.0	5000	P	JFET	TO-92	.80
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MFE3004	4.5	200 MHz	16	0.2	2000	N	MOS	TO-72	4.25
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ETs	Device Number	r _{ds (on)} (ohms)	C,,, (pF)	l _{ess} (pAdc)	Pol.	Туре	Pkg.	Price (100-up)
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ICN	MFE3003	200	1.0	100	Р	MOS	TO-72	5.40
	MPF159	300	1.3	100	N	MOS	TO-92	2.75
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ON READER-SERVICE CARD CIRCLE 22

GaAs laser hits 150-W peaks without cracking

Troughed construction minimizes surface damage; 100-W units on 1-mm die survive 40-hour life tests

Using a single gallium arsenide die, a British engineer has succeeded in developing a semiconductor laser that radiates 150-watt peak pulses and 1-watt mean output level when operated at 77° K (the temperature of liquid nitrogen). Moreover, he says he has overcome the problem of surface damage.

C. D. Dobson of Standard Telecommunication Laboratories, Ltd., Harlow, England, says that his selection of a novel troughed-junction geometry permits him to work with dice that measure 1 mm square colossal in comparison with the typical 4-mil-by-5-mil chips assembled into arrays in the United States.

High-power semiconductor lasers are known to have been destroyed because arsenic vaporized from the output surfaces of the optical lenses, leaving a highly reflective galliumrich surface. This occurs because absorption of radiation by inhomogeneities in the crystal results in local surface heating. Once initiated, surface damage proceeds in runaway fashion until the output face collapses. Microphotographs reveal furrows and globules that suggest the surface of the moon.

With a troughed-junction construction, damage to the front face can be avoided, Dobson says. The device has a diffuse junction which becomes rapidly shallower near the polished output surface. Thus the laser radiation, generated slightly to the p-side of the junction, exits from the low-absorption n-side.

The feedback from the front face of a troughed laser is said to be lower than that of normal lasers. Diffraction spreading in the region between the junction and the cleave reduces the total radiation reflected back into the junction. Dobson says that this construction successfully avoids surface damage and permits peak powers of 150 watts from 1mm-square dice for 5 μ s.

Daniel J. Horowitz, a physicist at the Night Vision Laboratory, Fort Belvoir, Va., heard Dobson read a paper in March at the Conference on Laser Engineering and Applications held in Washington, D. C. Horowitz commented that the power outputs claimed by Dobson were at least on a par with the best results reported in the United States. To the best of his knowledge, no one had ever done this with a single die.

The crucial question concerns deterioration—the laser may last for only a few pulses—and on this Dobson was not specific. "When you operate at low powers, gallium arsenide lasers last almost indefinitely," Horowitz pointed out. "But when you try for high power, there is massive scattering. The crystal cracks and the surface deteriorates. The laser may even continue to lase, but at much lower efficiencies."

In a subsequent letter to ELEC-TRONIC DESIGN, Dobson wrote:

"The lasers were tested with $5-\mu s$ pulses at 100-Hz repetition frequency. Output powers were increased until the device was destroyed. This generally occurred between 100 and 155 watts output. The power was increased in steps and run at each level for about 15 minutes. No life tests at these power levels have been carried out by us. "However," he said, life tests of production lasers by Standard Telecommunication Laboratories gave following results:

• "Fifty-watt lasers have been operated at 250-mW mean power levels for over 200 hours without deterioration; 90- and 100-watt devices have been life-tested for the

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Troughed laser avoids surface damage by causing beam to exit from low-absorption n-region.

standard test period of 40 hours at 1.5 kHz (5- μ s pulse length).

• "The 90- and 100-watt devices were lasers which occurred in the normal production spread and so far no special efforts have been made to test selected devices.

"Most were capable of 1 watt mean and were run for approximately 8-hour periods. The pulse length used in these experiments was 20 μ s with the laser producing 50 watts. A number of the more efficient devices were capable of 1.5 watts mean."

Gallium arsenide lasers are used in such military applications as:

Secure communications.

Missile tracking beacons.

• Invisible beams that when broken by an intruder set off an alarm.

• Illumination of battlefields so that it is possible to observe the action with special infrared viewers at night. If the enemy doesn't have these viewers, he sees nothing.

• Nighttime aerial photography.

Horowitz remarked that it is not necessary to have ideal lasers for these applications, since the laser is used mainly as an intense source of infrared light.

Dobson mentioned some difficulties:

"Gallium arsenide has not really been capable of producing very high average or peak powers. Therefore you assemble an array of sources and connect them optically in parallel as a source of infrared light. The poor directionality and coherence are unimportant. You can use an optical system with lenses to focus it down into a beam—an infrared searchlight. Or, you could have a more diffuse infrared floodlight."

He explained why most scientists were not attempting to get very high power out of a single gallium arsenide laser, as Dobson had done.

"Usually, to get very good efficiency, you make the laser very tiny, about 4 by 5 mils. Then you don't have to put in much current before the device starts to lase. This is important, because current put in below the laser threshold doesn't contribute to lasing."

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Process flashes color on monochrome TV

Unannounced transmissions over a local Los Angeles television station recently brought telephone calls and letters from viewers saying they were seeing colors flashing on their black-and-white television sets.

What they actually saw was a process called Electronic Color. The process simulates color by a principle of vision that causes a person to perceive color even when there is no color actually present in the scene. The process requires no attachments to the TV set.

The developer of the process, James Butterfield of the Color-Tel Corp. in Los Angeles, and his associates approximated the primary (red, green and blue) color codes that the eye normally generates. By properly pulsing white light, they were able to make the brain perceive the original color of the scene, even though the eye in fact saw only pulses of white light.

In color perception, Butterfield explained, the eye sends pulses of electrical energy to the brain. These pulses vary according to the color in the scene and the brain perceives color by detecting these pulse changes. The theory is not new but had never before been applied in this manner to television, he said.

Three color filters used

A scene viewed by a TV camera is analyzed into its primary colors by a device called a Color Translator attached to the front of the camera. The translator is a rotating disk containing six frames: three opaque frames and three frames each containing a primary color filter. The three filters are cyan (blue-green), magenta (reddishpurple) and yellow.

To produce red on a black-andwhite receiver, the red object is transmitted in black during the first TV frame, followed by two consecutive frames that are all white. Blue is perceived by transmitting two completely white frames, followed by one frame in which the blue object is transmitted in black. Green can be perceived by transmitting a white frame, followed by one in which the image appears in black, then an all-white frame and finally an all-black frame.

These code sequences are generated by rotating the filter disk in front of the television camera (see illustration). To cause the letter R to appear in red on a monochrome TV, the first filter (cyan) is placed in front of the camera lens. The cyan filter blocks the red from entering the camera, so that the letter appears in black on the screen.

When the rotation of the disk places the magenta and yellow filters in front of the camera lens during the next two successive frames, the red color in the scene will pass through. Since a monochrome TV camera cannot detect





colors, only the absence or presence of light, the red color is displayed in white during these two successive frames. So, the code required for generating red—a black image followed by two successive frames in white—is produced.

Other colors are simulated by mixing various combinations of the primary colors.

No threat to color TV

The three opaque frames are needed because the simulated color is not perceived well when the coding sequence immediately repeats itself. An electric motor synchronized with the vertical sweep of the television camera rotates the disk. According to Butterfield, the disk speed could be as low as 3 revolutions per second and as high as 20 rps, but 5 rps seems to work the best.

"Because of this slow revolution, the color seems to flash on and off when viewed. This tends to limit the process to special effects and would pose no threat to regular color television," Butterfield said. Moreover, the color does not appear as bright as a regular colorcast. When a colorcast is viewed on a color set, the transmitted colors tend to add to the regular color to produce a deeper color than would normally be seen.

So far the new process has been used only in television commercials to flash a product in color, in one area of the screen. The process can be coded on video tape and even photographed on black-and-white film.

Battelle Development Corp., of Columbus, Ohio, holds the patent rights to the process and has authorized Color-Tel Corp. to employ the process in making television commercials. So far it is being used by one soft-drink company to advertise its products. A spokesman for Battelle said the company is investigating other applications, such as computer readout systems, sonar displays, tactical display consoles, and even traffic lights.

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U.S. studies costly 'suicides' among lasers

Air Force survey concludes that self-destructive heat buildup is a threat to high-power progress

High-power pulsed lasers are showing an alarming tendency to destroy themselves and their component parts. This self-destructive behavior, reported in an Air Force survey, is not only costly but is posing a serious threat to wider applications for high-power lasers.

Nicks and fractures in laser oscillator rods, mirrors, prisms and lenses were analyzed in the survey by Carl Pitha, a research physicist in the Solid-State Sciences Laboratory at the Air Force Cambridge Research Laboratories, Bedford, Mass. He found that a heat buildup within the laser's oscillator rod apparently caused the self-destructive damage. In some cases the damaged lasers exploded, and in others the efficiency dropped.

The physicist's findings were based on a survey of current international literature in high-power laser research. He determined correlations between self-inflicted damage and the characteristics of the materials.

"All high-power laser systems are still essentially experiments," he said. "At the present time it is not possible to predict useful laser life; it may be hundreds of hours or just a few minutes."

A powerful laser, according to the physicist, is one that can produce power of at least 10^7 W/cm^2 on a remote target.

Pitha found from his survey that no laboratories were carrying out comprehensive studies of laser damage and that the mechanisms of damage were not understood by scientists. Further, he found that there were no foolproof passive tests that could be conducted on laser oscillator rod materials that would predict their performance and life.

Even after purchasing high-quality laser components and examining them closely to be sure they are free of visible defects, one still must take his chances, the researcher stated.

Of the hundreds of reports on laser research, Pitha selected 57 published in the last three years that gave descriptions and possible causes for damage to laser compo-



Self-destruction of laser components could impair development of high-power systems. The Air Force is investigating theories to explain the costly damage.

nents when operated under highpower conditions.

The physicist wanted to know more about how energy accumulates in all laser components, to the point where it becomes destructive, but he was most concerned with the vulnerable oscillator elements usually ruby or neodymium-doped glass. The growth, processing and composition of these costly elements (up to \$9000 each) is critical to all further high-power applications.

Damage to oscillator rods was classed as either internal or superficial and was related to the lasing conditions. Three primary mechanisms were suggested in the literature to account for energy buildup sufficient to touch off selfdestruction:

Self-focusing.

• Stimulated Brillouin scattering.

Multiphoton absorption.

These were believed to result in microplasm generation, which could produce both the internal and the superficial damage.

The damage to laser rods was often dramatic: the energy buildup was so great in some cases that the ends of the rods fractured and dropped off. In other cases nicks and fractures appeared at the ends or bubbles developed along the central axis. Internal hot spots created by the mechanisms appeared to be to blame.

Pitha said that some rods that appeared perfect in all respects had failed after a few minutes of use, while others with slight visible defects had lased effectively for hundreds of hours, but perhaps at decreased efficiency.

Internal 'lens' effect noted

One of the destructive causes, Pitha concluded, may be self-focusing cause of nonuniform refractive index within the rod. This "lens" effect may focus light energy into a small region, where it traps the laser beam. The intense energy concen-(continued on p. 46)



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Note: The 250 DA features exactly the same accuracy specifications as the 250 DE. However, the 250 DA Is AC line-operated. Price: \$550.00

Electro Scientific Industries

ON READER-SERVICE CARD CIRCLE 30

(Laser 'suicides', continued)

tration may lead to catastrophic fracturing or deformities.

Pitha says that lasing action is not uniform, even at high-power levels, and filaments of high energy tend to form axially in the rods.

Stimulated Brillouin scattering was cited as possible cause of destruction, although Pitha admits that there is little experimental evidence to support this cause in solids. Scientists are not overlooking the possibility that internally excited acoustic and optical waves can couple in such a manner that destructive forces build up within the crystal or glass structures.

Multiphoton absorption is seen as a cause of degeneration of rubies, but it is not known if it causes catastrophic failure. It refers to a process in which the electronic structure of the ruby is altered by overstimulation from the flash-tube pumping radiation.

A ruby rod is a sapphire (Al_2O_3) host lattice, doped with active chromium (Cr^{*3}) ions. Under high, intense light stimulation, the chromium ions tend to change valence to Cr^{*4} , leaving free electrons in the lattice. Under the influence of the laser beam, they could cause self-destruction of the rods. Rubies that are subjected to intense illumination over long period change from a red to an orange hue as a result of this action.

Possibly related to the three primary mechanisms for energy accumulation—to the extent that damage results—is the secondary mechanism called microplasma generation. This plasma, according to Pitha, is a local, ionized pocket within the solid. One or more of the primary mechanisms could create the heat necessary to cause the ionization. The ionized region is thought to absorb the laser beam so efficiently that the resulting superheating causes gross damage.

Pitha's research report is entitled Laser Damage: A Selected Literature Survey, and it is identified as AFCRL-67-0137.

A program is now under way in the Solid-State Sciences Laboratory to improve the techniques for forming laser materials. Pitha and other Air Force scientists would like to know more about factors involved in failure of lasing materials.

Ruby oscillator rods are important in Air Force special weapons projects because of their ability to produce the most powerful laser beams. As higher power applications are sought, the problem of predicting useful life will become more critical, Pitha says.

Japanese device corrects color blindness

Japanese scientists have reported the correction of color blindness by means of an electronically controlled headset that transmits an electric current to the color-blind person. The subject, who wears the headset on his temples for 20 minutes a day while reading or watching television, is said to learn to recognize colors after a three- to six-month training period.

The device that stimulates color discernment is called the Sunvister and was designed by Dr. Susumu Imamura of Kansai University. Dr. Makota Seki, who has conducted extensive clinical tests, reported that 77 and 42.5 Hz appear to be the most effective frequencies for stimulating a subject's sensitivity to red, green and blue, the primary colors. Apparently the brain perceives the color.



Electric current stimulates sensitivity to color in color-blind subject. Headset is controlled by solid-state circuits.

46

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LP 411	0-20 VDC*	1.2A	1.1A	1.0A	0.8A	119
LP 412	0.40 VDC°	0.70A	0.65A	0.60A	0.50A	114
LP 413	0.60 VDC°	0.45A	0.41A	0.37A	0.33A	129
LP 414	0-120 VDC	0.20A	0.18A	0.16A	0.12A	149
LP 415	0-250 VDC	80MA	72MA	65MA	60MA	164

*Overvoltage Protection available as an accessory - \$40.00 each.

 $^{\rm l}$ Prices are for non-metered models. For metered models, add suffix (FM) and add \$10.00 to price.



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ON READER-SERVICE CARD CIRCLE 32

NEWS

Army chooses radar altimeter for copters

The Army has found that existing radar altimeter circuits will meet its requirements for helicopters that hedge-hop over such ground cover as dense vegetation and snow.

Researchers at the Electronics Command, Fort Monmouth, N. J., found that two important characteristics in the altimeters will permit helicopter pilots to determine height precisely. The altimeter should transmit extremely short (nanosecond) pulses, and its receivers should find altitude by measuring the transit time from the leading edges of these pulses.

A spokesman at the Avionics Laboratory in Fort Monmouth explained that the altimeter for guiding a helicopter as it skims treetops or snowy ridges must determine its height accurately above this cover rather than over the terrain below it. Many altimeters studied by the Army transmitted pulses that penetrated the ground cover and gave the pilot misleading information about his actual height above the obstructions.

An Army team flew hundreds of hours, testing the radar altimeter circuits over Panamanian jungles and polar regions.

The altimeter characteristics that were found suitable included a transmitter frequency of 4.3 GHz and a repetition rate of greater than 30,-000 pulses per second. A special rangefinder, able to give readings accurate to a few feet, was used by the Army research team to verify the altimeter readings.

The team believes that its readings were more consistently accurate than any previously obtained over tricky terrain. However, it noted that they were not quite the 98 per cent accuracy obtained over concrete runways.



Radar altimeters for Army helicopters must tell the pilot his altitude over the top of dense ground cover.

TTL Trends from Texas Instruments



Today's Series 54/74 ICs point the way to the next dramatic step in solid state...MSI and LSI integrated equipment components.

Although vastly advanced in circuit complexity, this next generation of semiconductor devices will have much in common with today's Series 54/74 circuits (shown at left above), including utilization of the same basic TTL logic building blocks. In this and other ways, IECs will be natural extensions of today's Series 54/74 family of 39 functions and 180 device types.

By far industry's most complete logic line, Series 54/74 has been consistently expanded since the introduction of a few basic devices in 1964. The new high-speed and low-power circuits shown on the following pages are further additions to this growing family.

This provides you with new design opportunities now...and it also assures you a better interface with the TTL trends of the future.

New TTL additions to industry's most complete logic family

Industry's broadest family of TTL integrated circuits is now more complete than ever. To help you simplify designs, improve performance and reduce overall costs, we have added new circuits to our Series 54H/74H and 54L/74L lines.

New Series 54H/74H high-speed circuits feature 6 nsec propagation delay

New additions bring the number of circuits in this line to the 18 shown on page C.

Series 54H/74H circuits offer the highest speed available in saturated logic today...six nanoseconds per gate.

This means that, by using 54H/ 74H in the critical logic paths of your digital systems, you can achieve advanced levels of performance with minimum design complexity.

The circuits may also be combined with standard speed and lowpower TTL circuits in a single



system . . . giving fast response while keeping overall system power consumption low.

Check number 100 on the attached TI information service card for comprehensive data sheet.

New Series 54L/74L low-power circuits feature 1 mW per gate power drain

Six new additions bring the number of circuits in this line to the ten shown on page D.



At 1mW per gate, Series 54L/ 74L circuits offer a ten-fold power savings...yet are approximately twice as fast as other circuits with similar power dissipation.

This line is specifically designed for space systems, avionic systems and other applications where power consumption and heat dissipation are critical.

Check number 101 on the attached TI information service cardforcomprehensive data sheet.



Complex-function ICs help you reduce costs

You cut costs two ways when you use Series 54/74 complex-function integrated circuits in your designs. Overall savings in excess of 50 percent are often possible!

First, you pay less per circuit function! Since a major portion of all IC manufacturing costs are in the package assembly, fewer packages mean reduced costs to you.

Second, fewer packages also help you realize big savings at your plant...in inspection, handling, assembly, and inventory costs.

You also simplify designs because TI has already done a lot of the design work...and you improve reliability because more circuits per package mean fewer soldered joints and plug-in connectors.

Series 54/74 family is industry's most complete

Your new system can perform better and cost less when you employ Series 54/74 ICs, since you have the broadest choice of speed, power dissipation and cost-perfunction available. Now you can tailor the characteristics you desire into your system...to a degree never before possible.

Any way you look at it, today's best buy in digital integrated circuits is Series 54/74 from TI.

"Tougher than military"



Recently-completed reliability tests, such as the one for temperature-cycling shown here, have proved the ruggedness and durability of TI's plastic dual-in-line package for integrated circuits. Now you can take advantage of reduced initial costs – plus big savings in handling, assembly and testing – without compromising essential reliability.

Many of the tests in TI's plastic package reliability program far exceeded the requirements of applicable military specifications (such as MIL-STD-750A and 202C). For example, evaluations were made for shock to 5500 G, constant acceleration to 100,000 G, temperature cycling from -65° to +250°C and flammability to +1100°F. Units were exposed to salt, moisture and detergent bombs. They were vibrated at 60 G over a 100 to 2000 Hz range. They were subjected to solder-heat tests at 350°C. They were also life-tested for a total of 479,000 successful device-hours. Check No. 104 on the Service Card for the complete report.

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Monitor. This 84-page report provides results of TI's "Tougherthan-military" testing program for plastic IC packages. Data is presented on more than 539,000 device-hours of exhaustive testing... many exceeding the requirements of military specifications. Check number 104 below for your copy.



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ON READER-SERVICE CARD CIRCLE 106 FOR AMPLIFIERS AND 107 FOR MODULES

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up to 400 AMPS. D.C. in new weight-saving package

A tremendous break-through was achieved in space and weight reduction of air-borne power supplies when the Tung-Sol Y-series configuration was first developed. Now, this unique design has been adapted to the requirements of ground-based equipment, to provide the same advantages for applications in the 100 amp. to 400 amp. range.

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Output: 200 Amps. Size: 10½" L x 5" W x 6¾" H Weight: 13 lbs. 28 GP 400

Output: 400 Amps. Size: 13″ L x 6″ W x 7½″ H Weight: 26 lbs.

For full technical information write for Bulletin.

TUNG-SOL DIVISION Wagner Electric Corporation

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ON READER-SERVICE CARD CIRCLE 34

ELECTRONIC DESIGN 19, September 13, 1967

Moonstruck



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NEWS

Air Force conducts fm-digital 'contest'

The Air Force is comparing digital troposcatter equipment with conventional fm, to determine the best technique for transmitting data at high speed and low error rates.

Engineers at the Rome, N. Y., Air Development Center are conducting a five-month test program aimed at developing future systems for command and control transmission of video information and similar high-speed data.

Two test links have been established in New York State. Transmission equipment has been set up so that it is directed to a transmitter-receiver 200 miles away and to a receiver along the same path 100 miles away.

The data are being transmitted along the two links simultaneously, with use of both frequency-time code modulation and fm. The digital equipment for the tests was built by Martin Marietta's Orlando, Fla., division. The fm equipment is a standard AN/MRC-98 radio set.

Multiple frequencies used

Martin says its equipment transmits pulses sequentially on multiple frequencies to obtain fourth-order diversity gain. It uses time division multiplex and pulse code modulation to provide 24-channel operation.

Most troposcatter systems use fm with frequency division multiplexing for information processing, Martin Marietta engineers say. But they add that even with vast improvements, the digital data and error rates would not be adequate for the future needs of the Air Force and the Defense Communications Agency.

The Martin Marietta equipment is said to minimize fading by sending signals separated in frequency over a wide band, but it uses only a single antenna, transmitter and receiver at each terminal. Conventional equipment calls for multiple antennas, receivers and transmitters, to minimize signal loss during the rapid fading caused by multipath propagation.
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41.15-1

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For example, Radiation's RA-239 Broadband Amplifiers are used in the all-pass phase-delay filter shown at left.

Feed-back components are selected to determine the 90° phaseshift frequency, without regard to the active elements in this configuration.

The transfer characteristic is: $\frac{E_{out}}{E_{in}} = \frac{1-jx}{1+jx}$, where: $x = \frac{f}{f_o}$ Ein 90° phase-shift frequency is: 1

$$f_o = \frac{1}{2\pi RC}$$

X is the normalized frequency ratio referenced to the 90° phase-shift frequency.

For $f_o \leq 1$ MHz, output voltage is 21.6 VP-P.

A new line of universal building blocks for integrated analog circuitry is now available to design engineers. Radiation supplies three different types of IC operational amplifiers to serve your individual requirements: general-purpose, broadband, and high-gain amplifiers.

These amplifiers provide outstanding performance. Parasitics are eliminated, thanks to our unique dielectric isolation technique. Tighter tolerances and improved temperature coefficients are achieved through use of precision thin-film resistors over the oxide.

Thus, Radiation's technology simplifies system designs which were hampered by limitations imposed by conventional integrated circuit

Radiation IC Operational Amplifiers*

GENERAL PURPOSE BROADBAND HIGH GAIN Typical characteristics $(T_A = +25^{\circ}C)$ RA-238 RA-239 **RA-240** UNIT Phase margin 60 60 45 Degrees Bandwidth (unity gain) 15 MHz 7 6 3.2 23 3.2 V/µs Slew rate 33,000 2,700 Voltage gain 2,700 Offset voltage 2.0 2.0 mV 2.0 400 80 nA 80 Offset current ±5 ±1 ±5 ±5 ±5 ±1 µV/°C nA/°C Thermal drift Undistorted output swing 21 21 9(11.6)† Vp.p Power dissipation 90 160 90 mW Common mode rejection 100 100 100 dB Power supply rejection 100 100 100 dB Input bias current 0.4 1.0 0.4 щA

Standard temperature range: -55° C to $+125^{\circ}$ C. V = +25V; V⁻ = -15V. †V* = +20V; V⁻ = -20V.

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RELAYS?



THIS NEW DESIGNERS CATALOG BY BRANSON INCLUDES:



The Branson relay manual is complete with design data, application data, photos, type numbers, environmental, mechanical & electrical specifications.



ON READER-SERVICE CARD CIRCLE 38

Letters

Astable multi has 20% fewer parts

Sir:

I wish to point out a drafting error in the circuit I submitted with my Idea for Design, "Astable multivibrator has timing interval as long as one cycle per 6.7 minutes," ED 11, May 24, 1967, p. 110. C2 should be connected to the junction of R8and C3, and the junction of R8 and B1 of Q4 should be connected to the anode of the SCR.

I have since improved the circuit and reduced the component count. Q3, R5, R6, R8 and C2 have been eliminated from the original circuit, and CR1 (1N4148) has been added. The operation of the circuit (see below) remains functionally the same with the improved reliability inferred by the 20% reduction in parts.

The turn-on of the SCR is still accomplished with the positive

pulse generated on the B1 connection of Q1 at the end of the initial timing cycle. At this point the anode voltage of Q2 drops to approximately zero. C3 now begins to charge through R9, R10 and the forward resistance of Q2. At the end of Q3's timing cycle, a negative pulse is generated across Q2, momentarily biasing the pn junction in the SCR. This causes degeneration in the pnpn loop of the SCR, shutting the device off.

It is worth noting that this version of the circuit draws only minuscule current unless the SCR is on.

George W. Barrowcliff President

G. W. Barrowcliff Associates, Inc. Euless, Tex.



Astable multivibrator has greater reliability with fewer components.

Closed hollow tubing needs perforation

Sir:

Although Paul Moskowitz does not say so, his Idea for Design, "Standing waves eliminated within antenna elements," ED 12, June 7, 1967, pp. 90, 92, deals with *acoustical* standing waves, and not electrical waves as most readers might assume at first. Could the organ tones produced by tubes be a source of annoyance?

However, in Moskowitz's scheme there is one point of importance and potential danger that should not be overlooked: the effect of unidirectional transportation of atmospheric moisture. This is a peculiarity of any system consisting of a closed volume of air communicating with the outer atmosphere through a pinhole or capillary tube.

If one attempts to seal up a structure of this type, it is usually impossible to avoid pinholes through which air is pumped as atmospheric pressure and temperature vary with the day and season. Whenever the temperature falls, warm, moist (continued on p. 60) With the L and S bands covered, the overall frequency of the Polarad generator line is now extended from 21.0 to 0.95 GHz. Model 1105 covers 0.95 to 2.4 GHz. Model 1106 ranges from 2.0 to 4.6 GHz.

Both generators provide single-band continuous UNIDIAL® tuning with digital frequency indication accurate to $\pm 0.5\%$. Frequency stability is 0.0008% per line volt change, 0.005% per ° C change in ambient temperature. Output is calibrated from 0 to -127 dbm for Model 1105, ± 3 to ± 127 dbm for Model 1106, accurate to ± 2 db. A rear tuning shaft extension permits motordriven programming.

Use these generators alone, or rack or stack them with other Polarad modules to meet specific testing needs. Add a Model 3815 Frequency Stabilizer, for example, to obtain phase lock with crystal stability at any generator frequency. Add a Model 1001 Modulator for full FM, squarewave and pulse modulation. Signal sources, covering the same frequency ranges, are available too. Prices:

Model 1105 (0.95 — 2.4 GHz).. \$1900 Model 1106 (2.0 — 4.6 GHz)... \$1900 For information or a demonstration, contact your local Polarad field engineer or write Polarad Electronic Instruments, 34-02 Queens Boulevard, Long Island City, N. Y. 11101. Telephone: (212) 392-4500.

POLARAD

two new microwave signal generators extend Polarad's modular line down to 0.95 GHz.



The new series 1000 from AAI tests integrated circuits at a rate of about 180 tests per second. At this speed, crisp, clear readout is imperative. That's one of the reasons AAI specified IEE rear projection readouts. It's the World's most readable readout, because of the exceptionally bright, single-plane display.

AAI also wanted a readout as attractive as their circuit tester. In addition, they needed displays in various sizes, colors, symbols, characters and words. This they could only get with rear projection readouts. And this they got with the IEE Series 340, 120 and 10.

If you design, manufacture or market a product requiring visual display, specify IEE readouts. They can't be matched for readability, aesthetics or versatility. That's what AAI discovered.

The Rear Projection Readout: When a lamp at the rear of the readout is lighted, it illuminates one of 12 film messages, focuses it through a lens system,

and projects it onto the front viewing screen. The displayed message is clearly projected on a single plane, with no obstruction from unlighted filaments. It is extremely versatile, since anything that can be put on film can be displayed on an IEE readout.



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"When you're testing 6,000 circuits an hour, readout makes the difference."

Raymond W. Wells, V.P. & General Manager, AAI Pacific Division



ON READER-SERVICE CARD CIRCLE 40

ON READER-SERVICE CARD CIRCLE 41 ≯



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General Electric's new high performance 150-grid sealed relays are smallest where it counts most—only 0.320" high. What's more they come in 4 versions: 4 Form C, 2 Form C, 4 Form C AND-logic type, and a 50 milliwatt sensitivity 1 Form C (or 1A+1B).

Result: for the first time you can get really small size, a variety of forms to choose from, and exceptional performance all in one relay type.

These General Electric 150-grid space relays meet or exceed the environmental and mechanical specs of much larger Mil Spec micro-miniature relays. And compared to relays of comparable size, GE 150-grid space relays have 3 times the magnetic force and over twice the contact force of the nearest competitor.

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- Excellent minimum current switching ability
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- No flux contamination because of all-welded construction and design.

For more information on the small relay that's going over big, contact your General Electric Electronic Components Sales Engineer. He can tell you more about them and help with your individual application. Or write for bulletin GEA-8042B, Section 792-41, General Electric Company, Schenectady, New York 12305.

Specialty Control Department, Waynesboro, Virginia





ERA's Wide-Range, Variable, All-Silicon DC Power Modules at Low, Low Prices

ERA's new Value-Engineered DC Transpac® power modules provide allsilicon, DC power in a wide-range, variable, low cost module.

Stocking problems are reduced to a minimum and power module obsolescence is practically eliminated. Design changes are easily accomodated since all units can be set to desired voltages by a simple external tap change.

Output Voltage (DC)	Current (71°C)	Model	Price
4-32	0-750 ma	LC32P7	\$ 89.00
4-32	0-2 amps	LC322	\$115.00
4-32	0-5 amps	LC325	\$179.00
4-32	0-10 amps	LC3210	\$215.00
30-60	0-1 amp	LC601	\$145.00

Over-Voltage Protector Option: Add \$35.00 to above prices and Suffix V to Model No. (i.e. LC325V, etc.).

SPECIFICATIONS

Input: 105-125 VAC, 50-400 cps Ripple: Less than 800 microvolts RMS or .005%, whichevar is greater

Line Regulation: Better than \pm 0.01% or 5 my for full input change

Load Regulation: Better than 0.05% or

8 mv for 0-100% load change Voltage Adjustment: Taps and screwdriver odjustment

Short Circuit Protected: Automatic recovery Vernier Voltage: External provision Transient Response: Less than 50

microseconds

Operating Temperature: -20°C to + 71°C free air, full ratings Maximum Case Temperature: 130°C

Temperature Coefficient: Less than 0.01% per degrees C or 3 millivolts

Long-Term Stability: Within 8 millivolts (8 hours reference)

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ON READER-SERVICE CARD CIRCLE 42 60

LEITERS

(continued from p. 56)

air is freed of its moisture, which condenses into drops of water. This water stays inside the tube or hollow structure and gradually fills it up. Holes should therefore be drilled at the lowest points in order to relieve any pressure differentials and to let the water escape as soon as it condenses.

The writer's experience has shown this usually unheeded phenomenon to be a real source of corrosion and other damage, as shown by a few examples.

In earlier years street lighting was provided by incandescent lamps enclosed in glass "bells" or "jars" which were screwed in place with a rubber gasket to prevent the entry of moisture (it was fondly imagined). As a result it was a common thing to see these jars half filled with water collected by this moisture transportation mechanism, the lamp itself being half immersed in the collected water. Today there is always a hole provided at the lowest point of the glass enclosures and the gasket is no longer necessary.

In wartime Norway (1940-1945) private radios were confiscated, but some were hidden away. The writer was asked after Liberation Day to service a radio of a well-known U.S. make. The power transformers of these radios were famous for the care which went into their design and manufacture, and were specially sealed with insulating asphalt compound. Since pinholes were inevitable, the transformer contained a quantity of water in the windings after some 30 months of storage in a basement. So the transformer sizzled, gave off steam and failed electrically as soon as power was applied. Transformers of open design fared better.

Kaye Weedon

Civil Engineer Oslo, Norway

Analysis challenged in ladder network design Sir:

In reference to the article, "Ladder networks are easy to design" [ED 14, July 5, 1967, pp. 68-71], Eq. 17 is questionable.

THE COULLIDATION OF A TO THE Thévenin equivalent circuit in Fig. 7 is not considered. By superposition:

 $V_{TH} = V_1 [x/(x+1)]/$ [1+y+x/(x+1)] $+ V_{2}[y+x/(x+1)]/$ [1+y+x/(x+1)].

From this, Eq. 18 is in error (volts \neq ohms—a small oversight) and affects the remaining analysis, which assumes $R_{TH} = V_{TH}$ in Eq. 21.

The method is good and the article most interesting, but please clarify procedure if I have overlooked something.

Two other comments: Fig. 2b should be:







Dave McFerrin

Engineer EG&G, Inc. Las Vegas, Nev.

The author replies Sir:

McFerrin's comments about Figs. 2b and 5 are quite right. However, I plead innocent: those are drafting errors.

I disagree with him on Eq. 18 and Fig. 7, although the clarity could be improved—something for which I am to blame. Let me clarify the point by saying that either V_1 or V_2 could be used to compute V_{TH} . V_{TH} is not the total Thevenin source, but only the contribution of one of the input voltages at node 1. This is all that is required. Z is determined by setting its value so that V_2 contributes 1/20 of V_3 or V_1 contributes 1/240 of V_3 . With either V_1 or V_2 , the Thevenin resistance, and the corresponding voltage division (Eq. 19 for the case of V_2), the same answer will be ob-(continued on p. 64)

ELECTRONIC DESIGN 19, September 13, 1967



The great IR family portrait

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THE ANNULAR PROCESS AND

"FIELD RELIEF ELECTRODE" TECHNIQUES HAVE MADE POSSIBLE INCREASED-VOLTAGE DEVICES WITH INFINITELY GREATER ASSURANCE OF RELIABILITY. TODAY, A LARGE PERCENTAGE OF "GREATER PERFORMANCE" SILICON TRANSISTORS MAKES USE OF ONE OR BOTH OF THESE TWO KEY INVENTIONS. BOTH ARE PATENTED BY MOTOROLA.*



*Field Relief Electrode — Patent #3,302,076 Annular Structure — Patent #3,309,245 and #3,309,246

-where the priceless ingredient is care!



ELECTRONIC DESIGN 19, September 13, 1967

EFFECT:

... Total NPN/PNP Silicon Transistor Coverage



No matter what your application, chances are Motorola has a Silicon Annular transistor to fit it. The charts above are indicative of the broad voltage and current ranges covered. All are fabricated using the Annular Process, Field Relief Electrode or both. Result: State-of-the-art devices -- free of failure due to surface or bulk defects!*

If you've been hemmed-in by designs that you had to put "on the shelf" for lack of an appropriate or inexpensive Silicon transistor -- drag 'em out and dust 'em off! We've prepared a simplified, yet comprehensive cross-reference and selector guide for all types of Silicon transistors -- General Purpose Switches and Amplifiers, Saturated Switches and Small-Signal RF devices -- that shows you the kind of performance available. Send for it.

*Includes all 38 MIL-qualified switches and 34 low-power, general purpose transistors.

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ON READER-SERVICE CARD CIRCLE 44

ELECTRONIC DESIGN 19, September 13, 1967

0.1% accuracy, 0.001 Ω to 1000M Ω range



DMS-3200 Main Frame DP-170 Ohmmeter Plug-in

in \$320 \$240 \$560

The type DP-170 Ohmmeter Plug-in, when used with the DMS-3200 Main Frame, provides digital display of resistance measurements from 0.001 ohm to 1,000 megohms in ten ranges. The system offers accuracy capability of $\pm 0.1\%$ FS $\pm 0.1\%$ of reading. Direct linear readout of resistances above 1 megohm at the accuracies specified represents an industry first in digital instruments.

The measurement system is that of a true wheatstone bridge, with internal electronic automatic null-out and resultant resistance value display. Of special interest is the unusually low power applied to the resistor under measurement — maximum I milliwatt. Four-terminal input, with "guard" terminal permits accurate measurement of both extremely low and high resistances. Response time on all ranges except the highest is 1 second and a "null indicator" indicates when the bridge is balanced and a reading may be taken.

The three-digit, all-electronic display uses "Nixie" type readout tubes and includes automatic decimal point indication. 100% over-range capability is provided and display time is variable from .5 second to 6 seconds per reading with provision for holding a reading indefinitely.

Like other DP series plug-ins, the DP 170 is all-solid-state, uses glass-epoxy printed circuit boards, and is complete within a compact plug-in housing which slides into the plug-in port of the DMS-3200 Main Frame. Main Frame size is approximately 9"x7"x13" and combined weight is 13 pounds. Combination price is \$560.00.

The DP-170 is but one of a complete line of plug-ins designed for use with the Hickok DMS-3200 Digital Measuring System Main Frame. All plug-ins are available from stock through franchised Hickok Industrial Distributors.



DC Voltmeter Plug-in 0.1 mv to 100 v; 0.1% accuracy DP-100 — \$175 1 Mc Counter Plug-in 0.1 cps to 1 Mc; 0.005% accuracy DP-150 — \$175 Ohmmeter Plug-in 0:001 ohm to 1000 Megohms; 0.1% accuracy DP-170 — \$240 Capacity Meter Plug-in 1.0 pf to 10,000 Microfarads;

0.1% accuracy DP-200 — \$240 Event Counter Plug-in Counting Speed 1,000,000 pps DP-140 — \$75

THE HICKOK ELECTRICAL INSTRUMENT COMPANY 10514 Dupont Avenue Cleveland, Ohio 44108 ON READER-SERVICE CARD CIRCLE 45

LETTERS

(continued from p. 60)

tained for Z. The confusion perhaps arises from my reference to V_{TH} as a Thévenin voltage, when it is in fact only one of two independent sources, which can each be treated as a Thévenin source.

I hope this clarifies the matter. I also wish to thank McFerrin for going through all that analysis and for his interest in the subject.

Jay Freeman Electronic Engineer Sperry Gyroscope Co.

Great Neck, N. Y.

Aluminum and sodium can substitute for copper Sir:

We have read with considerable interest your recent editorial. "A systems approach is needed to cope with copper shortages" [ED 15, July 19, p. 45]. The arguments you advance against the substitution of aluminum and sodium for copper may be correct for the electronics industry, based on today's technology, but we question their long-term validity. We know for certain, however, that these arguments do not apply to the power-cable industry where large quantities of aluminum and relatively small quantities of sodium are replacing copper. Perhaps some of this power-cable technology will prove useful in electronics applications in the future. Under any circumstances, it is our conviction that we should use every means at our disposal to cope with the copper shortage, including substitution where feasible, and not be limited exclusively to the systems approach.

We would also like to lay to rest a misconception about sodium. Your statement, "only the fine lettering reminds us of the violence of the reaction when a trace of moisture penetrates the metal's polyethylene sheath," is not supported by experimental evidence. We have run innumerable moisture transmission tests at 20°C and 80°C without any semblance of a violent reaction. Further, we have repeatedly demonstrated, as have others, that sodium is essentially self-sealing when insulation punctures of less than 1/4 inch in diameter are encountered under water.

We would be the first to admit that under certain circumstances sodium can create a hazardous condition. This also holds true for electricity, certain chemicals and gases, and gasoline. With today's technology, these relatively dangerous commodities can be handled safely. The same now applies to sodium with which we have made considerable technical progress in recent years.

L. E. Humphrey

Vice President Nacon Corp. New York

Bloated bumpers don't alter the problem

Sir:

In reference to David H. Surgan's editorial, since when do autos have nickel bumpers that weigh more than the entire auto? There are about 50 million autos in the U.S.A. with, according to the editorial, 100 million tons of bumpers. From this it may be computed that each auto has about 2 tons of nickel bumpers!

Despite this flaw, the theme of the editorial is important and timely. It is about time that engineers took stock of other things besides the inputs and outputs of their black boxes and the nuisance of designing around the shortage of the moment. Engineering should involve an interest in the proper use and management of all resources. Let's get away from those black boxes occasionally and consider the broad picture.

Kenneth Hoffman

Electronic Engineer New York

(The weightiness of the problem influenced the figures! It should have been 2 million pounds (1000 tons) not 2 million tons.—Ed.)

Accuracy is our policy

In "Eye-catchers on display," ED 15, July 19, p. 22, the "Four-in-one 'personal console'," illustrated at the bottom left, is made by Westinghouse Electric Corp., not General Electric as stated.



Every military IC must operate at temperatures from-55°C to 125°C in our test chambers.

In order to pass its final test, each Sylvania IC must operate in four consecutive temperature-controlled chambers while a computer records the parameters of each circuit. We call this ultimate testing equipment "Mr. Atomic"—a system with a capacity of about a quarter-million ICs a week.

In each "torture chamber," the ICs are automatically inserted in a wheel that rotates them to the testing point while they're stabilized at test temperature.

The temperature of the first chamber is 75° C. The second is 0° C. The next is 125° C. Then, -55° C. In these four chambers, up to 100 D.C. tests are automatically performed. A fifth testing station, maintained at 25° C, tests up to 30 switching parameters

accurately down to a few nanoseconds. (See inset). Each input is individually tested.

Then Mr. Atomic (for Multiple Rapid Automatic Test of Monolithic Integrated Circuits) di-



rects the circuits to any of 20 bins, according to the computer's priority programs. You get only circuits that are fully guaranteed at temperature extremes—not at just room temperature only.

Sylvania Semiconductor Division, Electronic Components Group, Woburn, Massachusetts 01801.



Good, old-fashioned, Scottish thrift.

We've become so thrifty at Honeywell that we've pared the prices of taut-band meters down even lower than the prices of pivot-and-jewel meters. (About 10% lower, on the average.)

Now, if your shrewd business sense tells you we've left something out, you're right.

We've left out half





This taut-band meter is so ingeniously simple, there's hardly anything to go wrong.

volts

There's no friction in the moving system, so the pointer doesn't stick. (Better readout accuracy and repeatability.) And the meter's self-shielded. The low-cost taut-band meter from Honeywell. It comes in just about any style you'd like. Write Honeywell Precision Meter Division in Manchester, N.H. 03105 and we'll send you a brochure with all the sizes, styles and prices.



What's the secret of Honeywell's taut-band meter success?

100

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The solution to your problem may be sitting on a shelf

There is no question that the foundation of the electronics industry is a cataclysmic explosion of technology. Marketing managers all over the country call it the name of the game. Unlike other explosions, however, there is no danger that this one will get out of hand. On the contrary, it's the responsibility, indeed, the obligation, of each engineer and each manager to make the technology explosion work to the advantage of his designs, his company, and his industry.

Paradoxically, the main thrust of the electronics industry is communications. And yet, it is exactly in this area that a good deal of improvement is needed. Suppose, for example, that you or one of your engineers thinks up a great design, but your company, for marketing reasons, chooses not to use it. Would you tell your idea to your nearest competitor? This may come as a shock: it probably pays to divulge the idea, and, if your firm's legal department knows its business, it can pay well.

License arrangements can bring in cash for many designs and ideas that your company may not even know it has. Chances are that a perfectly "rentable" idea is sitting under a nearby blotter simply because it doesn't answer an immediate problem and someone upstairs lazily branded it proprietary. Properly marketed, even to a competitor, that idea can command a regular income, income that is pure profit to your company. Or your firm might swap the idea for an even more profitable cross-license, which solves one of its own sticky problems.

The chemical industry has demonstrated how profitable the rights to a license can be: DuPont, 3M, and Monsanto derive 57% of their combined total profits from products invented outside the firms.

It is a pity to see thousands of ideas, ideas that can move this industry forward, lie fallow just because they weren't needed by the company where they were developed. Perhaps they're unrented or untraded because no one's sure whether or not they are proprietary. Or maybe nobody has taken the trouble to clear them through security or the legal department.

Look around your firm. Dig up an idea, dust it off and suggest that it be used, sold or swapped. In one swap, your firm might get more than it gives. In another, it might give a little more than it gets. But one thing is certain: industries like the movie industry that have traditionally refused to trade information and licenses have withered and given way to newer, more vital industries.

Look around our industry. Those companies that are ready to use and swap licenses are the companies that move ahead. They are receptive to the ideas of others. Remember: a hundred thousand heads are often better than one.

ROGER KENNETH FIELD

4 ways to view displays with the Tektronix Type 564

splitscreen storage oscilloscope

The Tektronix Type 564 is virtually two instruments in one. It offers all the advantages of a storage oscilloscope plus those of a conventional oscilloscope.

Split-Screen Displays

An unique split-screen display area enables you to simultaneously use either half of the screen for storage and the other half for conventional displays, or use the entire area for stored or conventional displays.

Independent control of both halves of the screen permits you to take full advantage of the storage facilities. For example, you can use half the screen to store a reference waveform, the other half to display waveforms for comparison. You can erase or retain either half of the display area as you choose.

Bistable Storage Advantages

With bistable storage oscilloscopes, such as the Type 564 and Type 549, the contrast ratio and brightness of stored displays are constant and independent of the viewing time, writing and sweep speeds, or signal repetition rates. This also simplifies waveform photography. Once initial camera settings are made for photographs of one stored display, no further adjustments are needed for photographs of subsequent stored displays.

Storage time is up to one hour, and erase time is less than 250 milliseconds. An illuminated 8 cm by 10 cm graticule facilitates measurements and aids in taking photographs with well-defined graticule lines. Adding to the operating ease is a trace position locater that indicates, in a nonstore area, the vertical position of the next trace or traces.

Tektronix bistable storage cathode ray tubes are not inherently susceptible to burn-damage and require only the ordinary precautions taken in operating conventional oscilloscopes.

Plug-In Unit Adaptability

The Type 564 accepts Tektronix 2 and 3-series plug-in units for both vertical and horizontal deflection. Display capabilities of these units include single and multi-trace with normal and delayed sweep; single and multiple X-Y; low-level differential; dual-trace sampling; spectrum analysis, and many other general and special purpose measurements.

Type 564, without plug-in units	. \$ 925
Rack-Mount RM564	\$1025
Type 3A6 Dual-Trace Amplifier Unit DC to 10 MHz from 10 mV/div to 10 V/div. 5 display modes Internal signal delay line.	\$ 525
Type 3B4 Time Base Unit Sweep speeds from 0.2 μs/div to 5 s/div. Single sweep. Up to X50 direct-reading magnifier extends fastest sweep to 50 ns/div. U.S. Sales Prices FOB Beaverton	\$ 425



Entire screen can be used for a stored display.



Entire screen can be used for a nonstored display.



Each half of split-screen can be used independently for stored displays.

Either half of the split-screen can be used for a stored display, the other half for a nonstored display. (Shown below).







For a demonstration, contact your nearby Tektronix field engineer or write: Tektronix, Inc., P. O. Box 500, Beaverton, Oregon 97005.

ON READER-SERVICE CARD CIRCLE 48

Technology



The vivid colors of cholesteryl esters enhance high-resolution thermographic mapping. Easy

to apply and cheap, nondestructive liquid crystals can be used repeatedly. Page 71



Keep the price tag for power transistors low by looking beyond quantity breaks. Know how to avoid overspecification and how to make the most out of available devices. Page 110

Also in this section:

Complementary MOS arrays open the gate to nanopower IC logic. Page 81 Error-detecting codes need not present a design problem. Page 90 Unilateralization boosts the gain of hf FET amplifiers. Page 98 Delay distortion in amplifiers is minimized by an analytical approach. Page 116



Couch 2X relays in new 1/7 size meet MIL-R-5757D/19 in 1/25th of a cubic inch

The new Couch 2X 1/7 size rotary relay helps solve switching problems where space and weight are critical factors. Predecessors of the Couch third generation relays delivered unfailing performance in the missile, aircraft and space fields — and they still do. Today the new Couch 2X offers the same high degree of reliability in microminiature. Use them with complete confidence for signal switching with other circuit board components. Each relay delivered is fully tested. Additional screening tests are available at your option. Couch 2X relays are made in a variety of coil resistances, three terminal styles and in a wide choice of mounting styles. Other products available include: 10 amp rotary relays, ultra-sensitive crystal can relays and full-size rotary switches.



2X (DPDT) 2-pole	1X (SPDT) 1-pole
0.2" x 0.4" x 0.5"	same
0.1 oz. max.	same
Low Level to 0.5 amp	
@ 30 VDC	same
100 mw	70 mw
60 to 4000 ohms	125 to 4000 ohm
-65°C to 125°C	same
20 G to 2000 Hz	same
75 G, 11 Ms	same
	2-pole 0.2" x 0.4" x 0.5" 0.1 oz. max. Low Level to 0.5 amp @ 30 VDC 100 mw 60 to 4000 ohms -65°C to 125°C 20 G to 2000 Hz 75 G, 11 Ms

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ON READER-SERVICE CARD CIRCLE 49

Liquid crystals plot the hot spots.

Nondestructive thermal mapping of ICs is easy with low-cost cholesteryl esters.

Cholesterol, said to wreck havoc on your arteries, can work magic in a laboratory or on a production line. Certain cholesteric compounds exist in an unusual, suspended state between solid and liquid, with properties of both. In this *liquid crystal* form, (see box) cholesterol derivatives exhibit remarkable physical behavior, evidenced by brilliant color changes when the compound is subjected to changes in environment. These changes can be put to work thermally mapping electronic devices with the aid of cholesteryl esters, such as cholesteryl benzoate.

Cholesterol maps electronic arteries

Thermographic mapping with cholesteric liquid crystals can be a valuable nondestructive test for evaluating semiconductor devices and printed-circuit assemblies. The technique is versatile enough to be used both in the laboratory and in production. The high resolution (20 line pairs per millimeter) of the temperature profiles under dynamic test conditions makes the technique especially attractive for measuring integrated-circuit performance. Many standard methods fail because of the microscopic scale of critical areas of interest.

Other advantages offered by the technique are:

• It is straightforward and expeditiously gives a direct display of information.

• It demands no knowledge of the emissivity of different surfaces, because the iridescent display depends only on temperature and the angle of incident light.

• It requires none of the complicated and expensive scanning equipment necessary with infrared techniques.

• Experiments can be repeated over and over, because liquid crystals, unlike temperature paints, have reversible temperature dependence.

• The material is inexpensive. The cost of bulk cholesteryl esters is about \$25 for 50 cm³ and 1 cm³ of solution will cover about 100 cm²; thus \$25 will pay for testing some 5000 cm².

Cholesteryl esters can be compounded to display



1. **Temperature-vs-wavelength curves** show wide range of temperature dependences for seven different mixtures Cholesteryl esters are available over a -20° to 350° C range in increments from 3° to 50° C. Flatter curves indicate higher sensitivity over a smaller temperature range.

the entire visible color spectrum within any temperature range from as small as 3° C to as large as 50° C. The extremes within which it will operate are -20° to 350° C. Since the compounds are applied in very thin layers (10 to 25 microns), they have low thermal mass and can rapidly follow thermal effects that generate minute amounts of heat.

The color scattered by the liquid crystals is peculiar to a specific temperature, making the quantitative measurement of temperature possible. As the materials are heated, they will change from black to red, to orange, to yellow, to green, to blue, to violet, to black again. The temperature at which a specific color occurs and the temperature range from red to violet may be varied by the proper choice of compound.

The temperature dependence of a representative group of materials is shown in Fig. 1. The sensitivity and operating temperature of these compounds are a function of composition. A quantitative evaluation of a thermal pattern, as required for a thermal map, may be made with liquid crystals provided that certain criteria are observed. For qualitative purposes, however, the requirements may be relaxed. The criteria are:

The heat capacity of the object should be larger

Michael Lauriente, Advisory Engineer, Aerospace Materials, Westinghouse Electric Corp., Defense & Space Center, Baltimore, and **James L. Fergason**, Associate Director, Liquid Crystal Inst., Kent State Univ., Kent, Ohio.

Liquid crystals:

what we know...

Matter is thermodynamically classified into one of three phases—solid, liquid, or gaseous. While this classification ordinarily identifies the mechanical characteristics of a material, it does not necessarily identify its molecular arrangement. For this reason, crystalline classifications are also used. In the amorphous state, molecules form in random array and remain in random motion. In the crystalline state, molecules are firmly fixed in a three-dimensional crystal lattice.

In an intervening mesomorphic state (*liquid crystal*), large groups of molecules are able to move and turn about, yet retain some structural arrangement. Such mesomorphic substances simultaneously have properties of liquids and solids. The properties and combinations, however, are so diverse that subclassification is required. The cholesteric phase is such a subgroup.

In 1888, Reinitzer, an Austrian botanist, found that cholesteryl benzoate appeared to have two melting points: at 145°C the solid turned into a cloudy liquid, then at 179°C, the cloudiness suddenly disappeared and the liquid became clear. The first melting point occurs at the temperature at which the solid and mesomorphic phases are in equilibrium; the second melting point occurs at the temperature at which the mesomorphic and isotropic liquid phases are in equilibrium.

The cloudiness is due to the unusual optical properties of the substance in a mesomorphic state. Whereas ideal liquids would be optically isotropic—their optical properties are the same measured in all directions, liquid crystals exhibit optical characteristics similar to birefringent crystals, which are optically anisotropic. Their optical properties depend on the orientation of the crystal structure. The cloudy liquid state of cholesteryl benzoate is the result of many randomly oriented regions —ordered areas that are spontaneously anisotropic.

The color change of cholesteric materials is directly related to a change of shape of the delicately balanced component molecules. Upon heating, the helical "twist" of the over-all molecular configuration alters. This temperature dependence of the liquid crystal's optical properties is inherent and reversible.

The crystals selectively reflect only one wavelength at each angle. The resultant reflected beam is circularly polarized. When light is incident from several directions at once, a different wavelength is reflected at each angle and the resulting mix of colors is seen as iridescence. The change in temperature causes a shift in molecular structure and, thus, a different color at the same angle.

... And what we don't know

Research is still going on into the complete mechanisms under which cholesteric liquid crystals operate. And, designers are still uncovering new applications. Medical researchers have applied the technique to mapping veins and arteries, checking blood circulation, even detecting cancer. Liquid crystals can also detect vapors, such as unburned hybrocarbons in carbon monoxide,



Cholesteric liquid crystals have a readily alterable helical structure.

and offer the possibility of cheap warning devices. Aircraft manufacturers have painted entire wing structures to unearth poor welds.

As a result, suppliers of liquid crystals are loath to make any claims for the material—other than that it meets your temperature specifications. They are eager for all the feedback they can get. Westinghouse states that its Insulating Materials Div."would appreciate information at your convenience concerning the results of your application." than the heat capacity of the cholesteric film. The specific heat of most cholesteric materials is 1.5 J/cm^3 Since a film of 0.02 mm (20 microns) is required, the heat capacity per square centimeter of surface to be measured should be $3 \times 10^{-3} \text{ J/cm}^2$.

• The background should be black. This may be achieved artifically by using a black paint or black dye which is not oil-soluble. Dyes are preferable, for they allow the black background to be added with little increase in thickness.

• The device to be mapped must be larger than the limit of resolution of the liquid crystal. Practically, this is about 0.02 mm. If the heat pattern has significant structure below this limit, a cholesteric material is impractical.

• The rate of thermal change must be sufficiently slow to allow the cholesteric liquid to follow. The rate of change of color with temperature has a time constant which is variable from a few hours to 30 milliseconds. That is, if the temperature is changed instantaneously the liquid crystal will be within $1/\epsilon$ of its final color in one time constant.

• The temperature of the object to be measured must be in a range where cholesteric materials are available. Figure 1 shows curves for some typical types.

• The surface must be oil-resistant. This can be sometimes achieved by use of a water-soluble film such as polyvinyl alcohol.

Actual test procedure is simple

For applications where it is undesirable or impractical to place the material directly on the surface of the test specimen, the liquid crystal can be put on a hoop-supported Mylar film (Fig. 2) that can then be placed against the surface to be studied. Since the crystals scatter the incident light selectively, rather than absorbing it, the side against the surface should be painted black for high-contrast response. Clean the unblackened side of the Mylar with chloroform and/or acetone. With an eyedropper, apply the desired amount of liquid crystal to the cleaned Mylar surface and allow the solvent to evaporate. To aid evaporation, artificial means such as a 200-W light bulb 12 to 18 inches away may be used. Do not, however, fan or blow on the film. If the film is exposed to air currents before complete solvent evaporation, the coating will be uneven. The prepared hoop may now be placed over the area to be inspected.

To deposit the material with an eyedropper, it is best to wet the inside of the pipette first by drawing some of the liquid crystal solution up into it once or twice before attempting to expel it onto the film. Care should be taken not to allow any of the liquid to reach the rubber bulb of the eyedropper as the solvent will react to it and contaminate the liquid crystal sample.

If an area larger than 8 to 10 inches in diameter is



2. **Hoop-supported Mylar film is used** in situations where it is impractical to place the liquid crystals directly on the surface of the test specimen, Surface of the Mylar facing specimen can be painted black for better " readout."

to be covered, the cholesteryl ester can be sprayed on. For direct application, the following procedure should be followed:

• Clean the area with chloroform and acetone, petroleum ether, or some other solvent to remove contaminants such as oil, grease, dirt and fingerprints. Care should be taken that the solvent does not attack the surface it is in contact with.

• If not dark enough, the area should be coated with a black paint or other coating that is not vulnerable to chloroform or petroleum ether. Allow to dry.

• Apply a coat of the cholesteryl ester. The solvent must completely evaporate before use.

• When the solvent has evaporated, heat the coating approximately ten degrees above its operating temperature, then allow it to return to its operating temperature and color range. This is not essential but will improve the brightness of the colors.

• After the test is completed (it may be repeated as many times as desired), the cholesteryl ester layer can be removed with chloroform and either acetone or alcohol.

Thermal analysis is also possible

Another application of temperature distribution is to find discontinuities in diffusivity caused by voids, cracks, bad bonds or even pinholes in the dielectric.

The conduction of heat by diffusion, not radiation, is given by:

$$T^{*}T = \partial T_{\kappa} / \partial t.$$
 (1)

This equation may be used to define the propagation of thermal waves in solids. Take for example a semiinfinite solid with a sinusoidal temperature at the surface which varies around a temperature T_0 and has a frequency:

$$f = \omega/2\pi \,. \tag{2}$$

The wavelength of the thermal wave is: $\lambda = 2\pi (2K/\omega)^{1/2}$

where K is the diffusivity and ω is the angular frequency. It is propagated with a velocity of:

$$V = (2K\omega)^{1/2} = \lambda \omega/2\pi.$$
 (4)

As a typical problem to be solved in this manner, consider a block of material containing wires which are pulsed with current to heat them. The material has a diffusivity of 0.1. The wires are 1 mm below the

(3)

surface and 1 mm apart. Two questions can be answered:

• How soon after a pulse will the maximum temperature pattern appear at the surface?

• How long will it remain readable? It may be assumed that the maximum signal will occur when the 1-mm waves reach the surface and will dwindle through cancellation and damping as the 2-mm waves arrive at the surface.

First ω is found for 1 and 2 mm by means of Eq. 3:

$$(5^2/\lambda^2)2K = (4\pi^2)(0.2) = 0.8\pi^2$$

for 1-mm waves; and

 $\omega = (4\pi^2)$

$$\omega = 0.2\pi^2$$

(6)

for 2-mm waves.

Then a 1-mm wave will reach the surface at: $t = 2\pi / 0.8\pi^2 = 2.5/\pi \text{seconds.}$ (7)



3. **Differential-amplifier—SCR microcircuit is fired up** to show SCR in off and on states (photos, opposite page).

A 2-mm wave will reach the surface in $5/\pi$ seconds. Thus a clear-cut pattern of the wire will be on the surface between $2.5/\pi$ and $5/\pi$ seconds. This result would also indicate that a $2.5/\pi$ -s pulse could be pected to yield maximum information.

This technique is useful in estimating the results of a test. It is not an exact solution but can be used as a guide. A point to remember is the low velocity of propagation of a thermal wave. A good picture of thermal defects in a plate can be obtained just by watching the heat flow across the plate.

In general, the temperature pattern indicates the magnitude of energy liberated or absorbed by thermal diffusion. For electronic devices, an obvious test would be to correlate temperature with power dissipation. For example, a rectifying junction in the process of deteriorating or breaking down would exhibit a localized area of high temperature caused by high current density at the fault.

Microcircuit mapping is easy

Putting the technique to work is disarmingly simple. Several examples will illustrate this. A linear differential amplifier with an SCR firing circuit on a single chip (WC185T) is to be mapped. The equivalent circuit is shown in Fig. 3; the external firing circuit is connected as shown. In the breadboarded firing circuit of Fig. 4, three such chips are hooked up. Cholesteryl benzoate is applied directly to one of them (see the black area on middle chip). By dialing the appropriate chip, the SCR circuit is fired. The results are shown in the color photos on p. 75; the off state displays the red streaks.

A simple series resistor-diode combination (quad DTL NAND gate WM246G) is fired up with voltages from 6 to 12 volts (p.76). At the lowest voltage, the (continued on page 79)



4. Breadboarded circuit fires SCR. Middle chip (note black cholesteryl benzoate) is under test.

SCR in off state is represented by red streaked areas. Rest of chip is dissipating more heat; thus the green-blue background.

SCR in on state is dissipating more heat than rest of chip. Red streaks pass to green and blue and background gets bluer.

Deeper and deeper blues result as operating temperature increases. SCR here is still in on state, and is as blue as rest of chip.













Resistor-diode combo is fired up with voltages from 6 to 12 volts. At the lowest voltage (upper left), the resistor is barely visible as a red spot. As the voltage is progressively increased, this spot turns green to blue (lower left, upper right). The red area finally advances to the edge of the device. Circuit is Westinghouse WM246G quad DTL NAND gate.









Flexible strip of printed circuitry has one of its conductors fired up. Smaller color area (red) represents I ampere, the other (green) 2 amperes.



Silicon chip has assortment of devices with flying leads so you can breadboard a custom IC. Corner resistor has progressively increasing voltage applied to it. At the lowest voltage, the green area indicates the





high thermal dissipating area. The red area is at a lower temperature. With increasing voltage, the colors gradually progress to the deep blue.



Video amplifier in contact with a cold sink has 5 to 8 volts applied. "No voltage" state is colorless.....



low voltage is yellow-green,



and high voltage is green-blue.

(continued from page 74)

resistor is barely visible as a red spot. As the voltage progressively increases, this spot turns through green to blue and the red area advances to the edge of the device.

The versatility of the technique is underscored by its use with flexible printed circuitry (p. 76). One conductor is fired up. The smaller color area represents 1 ampere; the other, 2 amperes

The silicon chip shown on p. 77, has an assortment of devices on it with flying leads to a PC board, such as might be used to breadboard a "custom IC." It is significant in thermal mapping that the flying leads do not obscure the readout. In the photos a resistor in one corner has been fired up with increasing voltages. At the lowest voltage (top), the green indicates the area of high thermal dissipation, surrounded by a red area which is at a lower temperature. With increasing voltage, the colors gradually progress (middle) to the deep blue (bottom).

A video amplifier (WAl532) is shown on p. 78, fired up with voltages ranging from 5 to 8 volts while in contact with a cold sink. The photos show the effect of zero voltage (colorless, top), low voltage (yellow-green, middle), and high voltage (green-blue, bottom).

Other applications could be in testing platedthrough holes of PC boards for thermal faults, heat sink evaluation, in-process testing, reliability and quality control testing.

Applications beget applications

The same experiments that have led to the applications already described have suggested several other interesting potential uses for the material.

Liquid crystals, for example, could be useful in taking temperature profiles of the human body. In a typical experiment, a man's hand was painted with cholesteric compounds. A color change was evidence of a temperature change in the veins when the blood was blocked by pressure, or when the fingertips were cooled with ice cubes. A temperature drop at the fingertips, due to the dilation of blood vessels, was shown to occur when the subject inhaled cigarette smoke. Temperature profiles of the body could have applications in such medical areas as the detection of certain types of cancer.

Color changes also occur when the material is exposed to certain vapors. This might be exploited for chemical sensing simply, inexpensively and continually. A cholesteric material that detects unburned hydrocarbons associated with carbon monoxide might, for instance, be put to work as a warning device.

Cholesteric compounds are also sensitive to changes in shear. The reaction to rate of shear can be demonstrated by mounting liquid crystal material between a pair of glass slides and sliding, bending or pressing the slides together. The shear applied then produces a predictable color change.

Where do you buy cholesteryl esters?

Cholesteryl benzoate and other esters are available in bulk or in solution. Solutions are compounded with any organic solvent, such as benzene. Particular temperature ranges and sensitivities are dependent on the concentration of cholesteryl.

The Vari-Light Corp., 9970 Conklin Rd., Cincinnati, sells a complete kit for \$50. The kit contains three 50 cm³ liquid crystal solutions, 50 cm³ of black undercoat, an instruction book, four six-inchsquare black plastic films, a five-inch-diameter hoop, eye-droppers, brushes and other equipment. For further information, circle Reader Service No. 471.

The Insulating Materials Div. of Westinghouse Electric Corp., Trafford, Pa., markets the compound under the trade name Spectratherm. Prices run from about \$20 for 50 cm' to \$250 for 1000 cm' (bulk) and \$20 for 5 grams to \$250 for 100 grams (solution). Westinghouse also sells a water-soluble black paint for the black undercoat. Prices run from \$4 a quart to \$22.50 a gallon. For further information about Westinghouse's products, circle Reader Service No. 472.

The Distillation Products Industries Div. of Eastman Kodak, Rochester, N.Y., also sells cholesteryl benzoate. For further information, circle Reader Service No. 473.

A free reprint of this article is available, while supplies last. Circle Reader Service No. 474.

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Open the gate to nanopower IC logic

with complementary MOS arrays. Here's how to put the most on a chip.

The key to both large-scale arrays and micropower circuits is the development of a logic that dissipates very little power—particularly in the quiescent state. In a large logic array, transistors actually switch a rather small fraction of the time (they have, on the average, a duty cycle of from 0.1 to 10 per cent). Thus, most of the time they stand idle.

A magnetic core operating in a memory system is a good example of an element that does not dissipate at all in the quiescent 1 or 0 state, but only during each switching transient.

Logic microcircuits can be fabricated with complementary MOS transistors—p-channel and nchannel devices on the same chip—and they dissipate nanowatts of power when clocked at low frequencies.

The features of complementary MOS transistors that make them extremely suitable for largescale arrays and micropower circuits are the following:^{1,2,3}

Microwatt stand-by power.

• Operation that is highly tolerant of device characteristics.

- Simplified logic functions.
- Simple one-supply, direct-coupled logic.
- Large fan-out capability.
- Good noise immunity.
- High-speed operation.
- Attractive potential for large-scale arrays.

A number of complex complementary MOS arrays have been designed. Figures 1 and 2, for example, show a 36-bit complementary memory on a chip. A BCD-to-16-output decoder⁴ has been built that is effectively the equivalent of 16 fourinput NAND gates on one chip. Rise times as low as 50 ns have been measured, with fall times as low as 20 ns at each output into a 7-pF capacitor. Another large array is the 12-stage binary counter.⁵ A complementary MOS gate has also recently



1. This 36-bit memory was made with complementary MOS units for the U.S. Air Force.



2. These 4-32 transistors dissipate mere microwatts of quiescent power when the memory stands idle.

been offered for sale.⁶

One of the most important advantages of complementary MOS logic to the systems designer is its extremely low power dissipation. In noncomplementary logic, a major portion of the dissipated power is expended even while the circuit stands idle. Complementary MOS circuits dissipate very little power in this quiescent state. This is accomplished by the substitution of an oppositepolarity transistor for the load resistor of conventional transistor logic. Thus, the bulk of the power dissipation in complementary MOS logic takes place during the charging and discharging of ca-

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The basic MOS transistor

The MOS transistor is a device comprising source and drain diffusions, such as n⁺ into p-type silicon. The wafer is p-silicon, and the region between the source and drain diffusions is called the channel. An insulator (usually thermally grown SiO_2) is placed above the channel, and a metallic contact (the gate) is placed on the oxide directly above the channel. When the gate of an n-channel unit is positively charged, a negative charge is induced in the channel at the interface between the silicon dioxide and the silicon. This negative charge permits conduction between the source and the drain. The induced n-type channel at the interface penetrates only about 10 to 100 angstroms into the silicon. The device is basically a voltage-controlled switch with a capacitive input. The voltage at which the device initially turns on is called the threshold voltage, V_T .

N-channel MOS

The device shown is an n-channel MOS transistor. A complementary structure in which p⁺ material is diffused into n-type silicon is called a pchannel MOS transistor. Its operation is analogous to that of the n-channel unit: a negative voltage on the gate induces positively charged holes in the channel, which flow when a negative voltage is applied from drain to source. Figure 4 shows the transfer characteristics $(I_D \text{ vs } V_O)$ of typical nchannel and p-channel units. These transfer characteristics are obtained when the units are operated in the high-impedance region, that is, with a reasonably high drain voltage.

The curve of drain current I_D as a function of drain voltage V_D and gate voltage V_G is divided into three regions of operation: low-impedance on, high-impedance on, and high-impedance off. These regions are described by the following equations for the n-channel unit.

MOS design equations

Low-impedance on:

$$I_{D} = 2 K_{n} [V_{D} (V_{G} - V_{TN}) - (V_{D}/2)^{2}] + I_{L}; \quad (A)$$
$$(V_{D} < V_{G} - V_{TN}).$$

High-impedance on:

$$I_{D} = K_{n} [V_{G} - V_{TN}]^{2} + I_{L};$$
(B)
($V_{D} \ge V_{G} - V_{TN}$)

High-impedance off:

$$I_{\rm D} = I_L;$$
 $(V_g \leq V_{TN})$ (C)
where

$$K_n = (\mu_n \epsilon W/2 l t_{ox}), \qquad (D)$$

and where V_{TN} is the threshold voltage, μ_n is the effective surface mobility for electrons (less than bulk mobility), ϵ is the dielectric constant of the insulation, t_{ox} is the thickness of the insulator, W is the width (periphery) of the channel, l is the

length of the channel between source and drain, and I_L is the leakage current.

The equations for a p-channel MOS device are analogous to those for the n-channel unit except that the effective surface mobility of holes is taken into account.

The threshold voltage for an enhancement-type is given by:

$$V_{TN} = (t_{ax} S/\epsilon), \tag{E}$$

where S is the initial density of surface charge from the bulk material plus that contributed by surface states.

In the early days of the development of MOS transistors, instabilities in the gate oxide caused threshold vollage shifts on the order of one volt. With the advent of clean oxide processing, however, these MOS instabilities have been reduced to the point that shifts in the transfer curves (with a



SOURCE & DRAIN

positive gate bias of 10 volts on both n- and p-channel units) is less than 100 mV after 600 hours at $125 \,^{\circ}$ C. With a negative gate bias of 10 volts, the shift is less than 50 mV. These tests were made under actual conditions for normal logic-circuit operation.

For switching, it is important to know the input capacitance. This value can be represented to the first order by the capacitance of the oxide, C_{ox} , given by:

$$C_{ox} = (\epsilon W/t_{ox}). \tag{F}$$

This value is increased by the capacitance of the gate metallization which is not over the channel, as well as the case and stray wiring capacitance. For present MOS transistors, t_{ox} is in the order of 500 to 2500 angstroms and l is in the order of 0.1 to 1.0 mil. When l is decreased, the input capacitance is decreased and the current capability is increased; however, the drain-to-source breakdown voltage is decreased.

pacitance. This power, called the transient power, increases with the following:

Clock rate.

• Increasing voltage difference between the 1 and 0 logic levels.

Increasing capacitance of the logic elements.

Take, for example, a common circuit element such as the flip-flop. The total power, P, dissipated in a complementary MOS set-reset flip-flop circuit (Fig. 3) is given by:

$$P = 2 C_o V_i^2 f + P_s, (1)$$

where C_o is the node capacitance at each output node, V_i is the supply voltage, f is the operating frequency, and P_s is the stand-by power. The term $(2 C_o V_i^2 f)$ represents the switching or transient power.

For minimum switching power, the supply voltage, V_i , must be reduced to the lowest possible value. The gate threshold voltages for the p-channel units, V_{TP} , are usually larger than the gate threshold voltages for the n-channel units, V_{TN} , and therefore determine the minimum limit for the supply voltage.

From practical considerations, the supply voltage must be appreciably larger than V_{TP} to preserve circuit stability and speed capability. As shown in Fig. 4, typical values for V_{TN} and V_{TP} are 2 and 2.5 volts, respectively. The internal node capacitance, C_o , for the flip-flop circuit (Fig. 3) is given by:

$$C_o = 2C_{ds(p)} + 4[C_{dg(p)} + C_{gs(p)}] + C_{gs(n)} + C_{st}.$$
 (2)

In this equation, only the third and fourth terms, $C_{gs(p)}$ and $C_{gs(n)}$, are considered active capacitances, because they are the terms which modulate the channel conductance to produce the MOS action. The remaining terms are anomalous and tend to reduce speed and increase power dissipation. They therefore should be minimized. This minimization of inactive capacitances requires a device design in which the gate-to-drain and gate-to-source metallization-overlap capacitance is minimized.

Figure 5 shows the total power dissipation of the unloaded flip-flop circuit of Fig. 3 as a function of frequency (repetition rate). Figure 6 shows power dissipation as a function of capacitive load C_L (representing fanout) at a fixed frequency of 200 kHz and a supply voltage of -4volts. As Eq. 1 may lead one to expect, the data indicate a linear relationship with each of the independent variables C_L and f. The value of the stand-by power (which is given by the zero-frequency intercept of Fig. 5) is about 0.3 microwatt at 25°C. The stand-by power is proportional to the drain-to-source leakage current, which tends to be higher for n-channel units.



3. An R-S flip-flop made with four n-channel and four p-channel units. The capacitances indicated are those associated with the units that are germane to the operation of the flip-flop.



4. The transfer characteristics of both n- and p-channel MOS transistors are described by an equation of the form $I_d = K_n \; (V_G - V_{\rm tn})^2$, where $K_n = 1.2 \times 10^{-4} \; A/V^2$, its counterpart, $K_p = 4.7 \times 10^{-4} \; A/V^2$ and $V_{\rm ds} = 6 \; V.$

The inverter: keystone of the functional approach

The fundamental building block in the functional design of complementary MOS arrays is the complementary inverter. The basic inverter circuit is shown in Fig. 7. The logical voltage swing is from zero to the supply voltage, -V. When a voltage of -V (logical 1) is applied at the input, the n-channel unit is turned off and the p-channel unit is turned on; the output is then in the low state (logical 0). This state is illustrated in the lefthand curve in Fig. 7 by the intersection of the characteristics of the two units. When zero voltage (logical 0) is applied at the input, the p-channel unit is turned off and the n-channel unit is turned on. The resulting output of -V volts (logical 1) is shown in the right-hand curve.

Because in either state one unit is off while the other is on, the quiescent power is only that of the supply voltage multiplied by the leakage current of the off unit. The transient power is that resulting from the charging of input and output capacitances.

The basic inverter may be extended to true complementary logic. The following gates use negative logic, that is, negative supply voltage with negative-going logical 1. The logic NOR gate (see Fig. 8) has the p-channel units in parallel and the n-channel units in series. If this arrangement is reversed, that is, the n-channel units in parallel and the p-channel units in series, a NAND gate results. The MOS set-reset flip-flop is formed by the cross-coupling of two NOR gates, (see Fig. 3).

Because MOS complementary logic employs only one supply and a logic swing of the full supply potential, either negative- or positive-going logic can be easily used for the same gate. For example, in the gate of Figure 8, the pin connected to the V_i supply can be grounded and the ground pin can be connected to a positive supply. However, the gate which was formerly a NOR gate with negative logic becomes a NAND gate with positive logic. Thus the use of negative- or positive-going logic becomes the prerogative of the design engineer.

Another very useful circuit configuration is the transmission gate⁷ (Fig. 9), which can be used in counters, shift registers, and memories.8 The single-channel transmission gate charges or discharges a capacitive load such as a gate of another MOS transistor. This reversal of direction of current flow is possible because the MOS transistor is a bilateral device, that is, the functions of source and drain can reverse. The transmission gate, however, operates as a drain-loaded stage in one current direction and as a source-follower stage in the other current direction. The latter type of operation results in slow speed in largesignal applications and also causes the MOS transistor to shut off when the voltage difference from gate to source equals the threshold voltage. In other words, cutoff can occur prematurely in comparison with an ideal switch. Both these problems can be reduced or eliminated if the voltage swing at A (Fig. 9) is larger than the required output (capacitive-load) voltage swing.

Alternatively, the complementary transmission gate can be used. In this arrangement, one of the transistors always operates as a normal drainloaded stage. Transmission gates have been used in counters, shift registers and memories. For ex-



5. **Power dissipation increases linearly** with repetition frequency in a complementary MOS R-S flip-flop. The floating graph (upper left) shows the power dissipation as the origin is approached.







7. In the basic complementary inverter, the p-channel device is off when the n-channel device is on and vice versa. The input voltage that represents a logical 1 is +V; the zero voltage output that results from this input represents a logical 0.



8. The output of this three-input NOR gate is $A_1 + A_2 + A_3$, when the p-channel units are in parallel and the n-channel units are in series. Putting the n-channel units in parallel and the p-channel units in series produces a NAND gate. This gate is a basic logic element.



9. The p-channel transmission gate (left) can charge or discharge the gate of another MOS transistor. But premature cutoff can occur if the voltage between gate (A) and source equals the threshold voltage. The complementary transmission gate (right) always has one of its transistors operating as a normal drain load, so the voltage swing at A need not be large and there is no danger of premature cutoff.



10. When S_2 closes, in this simplified schematic of a complementary MOS shift register, the bit stored on the left side of the flip-flop moves onto the gates of its right side. S_1 locks data in the closed loop of the delay stage. In an actual device, S_1 and S_2 are implemented with complementary MOS pairs (see Fig. 5.).

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ample, a shift register based on the circuit of Fig. 10 uses transmission gates for S_1 and S_2 .

Capacitive storage and complementary MOS units can be used to form a true complementarylogic shift register consisting of two flip-flops per bit for two-phase operation. A significant reduction in components can be obtained, however, by use of the excellent capacitance-storage feature of the MOS gates. As shown in Fig. 10, S. and S_2 are in series with the cross-coupling connections of the flip-flop. When these switches are opened, two separate storage elements are formed - the left side and the right side of the flip-flop. Each side can store information for an extended but finite period of time. Digital information from the preceding stage is transferred to the left side. At the same time, the right side transfers its information to the left side of the next stage. If switch S_2 is closed first, and then S_1 , the 1 or 0 information transferred from the preceding bit is locked into the left side. A shift register based on these principles and using MOS units for switching is shown in Fig. 5. Effectively, this circuit is a simple MOS flip-flop for two-phase operation comprising only a few components.

Logic designs may be achieved by use of only NOR gates, and inverters. However, a somewhat more economical gate-design approach is to consider that common source and drain regions can exist in integrated-circuit form, and that the MOS transistor is topologically symmetrical. Thus, the designer can easily have access to more than one gate. For example, these ten different four-input functions can be made by simple interconnection changes on a quad of p-channel units:

- $\overline{A} + \overline{B} + \overline{C} + \overline{D}$ [NAND]
- $\overline{A} \ \overline{B} \ \overline{C} \ \overline{D}$ [NOR]
- $(\overline{A} + \overline{B})$ $(\overline{C} + \overline{D})$ [Exclusive-OR when C = A and D = B]
- $(\overline{A} + \overline{B})\overline{C} + \overline{D}$
- $(\overline{A} + \overline{B} + \overline{C})\overline{D}$
- $\overline{A} \ \overline{B} + \overline{C} \ \overline{D}$
- $(\overline{A} \ \overline{B} + \overline{C}) \ \overline{D}$
- $\overline{A} \,\overline{B} + \overline{C} + \overline{D}$
- $\bullet \overline{A} \overline{B} \overline{C} + \overline{D}$
- $(\overline{A} + \overline{B})\overline{C}\overline{D}$

The quad consists of five diffusion strips for source or drain and four gate strips. Figure 11 shows formations of the gate for the function (\overline{A} + \overline{B}) (\overline{C} + \overline{D}), which is the exclusive-OR function when $C = \overline{A}$ and $D = \overline{B}$. This type of design can be used for more complex functions such as half adders, half shift registers, and full adders.

The gates described have measured stage delays of 25 ns. Higher speeds will be achieved by reducing stray capacitance and increasing the gain of the MOS transistors. The speeds can be calculated





11. With this all-purpose complementary MOS gate, merely changing the metal interconnect pattern on the p-channel quad can produce any one of 10 four-input gate functions. As shown above, the unit is a gate, the output of which is represented by $(\overline{A+B})$ $(\overline{C+D})$.



by use of the MOS nonlinear equations (see box) and the circuit capacitance. Operation at maximum clock rates of up to 20 MHz is easily predictable from present results.

Design arrays using minterm equations

If the desired function can be expressed easily in equation form, the designer can use the truth table or minterm expression directly and then reduce the number of transistors. This procedure is similar to relay (switch) logic design. In this method the designer essentially starts with an outline such as that in Fig. 12. For a minterm of







13. The design of this adder has the function and its complement arranged on top of each other, just as in the truth table (left). When excess transistors are eliminated, the same circuit has eight fewer transistors (see right). This is done by scanning across the matrix and eliminating redundant variables. Reading left to right from the top, for example, A and A can be combined into one

transistor, as can be \overline{A} and \overline{A} . The second line has no adjacent redundant functions, so no terms involving B can be eliminated. On the third line, the two middle C transistors can be combined and, since the two \overline{C} units share a common conductor, either of the \overline{C} transistors can go. Similar operations on the lower half of the circuit produce the minimized summing circuit at the right.

function F, the output should be shorted to the supply $+V_a$; for the minterms of \overline{F} , the output should be shorted to ground. For example, Fig. 13 (left) shows the nonminimized logic design of the sum function of a full adder. The top four terms of the truth table show the minterms of the function S, and the bottom four terms show the minterms of the function \overline{S} :

 $S = \overline{A} \ \overline{B} \ C + \overline{A} \ B \ \overline{C} + A \ \overline{B} \ \overline{C} + A \ B \ C$

 $\overline{S} = \overline{A} \ \overline{B} \ \overline{C} + \overline{A} \ B \ C + A \ \overline{B} \ C + A \ B \ \overline{C}$

Of the 8 possible minterms for three input variables, this particular case breaks into four each in S and \overline{S} . (This even division is not necessarily the general case.) For the upper, or p-channel, MOS units, each series string represents a minterm of S. The complement of each variable in the minterm expression for S drives a gate in that string. In the lower, or n-channel, expression, the minterms of \overline{S} are used directly and each variable drives a gate of the series strings. If the common C and A units, and \overline{C} and \overline{A} units, are connected together, a minimized expression is obtained, as in Fig. 13 (right). The sum function requires 16 transistors. Two exclusive-OR circuits and one inverter also provide the sum function, using a total of 18 transistors by the gate approach. If the complements of the variables are not available, however, six additional transistors are required for inversion, for a total of 22 transistors.

Transistor count should not be the only criterion for design choice. Topological layouts that minimize silicon area and maximize the operating speed of the circuit must also be considered. Often, for example, two simple stages in cascade operate faster than one complex stage. A fair comparison between any two designs would be to adjust transistor sizes for equal speed in each design and then compare chip size.

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Error detecting codes are simple

to implement. Compare some of the popular detecting and correcting codes and the hardware needed.

Errordetection correctn communictns easilyappliedif mantoman.

The previous sentence is a mess, but the astute reader will quickly pick the "message" it carries from the "noise." The human brain does an amazing job of error detection and correction despite all kinds of aberrations in aural or visual transmission.

Electronic systems, unfortunately, are not nearly so clever. Computers, digital communications links, and other digital systems will faithfully follow any inaccurate instructions or transmit any faulty data fed to them. That is, unless the designer builds into them some sort of systems to sense, or even to correct, errors. There are several approaches to error control that have evolved over the last few years. Choosing the one to use and then implementing the error coding properly are important, because the error control circuitry can affect the speed and efficiency of the entire system.

Here are some of the more popular error control techniques, and a look at the hardware involved.

Parity check is most basic method

The most basic of all error-checking methods is the parity check. Validity is recognized by the parity of the transmitted word; that is, an extra digit makes the total number of 1s in the word odd or even. This extra digit is called a parity bit. The choice of a parity generator is governed to some degree by the type of code used, but odd parity is most generally used. With even parity an all-zero character can occur. This is undesirable since it may look as though the machine is off. It is possible to incorporate more than one parity digit in a word, as in the Hamming code, which will be examined later.

Figure 1a shows how a single-digit parity generator may be implemented in a typical six-bit data register. The clock input to the parity generator can be derived from either output of the final flip-flop. If even parity is to be generated, the double dotted line must be connected; the single line is used for odd parity.

It works like this: If the data register is loaded at

 t_0 , during clock times t_1 through t_6 data are shifted out of the high-order position of the register, that is to say, flip-flop F. The parity flip-flop changes state every time a 1 is shifted out. At t_7 the state of the parity flip-flop is gated onto the transmission channel. The parity bit must follow the data word since the parity flip-flop only counts bits as they are shifted out of the data register.

To recognize parity at the receiving station, a similar arrangement is used where the parity generator is clocked by the serial input data rather than the output (Fig. 1b). When the entire word has been shifted into the register at the receiving station, the parity flip-flop will indicate whether an odd or even number of bits was received. This will show whether an error



1. **Parity check circuitry** is similar for transmitting (a) and receiving (b). The parity bit is derived from a flip-flop.

Robert K. Jensen, Applications Engineer, Fairchild Semiconductor, Mountain View, Calif.

exists; it will not detect the position of the error.

It is important to note that, since a parity check detects only an odd number of errors, slightly under half of all errors will be detected, if the probability of an error is 0.5. The number of errors detected is not exactly half, because errors may occur in the parity bit itself during transmission or reception. In many systems, such as magnetic-tape recording, the probability of an error is much less than 0.5. This means that the probability of more than a single-bit error will be very low, so a parity check will catch considerably more than half the errors. A further minor disadvantage is that the transmission rate is decreased by the addition of parity bits. Against these shortcomings must be weighed the advantages. The parity check is simple to implement and is reatively effective in serial transmission over interface channels, as errors here are generally of the impulse type.

Longitudinal redundancy check is parallel code

In contrast to the serial parity check, the longitudinal redundancy check is a parallel type of checking code. A check bit, generated in a somewhat different fashion from the parity bit, is used for *each* bit within a word. To explain the basic notion, take two words of four bits each and operate on them to gen-

erate the check bits. The data words are: data word 1: 1110 data word 2: 0010

These words will be added together by binary arithmetic but all borrows and carries will be ignored. For example, 1 + 1 = 0 with a 1 to carry, but here we forget about the carry. Thus adding the two data words gives:

check word 1: 1100

As this addition is performed, the transmitter sends out first data word 1, then data word 2, and then the check word. At the receiver the two data words are added together, just as they were at the receiver, and a similar check word is produced. Now the receiver's check word is compared with the one sent from the transmitter, to see if they agree. This is done by means of an interesting property of this type of binary arithmetic: unlike symbols added together generate a 1, like symbols generate a 0. If the two check words are exactly alike, then all 0s will be produced; but if an error has been made, it will cause a 1 to appear in the answer:

No error With an error

Transmitter check word	1100	1101
Receiver check word	1100	1100
	0000	0001



2. Two registers are needed for the longitudinal redundancy check. Gating enables the check word to multiply further data words or to be transmitted. Color block shows the error detector used at the receiver only.

Initially data word 1 is the check word. This is then added to data word 2 to generate a new check word. It is then possible to add the new check word to a third data word, and so on. Thus the check word can be used to check a sequence of as many data words as desired.

Accuracy is not necessarily better than that of the parity check approach. Longitudinal redundancy is expensive to implement because it requires a flipflop for each binary digit in the code word.

An actual implementation for four-bit words is shown in Fig. 2. The sequence of events is as follows, using the previous example.

Initially a data word is loaded into the data register, d_1 through d_4 . The data register is then gated to the check register, giving the same word in both. The data word is then shifted onto the transmission channel, most significant bit first (d_4) . At this stage the check word could be gated into the date register and transmitted, or a second data word formed and a composite check word generated. Assume the second case. The data word is gated into the data register in the same manner as before, and then into the check register, causing a binary addition to the already existing check word. The data word is shifted

Table 1: Hamming-code check digits.

Total number of digits (n)	Number of infor- mation digits (d)	Number of checking digits (<i>P</i>)
1	0	1
2	0	2
3	1	2
4	1	3
5	2	3
6	3	3
7	4	3
8	4	4
9	5	4
10	6	4

Table 2: Truth table.

Digit position	Position number $P_3P_2P_1$
No error	000
1	001
2	010
3	011
4	100
5	101
6	110
7	111

out of the data register and then the check word is transmitted by gating it into the data register and shifting it out onto the transmission channel. At the receiver end, a check word is being generated in the same way. When the check word is received in the data register of the receiver, the two check words are added together in the check register, which should produce all 0s. The check register is then sampled to detect any 1s (color block in Fig. 2).

Hamming code is commonest in digital systems

The most widely used method of error detection and correction in digital systems is probably the Hamming code.¹ It provides an efficient approach to error detection and correction and is quite easy to implement. There is, however, a practical limit on the use of this code in that the number of checking digits required for a given word can become quite large (see Table 1). The code is constructed by incorporating more than one parity digit in any given word, that is, each parity digit checks the parity of certain groups of information digits within the word.

If an error occurs within the word, the parity bits taken together will give a binary number, the decimal equivalent of which will indicate the position of the error in the word. This locates the error, thus making it easy to correct.

As an example of the Hamming code, take data word, 0101, with the most significant bit at the left. The Hamming code requires that check bits are inserted at bit positions 1, 2, 4, 8, 16, etc., so this word will contain $101P_41P_2P_1$ after the check bits, P_1 , P_2 , P_4, \cdots , are generated. Each parity bit checks a group of word bits determined by reference to a truth table. If there are no errors, a parity sum of 000 will result for the parity digits. The truth table shows that a 1 in position P_1 would mean an error in position 1, 3, 5 or 7; a 1 in position P_2 would mean an error in positions 2, 3, 6 or 7; and a 1 in position P_4 would mean an error in positions 4, 5, 6 or 7.

The parity digits are determined by an even parity rule and inserted in the following manner. Taking P_4 first, bits 5, 6 and 7 have only one 1, so a 1 goes in P_4 . Bits 3, 6 and 7 have two 1s, so a 0 goes in P_2 . Bits 3, 5 and 7 have only one 1, so a 1 goes in P_1 :

and i have only one 1, so a 1 goes mit 1.							
Position	7	6	5	4	3	2	1
Digit type	d	d	d	P_4	d	P_2	P_1
Word	0	1	0		1		
Insert P_4 , (4-5-6-7) check	0	1	0	1	1		
Insert P_2 , (2-3-6-7) check	0	1	0	1	1	0	
Insert P ₁ , (1-3-5-7) check	0	1	0	1	1	0	1

If the received message is $0 \ 1 \ 1 \ 1 \ 1 \ 0 \ 1$, then the P_4 binary sum is 1, P_2 sum is 0, and P_1 sum is 1. This gives a position number of 101 (decimal 5), indicating an error in position 5. This is one of the nicest properties of the Hamming code—the position number has the value of the position it represents (see Table 2). Thus complementing position 5, and re-



CORRECT ENABLE -

ъ

3. A Hamming-code transmitter (a) has three parity generators tied together. The receiving station (b) detects the

error (colored section) and corrects by complementary addition (reversed color).



4. Modulo-2 addition is used to generate the cyclic check coded data (a). The received code will cancel the check

register content (b). If the check register does not cancel, the error flip-flop will indicate an error.

Generator polynomial	Will detect	Maximum message digits	Check Digits
P(X)		k _{max}	n—k
$1+X^{1}(11)$	Any ODD number of errors		1
$1 + X^{1} + X^{4}$ (11001)	Two errors; burst of 4 bits or less, 88% of bursts of 5 bits, 94% of larger bursts.	11	4
$1+X^4+X^9$ (etc.,)	Two errors; burst of 9 or less, 99.6% of bursts of 10 bits, 99.8% of larger bursts.	502	9
$1 + X^2 + X^{13} + X^{22}$	Two bursts of combined length of 12 or less any odd number of errors, a burst of 22 or less, 99.99996% of the bursts of 22 bits, 99.99998% of larger bursts.	22,495	22

Table 3: Cyclic-code generator polynomials

moving the parity digits, gives the original message word 0101.

Figure 3a shows a typical implementation for a Hamming-code transmitter. The word is loaded into the data register and the check digits determined by the array of exclusive-OR gates. At the receiver, Fig. 3b, the parity of each group of digits is examined by the set of exclusive-OR gates, giving a position number ZYX. If this number is 0, the error flip-flop will not be set and the received number is correct. A position number other than 0 will be detected by NOR gate E, and the error flip-flop will be set. The position number ZYX is decoded by the series of NAND gates. In the foregoing example, there was a position number of 101 (5). This will set gate 5, which complements flip-flop 5 and removes the error.

Cyclic codes becoming widely used

Because cyclic codes are well suited to randomburst error detection and relatively easy to implement, they have recently come into wide use. The name cyclic is used because any cyclic permutation of a code word will result in another code word.

A cyclic code message m, of n binary digits, will consist of k information digits and (n - k) check digits. The message m, or code polynomial, is derived from two algebraic representations of two binary words, that is to say, two arguments: a generator polynomial P(X) and a message polynomial G(X). The choice of a generator polynomial P(X) is directly related to the particular error pattern(s) which are to be detected. This can be seen from Table 3, which shows a few typical generator polynomials and the error patterns that they will correct. Their derivation is difficult 2 and will not be shown here.

The usual representation of these polynomials is an algebraic form where the binary digits are represented by X° (or 1), X^{1} , X^{2} , X^{3} ,... As an example, assume a generator polynomial, P(X), to be 11001. Algebraically represented, this quantity would be described as $1 + X + X^{4}$. (It is convenier... to show the high-order digit in the rightmost position as this is how it would normally appear in the transmission channel.)

By definition, a polynomial of degree (or highest power) less than n is a code polynomial and is said to be valid if, and only if, it is evenly divisible by the generator polynomial, P(X). Since the division is serial, bit by bit, the operations are in binary arithmetic but without carries or borrows.

Find the right code polynomial

The objective then, given a message polynomial and a generator polynomial, is to construct a code polynomial, F(X), which is evenly divisible by P(X). This is done in the following manner: $X^n - {}^*G(X)$ is divided by P(X) and the remainder is added to X^{n-k} G(X). A handy rule to remember is that the number of check bits, (n-k), is always less than the number of bits in the generator polynomial being used (Table 3). This is because binary division without carries or borrows will always give a remainder of one bit less than the divisor.

The received message, H(X), at the destination



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may contain an error or errors which must be detected. H(X) is, in essence, F(X) plus error(s), if any, introduced during transmission. This message, H(X), is then divided by P(X) at the receiving station, and any non-zero remainder will indicate that an error has occurred. Conversely, a zero remainder will indicate that either a correct message, or a message with an undetectable error has been received.

Suppose, for example, that the message word, G(X), is 101010, or $1 + X^2 + X^4$. This word has six information bits (k = 6) and the generator polynomial is 11001, or $1 + X + X^4$, which will generate four check bits (n - k = 4). Multiplying G(X) by X^{n-k} will give 0000101010, which when divided by 11001 leaves a remainder of 0101. The division without carries or borrows is:

11001)0000101010
	11001
	00011
	11001
	01101
	11001
	0101

The division proceeds from right to left because both divisor and dividend are written with the most significant bit at the right. The quotient is ignored since it is only the remainder that is to be added to the message word, to make it evenly divisible by the generator polynomial.

The four-bit remainder is one digit less than the generator polynomial, as noted in the rule stated previously.

When the remainder is added to the message word, a cyclic code message of 0101101010 is produced. At the receiver the code message is divided by the same generator polynomial; an all-zero result will indicate no error, while a non-zero result will indicate an error.

To correct errors in cyclic code systems, it would be necessary to construct a table of remainders, because each error pattern yields a unique remainder. Correction is thus possible with reference to a table, but this is not very practical from a hardware standpoint.

The algorithm that has been described lends itself to implementation with shift registers and modulo-2 adders (exclusive OR). The size of the shift register is determined by the degree of P(X) (Fig. 4).

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To demonstrate the usefulness of this approach, the stable gain of a nonunilateralized FET will first be computed. After detailed explanation of the unilateralization technique for a common-source FET, its gain will be calculated and shown to be considerably higher than its nonunilateralized equivalent (37 dB vs 10.3 dB).

Finally, to show the technique in practice, the design of a high-performance, single-stage unilateralized FET amplifier will be followed step by step. This 100-MHz amplifier has a stable power gain of 18 dB and a noise figure of less than 2 dB.

Look at a FET as a two-port network

The common-source configuration of a FET is illustrated as a two-port network in Fig. 1. It is completely characterized by Eqs. 1 and 2:

$$I_{1} = y_{is} E_{1} + y_{rs} E_{2}; \qquad (1)$$

$$I_{2} = y_{is} E_{1} + y_{rs} E_{2}, \qquad (2)$$

The y_{ε} (short-circuit admittances) in these equations are defined in Table 1.

These parameters are functions of frequency and the bias conditions of the FET. Their numerical values are typical 100-MHz values obtained from the 2N4416 data sheet. For convenience these values are listed in Table 2 and are valid for a drain-to-source voltage (V_{DS}) of +15 volts and a gate-to-source voltage (V_{GS}) of zero volts.

Linvill-Gibbons technique defined

Linvill and Gibbons have developed a technique

James M. Phalan, Section Head, Application Dept., Union Carbide, Mountain View, Calif.



1. **Common-source connection** of a FET demonstrates the meaning of Eqs. 1 and 2. The y parameters are defined in Table 1.

for determining the stability of two-port networks. The network is first terminated in the complex conjugate of its output admittance. Then a power gain, G_{aa} , for the network is defined as:

 $G_{oo} = |y_{f}|^{2} / [4g_{i}g_{o} - 2\operatorname{Re}(y_{f}y_{r})], \quad (3)$ where g_{i} and g_{o} are the real components of the input and output admittances, respectively, and Re $(y_{f}y_{r})$ is the real part of $y_{f} \times y_{r}$. A critical factor, C, is also defined:

$$C = 2G_{oo} \left| y_{r}/y_{l} \right|. \tag{4}$$

The stability criterion for a two-port network is then stated as follows: A two-port network is potentially unstable if the critical factor C is greater than unity or the power gain G_{oo} is negative. If, however, C is less than unity and G_{oo} is positive, the network is unconditionally stable and G_{oo} is within 3 dB of the maximum available gain, G_{MAX} , where:

$$G_{MAX} = 2G_{oo} \left[1 - (1 - C^2)^{1/2} \right] / C^2, \qquad (5)$$

for C < 1.

Linvill-Gibbons technique applied to a FET

The Linvill-Gibbons technique can be applied to a common-source-connected 2N4416 FET. First G_{oo} is computed from the 100-MHz y parameters in Table 2:

$$\frac{G_{00}}{=4(0.05)(0.02)-2\text{Re}(-j0.42)(4.5-j0.58)}$$

=42=16.2 dB, (6)

and:

$$C = 2 \times 42 | -j0.42/(4.5 - j0.58) |$$

= 7.78. (7)

 G_{∞} is positive but C is greater than unity, so the common-source configuration of the 2N4416 is potentially unstable at 100 MHz. C can be reduced to

unity by decreasing power gain G_{oo} by a factor of 7.78. This is done by shunting a conductance of the proper magnitude across the output of the FET. This conductance is then considered part of the output conductance of the device. The reduced power gain, G_{oo} , is:

$$G_{vo} = G_{oo}/7.78 = 42/7.78 = 5.4 = 7.34 \,\mathrm{dB},$$
 (8)

and C is now unity. Thus the maximum power gain, G_{MAX} , obtainable from the modified FET can be calculated from Eq. 5:

$$G_{MAX} = 5.4 \times 2 \left[1 - (1 - 1)^{1/2} / 1 \right] = 10.8$$

= 10.8 = 10.3 dB. (9)

Hence 10.3 dB is the maximum stable gain that can be obtained from the common-source connection of a 2N4416 operating at 100 MHz.

What is unilateralization?

As stated, a unilateral two-port network has a onedirectional property. A signal applied to the network's input port causes a current or voltage to appear at the output port; applied to the output port, no current or voltage is developed at the input network. Analytically this property is shown in the characterizing equations for a unilateral two-port network:

$$I_1 = y_i E_1; \qquad (10)$$

$$I_2 = y_1 E_1 + y_0 E_2. \tag{11}$$

Since $y_r = 0$, critical factor C must also be zero. Thus a unilateral two-port network is unconditionally stable.

A technique for unilateralizing the FET is given in the box. The characterizing equations for this configuration are:

$$I_1 = (y_{is} + y_{rs})E_1, \qquad (12)$$

$$I_2 = (y_{fs} - y_{rs})E_1 + (y_{os} + y_{rs})E_2.$$
(13)

Substituting the unilateral common-source y parameters of Eqs. 12 and 13 into the expression for G_{oo}

(Eq. 3) yields:

$$G_{00} = \frac{|y_{1s} - y_{rs}|^2}{4(g_{1s} + g_{rs})(g_{0s} + g_{rs})}$$
(14)

Since critical factor C is zero, G_{∞} is equal to the maximum available power gain. With the values in Table 2:

$$G_{oo} = G_{MAX} = \frac{\left| \frac{4.5 - j0.58 + j0.42}{4(0.05 + 0)(0.02 + 0)} \right|^2}{4(0.05 + 0)(0.02 + 0)} = 5000$$

= 37 dB. (15)

Consequently, 37 dB is the maximum power gain that can be obtained from a 100-MHz, unilateralized, common-source amplifier with the 2N4416 FET. In contrast, only 10.3 dB of stable power gain can be obtained from the same device in a nonunilaterized amplifier.

Design a 100-MHz FET amplifier

A simple, single-stage amplifier design shows how unilaterization is applied to the design of a commonsource FET amplifier. Its specifications are:

50-ohm source and load resistances.

- Center frequency = 100 MHz.
- Bandwidth $\Delta f \approx 5$ MHz.
- Power gain = 63 = 18 dB.
- Noise figure < 2 dB.

The first steps in the design are to specify the source and load conductances that will produce the desired 18 dB of gain. The source conductance, g_{ss} , influences the noise figure of the amplifier. From manufacturer's specifications, a noise figure of less than 2 dB at 100 MHz for the unilateralized, common-source connection of the 2N4416 can be obtained when $g_s = 1.0 \,\mu$ mho. Before the load conductance, g_L , can be specified, however, an expression for the amplifier power gain must be introduced. This gain, G_{TU} , is the transducer power gain for a unilateralized two-port network and is expressed in terms of the unilateralized common-source y parameters:

$$r_{U} = \frac{4 \left| y_{/s} - y_{rs} \right|^{2} g_{L} g_{s}}{\left[(g_{1s} + g_{rs} + g_{s}) (g_{qs} + g_{rs} + g_{L}) \right]^{2}} , \quad (16)$$

where:

G

$$G_{TU} = \frac{P_o}{P_{AVS}} = \frac{power \ output}{power \ available \ from \ the \ source}$$
(17)

All the values in Eq. 16 are known, with the exception of the load conductance, g_L , so that:

$$63 = \frac{4 \left| 4.5 - j0.58 + j0.42 \right|^2 g_L}{\left[(1 + 0.05)(g_L + 0.02) \right]^2}$$
(18)

Solving Eq. 18 yields, $g_L = 1.24 \mu \text{mhos.}$

With the source and load conductances specified,

Table 1: Definition of short-circuitadmittance parameters for common-source connection

- y_{is} Input admittance, output shorted.
- y_{rs} Reverse transfer admittance, input shorted.
- y_{ls} Forward transfer admittance, output shorted.
- y_{os} Output admittance, input shorted.

Table 2: 100-MHz common-source yparameters for 2N4416

$y_{is} = 0.05 + j2.3 \text{ mmhos}$ $y_{rs} = -j0.42 \text{ mmho}$ $y_{fs} = 4.5 - j0.58 \text{ mmhos}$ $y_{os} = 0.02 + j0.82 \text{ mmho}$

Unilateralization of an active device

Unilateralization is generally accomplished by neutralizing the effect of the reverse transfer parameter $(y_{rs}$ in Eq. 2) of an active device, which in itself, is not unilateral. This involves making the input pert of the resultant two-port network independent of the output port. A technique for unilateralizing a field-effect transistor is illustrated in the accompanying figure.

The two-port networks in the figure are in parallel. The short-circuit admittance matrix for the composite network can therefore be found by adding the short-circuit admittance matrices for the FET and the neutralizing admittance:

$$\begin{bmatrix} Y \end{bmatrix} = \begin{bmatrix} Y_{N_1} \end{bmatrix} + \begin{bmatrix} Y_{N_2} \end{bmatrix}, \quad (A)$$

where:

 $\begin{bmatrix} Y \\ N_{N_1} \end{bmatrix} = \text{Composite short-circuit admittance matrix,} \\ = \text{Short-circuit admittance matrix for FET}$

$$=\begin{bmatrix} y_{is} \ y_{rs} \\ y \ y \end{bmatrix} , \qquad (B)$$

 $\begin{bmatrix} Y_{N_2} \end{bmatrix} = \text{short-circuit admittance matrix for neutral-izing admittance}$

$$= \begin{bmatrix} Y_N & -Y_N \\ -Y_N & Y_N \end{bmatrix} .$$
 (C)



Thus

$$\begin{bmatrix} Y \end{bmatrix} = \begin{bmatrix} y_{is} + Y_N \ y_{rs} - Y_N \\ y_{is} - Y_N \ y_{os} + Y_N \end{bmatrix}$$
(D)

The reverse transfer admittance term, $y_{is} - Y_N$, of the composite two-port network is now set equal to zero, so that:

$$y_{rs} - Y_N = 0$$
 or $Y_N = y_{rs}$. (E)
Unilateralization is thus accomplished by letting
 $Y_N = y$

The equations characterizing the unilateral commonsource transistor are then:

$$E_{1} = (y_{is} + y_{rs})I_{1};$$
(F)

$$E_{2} = (y_{is} - y_{rs})I_{1} + (y_{os} + y_{rs})I_{2}.$$
(G)

the input and output circuit configurations can be selected. The configuration to be used is that in Fig. 2.

The input and output tuned circuits in Fig. 2 are identical, so only the design of the output circuit need be examined. The equivalent circuit of the output of the common-source amplifier is shown in Fig. 3.

The amplifier load, R_A , was specified as 50 ohms, but the load resistance that must be presented to the output of the amplifier is $1/g_L$; if $g_L = 1.24 \ \mu$ mhos, it is 808 ohms. An impedance transformation is therefore necessary and is accomplished by series capacitor C_A in Fig. 3. C_A is specified to transform load resistance R_A to 808 ohms at 100 MHz. This transformation is shown in Fig. 4. The value of C_A is found as follows:

$$X_{C_A} = R_A \left[(R_L/R_A) - 1 \right]^{1/2}$$

 $C_A = 1/\omega X_{C_A}$

= 50
$$[(808/50) - 1]^{1/2}$$
= 195 ohms;

and:

$$= 1/(2\pi \times 10^8 \times 195) = 8.15 \text{ pF}.$$
 (20)

The equivalent circuit capacitance, C_L , is similarly obtained:

$$X_{c_{l}} = X_{c_{A}} \left[1 + (R_{A}/X_{c_{A}})^{2} \right]$$

 $=195 [1+(50/195)^{2}] = 208 \text{ ohms}; (21)$

and:

3

 $C_L = 1/\omega X_{C_L}$

$$= 1/(2\pi \times 10^8 \times 208) = 7.65 \text{ pF}.$$
(22)

The circuit in Fig. 3, then, is equivalent to that in Fig. 5 for the frequencies in the vicinity of 100 MHz. The output capacitance, C_o , and conductance,

 g_o , of the unilateralized common-source FET are: $C_c = Im(v_c + v_c)/\omega$

$$= 0.4/(2\pi \times 10^8) = 0.635 \,\mathrm{pF}: \qquad (23)$$

and:

19)

 $g_o = \operatorname{Re}(y_{os} + y_{rs}) = 0.02 \,\mu \mathrm{mho}.$ (24)

To obtain the specified 5-MHz bandwidth, the Q of the output circuit should be:

$$Q = f_0 / \Delta f = 20. \tag{25}$$

where $f_0 =$ center frequency and $\Delta f =$ bandwidth. The Q of the input circuit is set equal to 7 to pro-

vide a much greater bandwidth than the output circuit's, so the input circuit has negligible effect on the amplifier bandwidth. The only unspecified components in the output circuit are L_2 and C_2 . Their values are dependent on the bandwidth requirements of the output circuit. In the case of L_2 :

$$R_L/\omega L_2 = Q = 20.$$
 (26)

 R_L , the total load across the amplifier's output, is the parallel combination of $R_L = 808\Omega$, and the FET's output conductance, g_o :

 $R_L = 1/(1/R_L + g_o) = 1/(1/808 + 0.02) \approx 800 \Omega.$ (27) Inductance L_2 is therefore:

 $L_2 = R_L / \omega Q$

 $= 800/(2\pi \times 10^8 \times 20) = 0.068 \ \mu\text{H}.$ (28) The capacitance that resonates with L_2 at 100 MHz is C_{τ} . Its value is:

 $C_{\tau}=1/\omega^2 L_2$

$$= 1/\left[(2\pi \times 10^8)^2 \times 0.068 \right] = 39.6 \,\mathrm{pF}, \ (29)$$

where:

$$C_T = C_o + C_2 + C_L = 39.6 \text{ pF.}$$
 (30)

Thus, from Eqs. 22 and 23:

$$1C_{0} = C_{T} - C_{1} = 31.3 \text{ pF}.$$
 (31)

In the final circuit (Fig. 6), C_2 consists of 27 pF in parallel with a 1-12-pF trimmer.

The design of the input tuned circuit proceeds in exactly the same manner as that of the output circuit. Calculations have therefore been omitted.

This amplifier design has been based on a FET unilateralized by the method outlined in the box. However, the unilateralizing admittance, Y_N , has not yet been specified. Y_N must be inductive since y_{rs} is imaginary and negative:

$$y_{rs} = y_{rs} = -j0.42 \times 10^{-3}$$
. (32)

The required inductor is thus computed from: $L_N = 1/\omega Y_N$

$$=1/(2\pi \times 10^8 \times 0.42 \times 10^{-3}) = 3.8 \ \mu H.$$
 (33)

The complete schematic diagram of the 100-MHz, unilateralized common-source amplifier appears in Fig. 6. Note that a 100-pF capacitor has been added in series with L_N .

Test data compared with design goals

An amplifier was constructed with the values of Fig. 6. The 7.32-pF input capacitor and the 8.15-pF output capacitor, since they are within 2% of the design values, provide the correct impedance transformation for the input and output networks.

The amplifier was designed to have a 5-MHz bandwidth; however, only 3MHz was obtained in the test amplifier (see Table 3) because of the nature of the unilateralizing scheme. Unilateralization is achieved by neutralizing the effect of the reverse transfer admittance, y_{rs} , with an inductance, L_N . L_N is adjusted to form a high-Q resonant circuit with y_{rs} at 100 MHz (y_{rs} is capacitive at 100 MHz). The resulting unilateralization is good at 100 MHz, but is less so at slightly higher or lower frequencies. A narrowbandwidth neutralizing circuit in turn limits the over-all bandwidth of the amplifier. This narrowband effect can, however, be eliminated by employ-



2. **Unilateralized common-source FET amplifier** configuration shows the meaning of unilateralization: decoupling of input-output. Component values are computed in Eqs. 16 through 33.







4. **50-ohm output impedance requirement** is met by a simple impedance transformation step. See Eqs. 19 through 22.



5. **100-MHz narrow-band equivalent output circuit** of a FET amplifier shows some of the values for the circuit of Fig. 3.



6. Complete schematic of the 100-MHz, unilateralized, common-source FET amplifier summarizes the design procedure. Note the shielding between input and output circuits. Gain can be adjusted over almost 50 dB by varying V_{GS}.



7. Large gain variation can be obtained. Any gain value is stable due to the unilateralization (C = 0).

Table 3: Test results

	Design goals	Test results
Power gain	18 dB	18.5 dB
Bandwidth	5 MHz	3 MHz
Noise figure	2 dB	1.5 dB



8. 0.5-MHz shift in the center frequency occurs as gain varies from minimum to maximum. This is due to the change in the gate-to-source capacitance caused by the change in V_{cs}.

ing a more complex, wide-bandwidth unilateralizing scheme.

Wide gain control is possible

The unilateral FET amplifier has several other interesting properties. For example, almost 50 dB of power gain reduction is achieved by decreasing the gate-to-source voltage, V_{GS} , from zero volts to -3.2volts (the pinch-off voltage of the 2N4416 FET employed in the test circuit). This ability to vary the gain over such a wide range gives the designer more freedom in applying agc. This gain-voltage characteristic is illustrated in Fig. 7.

The amplifier's center frequency, f_o , and 3-dB frequencies are plotted as functions of gain in Fig. 8. This curve reveals a 0.5-MHz shift in center frequency as gain varies from maximum to minimum. This frequency shift is due to the change in gate-to-source capacitance caused by a change in gate-to-source voltage. The result is a slight change in the resonant frequency of the input tuned circuit.

The unilateralized common-source amplifier provides better noise performance than an amplifier without unilateralization. Moreover, the unilateralized common-source configuration has better noise characteristics than either cascode or common-gate amplifiers employing the same 2N4416 transistors. The one drawback to unilateralization, in fact, is that it can be a tedious, time-consuming adjustment. With careful design and construction procedures, however, fixed component values may be used in the unilaterizing circuit, thus eliminating special adjustments.

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If your circuit calls for an operational amplifier, you have a noise problem. But there is no need to worry just because the noise specification for the amplifier seems high; the high gain of an operational amplifier steps up the signal as well as the noise. The way to minimize the noise is to match the source resistance to the optimum noise impedance of the operational amplifier.

Know the basics before beginning. The design will be better if you understand what causes noise, how noise figures are derived, why test circuits can help, and how the source-resistance optimization procedure works.

Considerable time will also be saved with the aid of a nomogram to help calculate the amplifier noise figure on the basis of the manufacturer's specifications.

Examine various noise sources

Ideally, in a signal-amplifying device, the signal is assumed to enter the device undisturbed by noise. Thus the limit placed on the sensitivity of the amplifier is set by the noise it introduces itself. A useful measure of the noise internally introduced by an amplifier is the noise factor, defined as the quotient of the noiseless amplifier signal-tonoise ratio over the actual amplifier signal-tonoise ratio:

$$F = \frac{(S/N) \text{ noiseless amplifier}}{(S/N) \text{ actual amplifier}}.$$
 (1)

Transistorized operational amplifiers for lownoise applications are very often discounted by designers after a cursory examination of the specifications of available devices. That is to say, the specifications quite often give a figure for noise voltage that, taken by itself, may look very high. For example, a typical figure may be 10 μ V. When this figure is considered along with techniques for optimizing noise performance, however, the noise introduced by such an amplifier is quite small. In some devices, notably those using field-effect transistor input stages, noise perform-

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ance is equal or superior to that obtainable with vacuum tubes.

Sources of noise originating outside the circuit can be reduced to tolerable levels by careful shielding, grounding, and filtering; but the noise generated in the amplifier and the surrounding circuitry—or, for that matter, in the signal source itself—creates a much more difficult problem. Transistorized amplifiers generate thermal noise (broad-band or white), flicker noise (1/f), shot noise, and partition noise. Generally, the amplifier manufacturer will optimize his circuit design to give the best results commensurate with cost and general-purpose requirements, but certain tradeoffs and component selections can further reduce noise in critical applications.

Understand noise figure

λ

In the case where the input noise to the amplifier is thermal noise generated in a signal-source resistance, the amplifier noise factor, F, may be defined in terms of input and output signal-to-noise ratios:

$$S_o/N_o = (1/F) (S_i/N_i),$$
 (2)

where:

 $S_o =$ signal power output of the amplifier,

 $S_i =$ signal power input to the amplifier,

 $N_o =$ noise power output of the amplifier,

 N_i = noise power input to the amplifier.

Noise factor is usually expressed in decibels and termed noise figure:

$$VF(dB) = 10 \log_{10} F.$$
 (3)

Alternatively, noise figure may be expressed in terms of rms signal and noise voltages as:

 $NF (dB) = 10 \log_{10} [(E_i/N_i)^2 / (E_o/N_o)^2],$ (4) where:

 $E_i = \text{rms signal voltage input to amplifier},$

 $E_o = \text{rms}$ signal voltage output from amplifier,

 $N_i =$ rms noise voltage input to amplifier,

 $N_o =$ rms noise voltage output from amplifier.

If the voltage gain of the amplifier, $A = E_o/E_i$, is substituted into Eq. 4, the equation reduces to:

NF (dB) = 10 log₁₀[(1/A²)(N_o/N_i)²]. (5) The rms thermal-noise voltage generated by a



1. The effective noise bandwidth of an operational amplifier is defined as the rectangular power pass-band equivalent to the actual power pass-band, as shown in the amplifier gain-vs-frequency plot.

source resistance, R_s , is:

$$N_i = (4 \ k \ T \ b \ R_s)^{1/2}, \qquad (6)$$

where:

 $k = \text{Boltzmann's constant (1.38 \times 10^{-23} \text{ J/}^{\circ}\text{K})},$

T =temperature (°K),

b =noise bandwidth (Hz),

 $R_s =$ source resistance (Ω).

At room temperature $4 k T = 1.6 \times 10^{-20}$ joules. Substituting this expression for N_i into Eq. 4 yields:

 $NF (dB) = 10 \log_{10} [(N_o/A)^2/4 k T f_B R_s].$ (7)

The noise bandwidth in Eq. 7 is now an effective noise bandwidth, f_B , to be defined. The term N_o/A is the output noise voltage referred to the input and may be considered an equivalent rms input noise voltage, e_i .

Examine effective noise bandwidth

The effective noise bandwidth of an amplifier is defined as the rectangular power pass-band equivalent to the actual power pass-band of the amplifier (Fig. 1), or:

$$f_B = (1/G_0) \int_0^\infty |G(j\omega)| df, \qquad (8)$$

where:

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2. Simple amplifier noise test circuit can be put together quickly. Care must be taken to use proper lead and inputoutput shielding to avoid hum pickup. The $5000 \cdot \mu F$ capacitor at the input gives a very low cutoff frequency.

 f_B = effective noise bandwidth,

- G_0 = amplifier power gain at dc,
- $|G(j\omega)| =$ magnitude of amplifier power gain as a function of frequency.

In Fig. 1, amplifier voltage gain is down 3 dB at frequency f_{H} . This frequency corresponds to the half-power point (-6 dB) of the power passband. When an amplifier is compensated for a 6 dB/octave rolloff, or filtered externally at 6 dB/octave with a simple RC network, the voltage gain as a function of frequency is:

$$A(j\omega) = A_0 / [1 + j(\omega/\omega_H)], \qquad (\omega = 2\pi f)$$
(9)

where A_0 is the dc voltage gain. Squaring this equation gives an expression proportional to the power gain as a function of frequency. Substituting this result into Eq. 8 and evaluating the integral give:

$$\begin{split} f_{B} &= \int_{0}^{\infty} |1/[1+j(\omega/\omega_{H})]|^{2} df \\ &= (\omega_{H}/2\pi) \int_{0}^{\infty} [d(\omega/\omega_{H})]/[1+(\omega/\omega_{H})^{2}] \\ &= (\omega_{H}/2\pi) (\pi/2) = (\pi/2) f_{H}. \end{split}$$

Thus the effective noise bandwidth for an amplifier rolled off at 6 dB/octave is $\pi/2$ times the 3-dB frequency of the voltage response.

Having defined the effective noise bandwidth



3. An equivalent circuit of an actual amplifier (a) can be constructed by including voltage and current noise sources (b).





4. **Plots of NF vs source resistance** indicate the difference between operational amplifiers with FET (a) and bipolar (b) input stages. While these curves were obtained with

the next step is to look at some of the noise voltage measurement methods and techniques.

Measure amplifier noise voltage

A simple test circuit for measuring broad-band amplifier noise voltage output is shown in Fig. 2. The 5000- μ F capacitor gives a very low cutoff frequency that may be considered as dc for most measurements. Filter components C_f and R_f are selected to attenuate the output signal —3 dB at the desired bandwidth, with further attenuation at 6 dB/octave beyond this point. Dividing the measured output voltage by the circuit gain (1000, in this case) gives the equivalent noise voltage, e_i . The expression for the noise figure then becomes :

$$NF (dB) = 10 \log_{10}(e_i^2/4 \ k \ T \ f_B \ R_S). \quad (10)$$

Extreme care must be taken when making the noise measurements. The test circuit should be well-shielded to avoid such things as hum pickup, input-to-output coupling and long leads that may induce measurement errors. When large source resistances are used, stray capacitance shunting R_s (not taken into account in Eq. 10) will cause attenuation of the high-frequency components of the noise voltage. In fact, this may even result in seemingly negative noise figures, which are physically impossible by definition.

In general, the techniques used in breadboarding any high-frequency circuit should be used.

specific units, the results apply generally to a variety of operational amplifiers. Note the big improvement in the noise figure in (a).

Only the most obvious few have been mentioned above. An additional one would be to provide good, solid grounds.

Use a noise-equivalent circuit

The evaluation of noise in operational amplifiers may be carried a step further without resorting to investigaton of the inner workings of the amplifier. Figure 3 shows a noisy amplifier with an input terminated in a source resistance R_s and an equivalent circuit consisting of an ideal, noiseless amplifier and two noise generators, $\overline{e_N}^2$ and $\overline{i_N}^2$.

Noise generators $\overline{e_N}^2$ and $\overline{i_N}^2$ represent mean square noise voltage and current, both proportional to noise power. They are not entirely independent of one another, but the results will not be seriously impaired if it is assumed that they are.

Since the input impedance of the ideal amplifier is infinite, the total noise power in the input circuit is proportional to i_{T^2} of Fig. 3, which consists of the contributions from the noise generators and the thermal noise in R_s :

 $\overline{i_r^2} = (4 \ k \ T \ f_B/R_S) + (\overline{e_N^2}/R_S^2) + \overline{i_N^2}.$ (11) The total rms input noise voltage is then:

$$(\overline{e_T}^2)^{1/2} = R_s(\overline{i_T}^2)^{1/2}$$

$$= (4 k T f_B R_S + \overline{e_N^2} + \overline{i_N^2} R_S^2)^{1/2}.$$
 (12)

The equivalent input noise voltage of an op-

erational amplifier at any given bandwidth, then, can be completely characterized with only two measurements: $(\overline{e_N}^2)^{1/2}$ is found by measuring the output noise when $R_s = 0$; and $(\overline{i_N}^2)^{1/2}$ is determined by measuring the output noise with R_s very large.

Equation 12 may be substituted into the expression for noise figure in Eq. 10, and an optimum source resistance can be found by taking the derivative with respect to R_s and setting the result equal to zero:

$$NF (dB) = 10 \log_{10} \{ [(e_T^2)^{1/2}]^2 / 4 k T f_B R_S \}$$
(13)
= 10 log_{10} [1 + ($\overline{e_N}^2 / 4 k T f_B R_S$)
+ ($\overline{i_N}^2 R_S / 4 k T f_B$)]. (14)

Differentiating Eq. 14 with respect to R_s , and letting the derivative equal zero yield the value of $R_{s(opt)}$:

$$R_{S(opt)} = (\overline{e_N^2}/\overline{i_N^2})^{1/2}.$$
 (15)

If the amplifier input is terminated in $R_{s(opt)}$, the best possible signal-to-noise ratio will be obtained. Experimentally, $R_{s(opt)}$ is found to be much less than the input impedance of the amplifier; it is independent of closed loop gain and is somewhat dependent on the effective noise bandwidth.

Optimize noise performance

The curves in Figs. 4a and 4b show typical noise performance of two general-purpose operational amplifiers. The model 1552 has FETs in the input stage; model 1506 has a bipolar transistor input stage. The curves apply at an ambient temperature of 25°C. The two types of input stages afford the designer considerable latitude in choosing the correct amplifier for a given application. The FET type is ideally suited for source resistances in the range of 100 k Ω to 1 M Ω , while the bipolar-transistor type gives good results with smaller source resistances between 10 k Ω and 100 k Ω . In applications where the source resistance may be chosen sonmewhat arbitrarily, it should be matched to $R_{S(opt)}$ of the amplifier for best noise performance. The selection of the bridge resistance of a strain-gauge transducer is an example. When the source impedance is fixed and amplifier response down to dc is not required, the usual alternative is to use a coupling transformer with a turns ratio of $n^2 = R_{s(opt)}/R_s$ to give optimum noise figure.

Nomogram gives NF at a glance

The nomogram in Fig. 5 is useful for rapid calculation of amplifier noise figure at 25°C, given the effective noise bandwidth, source resistance and equivalent input noise voltage. Alternatively, if the noise figure is known, the magnitude of the input noise voltage may be determined for variable source resistances and effective bandwidths. As an example, assume the measured equivalent

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5. Quick check of an amplifier NF can be made by using this nomogram, if the effective noise bandwidth, source resistance and equivalent input noise voltage are known.

input noise voltage of an amplifier, operating with a 10-M Ω source resistance and 100-Hz noise equivalent bandwidth, is 100 μ V rms. To determine the noise figure, connect a straight line between 100 Hz on the bandwidth scale and 10 M Ω on the source resistance scale. The point of intersection of this line and the pivot line is then connected with a straight edge to 100 μ V rms on the noise-voltage scale. The resulting straight line passes through 28 dB, which is the approximate noise figure. A detailed calculation of the noise figure for this example yields 27.96 dB.

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Get bargains in power transistors.

The tricks of the trade extend far beyond just looking for quantity breaks.

Saving money on power transistors is not just a matter of squeezing pennies through quantity buying, purchase agreements and similar trade practices. It also requires good circuit design and proper specification of the best available device, based on actual measurements instead of wishful thinking.

There are pitfalls to avoid when specifying power transistors. Take care to know them, and you may find yourself buying a perfect device for \$5 instead of \$50. Here are the rules to follow.

Consider device parameter trade-offs.

Compatibility of all desirable transistor characteristics is an ideal situation that is seldom realized. In actual practice, designs that optimize one or two groups of parameter usually conflict with other parameters. For example, the combination of semiconductor material resistivity, base width and dopant that gives high voltage and good safe operating area also results in relatively low gain. As indicated in Fig. 1, gain, frequency response, voltage, current capability, and cost must be balanced against each other in device design. Every transistor on the market represents one of many possible compromises. The user must decide which parameter, or parameters, are most important for an application and then select a device that is adequate in all respects, but which emphasizes these.

The thing to keep in mind is that the additional effort to optimize a circuit during the design stage is well justified by the possible savings that will accrue in the quantity production.

Don't specify nonexistent devices

Once a design has been settled, the transistor is essentially specified. When transistors first became available, many engineers started to use them with the same design approach that they took with tubes. That is, they would design for an arbitrary device and then look for a unit to meet the required speci-

Merrill Palmer, Applications Engineer, Motorola Semiconductor Products, Phoenix, Ariz. fications. All too frequently the result was a requirement for a high-voltage, high-gain transistor that was very costly, if available at all. Much better practice is to start the design with a survey of existing transistors and their availability in the required quantities.

To satisfy high-voltage or high-current requirements, for instance, the possibility of a series or parallel arrangement of lower-rated transistors should be considered. Frequently, slight circuit modifications will reduce total costs by permitting the replacement of a premium device with two or more less expensive ones.

Avoid an arbitrary requirement

Silicon transistors have been widely acclaimed for their high temperature capability, low leakage and high voltage. In fact, this acclaim has been so general that the sales appeal of "all-silicon" systems has prompted many manufacturers to disregard germanium power transistors for many applications. But germanium power transistors do have their advantages in many cases. In fact, despite silicon's appeal,

Table: Power transistors:germanium vs silicon

Germanium advantages	Silicon advantages
Lower V _{CE}	Higher voltages
Higher current capability	Lower leakage (I _{CBO})
Lower V BE (high g FE)	Higher operating temperature
Higher gain at most current levels	Good thermal stability
Lower prices	Higher frequency and faster switching
Good availability and delivery	Npn and pnp configurations

the 1966 sales volume of germanium power transistors was greater than that of silicon power devices. Some of the reasons for this are summarized in the Table.

Don't overlook the many standard devices

When a semiconductor manufacturer successfully designs a transistor for a specific but broad market, an EIA-registered or "in-house standard" device is normally established, with specifications which best represent the product. Good factory volume, standard testing and reasonable yields lead to low price and wide availability, which are important arguments in favor of selecting standard types. Standard types are usually stocked at the factory, and frequently at local electronics distributors as well. Second sources often exist for many standard types.

Don't plump for a special in haste

Special devices have their place, too. These include premium selections, substandard specials, and mechanical specials. Premium selection refers to devices which are standard types except for some additional testing, such as a higher minimum or lower maximum limit on some parameter, the addition of a parameter not covered by the standard specification, or matched pairs. For example, assume that the selection criterion for a device indicates the 2NXXX2, 2NXXX3, 2NXXX4, series, which have BV_{CEOS} of 50, 75, and 100 volts, respectively. If a BV_{CEO} of 120 volts minimum is required, perhaps a factory screening of 2NXXX4s could be arranged to provide premium-selection specials with the required BV_{CEO} . Some price increase would probably be necessary to pay the cost of the extra factory testing. If BV_{CEO} of 90 volts minimum is adequate, it may be less expensive to purchase a 2NXXX3 selected for 90-volt minimum BV_{CEO} than to purchase a 2NXXX4.

Don't overlook bargains in substandard devices

If the application in question does not require all the parameters or limits of a standard type of device, it may be possible to cut costs by relaxing the standard specification in some parameter. Because of the many variables of transistor production, a manufacturer may have a quantity of devices which are otherwise good transistors, but which fail to meet every specification of the standard. Such "fall-out" devices may be of limited marketability and therefore available at considerable savings.

Check the production yield of specials

The price tag of special devices is strongly influenced by the number of them that is yielded from the source device or product line. Histograms of raw line data for BV_{CEO} and h_{FE} of a repre-



1. **Parameter trade-offs** must be considered in specifying a power transistor. Time spent on circuit design and preparation of an "easy" specification will be well rewarded by the price of the transistor.



2. A typical distribution of a number of power transistors vs. collector-emitter breakdown voltage indicates that, to select 100 transistors with BV_{CE0} of 80 volts minimum, over 3300 devices would have to be tested.



3. Specifying a 1:1.25 ratio of minimum to maximum h_{FE} without restricting the actual values of h_{FE} indicates that a lower price could be obtained by buying devices with 40« h_{FE} «50 than in the range of 20 and 25, or 80 and 100.

sentative silicon-power-transistor line are shown in Figs. 2 and 3. From Fig. 2, the yield of this product to a BV_{CEO} greater than 80 volts is seen to be 3%, while 98% of the devices exhibit BV_{CEO} of 40 to 80 volts. To select 100 devices having a minimum BV_{CEO} of 80 volts will require testing over 3300 devices, compared with testing just over 100 devices if a BV_{CEO} of only 40 volts is required. For additional parameters, more testing is required. Thus the number of devices that have to be tested can be very large indeed if the yield to several parameters is low. From the standpoint of test costs alone, then, a low-yield special selection will be more expensive than one of high yield. Furthermore, if a manufacturer has no prospect of profitably disposing of the extra devices that he must produce to supply a given quantity of a low-yield special device, he may find it necessary to reflect this risk in his price. Obviously, the buyer of the device will ultimately pay for the luxury of specifying such a low-yield unit reflecting careless circuit design.

Don't hobble the manufacturer with rigid specs

Yield also comes into play in the specification of the limits that are subject to the designer's control. Suppose, for example, that a 1:1.25 ratio of minimum to maximum h_{FE} is desired, but that there is some room for choice of the actual values. From Fig. 3 it can be seen that under these conditions yield is maximized by specifying an h_{FE} of 40 to 50, and that yield is lower for h_{FES} of 20 to 25, or 80 to 100.

Don't call out incompatible parameter values

Another potential yield problem in specifying special devices is the interdependence of many parameters. Care must be taken not to specify nearly impossible combinations. Figure 4 shows by scatter plot the interdependence of h_{FE} at 20 amps and h_{FE} at 5 amps for a given power-transistor line. Approximate limits of correlation are shown by the dashed envelope. Very poor yield would be expected from a combined specification of $h_{FE} = 30$ to 60 ($I_C - 5$ A, $V_{CE} - 2$ V) and $h_{FE} \ge (I_C - 20$ A, $V_{CE} - 2$ V). As shown by the shaded area of Fig. 5, these two specifications are almost mutually exclusive within the envelope of correlation.

Don't ignore the factory representative

Perhaps the paramount rule to observe in considering the electrical parameters of devices that are premium or substandard specials is frankly and openly to discuss actual needs with the semiconductor salesman. His knowledge of his entire product line and access to information on factory yields, test costs, and "fall-out" inventory enables



4. A high price can be paid by the designer specifying contradictory parameters. In the graph above, very poor yield occurs for devices that have a combined specification of $h_{FE} = 30$ to $60 (I_c = 5 \text{ A}, \text{V}_{CE} = 2\text{V})$ and $h \ge 25 (I_c = 20 \text{ A}, \text{V}_{CE} = 2 \text{ V})$.

him to recommend the best device for the need, be it special or standard.

Try to save on assembly costs

Mechanical rework such as shortened leads, attachment of solder lugs or lead extensions, is usually available at a price, if it can be accomplished on completely assembled devices after they have been screened to the electrical specification. Additional expense associated with such mechanical specials may be more than offset by savings in assembly costs, for instance. The buyer should remain aware, however, that such modification may make it impossible for the manufacturer to market the devices elsewhere, should the buyer alter his design or place an excess order.

Hold down the testing, if possible

The cost of quality assurance and reliability testing must ultimately be reflected in the selling price of transistors. This cost is a function of the nature and amount of testing required. Compared with military specifications, money can usually be saved on industrial quality controls by permitting less than 100% processing, taking smaller samples, and making shorter life tests. Manufacturers' reliability programs can sometimes be used to bypass the high cost of extensive life and environmental testing as a means of reliability assurance. The specifier of industrial power transistors favors his pocketbook when he avoids unrealistic quality assurance requirements.

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Fig. 3 Standard Class 77 Relay in Socket with printed circuit terminals. Fig. 4 Standard Class 77 Relay mounted in a panel for solder connected wiring. Fig. 5 Special Class 77 Relay with printed circuit terminals Fig. 6 Standard Class 77 Relay with special mounting bracket

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Fig. 2 Standard Class 77

Relay in Socket with

solder terminals (same combination as

shown in Fig. 1)



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Reduce delay distortion at the source

in amplifiers, and end the need for complicated phase equalizers. Trouble usually lies in 3 circuit areas.

Delay distortion is a villain in an amplifier. It can wreck the fidelity of signals in analog equipment; it can cause time echoes, or ghosts, in video systems. Many engineers build complicated phase equalizers to counteract the trouble. But there is a simpler, more economical way to go about it: track the villain to its lair.

By analyzing three major areas in the amplifier and changing components that should not have been installed in the first place, you can usually cut delay distortion to a tolerable minimum.

This analytical approach is not hard to master. To understand it, examine a typical common-emitter amplifier, since this configuration contains all the sources of phase distortion in any amplifier.

But, first, how low should delay distortion be before it is considered tolerable? The answer depends on the precise way in which the circuit is being used and the types of signal being processed. In present voice-channel signal-processing equipment, attempts are being made to hold over-all delay distortions to 300 to 500 μ s. Since the bulk of the distortion is usually caused by various types of filters, such as multiplexing and single-sideband filters, the amount that can be tolerated in all amplifiers of the system is generally limited to somewhere around 50 to 100 μ s. Thus a lowdistortion amplifier might have only 5 or 10 μ s of delay distortion.

Phase vs frequency should be linear

The usual way to check phase distortion is to analyze the phase-vs-frequency function. Find the difference between the maximum and minimum phase slopes as follows:

$$(\partial \phi / \partial \omega)_{max} = \tau_{max},$$

$$(\partial \phi / \partial \omega)_{min} = \tau_{min}.$$

$$(1)$$

The difference is:

$$(\partial \phi / \partial \omega)_{max} - (\partial \phi / \partial \omega)_{min} = \tau_{max} - \tau_{min} \leq \Delta \tau_{max}.$$
 (2)

The expression $\partial \phi / \partial \omega = \tau$ is commonly known as the group delay. Group-delay distortion refers to variations in τ with frequency. These variations in the delay distort the waveform. In Eq. 2, $\Delta \tau_{max}$ is the allowable group-delay distortion. A positive value of $\Delta \tau$ is actually a group advance; negative values, however, represent delay.

Three simple circuits that can have a significant effect on τ are found in the popular R-C-coupled common-emitter transistor amplifier (Fig. 1). But the data apply to all similar R-X networks. For example, the R-C low-pass and high-pass data are equally valid for R-L networks.

Three circuits cause distortion

The three sections of circuitry that contribute to delay distortion in this amplifier are the emitter (Fig. 2a); the high-pass filter, formed by the coupling capacitor and the load resistor (Fig. 2b); and the low-pass filter, formed by the shunt capacitance and the collector and load resistors in parallel (Fig. 2c).

The delay distortion caused by R-C low-pass and high-pass circuitry (Figs. 2b and 2c) is identical. The transfer functions in each case are:

(Low pass)

$$e_o/e_{in} = [1/j\omega C]/[R + (1/j\omega C)]$$

= 1/(1+j\omega CR); (3)

(High pass)

$$e_o/e_{in} = R/[R + (1/j\omega C)]$$

= $j\omega CR/(1+j\omega CR)$. (3a)

The phase is:

$$\phi_l = -\arctan \omega CR, \qquad (4)$$

$$\phi_h = \pi/2 - \arctan \omega CR. \tag{4a}$$

Differentiate the phase to obtain the group delay: $\tau_l = \partial \phi / \partial \omega = - \{RC / [1 + (\omega CR)^2]\};$ (5)

$$\tau_h = \partial \phi / \partial \omega = - \{ RC / [1 + (\omega CR)^2] \}.$$
 (5a)

Let $RC = 1/\omega_0$:

$$\tau = -\{1/\omega_0\}\{1/[1+(\omega/\omega_0)^2]\} = -\{1/\omega\}\{[\omega/\omega_0]/[1+(\omega/\omega_0)^2]\}.$$
 (6)

Equation 6 can be plotted in two ways. One method is based on values for $\omega_{0\tau}$, using the middle term in Eq. 6 (see color curve in Fig. 3); the other way is based on ω_{τ} (the black curve in Fig. 3), using the last term in Eq. 6.

To determine the distortion at various frequencies for a given filter, use the curve for $\omega_0 \tau$. To determine the constants that yield a desired response at a given frequency, use the curve for

Jerome H. Horwitz, Staff Engineer, TRW Electronic Components Div., Camden, N. J. *Work was done at General Atronics Corp., Philadelphia.

 ω_{τ} . Remember to consider all frequencies in the band and not just those at the band edge.

The third source of delay distortion is the emitter circuitry (Fig. 2a). Its effect may be computed by analyzing its input impedance. The current in the collector is:

$$I_c \angle \theta = i_c = e_{in}/Z, \qquad (7)$$

and its phase angle is: $\theta = - \arctan$

$$= -\arctan(\mathrm{Im}|Z|/R_e|Z|). \tag{8}$$

Now the impedance may be read off from the circuit diagram as:

$$Z = R + \{ [R_E(1/j\omega C)] / [R_E + (1/j\omega C)] \}$$

$$= (R + R_E + j\omega CRR_E) / (1 + j\omega CR_E).$$
(9)

The phase angle of the impedance is:

 $\phi = -\{ \arctan[\omega CRR_{E}/(R+R_{E})] - \arctan \omega CR_{E} \}$ = arctan ωCR_{E} - arctan $[\omega CRR_{E}/(R+R_{E})]$. (10) Let:

$$R_E C = 1/\omega_a \tag{11}$$

and

 $[R_E R/(R_E + R)] C = 1/\omega_b = R/[(R_E + R) \omega_a].$ Notice that $\omega_b \ge \omega_a$. Then Eq. 10 becomes:

 $\phi = \arctan(\omega/\omega_a) - \arctan(\omega/\omega_b)$, (12) yielding the group delay:

$$\tau = \partial \varphi / \partial \omega = \frac{1}{1 + (\omega / \omega_a)^2} \frac{1}{\omega_a} - \frac{1}{1 + (\omega / \omega_b)^2} \frac{1}{\omega_b}.$$
(13)

There are three independent variables in Eq. 13: $\omega_{a\tau}$, $\omega_{b\tau}$ and ω_{τ} . Therefore normalizing τ with any one of the variables still leaves it necessary to plot a family of curves.

To plot these curves, Eq. 13 has first to be solved for the variable of interest. The three equations are:

$$\omega_a \tau = \frac{1}{1 + (\omega/\omega_a)^2} - \frac{\omega_a/\omega_b}{1 + (\omega_a/\omega_b)^2 (\omega/\omega_a)^2}; \quad (14)$$

$$\omega_{b}\tau = \frac{\omega_{b}/\omega_{a}}{1 + (\omega_{b}/\omega_{a})^{2} (\omega/\omega_{b})^{2}} - \frac{1}{1 + (\omega/\omega_{b})^{2}}; \quad (15)$$

$$\omega \tau = \frac{\left[\omega/\omega_{b}\right] \left[(\omega_{b}/\omega_{a}) - 1\right] \left[1 - (\omega_{b}/\omega_{a}) (\omega/\omega_{b})^{2}\right]}{\left[1 + (\omega_{b}/\omega_{a})^{2} (\omega/\omega_{b})^{2}\right] \left[1 + (\omega/\omega_{b})^{2}\right]}.$$
(16)

The curves for $\omega_{a^{\tau}}$ are plotted in Fig. 4; the curves for $\omega_{b^{\tau}}$ in Fig. 5; and those for ω_{τ} in Fig. 6.

The errors of approximation

At the expense of precision, the delay distortion may be determined quickly by approximation.

For the high- and low-pass filters, the result will be high by less than 1% if ω/ω_0 is smaller than 0.1 or larger than 10. Following are the approximations:

$$\lim_{(\omega/\omega_{n})\to 0} \omega_{0} \tau = -1, \qquad (17)$$



1. The R-C-coupled common-emitter transistor amplifier includes all troublesome circuitry that causes delay (or phase) distortion in amplifiers.



2. The three causes of delay distortion are the emitter (a), the high-pass filter, formed by the coupling capacitor and the load resistor (b), and the low-pass filter, formed by the shunt capacitor and the collector and load resistors in parallel (c).



3. Delay distortion of the high-pass and low-pass filters. One curve (color) represents the distortion in terms of ω_0 , where $\omega_0 = 1/RC$; it should be used to determine the distortion at various frequencies of a given filter. The other curve (black) is based on a given frequency, ω ; it is useful when the constants that yield a desired response at a given frequency must be found.

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4. The delay distortion of the emitter circuitry (a) is normalized with respect to $\omega_{\rm a}$ (or $1/R_{\rm E}$ C). An expanded view of the zero crossover area is shown in (b). The emitter resistor, R_E, is bypassed by C, as shown in Fig. 2.



5. Delay distortion is normalized with respect to $\omega_{\rm b}$, which includes the emitter resistor, R, the bypassed emitter resistor, R_E, and the bypass emitter capacitor, C.

$$\lim_{(\omega)\to\infty} \omega_0 \tau = -(\omega_0/\omega)^2, \qquad (18)$$

$$\lim_{(\omega_{0})\to 0} \omega_{\tau} = -\omega/\omega_{0}, \qquad (19)$$

$$\lim_{(\omega/\omega_0)\to\infty} \omega_{\tau} = -\omega_0/\omega.$$
(20)

For the emitter circuitry three groups of approximations may be devised.

(ω

(α

(

1. The solution will be low by less than 3% if ω/ω_a is smaller than 0.1 or larger than $10(\omega_b/\omega_a)$ with these approximations:

$$\lim_{(\omega/\omega_a)\to 0} \omega_{a\tau} = [(\omega_b/\omega_a) - 1]/(\omega_b/\omega_a), \quad (21)$$

$$\lim_{(\omega/\omega_a)\to\infty} \omega_{a\tau} = -\{[(\omega_b/\omega_a)-1]/(\omega/\omega_a)^2\}. (22)$$

2. The values will be low by less than 3% for $\omega_b/\omega < 0.1$ with the approximation:

$$\lim_{(\omega_b/\omega)\to 0} \omega_{\tau} = (\omega_b/\omega) [(\omega_a/\omega_b) - 1].$$
(23)

3. If (ω_a/ω_b) $(\omega_b/\omega) > 10$, the following approximations will yield errors of less than 3%:

$$\lim_{(\omega_b/\omega)\to\infty} \omega\tau = \frac{1-(\omega_a/\omega_b)}{(\omega_b/\omega)(\omega_a/\omega_b)},$$
 (24)

Then there are some simplifications that cannot be easily grouped by tolerances. These, however, help the designer to assess the trends associated with their limits.

$$\lim_{(\omega_{h}/\omega_{a})\to 1} \omega_{a\tau} = 0, \qquad (25)$$

$$\lim_{(\omega_{b}/\omega_{a}) \to \infty} \omega_{a\tau} = 1/[1+(\omega/\omega_{a})^{2}], \qquad (26)$$

$$\lim_{(\omega_{a}/\omega_{b})\to 0} \omega_{\tau} = -\{(\omega_{b}/\omega)/[1+(\omega_{b}/\omega)^{2}]\}, \quad (27)$$

$$\lim_{(\omega_a/\omega_b)\to 1} \omega_\tau = 0.$$
(28)

Wide-band design starts at collector

Suppose that an amplifier stage is to be built with delay distortion of 25 μ s or less between 250 and 6000 Hz. Assume a total stray and shunt capacitance of 30 pF, a load of 5000 ohms, a desired voltage gain of 10, and a total dc emitter resistance of 2000 ohms.

The collector circuitry should be designed first, because it will normally cause lower delay than the emitter circuitry. Designing the collector area first will also leave the engineer with a more realistic goal for the emitter design.

The delay in the collector circuit will be limited to 5 μ s at each end of the band. This is an arbitrary choice, based on the fact that, since there is voltage gain, the emitter resistance will be considerably less than that in the collector and this will make it more difficult to reduce delay distortion at the lower band edge. The low-pass



6. Delay distortion is normalized with respect to the frequency, ω (a). The zero crossover region is expanded in (b). The curves in both figures are plotted with the

aid of Eq. 16. Point C in (b) represents one step in the design example worked out in the text. It yields the frequency, f_b , which is needed to find capacitor C.

and high-pass circuitry will be treated separately.

For the low-pass circuitry, $\omega = 2\pi \times 6000$ Hz. Therefore:

 $\omega_{\tau} = 2\pi \times 6000 \times 5 \times 10^{-6} = 0.06\pi = 0.188.$

From Fig. 3 (point A), this normalized delay can be seen to occur for a normalized frequency of:

$$\omega/\omega_0 = 0.195.$$

This gives:

 $\omega_0 = \omega/0.195;$ $f_0 = f/0.195 = 6000 \text{ Hz}/0.195 = 30,769 \text{ Hz}.$

Since f_0 is the frequency at the 3-dB points, the low-pass cutoff must be greater than 30,769 Hz. The 5-k Ω load and the 30-pF capacitor give a cutoff ($R = 1/\omega C$) of approximately 1 MHz—a wide margin of safety.

For the high-pass case, $\omega = 2\pi \times 250$ Hz; hence: $-\omega_{\tau} = 2\pi \times 250 \times 5 \times 10^{-6} = 0.0025\pi = 0.0078.$

This value of normalized delay occurs outside the range of the graph in Fig. 3. Since high-pass is being dealt with, the desired value lies off scale to the right (see point *B*). Therefore the approximation of Eq. 20, which is valid for a large ω/ω_0 , is used:

$$\lim_{(\omega/\omega_{o})\to\infty}\omega_{\tau}=-\omega_{o}/\omega=-1/(\omega/\omega_{o}).$$

Thus:

 $\omega_0/\omega = -\omega_{\tau} = 0.0078;$ $\omega_0 = 0.0078\omega;$ $f_0 = 0.0078f = 0.0078 \times 250 = 1.95 \text{ Hz}.$

The high-pass cutoff must be less than 1.95 Hz then. To meet this requirement with a 5-k Ω load, the capacitance must be calculated:

 $R = 1/\omega C = 1/(2\pi f C);$ $C = 1/(2\pi f R) = 16.4 \ \mu F.$

A 22- μ F off-the-shelf capacitor may be used.

Before proceeding to the emitter design, check the delay distoration in the collector circuitry.

At 250 Hz, the total delay is the sum of the high-pass and low-pass delays:

(High pass)

 $f_0 = 1/(2\pi RC) = 1/(2\pi \times 5 \times 10^3 \times 22 \times 10^{-6})$ $= 1000/220\pi = 1.448$ Hz; $f/f_0 = 250/1.448 = 172.7.$ From Eq. 20: $\omega_{\tau} = -1/172.7;$ $\tau = -1/(172.7 \times 2\pi \times 250)$ $= -1/(500\pi \times 172.7)$ $= -2000/172.7\pi = -3.7 \ \mu s.$ (Low pass) $f_0 = 1$ MHz; $f/f_0 = 250/10^6 = 0.00025.$ From Eq. 19: $\omega \tau = -0.00025;$ $\tau = (-0.25 \times 10^{-3}) / (2\pi \times 250) = -0.159 \ \mu s.$ Thus: $\tau_{total} = -3.9 \ \mu s.$

At 6 kHz, the delay is again the sum of the two: (*High pass*)

 $f/f_0 = 6000/1.448 = 4143.6.$ From Eq. 20:

 $\omega_{\tau} = -1/4143.6;$

 $\tau = -1/(4143.6 \times 2\pi \times 6000) = -0.0064 \ \mu s.$

(Low pass)

 $f_0 = 1$ MHz;

 $f/f_0 = 6 \times 10^3 / 10^6 = 6.10^{-3}.$

From Eq. 19: $\omega = -6 \times 10^{-3}$

$$\pi = -6 \times 10^{-7}$$

 $\tau = (-6 \times 10^{-3}) / (2\pi \times 6 \times 10^{3}) = 0.159 \ \mu s.$

Thus:

 $\tau_{total} = 0.165 \ \mu s.$

For R-C low-pass and high-pass circuitry, the magnitude of τ always decreases with increasing frequency (see Eqs. 5 and 5a). The minimum delay therefore occurs at 6000 Hz and the maximum at 250 Hz. The delay distortion thus becomes:

$$\Delta \tau = \tau_{max} - \tau_{min} = -0.165 - (-3.9) \\ = -3.7 \ \mu \text{s.}$$

This is well within the 5- μ s distortion permitted in the collector circuitry.

The last step is the design of the emitter. It can have up to 20 μ s of delay distortion at 250 Hz. Hence:

$$\omega_{ au} = 2\pi \times 250 \times 20 \times 10^{-6}$$

= 10⁴ \pi \times 10^{-6} = 0.0314.

To meet the specifications for gain and dc resistance, the two resistances in the emitter circuitry should have values of 500 ohms and 1500 ohms. The larger value is that of the bypassed resistor. The unknown factor is the bypass capacitance. Its value is found with Eq. 11:

$$\omega_a = 1/R_{\scriptscriptstyle E}C, \ \omega_b = (R_{\scriptscriptstyle E}+R)/R_{\scriptscriptstyle E}G$$

Then:

$$\omega_a/\omega_b = R/(R_E + R) = 500/2000 = 0.25.$$

Point C on Fig. 6b shows that $\omega_b/\omega < 0.042$. In this chart the values for the lower band edge can be found at the left-hand side, which corresponds to $\omega > > \omega_b$. Thus:

$$f_b < 0.042 \times 250 = 10.5$$
 Hz.

Then:

$$C = (R_E + R) / (R_E R \ 2\pi f_b) = 2.10^3 / (500 \times 1500 \times 2\pi \times 10.5)$$

Thus a $47-\mu F$, or perhaps a $100-\mu F$, unit could be used.

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ELECTRONIC DESIGN 19, September 13, 1967

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Voltage follower has high impedance can handle large signals

Figure 1a shows a unity-gain voltage follower which can be built easily and is useful for coupling high-impedance sources into low-impedance loads. The circuit consists of a differential amplifieremitter-follower combination with direct (unity) feedback to the inverting input of the amplifier. The collector of input transistor Q1 is also bootstrapped to the output. For signal input changes of a given direction, all signal points in the circuit move in the same direction by nearly the same amount. There are no antiphase voltage components in the circuit with respect to ground. Other characteristics are:

• Input impedance is over 50 M Ω at dc and low frequencies.

• Input capacitance is less than 0.5 pF. This value can be further reduced by placing the entire circuit in a shield can connected to the output.

• The offset voltage $(e_o - e_{in})$ can be adjusted to zero at zero volts input by trimming either R1 or R2.

• Q1 operates at $V_{CB} = 0$ V. The I_{CBO} for this transistor is thus zero, and the input current consists of only the bias current for Q1 plus $i_0/\beta_1\beta_3$. Q2 operates at $V_{CB} = 0.5$ V. The I_{CBO} term for this transistor is also negligible.

• Output impedance is 10 ohms for the values shown. If Q_1 and Q_2 are operated at $I_c = 50 \ \mu A$ (by reducing R_1 and R_2), output impedance can be as low as 2 ohms. It may be necessary to add C_1 to prevent high-frequency oscillations. The circuit



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Input impedance of 50 megohms at dc and low frequencies is obtained in this unity-gain voltage follower (a). For ac input coupling, two capacitors and two resistors should be added (b).

is useful to over 500 kHz. By replacing Q1, Q2, and Q3 with high-frequency types and increasing the current level, operation into the megahertz range can be achieved.

• Large signal-handling capability. Replacing Q3 with a high-voltage type and increasing the supply voltages allows the circuit to handle large voltage swings. Large negative excursions may be obtained by increasing the ratio R1/R2 at the expense of nonzero offset voltage. Q1 and Q2 are low-voltage types regardless of the signal level. If more output current is required, an additional emitter-follower can be inserted between Q2 and Q3. The circuit as shown requires a dc return for the input bias current. If ac input coupling is required, the input resistor can be bootstrapped to the output as shown in Fig. 1b. Open-loop gain measured 115 and the closed-loop gain measured $e_o/e_{in} = 0.995$.

Allan G. Lloyd, Project Engineer, Avion Electronics, Inc., Paramus, N. J.

Vote for 110

A/D multiplier/divider has UJT as relaxation oscillator

A simple multiplier/divider is based on the unijunction transistor as a relaxation oscillator. A simplified block diagram (Fig. 1) illustrates the
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ON READER-SERVICE CARD CIRCLE 63



1. Multiplier/divider circuit (a) operates as shown in the timing diagram (b).

basic principle. The inputs required are two consecutive gate pulses with durations respectively proportional to the two given quantities, and a third gate pulse equal to the sum of the other two. The output is a number of sawtooth pulses proportional to the product or ratio of the consecutive pulses.

During the quiescent period $(\langle t_1, \rangle t_2)$, capacitor C_1 is connected to the $+E_1$ line, and capacitor C_2 is grounded. During interval t_1 , C_1 is grounded through a constant-current circuit, so that its voltage drops linearly; at the end of interval t_1 , the residual voltage on C_1 is equal to E_1 less a voltage proportional to t_1 . During interval t_2 , C_1 is floating; C_2 is connected through a constant-current circuit to voltage $+E_2$ equal to nE_1 , where n is the intrinsic standoff ratio of the UJT. The UJT then operates as a relaxation oscillator during t_2 , and the number of sawtooth pulses is proportional to the duration of t_2 .

The number of pulses is inversely proportional to V_{R2} , and therefore directly proportional to the product of t_1 and t_2 . By proper choice of C_1 , C_2 , R_3 and R_8 , the number of pulses can be made equal to the product of the numerical values of the gate generating functions. Since V_E cannot go above nV_{R2} , there is no output for $t_1 = 0$, and the constant term (E_1) has no effect. There is a minimum operating voltage for the UJT, and there is a small nonlinear interval for voltages greater than the minimum.

As a divider or ratio computer (Fig. 2), the constant-current source charges C_1 during interval t_1 . The number of pulses is then inversely proportional to t_1 and directly proportional to t_2 , and is therefore proportional to the ratio t_2/t_1 .



2. Ratio computer circuit is used to determine ratio of t_2 : t_1 , which is proportional to the number of input pulses.



3. Length of objects passing a point with constant, unknown velocity is determined by this circuit.

The circuit shown in Fig. 3 was developed to measure the length of objectives passing a point with constant but unknown velocity. A pulse is generated proportional to the time required for the object to cover a known distance between two points; this is t_1 . A second pulse is then generated proportional to the time required for the object to pass the second point; this is t_2 . Hence:

 $t_1 \propto known \ length \ (L_k)/velocity \ (V);$ $t_2 \propto unknown \ length \ (L_u)/velocity \ (V).$ Then:

$$t_1/t_2 = (L_k/V)/(L_u/V) = (L_k/V)(V/L_u) = L_k/L_u$$



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IMPEDANCE INSTRUMENTS 1071B

The solution for L_k is:

$$L_u = L_k(t_2/t_1).$$

Charles D. Volz, Fishery Research Biologist,
U.S. Department of the Interior, Seattle.
VOTE FOR 111

Get sharp edges from an astable multivibrator waveform

The waveform usually obtained at either collector of the basic astable multivibrator of Fig. 1a is shown in Fig. 1b.

The negative-going edge is sharp because as soon as one transistor, say, Q1, turns on, the baseemitter junction of Q2 is reverse-biased, restrict-





Sharp pulses are obtained (e) from an astable multivibrator (d) by modifying standard configuration (a) with the addition of Q3 (c) to supply charging current for the capacitor. The waveforms before modification appear in (b).

ing the load on Q1 to R_1 and R_2 in parallel. Furthermore, the transistor has a low output resistance in the on condition.

During the positive-going transition, not only is the transistor in a high resistance condition with its base-emitter junction reverse-biased, but it also is necessary to recharge C_1 . The charging current path for C_1 is R_1 and the base-emitter junction of Q2; the time constant of this path is approximately C_1R_1 . This is what causes the positive-going edge to exhibit the exponential rise shown in Fig. 1b.

To change C_1 in order to reduce the time constant would necessitate a corresponding change in R_2 to avoid affecting the frequency of oscillation. Reduction of R_1 in an increase in Q1 collector current. Both these approaches have only limited application, so essentially the same defect in the output waveform persists.

The solution to the problem involves the use of another transistor to supply the charging current to C_1 (Fig. 1c).

When Q1 collector voltage begins to rise, Q3 is turned on and supplies the necessary charging current to C_1 . Thus the charging current which passes through R_1 is reduced by the current gain of Q3. Diode D1 is placed in parallel with and in opposition to the base-emitter junction of Q3 in order to maintain the signal path between Q1collector and Q2 base when Q1 is turning on.

If outputs are to be taken from both Q1 and Q2 collectors, a similar modification can be made to the C_2 charging path. The same improvement can also be applied to monostable circuits, should it be necessary to obtain an output from the capacitance driving collector.

Figure 1d shows an astable circuit with the modification applied to one collector. The resulting collector waveforms, in which the improvement is clearly seen, are shown in Fig. 1e.

T. M. Jarvis, Products and Instruments Div., Bell Aerosystems, Buffalo, N. Y.

VOTE FOR 112

Switch your ac loads at zero voltage or current

The purpose of the circuit (Fig. 1a) is to reduce the level of radio frequency interference (RFI) usually generated when an ac device is turned on and off. The RFI can be reduced by turning the device on when the line voltage is near zero and turning it off when the current is near zero.

This is accomplished by controlling the device with a Triac, which is triggered on when the voltage is near zero and commutates off when the



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(b)

1. Zero voltage or zero current switching of an ac load on and off, respectively, is possible with the simple circuit (a). In all waveshape photographs upper traces are load voltages, lower traces are load currents. On characteristics

d

load current goes through zero.

The circuit works as follows. If switch S1 is open, T1 will fire at the beginning of every line alternation and prevent T2 from firing. Capacitor C1 introduces enough leading phase shift to ensure that T1 always fires before T2 can. Diodes D1through D4 prevent the V_{on} of T1 from appearing at the gate of T2.

When S1 is closed, T1 commutates off at the next zero crossing. Resistor R2 can now supply gate current to T2 so that it fires at the beginning of each following line alternation until S1 is again opened. When S1 opens, T1 turns on and again prevents T2 from receiving any gate current. T2 therefore turns off the next time its anode current goes through zero. T1 is not controlled synchronously with the line, but since its anode voltage never exceeds ± 3 volts, the RFI generated is



(zero voltage) for a resistive and inductive load are shown in (b) and (c), respectively. Off characteristic (zero current) for a resistive and inductive load appear in (d) and (e), respectively.

(e)

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negligibile for all practical purposes.

The photographs (Figs. 1b through 1e) show that T2 always turns on when the voltage is near zero and turns off when the current is near zero.

William B. Miles, Charles Bruning Co., Mount Prospect, Ill.

VOTE FOR 113

Small fuses measure pulse duration

In applications such as the measurement of voltage breakdown, relay contact-bounce, dynamic mechanical system operation or dynamic response in electrical circuits, it is sometimes necessary to get an indication of transient or sequence time which exceeds a fixed interval. When this situation occurs infrequently and at unpredictable times, commonly used methods such as CR oscillographs and chart recorders become difficult to use, particularly if the expected time intervals are short.

A convenient and inexpensive way to make such measurements is to use a circuit with low-current fuses and a simple transistor switch. Figure 1 shows a transistor switch circuit which is responsive to positive-going pulses. A fuse rated for low current and a series current-limiting resistor make up the collector load. When the switching transistor saturates, its collector supply voltage appears essentially across the series fuse-resistance combination. The time for fuse burn-out after saturation will depend on the fuse rating, resistor value and applied voltage and can be adjusted by choosing these parameters. The occurrence of a burned-out fuse is an indication of the occurrence of a voltage pulse longer than the adjusted value.

In some applications the fuse and resistor can be used without a transistor. However, the use of a transistor offers these advantages:

• The voltage across the fuse circuit is always the same during the pulse interval, regardless of the pulse characteristic. This means calibration is easier and lends itself well to analysis.

• The lower limit of measured time intervals is extended because of the power gain of the transistor.

• The measuring circuit can be effectively isolated from the source.

Two transistor-fuse-resistor circuit combinations, adjusted for fuse burn-out at different times, can be used to measure the occurrence of a pulse between the set limits of these two circuits. A second transistor feeding the input to the



FOR MEASUREMENT OF 0.1 ms PULSES: V=15 VOLTS, R = 200 Ω V=10 VOLTS, R = 150 Ω V=75 VOLTS, R = 100 Ω V=75 VOLTS, R = 90 Ω

1. Value of R in series with a fuse determines the width of the pulses that can occur at the input before the fuse "goes." A range of values is given for various supply voltages. 30-V dc bias across 1.8 k Ω and 39 k Ω secures "hard" off condition for the transistor switch.



Current vs time for burn-out of a variety of fuses gives an idea of the wide choice available.



3. Fuse burn-out time for capacitor discharge test. Burnout time was adjusted for 100 μ s. The timing was within 10% from fuse to fuse.



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Figure 2 shows the time-current characteristics for various Bussman fuses. A selection can be made from this to produce some desired time interval.

A number of these fuses were tested for burnout time at fixed voltage and series resistance and found to be surprisingly uniform. Of the ones tested all had a burn-out time within about 10%. The current-limiting resistance includes about five times the fuse "cold-resistance."

Fuses of 1/20- and 1/100-ampere rating were tested with various series resistance values and applied voltages from about 10 to 25 volts dc with no transistor. Time to fuse burn-out was found to be variable from about 50 μ s to 3 ms by adjustment of the voltage and series resistor.

Seventy capacitors were given a continuous 140-hour test under voltage at elevated temperature to measure the occurrences of breakdown voltage pulses which lasted longer than 0.1 ms. A multiple-channel circuit like that of Fig. 1 was used. Figure 3 shows the time-current response to burn-out taken with this circuit adjusted for 0.1 ms.

Taft R. Wrathall, Lockheed Missiles & Space Co. Sunnyvale, Calif.

VOTE FOR 114

Single IC generates wide-range, variable-width pulses

A variable-width, one-shot circuit with a ratio of maximum-to-minimum pulse widths of more than about 100:1 is difficult to design. A circuit which overcomes this difficulty is described here.

Figure 1a shows a circuit for a conventional one-shot with a minimum width of T_1 and a maximum width of T_2 . The delay line is chosen to



Pulse widths variable from 0.1 to 180 μ s are obtained from the circuit (c) using a single 946 quad two-input gate. It has rise and fall times of less than 40 ns. Basic arrangement is shown in (a) with its waveshapes in (b).

have a delay, T_D , slightly longer than T_1 . The output pulse then has a minimum width of $(T_1 T_D$) and a maximum width of $(T_2 - T_D)$. In practice this easily covers a range from 0.1 μ s to several hundred microseconds, a ratio of several thousand to one. Figure 1b shows waveshapes for two different input pulse widths.

Figure 1c shows a practical circuit using a single 946 IC quad two-input gate to generate pulse widths variable from 0.1 to 180 μ s. Rise and fall times are less than 40 ns. For simplicity, the delay line was replaced with a simple LC ringing circuit. The output pulse begins about 5 μ s after application of an input trigger.

(This work was performed under the auspices of the U.S. Atomic Energy Commission.)

Curtis Sewell, Jr., Lawrence Radiation Laboratory, University of California, Livermore, Calif.

VOTE FOR 115

IFD Winner for May 24, 1967

Peter F. Moulton, Design Engineer, Research Section, Science Committee on Psychological Experimentation, Cambridge, Mass.

His Idea, "FET stabilizes Zener current in a simple voltage regulator," has been voted the \$50 Most Valuable of Issue Award.

IFD Winner for June 7, 1967

Gordon J. Deboo, Ames Research Center, NASA, Moffett Field, Calif.

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NASA TECH BRIEFS



Fluid control circuit operates on low power

Problem: Actuate fluid control circuits with low-level electrical signals. Commercial solenoiddriven valves have been used for this application but they require relatively high power levels.

Solution: A standard, commercially available electromagnetic relay switches a fluid amplifier that, in turn, drives a spool valve.

The relay contacts are replaced by nozzles connected to the control ports of a bistable fluid jet amplifier. In operation, the spring-loaded armature of the relay caps one nozzle or the other as the relay is activated and deactivated. The fluid jet amplifier controls the spool of a mechanical valve handling high volumes and flow rates.

The electromagnetic relay reduces the power requirement to only 10 mW, compared with 10 watts required by a solenoid.

Cascading the fluid amplifiers and replacing the relay with a torque motor (permanent-magnet polarized relay) would make it possible to reduce the power required to less than 0.1 mW.

Frequency response greater then 20 Hz may be obtained with the torque motor arrangement; solenoid valve rates are below 5 Hz.

The low input power needed permits direct connection with digital computer outputs without amplification.

Inquiries concerning this innovation may be directed to: Technology Utilization Officer, Lewis Research Center, 21000 Brookpark Rd., Cleveland, Ohio 44135 (B67-10042).

Bifilar winding on relays suppresses high transients

Problem: Rid electromagnetic devices such as relays and solenoids of transients. High transients are caused by the rapid decay of the electromagnetic field when the current to a device is removed. *(continued on p. 138)*

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NASA TECH BRIEFS

Previous methods of suppressing these transients caused polarity dependence and increased size, weight, and contact resistance due to outgassing.

Solution: Create a high coupling coefficient by putting a bifilar winding around the magnetic core, alternately spaced vertically and radially from the core.

Actuation winding W1 about the core (see schematic) is connected to a power supply through switch S1. A transient voltage-suppression coil, W2, is wound alternately, with its ends connected to form a short-circuited winding. Leg L1 projects laterally from the base of the core. It has a vertical leg L2 extending upward to slightly above the top of the core, where spring-loaded contact assembly C extends laterally. A load is connected in series with the device through S2.



When S1 is closed, current flows through W1, creating a strong magnetic field that pulls C down to contact the core. At the same time it closes S2, connecting the load to the power supply.

Opening S1 de-energizes W1, causing the magnetic field to decay rapidly. This produces a transient voltage. The high coefficient of coupling between W1 and W2 makes the transient bleed off through W2 rather than through any other dissipative path. When the magnetic field has decayed sufficiently, spring-loaded contact C returns to the horizontal, opening S2 and removing the load.

This method affords a simple and inexpensive suppression of high transient voltages that would otherwise cause interference in the operation of sensitive components adjacent to the device.

To control the effect on relay release time, it is necessary to select the proper wire size for W2.

Inquiries concerning this invention may be directed to: Technology Utilization Officer, Kennedy Space Center, Fla. 32899 (B67-10031).

This is the invention of a NASA employee and a patent application has been filed. Inquiries concerning license rights may be made directly to the inventor, Charles Wayne Marion, at Kennedy Space Center, Fla. 32899.

ON READER-SERVICE CARD CIRCLE 71



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Collins' prototype crystal filters with the response illustrated above are now available in the 100-kHz to 500-kHz region both in upper and lower sideband designs and to the following specifications:

> Bandwidth at 1 db: Bandwidth at 60 db: Carrier rejection: Passband ripple: Differential delay:

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– 20° C to + 70° C. 1.9″ X 2.8″ X 0.7″ max.

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Immediate openings exist at Seattle on the SRAM, Minuteman and Lunar Orbiter programs. Assignments in test technology include data systems and instrumentation and test data handling and processing. Qualifications include a B.S. or M.S. in electrical engineering and two to five years applicable experience. Flight technology positions are available in flight control and flight mechanics. Qualifications include a B.S. or M.S. in electrical engineering with two to five years experience.

Additional Seattle openings exist in developmental design and electronic packaging. Design assignments are in airborne control systems, ground system electrical power systems, and environmental control, and require a B.S. degree in an applicable discipline plus related experience. Electronic packaging qualifications include a B.S. in electrical engineering plus applicable experience.

A number of openings also exist on the Apollo/Saturn V program. At Huntsville, assignments in flight mechanics and flight evaluation include operational trajectories, mission analysis, trajectory analysis, postflight trajectories, flight simulation development, and flight dynamics. Qualifications include a B.S., M.S. or Ph.D. in electrical engineering. Openings also exist for electrical/ electronic engineers at Kennedy Space Center.

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ON READER-SERVICE CARD CIRCLE 73

Book Reviews

ELECTRONIC COUNTING CIRCUITS



Counting circuits

Electronic Counting Circuits, J. B. Dance (American Elsevier, New York), 390 pp. \$16.75.

This book demonstrates the difficulty of publishing simultaneously in the U.K. and the U.S.A. Although English is the common language, there are distinct differences between the two countries in technology and practice. Nowhere is this more true than in electronics, where European research and development lags behind current U.S. practice in many areas.

The author aims to provide "a comprehensive account of the functioning of all electronic counting circuits." While he may have written a book that adequately covers the British state of the art, it does not do the same for the U.S. Too much emphasis is on tube circuits, particularly where they have been superseded in U.S. practice, as in driver circuits for Nixie tubes. The chapters on gas-filled counting tubes and beam-switching tubes do adequately cover the operation of these tubes, however, and may be of use where they relate to U.S. practice

Since the book contains a great deal of information that may be helpful to the junior engineer, the book may find its way into college libraries. But its readers will suffer because so little has been done to



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edit it for American engineers. It should not have been too much effort to eliminate "thermionic valves," "H.T. supply" and similar terms not common in the U.S.

That there is a need for a good reference work on counting circuits is undeniable. That Mr. Dance has failed to provide a suitable one for U.S. engineers is an object lesson for future authors.

-Jeffrey N. Bairstow

Laser guide

Gas Lasers, C. G. B. Garrett (Mc-Graw-Hill, New York), 135 pp. \$10.95.

This book on gas lasers is a simple guide to their origin, development, physical properties and construction. The treatment is basic enough to be applicable to other types of lasers. It traces the development of the gas laser and the origin of optical gain, and discusses the interaction between the gaseous medium and optical cavities. The mechanisms of specific laser systems are then dealt with.

Detailed descriptions are given of the helium-neon, argon ion, carbon dioxide and far infrared lasers, as well as the power output, directional distribution, and phase and amplitude fluctuations of the emerging light beam. The effects of intercavity modulation, frequency and amplitude stabilization, Q switching and the application of magnetic fields are touched on.

Switching circuit theory

Basic Switching Circuit Theory, Moshe Krieger (Macmillan, New York), 256 pp. \$9.95.

This is an introduction to switching-circuit theory. Emphasis throughout is on general principles rather than on particular devices. Its aim is to provide the student with the theoretical tools necessary to understand complex digital systems such as digital computer units, automatic telephone exchanges, and digital telemetering devices. Many excellent graphs and illustrations are included, as well as a large number of problems designed to clarify the topics further.

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Products



Tiny tophatted light-emitting diode produces a visible 0.8 footlamberts per mA. Page 148



Self-tuning sweeper runs automatically. An electronic attenuator is the key. Page 190

Complementary MOS arrays open the gate to LSI. Coming . . 500 per chip. Page 174

Also in this section:

Resistors trimmed ultrasonically to 0.5% of specifications. Page 186
Frequency counter ranges to 18 GHz with an 8-digit display. Page 192
Alloy diffused epitaxial germanium transistor handles 25 amperes. Page 179
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Tiny light-emitting diode has 40- footlambert visible output

General Electric, Miniature Lamp Dept, Nela Park, Cleveland. Phone: (216) 266-2121. P&A: \$16.60 each; 6 to 12 wks.

A tiny light-emitting diode uses a silicon carbide crystal in a glass lens to create a visible output of 40 footlamberts at 50 mA. The low-voltage lamp operates at 2 to 5 volts and draws 50 to 75 mA.

The lamp is mounted on a TO-46 gold header (Fig. 1) and the wire leads will fit into a standard transistor socket. The crystal chip provides 0.8 fL per milliampere of light in a 180° radiation pattern. The light pattern (Fig. 2) emanates for-

ward as a focused beam and the light can be seen from all sides of the glass tophat. There is no suspended filament in this diode and the glass encapsulation is solid. Because of this, the lamp is not prone to failure due to vibration or shock. If operated at 5 V at 75 mA and from -65° to 125° C an unlimited life expectancy is claimed.

The color of the light and the efficiency of the diode are dependent on the amount of impurities in the silicon carbide chip. The color of the light is yellowish with a green tinge. This is the only color available as the glass encapsulation cannot be tinted. The lamp operates on



1. Forty-footlamberts from a tiny crystal. The silicon carbide chip mounted on a TO-46 header is capped by a glass tophat and lens.



2. Full 180° visibility is afforded. The radiation pattern emanates from the lens as a focused beam visible anywhere in a 180° arc.

a narrow spectral emission band (5500-6300 Å) and any tinting would severely limit its light-producing capabilities.

The efficiency of the device is reported to be 10^{-6} photons per electron and its peak light wavelength is about 5900 A. Early manufacturing problems with silicon carbide crystals and their repeatable characteristics on a production-line basis have to be eliminated.

Used as warning lights, the lamps were subjected to a shock and stress test and no failures were reported. They lend themselves to aviation uses and to applications in computers and space equipment as indicator or action-triggering lamps. With a rise and fall time of 100 μ s there is virtually no delay in response. Design and development is under way for more lamps of the tophat configuration; other sizes and shapes, even less lamplike in appearance, are on the drawing boards.

CIRCLE NO. 250

Self-luminous light uses Krypton 85



American Atomic Corp., 425 S. Plumar, Tuscon, Ariz. Phone: (602) 622-4881. Price: from \$101.

This unit incorporates a one-millicurie self-luminous light source in a temperature-compensating oven for stability. It has application wherever low-level light is used as a secondary standard for accurate light measurement. The light source can be made with any one of a group of standard phosphors covering the complete visible spectrum to match spectral characteristics of the light being measured. The source also offers the benefit of Krypton 85 half-life of 10.3 years and constant availability.

CIRCLE NO. 251

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Write Babcock Relays, Division of Babcock Electronics Corporation, 3501 Harbor Boulevard, Costa Mesa, California 92626; or telephone (714) 540-1234.





The Babcock Model BR7 relay will perform from dry circuit to 10 amps., with universal contacts, and is designed to meet critical aerospace applications.

SPECIFICATIONS

81258 1.300" h. x 1.075" 1. x .515" w WEIGHT: Approx. 1.0 oz. CONTACT ARRANGEMENTS: SPDT and DPDT PULL-IN FOWER: Low as 80 mw. LIFE: 100,000 operations, min. TEMP. RANGE: -65°C to +125°C

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COMPONENTS

Transponder delay line codes and decodes



Computer Devices Corp., 63 Austin Blvd., Commack, N. Y. Phone: (516) 543-4220. P&A: \$200; 8 weeks.

The model D120 has 28 taps, which have the basic 1.45 μ secs spacing, and a maximum delay of 25 μ secs. Characteristic impedance is 180 Ω . The unit is stable over an operating temperature of -40° C to $+71^{\circ}$. Built to military specifications, it has a 6 dB maximum attenuation, and maximum distortion is held to under 8%. The delay line, hermetically sealed and mounted on a plug-in printed circuit board measures 4-3/8 x 5 x 3/4inches high.

CIRCLE NO. 252

Microwave tube base has ceramic seal



Ceramic International, 39 Siding Pl., Mahwah, N. J. Phone: (201) 529-2800.

These metalized units can withstand a 1000 V breakdown test and will not leak after a 72 hour bakeout 550 °C. In the manufacture of the tube base, the nickel tubes, solid at one end, are sealed into the ceramic insulators. Then, the outside diameter of the ceramic is sealed to the nickel tube base. In addition, an evacuation tube of copper is also sealed into the nickel base.

CIRCLE NO. 253

if your military stack doesn't have this trademark, it's a modified commercial design

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Brush assembly of conventional synchro after 1000 hours



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Harowe brushless synchros come in sizes 8 through 11 as standard, larger sizes as special, for all common functions. Write for complete specs—



ON READER-SERVICE CARD CIRCLE 80

COMPONENTS

Half-inch characters in smaller Nixies



Burroughs Corp., Electronic Components Div., Plainfield, N. J. Phone: (201) 757-5000.

This tube features character height of 0.52-inch in the tube bulb diameter of 0.6-inch. This permits a small display while maintaining high readability. The B-5560 is designed to mount upside-down to allow the driving circuitry to be mounted above the readout and the control knobs directly below the tube display. The tubes are designed for mounting on 0.62 inch centers.

CIRCLE NO. 254

Trigger transformer draws 300 V



U.S. Scientific Instruments, Inc., 36 Pleasant St., P. O. Box 9, Watertown, Mass. Phone: (617) 923-0940. P&A: \$12 to \$25; stock.

Input drive, compact trigger transformers provides coverage of flash tube external triggering. Trigger pulses up to 12 kV, 22 kV, and 42 kV are provided. These units are designed specifically for solid state driving circuitry. Rated outputs are obtained with a maximum of 300 V input drive. All units are vacuum potted in epoxy.

CIRCLE NO. 255

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Select the function you want.

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ON READER-SERVICE CARD CIRCLE 81

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Now, here is a new miniature coaxial cable providing extremely tight electrical characteristics and a greatly extended parameters in a variety of applications including low noise amplifiers, microwave transmission, high speed computers, airborne instrumentation and radar.

Offered in three sizes off-the-shelf: .141", .085" and .070" and in random lengths from 12 feet to 200 feet, construction consists of a silver-plated Copperweld inner conductor with a TFE Teflon dielectric and solid copper sheath. Electro-plated silver, gold and tin outer sheaths are also available from stock.

The new miniature coax offers very low attenuation across Gc band, low VSWR, no radiation or performance deterioration at environmental extremes and maintains impedance despite extreme bends. This latter feature makes it possible to cut, bend or form the coax into coils for the achievement of signal delay or a variety of shapes for the fabrication of special assemblies for RF front ends and amplifiers without problems of loss or coupling to impair reliability.

This new miniature coax has been qualification tested for severe aerospace applications and has performed remarkably well where desired electrical performance cannot be achieved with conventional braided cables.

Why not write today for your free miniature coaxial cable folder which includes actual samples of the coax plus complete descriptive data?



ON READER-SERVICE CARD CIRCLE 82

COMPONENTS

Solid-state power supply offers 2 or 3 phase



Elgar Corp., 5267 Linda Vista Rd., San Diego, Calif. Phone: (714) 297-3892.

This ac power source is an all-silicon solid-state instrument with output power of 0 to 150 VA, two or three phase. It offers operational flexibility through a family of interchangeable plug-in oscillator modules. The model 150 is designed for continuous operation in powering test equipment for airborne or overseas use, synchronous frequency instruments and also for general laboratory use. The basic power amplifier unit accepts any of 40 standard plug-in oscillators with fixed or variable output frequencies from 45 Hz to 10 KHz; accuracies range from $\pm 0.1\%$ to $\pm 0.0001\%$. Special oscillators are available to provide dual frequency and programmable operation. Output may be shorted indefinitely as the unit recovers immediately when the short is removed. The model 150 is packaged for rack mounting and has front-panel on-off switch, meter, and control for output voltage. CIRCLE NO. 256

Trimmer capacitors in two configurations

Erie Technological Products, 12030 W. 12th. St., Erie, Pa. Phone: (814) 456-8592.

The vertical tuning style and the horizontal tuning style both have a diameter of 0.218-inches and occupy .007 cubic inches of space. Each now offers four capacitance ranges, a 5-25 pF tuning range, and the additional ranges of 1.0 to 3 pF, 2.5 to 9 pF and 3.5 to 20 pF. Rated at 100 WVdc to 85° C and 50 WVdc to 125 °C, the units operate over a temperature range of -55° C to 125°C.

CIRCLE NO. 257

NOW THERE'S A **QUALITY MINIATURE** POTENTIOMETER THAT'S A

The rotary action switch we've added to our Model 3 Potentiometer eliminates the need for a separate switch (or a larger potentiometer - line switch combination) and gives added design flexibility for compact military, instrument and communication applications. The Model 3 was



explicitly designed for these demanding applications. Its enclosed housing makes it impervious to dust, contamination and moisture. In addition to these design features, the Model 3 is available in many styles with wirewound and carbon composition resistor elements for wide applications.

... SO SWITCH TO IT

POTENTIOMETER			Number of Styles	Terminals	Constructions	Operating		Resistance	SWITCH	
Wattage	Dia.	Element	Available	Available	Available	Temp.	-	Range	Туре	Rating
1/4 & 1/2	43/64	Composition	Single Dual-concentric	Solder type terminals and	Industrial	Mil 55° C to + 120° C	Ind - 55° C to + 85° C	100 ohms to 10 meg.	S.P.S.T. Left End Throw	1 AMP @ 125V AC·DC 4 AMPS @ 20V DC
1-1/2 & 2	437.04	Wirewound	shafts, multiple sections Single shafts, multiple sections	PC terminals parallel to shaft	Military	— 55° C to + 135° C	– 55° C to + 105° C	4 ohms to 30 K Ω	S.P.S.T. Right End Throw D.P.S.T. Left End Throw	1 AMP @ 125V AC-DC 4 AMPS @ 20V DC 1 AMP @ 125 V AC-DC 4 AMPS @ 20V DC



For complete information on how our Model 3 product line can make your design problems a snap, use the reader service card. We'll also send you information on our other products.

CENTRALAB Electronics Division . GLOBE-UNION INC. 5757 NORTH GREEN BAY AVENUE . MILWAUKEE, WISCONSIN 53201

capacitors / packaged circuits / rotary and push button switches / potentiometers / technical ceramics / solar cells / semiconductors

ON READER-SERVICE CARD CIRCLE 83

ELECTRONIC DESIGN 19, September 13, 1967





You always come out ahead with Eagle relays. After all, we developed them because we use relays in our other products, and we just couldn't find any relays on the market to meet our standards of reliability.

The chart below shows that we have succeeded in building a better relay.

Now, we ask that you be the official judge! **FREE RELAYS AND OFFICIAL SCORE CARDS** are available for your own test . . . under your own conditions. To receive an official entry blank to the great relay race, contact R. W. Emelander, Eagle Signal Division, E. W. Bliss Company, 736 Federal Street, Davenport, Iowa 52808. Or circle the reader service number below.

	COMPETITIVE BRANDS						EAGLE
CONTACTS	"A"	"B"	"C"	"D"	"E"	"F"	RELAYS
Arrangement	3 PDT	3 PDT	3 PDT	3 PDT	3 PDT	3 PDT	3 PDT
Rating	5 Amp.	5 Amp.	5 Amp.	5 Amp.	5 Amp.	5 Amp.	5 Amp.
LIFE	15,061,261	14,077,866	28,808,000	21,625,333	16,923,133	29,433,600	34,492,950
Mechanical	Operations	Operations	Operations	Operations	Operations	Operations	Operations
ELECTRICAL	295,466	490,433	129,600	235,700	778,200	921,400	948,675
5 Amp. Resistive	Operations	Operations	Operations	Operations	Operations	Operations	Operations
1.6 Amp Inductive	488,666	1,071,666	496,000	284,333	3,529,466	1,842,000	3,102,200
	Operations	Operations	Operations	Operations	Operations	Operations	Operations

These "track records" show that Eagle Relays have a consistently longer life. A 20% greater life than the closest competitor at 5 amps resistive. An almost 70% greater electrical life than the average of the six competitors tested.

Like to prove us wrong? Chances are you're more likely to set a new "track record!"



COMPONENTS

Instrument servo is gearless



Vernitron Corp., Torrance Division, 1742 S. Crenshaw Blvd., Torrance, Calif. Phone: (213) 328-2504. P&A: \$500; 4 to 6 wks.

This remote positioner converts synchro data input to shaft position in 26 V systems. It is contained in a standard BuOrd size 23 synchro housing. This unit weighs 23 ounces. Offering high torque, high accuracy, fast response and high input impedance this gearless servo can be driven by any size synchro transmitter (it requires signal level power only). Torque gradient is 7.5 oz.-inch per degree and peak torque is 3.5 ounce-inch.

CIRCLE NO. 258

Dual power supply has one knob



Kepco, Inc., 131-38 Sanford Ave., Flushing, N. Y. Phone: (212) 461-7000.

For balanced loads, op-amps, or circuits requiring a tracked plus and minus voltage this power supply provides balanced plus and minus voltages with ratings of ± 15 V at 1.5 A, ± 40 V at 0.5 A and ± 100 V at 0.2 A, all adjustable from zero to the rated voltage, with a single control. Fully metered, including the current from each section and its voltage, the power supplies may also be used in series.

CIRCLE NO. 259



Most complete line of reference zeners with stability to 0.5 mv/year ...

Semcor TCRE's!

Semcor pioneered the first silicon diffused junction TCRE in 1957. And kept right on going and growing. Today, many call its line of precision zener reference elements "the finest, most reliable and complete in the industry." Look it over and you'll agree. Voltage from 6.2 V to 200 V. Temperature coefficient from .01 % / °C to .0002 % / °C. Temperature range as wide as -55 °C to + 185 °C. And long-term stability as low as .5 mv/ year. Whether your need is for a standard or custom unit, don't speculate. Get everything you seek in a reference element — plus prompt delivery — by specifying Semcor. A division of Components, Inc. — industry's leader in fair pricing, prompt delivery and superior reliability of electronic components. For full information, see your nearest dealer or write: 3540 W. Osborn Road, Phoenix, Arizona 85019. Phone 602-272-1341.



COMPONENTS, INC. SEMCOR DIVISION



In critical situations where reliable, accurate recording of CRT phenomena is vital, Beattie-Coleman Oscillotrons are specified more often than any other 'scope camera.

For use in field test instrumentation, the MII Oscillotron (above) is designed to withstand severe environmental conditions, shock, vibration and dust. Recording ratio is adjustable from 1:1 to 1:0.5. Camera is hinged to swing away at lens as well as at 'scope, permitting camera mount to serve as a shadow box for the CRT. Result: parallax-free adjustments are possible under high ambient light with minimum phosphor excitation. Indicator light gives assurance shutter is open. Records fast transients with either Polaroid or 4x5 sheet film.

Model 565A has 86mm f/1.2 lens for recording nanosecond traces at 1:1 ratio. Other Oscillotrons for 35mm rapid sequence or streak recording. Whatever your needs, from routine lab use to highly critical field tests, we have a model to do the job or will design one for you. Send for brochure.

Coleman Engineering Co Inc., Box 1974, Santa Ana, Calif 92702



COMPONENTS

Broadband attenuators from dc to 12.4 GHz



Omni Spectra, 24500 Hallwood Ct., Farmington, Mich., Phone: (313) 255-1400. P&A: \$80 (1-9); stock.

The units are available in values of 3 dB, 6 dB, 10 dB and 20 dB with tolerances of \pm 5% for attenuation values of 6 dB or greater and \pm 0.3 dB for the 3 dB units. All models are 3/8 inches diameter by 1-1/4 inches in length. The VSWR is 1.20 max. from dc to 4 GHz and 1.35 max. from 4 to 12.4 GHz. Temperature stability is 10⁻⁴ dB/dB/° C. Maximum average power rating at 85° C is 0.5 W.

CIRCLE NO. 260

Solid-state delay in module form



Parko Electronics Co., 1540 S. Lyon, Santa Ana, Calif. Phone: (714) 547-0184. P&A: \$10; stock.

The module, together with an electromechanical relay, may be used in most applications requiring an electronic time delay relay, or individually as a series element in a relay logic system. Factory preset time delays are available from 100 ms to 90 seconds over an operating temperature range of -20° to 75° C. The unit is also available in an adjustable version designed for through-panel mounting.

CIRCLE NO. 261

ON READER-SERVICE CARD CIRCLE 86

 RAYTHEON
 DUAL RM930'S
 RAYTHEON
 DUAL HLT²L'S
 RAYTHEON

 DUAL RM709
 RAYTHEON
 DUAL RM930'S
 RAYTHEON
 DUAL HLT²L'S

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 DUAL RM930'S
 RAYTHEON
 DUAL HLT²L'S
 DUAL RM709

Go to the head of the line!

You'll save 20% when you replace two IC's with a Raytheon Dual. Our line includes the RF 120 and RF 130 HLT²L 50 MHz Dual J-K Flip-Flop, the Dual RM709 High Gain Op Amp, and a full complement of RM930 Series Dual J-K Flip-Flops.

All Raytheon RM Duals feature full Military Quality guaranteed over a temperature range of -55°C to 125°C and true hermetic seals guaranteed to 5x10⁻⁸cc/sec Helium. Raytheon's quality assurance program guarantees a product that exceeds MIL Spec requirements.

For complete information on our line of duals write: Raytheon Company, Semiconductor Operation, 350 Ellis Street, Mountain View, California 94040.



UNMATCHED ACCURACY AND RELIABILITY WITH MIDGI-FRIM ¹/₂" and ³/₈" SQUARE TRIMMERS



MIDEL-TRIM wirewound-square trimming potentiometers are the most reliable and accurate square trimmers available in the industry. They meet or exceed the most demanding requirements of all applicable missile and aerospace specs, including MIL-R-27208B.

MIDCI-TRIM pots are designed with fewer moving parts than most conventional square trimmers. A drive wheel replaces six parts or functions common to other square trimmers and functions as a mechanical actuator, slip ring, spring preload, slip clutch, and positive rotating stop.

MIDCI-TRIM pots feature a stainless steel adjustment screw insulated from the contact mechanism, which makes the case completely non-conductive.

MIDCI-TRIM pots contain precious metal alloys of platinum, silver, and gold, together with low-temperature coefficient resistance material, that provide minimum resistance change over wide temperature ranges.

MIDCI-TRIM pots offer many other features that you can't find in other square trimmers. A new four-page, two-color brochure details them.

Write for yours today — no obligation, of course.



 $cermet/metalfilm/wirewound/slidewire/multi-element\ trimming\ potentiometers$

465 WEST FIFTH STREET, SAN BERNARDINO, CALIFORNIA 92401 PHONE: (714) 885-6847, TWX (910) 390-1157

ON READER-SERVICE CARD CIRCLE 88

COMPONENTS

SCR firing circuit all solid-state



Crydom Laboratories, Santa Ana, Calif. Phone: (714) 540-1390.

A three phase 440-V $\pm 10\%$, 47to-63-Hz input solid-state firing circuit module meets MIL-E-16400, MIL-F-14072, MIL-Std-130, MIL-Std-202 and MIL-Std-454. Calculated mean time between-failure is at least 50,000 hours. The module features hard gate firing, 50- μ W control sensitivity, high linearity, negative gate bias, and 100-ns rise time to protect against SCR failures.

CIRCLE NO. 262

Pre-insulated splice cuts through insulation



AMP, Inc., Harrisburg, Pa., Phone: (717) 564-0101.

Splicing connectors can be applied at rates of 1400 to 2400 connections in an 8-hour shift. Each connector accepts up to four wires. The unstripped conductors and the splicing connector are placed in the crimping tool. Then the wires are cut to length and forced into position between slotted lances of the connector. Further closing of the handles drives the wires deeper into the slots, which cut through the insulation and make contact with the wire conductor. Used with solid wires from AWG #19 to #26, the connectors make contact with each wire at four points.

CIRCLE NO. 263


Dale G resistors give you precision power in the smallest packages available. Test them with equivalent size wirewounds and you get 1.4 to 4 times more power. Run them at mil power levels and you get stability that can't be challenged. Dale's silicone-coated G Series combines the superior conductivity of beryllium oxide cores with the exclusive wire and wirewinding techniques perfected in the Minuteman High Reliability Development Program. When you need more precision power in less space, or more stability without increasing size-call Dale. Only the G Series gives you this freedom of choice.

SURVEYOR RELIABILITY

demands are met by AGS wirewounds – the established reliability version of Dale's G Series. Supplied to Hughes Aircraft Company, builder of the Surveyor, AGS resistors have a proven failure rate of .000113% per thousand hours (60% confidence level, 50% rated power at 25° C ambient). They are the world's most reliable wirewounds.

G SERIES SPECIFICATIONS

BALE	MIL-R	DALE R	ATING	RESISTANCE RANGES (OHMS)			
TYPE	TYPE	U	٧	.05%, .1%, .25%	.5%, 1%, 3%		
G-1	RW-81	1.0 W	-	10 to 950	1 to 3.4K		
G-2	-	1.5 W	-	10 to 1.3K	1 to 4.9K		
G-3	RW-80	2.25 W	-	1 to 2.7K	1 to 10.4K		
G-5	-	4.0 W	5 W	1 to 6.5K	1 to 24.5K		
G-5A	-	4.5 W	6.5 W	1 to 11.4K	.1 to 42.1K		
G-5C	-	5 W	7 W	1 to 8.6K	.1 to 32.3K		
G-6	-	6 W	8 W	1 to 12.7K	.1 to 47.1K		
G-10	-	7 W	10 W	.5 to 25.7K	.1 to 95.2K		
G-12	-	10 W	12 W	.5 to 41.4K	.1 to 154K		
G-15	-	15 W	18 W	.5 to 73.4K	.1 to 273K		

MIL SPEC: G Series resistors meet the requirements of MIL-R-26D as well as the older MIL-R-26C and MIL-R-23379 specifications.

STABILITY: G Series resistance shift is less than 50% than that of conventional wirewounds of equivalent size (Dale RS) operated at the same ratings. **STANDARD VARIATIONS:** G Series resistors are available with radial leads (Type GL) and with non-inductive (Aryton-Perry) winding (Types GN and GNL).

COMPARATIVE SIZE:

Dale G-5C resistance element (left) rated at 5 watts compared with conventional RS-5 watt wirewound element.

For action, call Dale: 402-564-3131. For information, circle 181 for Catalog A.



DALE ELECTRONICS, INC. 1300 28th Ave., Columbus, Nebraska 68601 In Canada: Dale Electronics Canada, Ltd.



Printed in U.S.A.

DALE Metal Film Resistors

...chosen for long life in the Westinghouse PRODAC System

GENERAL SPECIFICATIONS TYPE MF* MIL-R-10509F

DALE TYPE	MIL. Type	125 C RATING (Char. C & E)	70 C RATING (Char. 0)	RESISTANCE RANGE (Ohms)
MF50	RN-50	1/20 w	1/10 w	30.1 to 80.6K
MF-1/10	RN-55	1/10 w	1/8 w	30.1 to 301K
MF-1/8	RN-60	1/8 w	1/4 w	10 to 1MΩ
MF-1/4	RN-65	1/4 w	1/2 w	10 to 1 MΩ
MFS-1/2	RN-70	1/2 w	3/4 w	10 to 1.5 MΩ
MF-1	RN-75	1 w	-	25 to 2.6 MΩ
MF-2	RN-80	-	2 wł	100 to 10 MΩ

"Also available in conformal coated (MFF) styles. tChar. B.Tolerance: $\pm 1\%, \pm 5\%, \pm 25\%, \pm 10\%$ standard. Characteristics D, C, or E apply depending on T.C. required.

DALE

Computers for industrial process control demand long resistor life. To insure this, Dale Metal Film resistors are used extensively in the versatile Westinghouse PRODAC System. Value analysis dictated the choice – with the long life characteristics of metal film winning over the lower price of carbon and carbon composition types. Dale verifies this reliability with long-term load life tests (see below). Delivery is reliable, too. Expanded production facilities can put quantities up to 50,000 in your plant in 2 weeks (1% tolerance units). We'll prove it – call 402-564-3131 today.

NEW METAL FILM LOAD LIFE DATA

Dale MF resistors have undergone 16,320,000 hours of load life testing without a failure (100% rated power, 70°C; failure defined as $\Delta R > 1\%$). Based on these tests, the MF resistor has a proven failure rate of .004% per 1,000 hours (60% confidence at 50% power, 70°C ambient). Write Dale for complete test data.

FOR COMPLETE INFORMATION CIRCLE NO. 181

) for optimum value in industrial resistors

DALE ELECTRONICS, INC., 1300 28th Ave., Columbus, Nebr. 68601 In Canada: Dale Electronics Canada, Ltd.

Variable delay line withstands 600 G's



Digital Devices, Inc., 200 Michael Dr., Syosset, N. Y. Phone: (516) 921-2400.

This delay line provides a continuously variable delay between 2 μ s and 35 μ s and will operate at frequencies up to 2 MHz. The unit is housed in a milled, o-ring sealed package and has performed in severe military environments, operating from -40 ° to +85 ° C, and surviving mechanical shock of 600 g. It is $1-1/4 \times 3/4 \times 10$ -inches, and mounting is by threaded #6-32holes in base. Connectors are solder pins at input, coaxial connector at output. Delay is varied by turning a shaft which protrudes from one end of the case. Normal performance is 167 ns/turn. Linearity is better than $\pm 0.25\%$.

10 stage photomultiplier 0.75-inch in diameter



RCA Electronic Components and Devices, Harrison, N. J. Phone: (201) 485-3900.

This tube is a 3/4-inch diameter, 10-stage head-on-type having an overall length, excluding semiflexible leads, of 3.94 inches. It employs a bialkali photocathode, has copper-berrylium dynodes, and features high quantum efficiency, low-dark current, and good time resolution characteristics. The RCA-4516 is intended for compact systems and measurement applications.



Victoreen high-voltage vacuum tubes make ideal components for switching circuits... as series or shunt regulators... as deflection amplifiers... or as inputs to pulse forming networks. Other uses which can capitalize on their superior performance, longer life, and compactness include — dunking, clamping, and crowbar circuits

Tube illustrated above, the Victoreen 6842 pentode with plate voltages to 4 kV, is shown in a typical shunt regulator circuit with two Victoreen Corotron corona type voltage regulators. Other tubes in the Victoreen line include —

- 7683 Triode or pentode with plate voltages to 1 kV
- VX-80 Triode with plate voltages to 4 kV
- VX-76 Pentode with plate voltages to 5 kV
- 7235 Triode with plate voltages to 10 kV
- 7234 Pentode with plate voltages to 10 kV
- **VX-107** Beam pentode with plate voltages to 15 kV
- VX-68 Vacuum high-voltage rectifier with 28,000 PIV; application as rectifier or clipper

VICTOREEN INSTRUMENT DIVISION 10101 WOODLAND AVENUE • CLEVELAND, OHIO 44104 IN EUROPE: GROVE HOUSE, LONDON RD., ISLEWORTH, MIDDLESEX, ENGLAND



OBOTRON

CIRCLE NO. 265

"Another unusual hermetic seal problem?"



STANDARD TYPES AND SPECIALS -

E-I Glass-to-Metal Seals more than meet today's highly critical electro-environmental specs

- IN MILITARY/COMMERCIAL APPLICATIONS!

Service-proven in every type of electrical and electronic requirement, E-I hermetic seals are finding even wider application in the more sophisticated under-sea/aero-space instruments, equipment and systems. These high dielectric strength seals are being produced to meet unusually severe shock and vibration conditions, and wide fluctuations in temperature and humidity. E-I glassto-metal terminations offer the advantages of vacuum-tight sealing with maximum rigidity and durability, miniaturization and design standardization. Where custom seals or unusual lead configurations are required, E-I sales engineers will make recommendations from your blueprints, sketches or data.

Write for the E-I Catalog — A practical, concise volume edited and compiled for the engineer / designer / specifier. Contains data and specifications covering a diversity of glass-to-metal seals in a wide range of types and sizes, including hundreds of stock items. Please address requests on company letterhead.





Patented In U.S.A., No. 3,035,372; in Canada, No. 523,390; in United Kingdom, No. 734,583; other patents pending. ON READER-SERVICE CARD CIRCLE 90

COMPONENTS

Rectifier modules handles 10 A



Unitrode Corp., 580 Pleasant St., Watertown, Mass. Phone: (617) 926-0404.

The UG series of high-current, high-voltage "doorbell" rectifier modules has ratings up to 8.25 A in air and 10 A in oil and from 2.5 kv to 10 kv. Modules are available in both regular and fast recovery versions. Each module has individual controlled-avalanche, voidless, glass-sealed, whiskerless junctions. Modules are vacuum encapsulated with high-thermal-conductivity filled epoxy. Each module has threaded connectors for stacking.

CIRCLE NO. 266

Modular memory systems fully expandable



Interdata, 17 Lewis St., Eatontown, N. J. Phone: (201) 542-3094.

Expandable, 2 1/2 D core memory modules have read-write cycle time of 900 ns and 1.4 ns. Drivers and readout circuits are included in basic modules of 16 or 18 bits, 2048 or 4096 words, and 8 or 9 bits, 1024 or 2048 words. Input/output signals are zero and five volts: logic levels are compatible with DTL and TTL logic. Standard interface options are available using Interdata logic components including random access address register, data register and control; and also, direct memory access channel control with priority logic.

NPN/PNP HI-QUALITY! HI-VOLTAGE! SILICON TRANSISTORS

...Attention Designers! Satisfy Your Most Demanding High Voltage Requirements with the Following Premium Devices From...

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 $h_{FE} > 25$ from $I_c = 1.0$ MA to $I_c 100$ MA *MD-14 OUTLINE





INDUSTRO TRANSISTOR CORP.

35-10 36th Avenue • LONG ISLAND CITY, NEW YORK 11106 TELEPHONE (212) 392-8000

COMPONENTS

Center jack connector eases mating



Hughes Aircraft Co., 500 Superior Ave., Newport Beach, Calif. Phone: (714)-548-0671.

This connector utilizes a center jack screw instead of the conventional coupling nut, thus decreasing the wasted space. Increase in contacts per shell over existing connectors ranges from 20% to 30%. Available in environmental, non-environmental, and potting types, the connectors are designed for military, space and computer applications. The new connectors have nonmagnetic stainless steel shells and glass-filled diallyl phthalate inserts. They will be available in sizes 8, 10, 12, 14, 16, and 18.

CIRCLE NO. 268

Digital power supply ranges to 30 kV

Cober Electronics, Inc., 7 Gleason Ave., Stamford, Conn. Phone: (203) 327-0003. Availability: 90 days.

The model G-1367 power source meets the requirements of Mil-E-5400 and is used as the power source to drive advanced directview storage tubes in airborne display systems. The six-pound package includes two 13 kV at 1.2 mA and one 2.5 kV at 2.2 mA power supplies as well as digital logic. The digital logic permits the unit to respond to remote commands and provides self-monitoring. A special circuit affords turn-off of 13 kV in a way that overcomes the effects of long (RC) time constants. Regulation is $\pm 0.5\%$ ripple, less than 0.005%, and 25,000-hour MTBF, assured by the use of microminiature IC's.

CIRCLE NO. 269



"PRIME" maneuvering re-entry vehicle

Designed by MARTIN MARIETTA for the U.S. Air Force, "PRIME" is a research vehicle which may lead to a generation of lifting body spacecraft which will fly home from space to landings at conventional airports. VECO was recently awarded the "PRIME" Achievement Award for its thermistor contribution to this successfully concluded Air Force program.

There is no denying that PRIME is "way out." However, VECO specializes in "down to earth" thermistor applications. VECO thermistors are being used more and more frequently in every-day products for home, office and industry.

Wherever PRECISE measurement, compensation and control of temperature is needed and where INSTANT RESPONSE is essential, engineers and designers are finding that VECO thermistors can do the job better, with greater reliability and often at less cost than conventional devices.

VECO's engineering staff is available to assist you in the application of thermistors to your products.

Write for Catalog MGP681

VECO First in Progress • First in Service





IRON POWDER CORES

From 5" dia. to Subminiature Toroids

Arnold has total capability across all design configurations—toroids, insert cores, threaded cores, plain cores, bobbin cores, sleeve and hollow cores, cup cores and subminiature toroids. All the necessary raw materials are carried in stock to provide optimum performance over the specified frequency spectrum. Our facilities include the most modern powder processing, pressing, quality control and final test equipment available in the industry.

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Arnold is also Permanent Magnets Tape Wound Cores MPP Cores Magnetic Shielding Electrical Alloy Transformer Laminations Transformer Cans and Hardware Silectron Cores Special Magnetic Materials.



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miniature microwave mixers

The new BMM-2 Series of miniature balanced mixers measure less than 1/4 cubic inch and weigh only 12 grams. Their small size was made possible by use of Merrimac's novel ultraminiature hybrid coupler. Various mixers in this series can be supplied from 1 GHz to 5 GHz. The BMM-2-2.2K, for example, covers the 2.1 GHz to 2.3 GHz range. Its noise figure is 6.5 db. These mixers were designed for such applications as telemetry, radar, communications and navigation systems.



MERRIMAC RESEARCH AND DEVELOPMENT, INC. 41 FAIRFIELD PLACE, WEST CALDWELL, N. J. 07006 • 201-228-3890

COMPONENTS

Trimmer resistors from 100 $_{\Omega}$ to 15 M $_{\Omega}$



Centralab Electronics Division of Globe-Union Inc., 5757 N. Green Bay Ave., Milwaukee. Phone: (414) 962-9200.

Rated at 1/4 W per section at 70°C, these trimmers offer resistance values ranging from 100Ω to 15 M Ω . The maximum voltage is 350 V; this voltage may be exceeded in special applications. The unit is built upon a base plate of alumina to provide head dissipation. Thickness of the plate is 0.025 inch. The carbon composition resistance is bonded directly to the plate. In addition to variable resistors, units can be supplied with fixed resistance. Multiple units with two, three or four sections are also available. Leads are furnished in three tab styles for insertion into printed board, chassis-wire leads are also available.

CIRCLE NO. 270

Hybrid FET op amp self-stabilizing

Zeltex, Inc., 1000 Chalomar Rd., Concord, Calif. Phone: (415) 686-6660. P&A: \$90; 2 weeks.

A subminiature differential FET op amp of hybrid IC construction permits installation without the need for external stabilizing networks. In contrast to conventional monolithic chip amplifiers, the cube has a constant 6-dB-per-octave rolloff characteristic. It requires only the addition of input and feed back components to become operational. Dc open loop gain is 200,000 (typical); voltage drift, $20\mu V/^{\circ}C$ max; input impedance 10,000 MQ; common mode rejection 5000:1. Output capability is ± 10 V at 4 mA with shortcircuit projection.





CURICAL, Degree 2 or srs. experience in the oppil i sensing, measuring and c c c circuits and devices to cluring calibrent. Mill mee und. Development gen addium size LOCAL corr auting amory to 5

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Position Wanted

Capable, handsome, hard-working young rectifier with good references ...

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67R10



Little Emancipator

You can often do away with a separate slave relay or lock-in circuitry, simply by using a Heinemann Silic-O-Netic® timedelay relay. Its continuous-duty coil can remain energized indefinitely after actuation, and its SPDT or DPDT contacts are rated up to 5 amps (for the Type B shown). Your saving shows up not only in circuit simplicity but in the remarkably low price of the one relay that'll be doing two jobs extremely well.

The five Silic-O-Netic models offer a combination of fast recycling, good repeatability, and low power consumption—virtues inherent in their hydraulicmagnetic actuation. No thermal elements, and therefore no "heat-loading" on frequent recycling, and only minor effect on delay from ambient-temperature changes.

We offer sixteen standard timings, 1/4 to 120 seconds, with reset time about 25% of the rated delay; coil ratings (for continuous duty, mind you) up to 250 vac or 120 vdc; plug-in models, hermetic models, openframe and enclosed-contact models. Bulletin 5006—yours for the asking—will tell you all you want to know about these versatile little t/d relays. Heinemann Electric Company, 2616 Brunswick Pike, Trenton, N. J. 08602.



COMPONENTS

Low pass filter cuts off from 2 Hz to 20 kHz



Data-Control Systems, Inc., East Liberty St., Danbury, Conn. Phone: (203) 743-9241.

The variable low pass filter has a cutoff frequency range from 2 Hz to 20 kHz. The characteristics of this filter may be switched from a 4-pole Butterworth Filter to a four-pole linear phase filter. The error in switching between these two characteristics at any cutoff frequency is 1 dB. The magnitude of the cutoff frequency is controlled by a 4-position range switch and a continuously variable multiplier which is calibrated from 2 to 20. The cutoff frequency depends on the multiple of the particular range and the value of the continuous frequency control. With the frequency control at any fixed value, the error in switching between any two ranges is onetenth of an octave.

CIRCLE NO. 272

Quadrature rejector denies at 30 to 1

Control Technology, Inc., 41-16 29th St., Long Island City, N. Y. Phone: (212) 361-2133. P&A: \$135; stock.

The model 324 accepts 400 Hz ac input signals and converts them to phase sensitive dc signals for use in instrumentation and servo systems. The maximum output voltage is \pm 20 V dc. This output voltage is proportional only to the inphase component of the input voltage. The effective quadrature rejection ratio is 30 to 1 minimum. The input impedance of the unit is 19 Kv and the voltage gain is 30. This gain may be externally adjusted. The only power supply for this solid-state unit is 28 V dc and an ac reference. The temperature range is -55° C to 100° C. CIRCLE NO. 273



A new miniature sensitive relay from RBM CONTROLS IT WILL BE COPIED BUT NEVER EQUALED

Reason—RBM CONTROLS has more production and quality control experience and has built more sensitive miniature relays than any other manufacturer in the industry.

The new miniature Type 64 is an isolated contact relay for maximum sensitive applications where reliability, rugged construction and low cost are of major importance. This low level circuit switching relay is designed for compact areas and may be stack assembled in close proximity to each other. A protective nylon cover eliminates physical contaminance or mishandling. A variety of mounting brackets are available making this relay the most versatile in the industry.



CONTACTS

Ratings: 28V DC or 115 V AC 1 Amp (Non-inductive) Form: SPNO, SPNC, SPDT Type: Cross-Bar

COIL RATINGS

Maximum—1 Watt Minimum—.050 Watt Resistance—10,000 Ohms Max.

TERMINALS

Contact: To Mount To Printed Circuit Board

APPROXIMATE DIMENSIONS

Overall Including B	rackets an	d Mo	untings	;)	
Printed Circuit	L 1-3/16"	хW	3/4"	хΗ	1-3/8"
Bottom	L 1-3/16"	хW	3/4"	хΗ	1-9/16"
Тор	L 1-11/16"	хW	3/4"	хΗ	1-15/32"
Parallelogram	L 1-3/16"	хW	1-7/16"	хН	1-15/32"

MOUNTINGS

Printed Circuit Board Bottom Top—Parallelogram Replacement Top Mounting Also Available



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Here is complete descriptive data on the Cutler-Hammer switch line, designed especially for military applications—everything from push-buttons to "Positive Action" switches (nearly everything that flies uses Cutler-Hammer "Positive Action" switches!). Reader Service No. 202



Commercial Specialty Switches New 36-page catalog is filled with detailed information on hundreds of switches for every application . . . appliances . . . power and hand tools . . . photo equipment . . . business machines . . . you name it ! Truly a buyer's guide for quality switches. *Reader Service No.* 203



Power Relays

Here is the book on performancetested relays designed for electrical control on aircraft, space vehicles, ordnance, ground-support equipment. Contains illustrative photos, engineering data, drawings and ratings on our full line of power relays. *Reader Service No. 204*



Shallcross Rotary Switches

A brief but complete booklet on all Shallcross rotary switches. Series 1 (1-inch deck), Series 2 ($1\frac{3}{4}$ -inch deck), Series 4 ($2\frac{1}{2}$ -inch deck) round ceramic switches and oval ceramic switches. Includes description, design characteristics and selection tables. Reoder Service No. 205



COMPONENTS

Transducers function on 60 Hz level



Robinson-Halpern Co., 5 Union Hill Rd., W. Conshohocken, Pa. Phone: (215) 825-1700.

These high sensitivity linear position transducers which provide infinite resolution are available in 20 different models providing ranges of travel from ± 0.010 to ± 4.0 -inches. Features of the 230 series are high sensitivity (to 100 mV/mil) and linearity to 0.05%. Long life is assured because there is no wearing of the parts during operation. These transducers are electromechanical transformers that convert the linear displacement of a ferro-magnetic core to an ac output signal by changing the differential flux linkage between windings. For installation ease, a wide variety of mounting options are available.

CIRCLE NO. 274

FET preamplifier uses 6 V battery

United Detector Technology, P. O. Box 2251, Santa Monica, Calif. Phone: (213) 393-3785.

An internal 6 V mercury battery provides the total power requirements for the amplifier and provides bias voltage for a silicon photodiode or other radiation detectors. It is only necessary to connect the photodiode to the input connector of the model 100 and an oscilloscope or ac-vtvm to the amplifier output connector to form a sensitive photodetection system. The internal batteries and completely shielded case enclosure eliminate ground loop hum pickup. Wideband input noise voltage is less than 15 µv rms. The input impedance is 1 m Ω , giving a wideband noise equivalent current input of less than 15 pA.



A AT Series Assemblies —Combine up to 12 SPDT Switches.

B TL Series—Meets MIL-S-3950A. Momentary, maintained, pull-to-unlock. 1, 2, or 4 pole.

C TW Series—Miniature, 5 amp. SPDT or DPDT, 2 and 3 position.

D TS Series—Up to 25 amp. 1 or 2 pole.

E ET Series—Magnetichold, remote-release. Environment-proof.

F TP Series—Rockeractuated, 20 amp. 1, 2, or 4 pole.



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You may even pick up some extra design ideas as well ways to simplify circuit design, combine functions in fewer controls, improve operator efficiency.

Catalog 73 gives details. Call a Branch Office or Authorized Distributor (see Yellow Pages, "Switches, Electric"). Or write . . .



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Here's how to cut the time it takes to solve your control/alarm problem. Hook up sensor, load and power source to a MAG-SENSE® control/alarm module and adjust the setpoint. That's it. No time wasted designing and debugging a circuit. And while you're saving time you'll be saving money, getting proven-in-service reliability.

Capabilities? All MAGSENSE modules offer 100-billion power gain, accept inputs as low as 10 microvolts or 1 microamp directly without preamplification. Completely isolated inputs are unaffected by common mode voltages as high as 110vac, 60Hz, or overloads as large as 1000-times full scale input. Typical accuracy is $\pm 0.5\%$ full scale. And they all



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PERELA

POWER

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MAGSENSE Sales, Dept. 205 Analog-Digital Systems Division Control Data Corporation 4455 Eastgate Mall La Jolla, Calif. 92037 Phone 714/453-2500

COMPONENTS

Dc potentiometer accurate to 0.1 ppm



Hallmark Standards Inc., 145 Library La., Mamaroneck, N. Y. Phone: (914) 698-8460. P&A: \$7890; 120 days.

The current comparator operates on the principle that when the ampere-turns imposed on a magnetic core by current in two windings are equal and opposite, the flux in the core is zero. The zero flux condition is used in a feedback circuit to automatically adjust the current in one winding so that it maintains ampere-turn balance. A constant current source supplies current to a winding in which the turns are variable from zero to 1000 in steps of 1 in 10^7 of the total.

CIRCLE NO. 276

Military relay handles 10 Å



C. P. Clare & Co., 3101 Pratt Blvd., Chicago. Phone: (312) 262-7700.

Incorporating all of the features of Clare F relays, the PF provides contact ratings from low level to 10 A, with an operate and release time of 7 ms and initial circuit resistance of 10 m Ω (20 m Ω after 100,000 operations at 10 A, 28 vdc.) The PF is rated at -65° C to $+125^{\circ}$ C, with a vibration of 20 g's peak acceleration, 10-2000 cps, and shock of 50g's half sine wave, 11 ± 1 ms.



These high power diffused silicon transistors in the TO-82 package are also available as JAN devices for your military requirements.

ELECTRICAL CHARACTERISTICS (Tc=25°C, unless otherwise noted)							
CHARACTERISTIC	TEST CONDITIONS	2N1015 SERIES MIN. MAX.		2N1016 MIN.	UNITS		
Breakdown Voltage, Collector to Emitter, BVCEO (SUS)	*Ic=100mA, IB=0	30 A-60 B-100 C-150 D-200 E-250		30 A-60 B-100 C-150 D-200 E-250		Volts Volts Volts Volts Volts Volts Volts	
Collector Cutoff Current, Icex	VCE=rated voltage VBE=1.5V, TC=150°C		20		20	mA	
Emitter Cutoff Current IEBO	VEB=25V, IC=0, TC=150°C		20		20	mA	
D.C. Forward Current Gain, h FE	*Ic=2 Amps, VcE=4V *Ic=5 Amps, VcE=4V	10		10			
Saturation Resistance, rcE (sat)	*Ic=2 Amps, IB=300mA *Ic=5 Amps, IB=750mA	0.3 Typical	0.75	0.2 Typical	0.5	Ohms Ohms	
Base to Emitter Voltage, VBE	*Ic=2 Amps Vce=4V *Ic=5 Amps, Vce=4V	1.5 Typical	2.5	1.7 Typical	3.5	Volts Volts	

*Pulse Cond. 300 $_{\mu}$ sec., 2% duty cycle.

Available from stock.

For further information contact your local representative, distributor or





TUNABLE LOW PASS FILTER

SILICONIX ASSUMES NO RESPONSIBILITY FOR THE CIRCUIT SHOWN, OR DO THEY REPRESENT OR WAR-RANT THAT IT DOES NOT INFRINGE ANY PATENTS.

Corner frequency of this RC pifilter is voltage tunable over a 100:1 range. A Siliconix VCR* has the equivalent circuit—drain resistance and gate capacitances form the pi. V_c controls the variation by changing r_{di} . To change the frequency range ... shunt the gate and source with fixed capacitors.

Build this or other circuits with the VCR FET Designer's Kit "DK6"—includes 6 VCR FETs worth \$30—available from your distributor for \$19.50. Check inquiry card or write . . . we'll be happy to send literature.

* VCRs are voltage controlled resistors — a new family of FET devices — featuring a variable resistance range of typically 10,000 to 1.



ON READER-SERVICE CARD CIRCLE 137

Complementary MOS ICs bring LSI a bit closer

RCA, Electronic Components and Devices. 415 S. 5th St., Harrison, N. J. Phone: (201) 485-3900. P&A: \$17.50; evaluation quantities.

Monolithic n- and p-channel complementary MOS ICs provide one low-cost approach to large scale integration (LSI) of complex functions. RCA's type TA5361 is a dual 3-input NOR gate plus inverter mounted in a 14-lead ceramic dualin-line package. Reversing power supply and ground connections makes the NOR gate a NAND gate (see schematic.)

The complementary mode of operation provides advantages over both bipolar transistor and singlechannel MOS transistor logic circuits. It eliminates the need for multiphase clocks, permitting operation from a single power supply. It provides distinct hard logic levels at ground and power supply voltages, increasing fan-out capability to 50, and greatly reduces power supply requirements because of the low quiescent power characteristic.

The new chip is designed for use in computer memories, calculators, displays and peripheral equipment as well as communications and data transmission systems.

In most digital equipment only a very small fraction of the digital elements are switching during any given time period. MOS transistors that are connected in the complementary mode of operation offer very significant power saving opportunities inasmuch as all the power dissipated by the circuit occurs during switching and practically none (nanowatts) is dissipated when the circuit is in either the high or low quiescent state.

Fabrication difficulties in diffusing p and n-channel enhancement-type MOS transistors on the same substrate have kept technology on the advanced development level. However, operating circuits with more than 500 MOS transistors have already been fabricated (see page 81).

Complementary MOS processing has fewer steps and requires fewer photo masks than the bipolar process, thus cutting costs and lead times on delivery. MOS transistors are less than one-quarter the size of bipolars, and resistors are not required in complementary circuits. Transistors of opposite polarity take the place of load resistors.

CIRCLE NO. 278



Current cannot flow directly to ground in this complementary MOS array. It dissipates 10 nanowatts when idle and 400 microwatts at 100 kHz.



The HD-1A sweeps this entire spectrum 1000 kHz to 900 MHz

Significant? Yes, because the HD-1A Sweep Generator can thereby replace a half dozen or more instruments at a cost less than most any one of them. It will do the work of a number of signal generators and provide a swept output instead of a single frequency. Its wide range includes an area of the spectrum normally covered by several ordinary sweep generators.

The HD-1A can also be used as a frequency analyzer providing data for frequency identification and stability, signal purity, and signal strength.

And it does all this at a cost of only \$995.00

SPECIFICATIONS

Catalog \pm 70 includes complete data on the HD-1A, 30 other Telonic Sweep Generators and accessories as well as a complete section on Applications. Write for your copy today.



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ELECTRONIC DESIGN 19, September 13, 1967



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Laboratory For Electronics, Inc. WALTHAM, MASSACHUSETTS 02154 Tel: 617-894-6600 • TWX: 710-324-0681 ON READER-SERVICE CARD CIRCLE 139

MICROELECTRONICS

Wideband amplifier in TO-5 case



Halex, Inc., 139 Maryland St., El Segundo, Calif. Phone: (213) 772-2545.

The model HX610 has built-in bypass capacitors and requires no external components for use as an amplifier with a voltage gain of 26 dB. Maximum output voltage swing with no load is 20 Vpp to 8 MHz; derating is to 2.5 Vpp at 100 MHz, and with 100 Ω load is 5 Vpp to 40 MHz with derating to 1.2 Vpp. The Hx610 can switch up to 200 mA to an inductive load. Numerous circuit points are available externally and the device can be connected as a flip-flop, monostable multivibrator, phase detector, voltage controlled oscillator, or, by adding a crystal, as a crystal oscillator. By adding an external 150 Ω resistor, unity gain can be achieved.

CIRCLE NO. 279

Amplifier array uses ICs

RCA, 30 Rockefeller Plaza, New York. Phone: (212) 265-5900. Price: \$1.50 (1000 up).

An ultra high-gain, wideband IC amplifier array, designed for application in circuits such as remote control amplifiers in TV receivers and in hearing aids, has a gain of 129 dB at 40 kHz. This linear monolithic silicon IC offers opportunity for amplifier compactness with good low-noise performance, highfrequency response and maximum system flexibility. The CA3035 and CA 3035VI arrays consist of three separate amplifiers. Each can be operated independently or in cascade with the others. The gain and bandwidth for each can be adjusted separately with suitable external circuitry.



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We'd like to send you detailed performance data on nonflammability. Write Du Pont Company, Room 5268, Wilmington, Del. 19898.

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Here's our best price yet for a high-gain, low-noise 200 MHz (MOS) FET



RCA's 3N128 N-channel, depletion-type MOS field-effect transistor actually offers dynamic range 10 times greater than conventional bipolar transistors. Because of the resulting improvement in cross modulation and spurious response, the 3N128 is recommended as an RF amplifier *and* a mixer to further improve performance in these areas. You can use standard vacuum-tube-type biasing and AGC requires virtually no power.

Extremely low feedback capacitance (0.2 pF max.) and high transconductance (5,000 μ mhos min.) allow the 3N128 to deliver more useable VHF power gain without neutralization than a junction-gate FET can deliver with neutralization. Even more stable gain can be achieved with neutralization.

Operation over wide temperature ranges is possible because the extremely low gate leakage current (0.1 pA typ.) is not temperature sensitive. In fact, when you consider the combination of large signal-handling capability, exceptional performance characteristics, and low, low price, the 3N128 is an absolute necessity for critical front-end designs—and for a broad range of industrial, commercial and military applications in instrumentation, controls, and appliances, where high input resistance (10^{14} ohms typ.) is important.

So call your RCA Field Representative for complete information. For technical literature and application notes AN-3193 and AN-3341, covering RF amplifiers and mixers, write RCA Commercial Engineering, Section EG9-1, Harrison, N.J. 07029. Check your RCA Distributor for his price and delivery.

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- HIGH TRANSCONDUCTANCE 5,000 µmho min.
- HIGH POWER GAIN @ 200 MHz 18 dB typ.
- LOW NOISE FIGURE @ 200 MHz 4 dB typ.

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MTBF up to 100,000 hours Components:

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- .02% Regulation 500 microvolts ripple & noise
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		PACKAGE	PACKAGE	PACKAGE	PACKAGE						
	3.53	AA	AB	AC	AD	AE	AF	AF	AG	AH	AL
	Nominal Volts	AMP	ERES					_			
	3	.8	1.5	3.8	5.3	11	14	20	24	32	36
	5	.76	1.45	3.7	5.2	10.5	13.5	19	23	31	35
	6	.74	1.4	3.5	5.2	9	13	18	22	30	34
	9	.53	1.2	3	4.4	8	12	15	20	27	32
	12	.53	1	2.8	4	7.3	10.5	15	17	22	28
	15	.42	.75	2.7	3.5	6.3	10	13	17	21	28
1	18	.39	.69	2.6	3.2	5.8	9	12	16	19	25
1	21	.35	.6	2.2	2.9	4.7	8.5	11	14	17	22
	24	.34	.53	. 2	2.7	4.6	8	9.2	13	16	20
	28	.34	.53	1.9	2.5	4.6	7.5	9.2	12	15	19
	32	.22	.51	1.2	2.2	3.9	6	8.7	11	13	18
	PRICE:	\$65.	\$75.	\$115.	\$135.	\$175.	\$195.	\$25 5.	\$295.	\$390.	\$420.
	36	.21	.44	1.1	2	3.6	5.7	7.3	10	12	17
		.21	.37	1.1	1.8	3.3	5.5	6.2	9	12	15
		.21	.35	1	1.7	3	5	6.1	8.5	10	12
		.21	.34	.85	1.6	2.8	4.1	6.1	7.4	8.7	11
	60	.2	.30	.75	1.4	2.6	3.7	5	6.5	7.8	9.5
	68	.19	.28	.7	1.3	2.4	3.5	4.7	5.9	7.2	9
	76	.17	.25	.65	1.2	1.7	3.3	4.4	5.5	6.6	8.5
	84	.17	.24	.6	1.1	1.6	3.1	4.2	5.1	6	8
	92	.088	.23	.5	1	1.5	3	4	4.4	5.6	7
	PRICE:	\$75.	\$85.	\$125.	\$145.	\$185.	\$235.	\$285.	\$375.	\$405.	\$455.
	100	.088	.16	.37	.58	1	1.8	2.1	2.8	3.3	4.3
1	120	.088	.16	.3	.54	.98	1.7	2	2.6	3.1	4
	135	.084	.15	.28	.52	.88	1.6	1.9	2.4	2.9	3.5
	150	.08	.14	.25	.48	.81	1.4	1.8	2.1	2.7	3.1
	165	.078	.11	.23	.42	.59	1.3	1.6	2	2.4	2.9
	180	.072	.11	.2	.36	.58	1.2	1.5	1.8	2.2	2.5
	195	.062	.1	.17	.33	.52	1.1	1.3	1.7	2.1	2.3
	210	.052	.09	.14	.28	.5	.8	1.1	1.6	1.9	2.2
	230	.042	.07	.12	.24	.46	.73	1	1.5	1.8	2.2
	250	.026	.06	.1	.21	.42	.63	.84	1.4	1.7	2.1

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PRICE: \$95. \$105. \$135. \$160. \$195. \$245. \$295. \$395. \$420. \$470.

D 107



RELATIVE SIZES OF C-SERIES PACKAGES



SEMICONDUCTORS Op amp rises in 2 μs



Zeltex, Inc., 1000 Chalomar Rd., Concord, Calif. Phone: (415) 686-6660. P&A: \$75; 1 wk.

This single-ended inverting amplifier is for use in analog computation, voltage comparators, high speed integrators, D/A conversion and buffering applications. Settling time is less than 2 μ s to reach 0.01% of full output when it is used as a 10 K inverter. The model 147 requires a power supply of ± 15 V dc (10 mA quiescent) and has a rated output voltage of ± 10 V at 20 mA. The gain-bandwidth is adjustable between 10 and 100 MHz.

CIRCLE NO. 362

Germanium transistor handles 25 A

Motorola Semiconductor Products, Inc. P. O. Box 955. Phoenix. Phone: (602) 273-6900. Price: \$2.25 to \$2.60.

These transistors have a power handling capacity of 80 to 120 V at 8 A. In addition, they have a current gain of 25 at 8 A, low saturation voltage of 0.6 V at 25 A and a switching time of 9 μ s at 10 A.

Using the alloy diffused epitaxial construction process, the MP2200A-2400A switch family is ideal for core driver, power conversion and high-voltage switching. Starting with a low resistivity diffused base (for high gain), an aluminum doped emitter is alloyed into the die (for sustained h_{fe} at high current) to form a comparatively wide base width for good safe operating area. Presence of the diffused structure in the base minimizes the switching losses ordinarily incurred as a result of a wide base width. COOL

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One of the new Ellis and Watts Heat Exchangers may be the answer to a need for tailoring a cooling system to your type of electronic equipment. Minimum space, low noise level and optimum performance have been achieved in each of a wide range of designs which include indoor/outdoor types in ratings from 5 to 300 KW. Proved in military, aerospace and commercial applications, these designs offer flexibility for quick modification to meet any specific cooling requirements.

Why not put the widely recognized Ellis and Watts custom-cooling "know-how" to work for you. Write us at the address below.



*Liquid-to-Liquid Heat Exchangers also available.



ELLIS AND WATTS COMPANY Ellis and Watts Company, P.O. Box 36033 Cincinnati, Ohio 45236

CIRCLE NO. 281

送油出的。

SEMICONDUCTORS

Varactor chips eight mils thick



MSI Electronics, Inc., 34-32 57th St., Woodside, N. Y. Phone: (212) 672-6500. Price: \$4.50 to \$16.50.

Eight-mil-thick varactor diode chips are available in capacitance ranges from 3 to 18 pF. The junction contact depends upon the capacitance value, and varies from 0.002 to 0.011 inches diameter. Each chip can dissipate up to 300 mW when suitably mounted for heat dissipation. Operating junction temperature is 150° C, although the chip will withstand temperatures for a short period of up to 400° C.

CIRCLE NO. 282

Bridge rectifiers rated to 2500 V



Sarkes Tarzian Inc., 415 N. College Ave., Bloomington, Ind. Phone: (812) 332-1435. Price: \$3.27 to \$8.47.

Insulated-case rectifier assemblies reduce assembly time, since they mount to PC board or chassis with one screw. Terminals are silver-plated brass for easy soldering. The rectifiers are 3/4 inch square and 3/8 inch thick. Electrical ratings include PIVs of 1500, 2000 and 2500 with current ratings of 1.2, 0.8 and 0.6 A for resistive-inductive loads with ambient operating temperatures up to 75° C.

Silicon rectifiers handle 200-A surges



Electronic Devices, Inc., 21 Gray Oaks Ave., Yonkers, N. Y. Phone: (914) 965-4400. P&A: 64¢ (100 lots); stock.

A series of 2-A axial lead silicon rectifiers with a surge rating of 200 A is 0.2 inches in diameter and 0.38 inches long with a transfer molded, void-free body. PIV ratings range from 50 to 1200 V. The 2-A rectifiers replace 7/16-inch units and eliminate heat sinks. They are designed for mounting on component boards or for use as bridge assemblies.

CIRCLE NO. 284



U.S. Patern No. 3,126,44

POLASHEET II

flexible, compressible, cutable pressure and RFI connector seal

With sealing pressures up to 30 psi, Polasheet II yields overall system attenuation of 125-135 db. Polasheet is oriented wire imbedded in silicone rubber sheets from .062" that can be cut or

stamped into resilient, flexible gaskets of any shape. Requires no machined surfaces because it's compressible. About 12¢ per sq. in. Free samples, prices, literature. Write today.

METEX Corporation 970 New Durham Road, Edison, N.J. 08017 (201) 287-0800 • TWX 710-998-0578 West Coast: Cal-Metex Corp., 509 Hindry Ave., Inglewood, Calif.



CONTROL CABINETS HAVE NEVER BEEN IN BETTER SHAPE

A new innovation in cabinet design with wide applications in the realm of enclosures for controls. The cross section of the frame is a replica of the widely recognized Bud Radio insignia.

Rigid frame is all steel welded construction. Removable front panel and hinged door of equal size permits easy installation of components.

Skillfully planned so that entire contents are available for service and inspection when door is opened. Viewing angle of both panel and door

provides comfortable observation of dials, meters, switches, lights, etc. Attractively finished with scratch resistant vinyl texture charcoal gray body with light gray enamel front panel and rear door. Three sizes available.

These exciting new housings are available from your nearby authorized Bud distributor. If you don't know him, let us introduce you. Write us for his name. You can obtain complete descriptive literature from him, or we'll be glad to send it to you.





4605 East 355 Street, Willoughby, Ohio 44094



Program wiring patterns from A to Z with automatic **(Dire-Orcp** machines

Only automatic "Wire-Wrap" machines provide the flexibility required for point to point wiring of modular electronic panels. Just program the circuit with punched cards or tape. Then "Wire-Wrap" machines take over —connecting wires at an average of 5 seconds per wire—as much as 25 times faster than hand soldering in most applications.

Reliability—These solderless wrapped connections are permanently tight—unaffected by temperature changes, atmospheric corrosion, vibration. More than 37 billion such connections are in use today without a single reported failure.

Economy—Cost savings in excess of 92%are common when compared to soldering and other techniques. Additional benefits include: No thermal damage to heat-sensitive materials . . . elimination of fire hazards . . . connections that are easily removed in plant or in the field.

Write for Bulletins 14-1, 14-121.



Typical wiring patterns made with automatic "Wire-Wrap" machines.

ON READER-SERVICE CARD CIRCLE 146

SEMICONDUCTORS

Npn Darlingtons for small signals



Solitron Devices, Inc., Transistor Div., Riviera Bch., Fla. Phone: (305) 848-4311. stock.

These npn silicon planar Darlington transistors contain two transistor chips mounted in either a 3-lead or 4-lead TO-18 package.

These signal transistors are also available as matched pairs. Typical characteristics include B_v greater than 85 V, emitter break-down voltage greater than 12 V, gain at V_{CE} =5V, IC=10 mA typically 10,-000 to 50,000.

The Darlingtons can be used as constant current supplies, voltage comparators, power supplies, high input impendance networks and boot-strap timers.

CIRCLE NO. 285

Current regulating diodes use field effect

Motorola Semiconductor Products, Inc., P. O. Box 955, Phoenix, Phone: (602) 273-6900. P&A: \$4.90 100 up; stock.

A series of current regulating diodes covers the current range from 0.22 to 4.7 mA. The 1N5283 through 1N5314 current regulators are field-effect diodes that establish a constant current flow independent of voltage. The current regulating function is for use as a constant current source in differential amplifier circuits, ramp generators, transistor biasing, or as an active, high impedance load for high voltage-gain amplifiers. In voltage reference circuits, the current regulator can supply a temperature compensated zener diode with a constant current to limit zener voltage changes caused by large current excursions in an unregulated circuit.

CONTACT SILVER USAGE UP TO 70% AND SLASH COST OF CONTACT SUB-ASSEMBLIES





Typical Contact Blade Subassembly manufactured under the patented Deringer Duell* Head Process.

*U.S. Patent 3,311,729

Base metal here instead of silver can mean tremendous savings to you at no sacrifice in contact reliability. In certain applications, arcing and erosion have been reduced and improved mechanical life has resulted.

This very substantial reduction in the amount of silver used in a contact is particularly important when you consider the scarcity of silver and the increasing cost of silver. To coin a phrase, Deringer gives you more for your silver dollar! How much more? Well, in sub-assemblies of the type shown above, Deringer can provide the entire sub-assembly for what was the cost of the contact alone.

The exclusive new Deringer Duell* Head Process is just one new development aimed at helping you reduce the cost of your electrical contacts and sub-assemblies. For a review of your contact applications to determine if one of Deringer's unique manufacturing processes can save you money while maintaining or improving reliability, contact Deringer.



METALLORGICAL CORPORATION

1250 Town Line Road - Mundelein, Illinois 60060

PRODUCTION EQUIPMENT

Laser system trims resistors



Spacerays, Inc., Northwest Industrial Park, Burlington, Mass. Phone: (617) 272-6220. Price: \$17,-500.

A complete production resistor trimming unit, this system can bring resistance values to within a 0.1% tolerance after the resistor has been hermetically sealed. The resistor is installed by the operator, the value is classified and the system begins a step-by-step evaluation, trimming, and repositioning process.

CIRCLE NO. 359

Transistors analyzed go/no-go



Lectrotech, Inc., 1221 W. Devon Ave., Chicago. Phone: (312) 764-7005. P&A: \$87.50; stock.

Transistor leads do not have to be unsoldered or clipped for in-circuit tests with this tester to measure ac gain. Out-of-circuit tests measure beta or gain on two scales: 0 to 250 and 0 to 500. Biasing is automatic and no calibration is required. The TT-250 measures transistor leakage (I_{CBO}) , directly in microamperes. All testing is nondestructive, whether in or out of circuit.

CIRCLE NO. 360

Photoresist dispenser for microcircuit coating



Headway Research, Inc., 3713 Forest Lane, Garland, Tex. Phone: (214) 272-1566.

Once this machine is loaded, the rest of its cycle works automatically. Spray rinsing, photoresist dispensing and spray developing following the image exposure are automated. The speed is changed automatically during a cycle. In the complete cycle the substrates are spray rinsed and dried while spinning. Spinning then stops and resist is dispensed. Wafers spin at the second preset speed, evenly spreading and drying. The basic spinner comes with 1 to 5 heads.

CIRCLE NO. 287

Save with NEG'ATOR counterbalances

Save space, save weight, save more on production costs too, with Hunter's NEG'ATOR springs in your products. A strip of spring steel forming a pre-stressed coil, the NEG'ATOR resists uncoiling with uniform pull, thus providing the same rated force at any extended length. So with the NEG'ATOR constant-force spring, you'll eliminate deadweights and linkages, and simplify mountings. For complete information on the cost and design advantages of the NEC'ATOR spring write Ametek, Inc., Hunter Spring Division 27 Spring Street Hatfield, Pennsylvania 19440.



ON READER-SERVICE CARD CIRCLE 148



ELECTRONIC DESIGN 19, September 13, 1967

Chapter VI.

* The Word from GENISCO.

NICE TRY, GUYS

Man's first acrospace project, Babel I, utilized a straight-forward design concept: Travel into outer space would be effected by climbing a tower. However, it did not meet noise specifications, and the mission was aborted.

Now that Genisco offers a complete selection of power line filters and shielded enclosures you can avoid analogous difficulties.

Rated from 30 amps to 200 amps, 120 V to 250 V, single or three-phase power lines, the three series are designed for typical circuit breaker panelboards with or without requirements for power line filtering, and for use in shielded rooms and for installations requiring electrical distribution.

Although these power line filter assemblies meet MIL-F-15733, we do not recommend their use in towers extending beyond terrestrial limits. This application is not approved by The Chief Design Engineer.

ON READER-SERVICE CARD CIRCLE 121

DIDJA HEAR THE ONE ABOUT THE

BI-PLANAR NAB 14" REELS? Seems like there's this Model 10-276 magnetic tape recorder for aircraft, shipboard, or field portable use. Now, it has this low inertia capstan drive motor, and 6 speed selectable servo to eliminate belts, pulleys, and like that. And get this: no pinch rollers and solenoids to create flutter and skew! Well, these Genisco guys are making a mint on the thing, but they figure they'll come out with a Model 10-286 with 14" instead of 8.5" reels for customers who need longer record time! Then they go and stack the reels in a bi-planar configuration to save space. The funny thing is it works great. Not much of a story maybe, but they sure are nice tape recorders.

ON READER-SERVICE CARD CIRCLE 122

Earn Big \$\$\$\$ as a Telemetry Person !!!

яюьтш

Now you can learn telemetry in the privacy of your own home! Take this free aptitude test NOW!

1. (T) (F) A telemeter is what they put on the back of the TV to find out what you watch.

2. (T) (F) A telemetering checkout station is where you sign out for a telemetering.

Congratulations! You've just won our free correspondence course! Naturally you'll now want a Model A-180 or A-186 completely portable ground station. The A-180 completely de-multiplexes any standard FM/FM Signal. Ideal for checkout of airborne or sledborne applications. The A-186 has fourteen stunning channels. Its receiver is continuously tunable over the 215MC to 260MC band. So get on the road to success! Buy some of our telemetry stuff. ON READER-SERVICE CARD CIRCLE 123

WHEEP! WHEEP! WHEEP!

As your missile speeds downrange you are secure in the knowledge that its electroexplosive device can be armed only by the precise signal you alone can send.

Or, horror of horrors, by an unfiltered random burst of identical frequency and duration.

As perspiration beads your brow you feel a sudden fondness for Genisco, renowned experts in RF hazard testing. How nice of them, you think, to have in stock or to design just the filters for the RFI and EMI protection my firing circuits need.

By golly, you conclude, next one of their ads I see I think I'll just

ON READER-SERVICE CARD CIRCLE 124

IT JUST KEEPS ROLLIN',

KEEPS ON ROLLIN' AROUND.

Going round and round is our new Model 1147 rate-of-turn table's main trick. It keeps at it no matter how much you abuse it.

Hydrostatic bearings give precise dimensional stability, excellent alignment, low runout and eccentricity, low mechanical noise, and long happy life. It rotates smoothly at less than sidereal rates (0.004°/sec.). And it's just as smooth up to 1500°/sec. Which is why particularly brilliant (and handsome) engineers picked it as the AGE gyro test table for the F-111 Aircraft System.

Great for the lab or just to tote around de field.



GENISCO TECHNOLOGY CORPORATION 18435 SUSANA ROAD COMPTON, CALIFORNIA 90221

PRODUCTION EQUIPMENT

Ultrasonic trimmer specs resistors



Axion Corp., 6 Commerce Park, Danbury, Conn. Phone: (203) 743-9281.

Avoiding the hazard and exhaust system of the abrasive dust method, this unit removes resistor material by ultrasonically vibrating a diamond tip. The substrate is moved in the X or Y direction automatically.

The resistor to be trimmed is connected by probes into a bridge circuit which automatically turns off the ultrasonic action when the resistor is within 0.5% of the target value.

The substrate is held on a motorized X-Y table by a vacuum chuck. Up to twenty probes can be set to contact various resistors, and are selected two at a time.

CIRCLE NO. 288

Vapor solvent cleaner sweeps ultrasonically



Tronic Corp., P. O. Box 247, Belmont, Calif. Phone: (415) 593-1487.

A portable solvent boil-chill-vapor cleaning system uses ultrasonics. Incorporating automatic continuous solvent reclamation and refrigerated coil cold condensation technique, the unit is a completely portable console. It requires no external plumbing, water, or venting. CIRCLE NO. 289

Direct Reading Precision Phase Measurements to 1MHz



The Aerometrics Model PM-720 Phase Meter covers from 0 to 180 degrees in four ranges. For measurements above 180 degrees, the PM-720 utilizes automatic lead-lag indicator lights to give direct reading capability to 360 degrees. The amplitude ratio of the two input signals car. be as high as 5000 to 1 with sensitivity of 100 mv (p-p) to 500 v (option available to 1 mv). For direct meter readings the accuracy is $\pm 2\%$ but increase accuracy of $\pm .2\%$ can be obtained by utilizing the DC voltage output which reads directly in degrees on a DVM. The compact, all solid state construction offers true portability (total weight 7 pounds). Aerometrics also offers Model PM-730 which covers 0 to 360 degrees in four ranges. The frequency is extended to 1 MHz. The PM-730 also offers the unique advantage of measuring phase relationship between dissimilar wave forms.

Do you have Phase Measurement Problems to 750 MHz?

The PM-730 can be used with the Aerometrics Model SA-300 pulse sampler to give precision phase measurements to 750 MHz. For further information, write or visit us at the ISA.

See us at ISA Exhibit Booth No. 544



750 MHz Sampling Oscilloscope for \$995



If your present oscilloscope has a minimum band width of 50 KHz, you can convert it into a high speed sampling oscilloscope using the Aerometrics dual channel pulse sampler. The Model SA-300 may also be used with an inexpensive X-Y recorder for permanent recording of fast computer wave forms, radar pulses, semiconductor characteristics, etc. The all solid state Aerometrics sampler offers rise time of typically one nanosecond and sweep speeds from 10 nanoseconds to 5 microseconds per full sweep. Like other Aerometrics instruments, the SA-300 features portability through compactness and light weight.

Multimeters, Pulse Generators & Electronic Counters

A full range of instruments which excel in precision, compactness, ruggedness, portability and flexibility-the most dependable instruments you'll ever use-and all in competitive price ranges. Be sure and check Aerometrics' specifications before investing in test equipment.



See us at ISA Exhibit Booth No. 544



Flip-chip bonder uses tape input



Bulova Watch Co., Valley Stream, N. Y. Phone: (516) 561-2600.

This unit will place and bond up to 1,000 flip-chips an hour. It uses standard 1-inch EIA 8-channel 1-2-4-8 coded tape prepared by commercial tape-punching equipment. It extracts semiconductors from magazines and then bonds them to circuits either ultrasonically or by thermo-compression. Prior to each bonding cycle one of three magazines is selected and positioned at the pickup station by tape instructions while the substrate is repositioned to the required placement coordinates. The machine will accept thin or thick film substrates in a variety of sizes containing any number of subcircuits. An automated keyboard control permits selective skipping of substrate circuits which have been classified as defective in prior testing operations. Optional devices permit automatic probing and marking of defective substrate circuits prior to bonding. CIRCLE NO. 290

Thermoplastic cases in many shapes

Skydyne, Inc., River Road, Port Jervis, N. Y. Phone: (914) 856-5241.

MIL-spec cases are available with shock-absorbing interior equipment mounting facilities and a selection of optional hardware. The thermoplastic, A.B.S., withstands high and low temperature extremes, has a high strength-to-weight ratio and ductility and the resulting energyabsorbing capability.

PRODUCTION EQUIPMENT

Pencil soldering side by side



Development Associates Controls, 725 Reddick Ave., Santa Barbara. Phone: (805) 963-3708. P&A: \$315; stock.

This unit includes a pencil type hand piece with miniature parallel gap electrodes. The walnut cased soldering controller is $6 \times 4 \times 4$ -inch. Both the time and current may be preset to control the amount of energy delivered to the connection. Time settings from .1 to 1.0 seconds may be adjusted by the time control knob while the current may be preset at any level from 10 to 35 A.

CIRCLE NO. 292

Double-barrel kiln operates to 1600°C



BTU Engineering Corp., Bear Hill, Mass. Phone: (617) 894-6050. P&A: \$15,000 to 16,000. 12 weeks.

For metallizing titanites, ferrites, and ceramic parts including co-firing of moly-manganese and alumina substrates, this kiln is capable of brazing, annealing, and sintering processes. Using twin tubes, the unit has an operating range of 800° to 1600° C, with an accuracy of $\pm 1^{\circ}$ C.

CIRCLE NO. 293

Card extractors for data systems



Protolab, 294 Polaris Ave., Mountain View, Calif. Phone: (415) 961-8033. P&A: \$10 to \$35; 3 to 6 wks.

This self-storing card extractor is designed for removing PC cards. It can be used by manufacturers of computer, data handling, telemetry, instrumentation, telephone and ground-support equipment. The spring-loaded unit will protect systems and instruments from damage caused by hand handling and reduce card changing time.



Design engineer Bob Alden searched for nearly an hour to find he didn't have the information he needed.

if i could have told him that in less than 5 minutes.

But chances are 10-1 IRI has it...and it's up-to-date, too! IRI-Information Retrieval Incorporated-is a vendor catalog file on microfilm. Because we make no charge to vendors, and microfilm everything they have in print, IRI has more than 600,000 pages of vendor data including application notes, reliability tests, price lists and names of distributors and reps. What's



more, the IRI Vendor Catalog File on microfilm is updated every 60 days!

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In addition, the IRI system is tailored to your needs by adding the vendors you want - at no extra cost. We start with a basic file of the most wanted information. Up-date it regularly, and then-to top it off-"personalize" the file by adding the complete vendor information you request. Small wonder IRI enables you to eliminate your hard copy central catalog file. It also reduces storage area by 98%, cuts redundancies in design efforts and increases sources and use of standard items.

You can install an IRI vendor catalog file on an annual subscription for less than the cost of a file clerk, IRI vendor catalog files are in use in plants with as few as fifty employees. The "user-orientated" idea not only provides current knowledge of the component state-or-the-art, but also expedites the purchasing function. Because of its complete coverage, more than 60% of our recent installations have replaced other microfilmed vendor catalog systems.

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I'm not ready for a demonstration at this time, but please send further information.

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Number of employees at this location _____

We have have not had experience with a vendor catalog file on microfilm.

In addition to the Vendor Catalog File on microfilm we're interested in 🗌 Mil Specs 🔲 Mil Standards.



we call them Scott-1 transformers and originally built them for military data systems. They meet the requirements of Mil-T-27B, Grade 5.

SPECIFICATIONS								
	9652	9514						
Input Voltage		00.14						
(line to line)	11.8 V	90 V						
Imput Impedance (at rated voltage)) 100 K Ω	1 Meg Ω						
Output Impedance	$e = 11 \Omega$	11Ω						
Null	<1 mv	<1 mv						
Output Voltage (RMS)	5 V	4.25 V						
Frequency	400 Hz	400 Hz						
Accuracy (depending on								
load)	30 secs	30 secs						
Temperature Range	–55°C to	+125°C						

GO THE OTHER WAY, TOO. Units for resolver to synchro conversion also available. Ask for data.

Circle the Readers Service Card Number for complete electrical, mechanical and price information.



TEST EQUIPMENT

Uhf sweep generator runs automatically



Sweep Systems Inc., 3000 Shelby St., Indianapolis. Phone: (317) 787-8275. P&A: \$425; 60 to 90 days.

This solid-state uhf sweep generator features completely automatic frequency tuning and rf attenuation eliminating all operator control. An electronic attenuator provides a 30-dB dynamic range of rf output level, and the tuner i-f or TV receiver second detector demodulated curve is maintained at a constant amplitude over this range. The instrument automatically tunes itself to keep the signal in the center of the display regardless of frequency or sweep width. In the event of shorted tuning plates or of loss of output, the instrument provides a 460-MHz sweep width until a signal is located. It then returns to the normal operating parameters for which it was preset. A remote control head is available which can be programed to control tuning, sweep width, rf output and markers depending upon the alignment sequence. The instrument and the circuitry will operate either from the tuner i-f output or the demodulated i-f signal of the TV receiver second detector.

CIRCLE NO. 295

Ac power supply ripples 1% rms

Pek, Inc., 825 Evelyn Ave., Sunnyvale, Calif. Phone: (408) 245-4111. P&A: \$675; stock.

This unit is designed for low current operation of short arcs and similar lamps. It features a current ripply of less than 1% rms. Power output, rated at 200 W, is adjustable over a 60 W range. The M703 is metered for lamp voltage and current, and has a built-in starter. The unit operates on 117 VAC $\pm 5\%$.

CIRCLE NO. 296

Current drivers give 20-ns pulses



Computer Test Corporation, 3 Computer Dr., Cherry Hill, N. J. Phone: (609) 424-2400.

An external range switch permits changing the amplitude of the output current pulse of these drivers in steps which are adjustable to within $\pm 15\%$ of the nominal setting.

Amplitude is continuously variable from 10 mA to 1 A in four overlapping ranges. The drivers are designed to operate with forward and back voltages of 75 V. Both pulse width and delay can be independently varied. Width is adjustable from 20 ns to 10 μ s and delay from 10 ns to 10 μ s.

CIRCLE NO. 297

Monitor scope sensitive to 1 mV per inch



ITT, 320 Park Ave., New York. Phone: (212) 752-6000.

A large-screen monitor oscilloscope has a sensitivity of 1 mV per inch with differential input. The instrument is applicable to telemetry, analog computer readout, highspeed X-Y plotting, sweep generation, data sampling and detection of envelopes. It has solid-state design, a 17-inch aluminized CRT and may be adapted for rack or bench mounting. Resolution is 20 lines per cm and linearity is 1%.

Reed Relay Problems?

A special hi-reliability relay for the Hawk Missile.

Can We Solve <u>Your</u> Problem?

Operating Inputs: Iow as 1mA, and 15mW. Standard Coil Voltages: 6, 12, 24, 32, 48V in stock for immediate delivery.

Special Voltage or Resistance, multiple windings for flip flop, memory and crosspoint selection applications — to customer specifications.

Relay Contacts in Form A, B, C and latching. Also high vacuum type 5000V Form A.

Write for catalog and prices of our standard line of magnetic reed relays. For special requirements, give complete details for quotation.



59 Pavilion Ave. Providence, R. I. 02905 Phone: (401) 941-3355

ON READER-SERVICE CARD CIRCLE 155

FOR THE GUARANTEED ANSWER TO YOUR AIR MOVEMENT NEEDS ASK FOR THESE BULLETINS



In the Howard CYCLOHM Fans and Blowers they describe, you get this unique combination of values:

MORE AIR AT LESS COST For proof, see the performance data and price schedules in the Bulletins.

GUARANTEED PERFORMANCE All CYCLOHM air movement units are Powered by the Howard Unit Bearing Motor, guaranteed for 5 years to require no maintenance or re-lubrication.

IMMEDIATE DELIVERY

of standard models. For availabilities contact Standard Motor Product Sales, 23 Broadway, Des Plaines, III. 60016 (TWX 910-233-1658).

3 Good Reasons for Requesting Bulletins 8-01 and 9-03 describing Fans and Blowers with Air To Spare.



How many kinds of plugwires do you need next week?

MAC ships off-the-shelf!

Getting a signal from P to Q — or from P₁, P₂, P₃ to Q, or from P to Q₁, Q₂, Q₃, etc. — is the function of the plugwire. MAC maintains a programmer's paradise of plugwires in inventory: single conductor, coaxial, dual conductor, two conductor shielded twisted pair, Y-type with 3, 4, 5 or 6 pin common, just to start the list. Color coded, 6" to 36". Bring order out of chaos. Off-the-shelf. You supply the order, we'll take care of the chaos.



TEST EQUIPMENT

Frequency counter ranges to 18 GHz



Hewlett-Packard, 1501 Page Mill Rd., Palo Alto. Phone: (415) 326-7000. Price: \$3000; stock.

A frequency counter counts directly from 10 Hz to 135 MHz. With available frequency converter plugins, its measurement range can be extended up to 18 GHz. The stability provided by the time base oscillator makes 11-digit resolution practical (3 digits displayed by a frequency converter plug-in, 8 digits displayed by the counter itself.) The circuit handles input levels from 100 mV rms up to 10 V rms without requiring any signal level adjustment. Null detectors get nanovolt ranges



Julie Research Laboratories, Inc., 211 W. 61 St., N. Y. Phone: (212) 245-2727. P&A: \$500; 30 days.

When combined with a modified basic null detector, this nanovolt preamp provides the user with a universal detector capable of all nVto-mV measurement applications. The device, which has a sensitivity or noise threshold of 1 nV and a resolution of 1 nV on the X1000 range, is for applications where standard galvanometers won't do because of inadequate sensitivity, low input impedance or insufficient stability. The input range of the unit is $\pm 1 \text{ nV}$ to $\pm 30 \text{ mV}$ full scale in 15 steps. CIRCLE NO. 311

L-band signal generators to 21 GHz

Polarad Electronic Instruments, 34-02 Queens Blvd., Long Island City, N. Y., Phone: (212) 392-4500. Price: \$1900.

With the introduction of a new L-band microwave signal generator, Polarad now offers a complete line of generators from 0.95 to 21 GHz.

The L-band unit, model 1105, covers the 0.95 to 2.4 GHz range. Like the other generators in the line, it can be used alone or racked or stacked with other modules. A frequency stabilizer can be added for phase locking the generator over its active range to crystal stability. FM, squarewave and pulse modulation is obtainable by the addition of a modulator. The line also includes signal sources covering the 0.95 to 11.0 GHz range. Frequency stability is 0.0008% per line voltage change and 0.005% per °C change in ambient temperature.

CIRCLE NO. 312



CIRCLE NO. 299



CRADLECLIPS

Versatile high quality harnesses for both supported and unsupported wiring systems. Fast and simple to use. Permit on-the-spot wiring changes without replacement or use of tools. Insuloid Cradleclips can be attached to panels by conventional means either from front or rear. Provide better air circulation and heat dissipation because harnesses are raised.

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ELECTROVERT	
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SOLD COAST-TO-COAST THROUGH AUTHORIZED DISTRIBUTOR

ON READER-SERVICE CARD CIRCLE 159 ELECTRONIC DESIGN 19, September 13, 1967
A payroll savings plan to benefit employee and employer alike.

As an employer, you and thousands more can have a voice in the stability of our economy and country by encouraging employee participation in the Payroll Savings Plan for U. S. Savings Bonds.

The reasons for setting up such a plan have always been sound — security for our country and systematic savings for your employees. Today these reasons are sounder than ever. So too are the rewards.

A brand new U.S. Savings Note called the "Freedom Share" carrying a new high rate of interest is available from the Treasury Department. Freedom Shares are companion notes to the popular Series E Bonds, and available through a regular plan like Payroll Savings.

When your employees purchase E Bonds they have the option to buy Freedom Shares, too, to an approximate one-for-one basis. Freedom Shares earn 4.74% when held to maturity of $41/_2$ years and must be held for at least one year. They are available in four denominations ranging from \$25 to \$100, and only one deduction is necessary to apply toward the Bond/Freedom Share "package."

A complete kit is available with all the information and material you'll need to set up the plan. Write for it today.

Treasury Department, U. S. Savings Bonds Division Washington, D. C. 20226

Dear Sirs:

Please send me a kit containing all I will need to set up a Payroll Savings Plan in my plant.

_____State____Zip

Name____

Position_____ Company_____

Number of Employees____

Address___

City____



In your plant ... promote the PAYROLL SAVINGS PLAN for U.S. Savings Bonds

The U.S. Government dues not pay for this advertisement. It is presented as a public service in cooperation with the Treasury Department and the Adverticing Council.



supply a complete range of voltages from 2,000 to 50,000 volt capacitors from our expanded "stock on hand". Don't take excuses, we'll supply you faster than at any time in our many years in the field— BETTER PRODUCTION FROM US— BETTER DELIVERY FOR YOU!

Write for complete list of Standard High Voltage Capacitors in stock—or, send specifications for custom guotations.



Plastic Capacitors, INC. 2620 N. Clybourn · Chicago 14, III. DI 8-3735

ON READER-SERVICE CARD CIRCLE 182

TEST EQUIPMENT

Rms-dc standard is 0.01% accurate



Metrics Div. of the Singer Co., 915 Pembroke St., Bridgeport, Conn., Phone: (203) 366-3201.

This automatic true rms transfer standard is capable of making a transfer measurement in under 10 seconds for frequencies from 2 Hz to 10 MHz. Accuracies range from .01% for ac signals up to 1000 V and to 20 kHz to 5% for ac signals up to 8 V and to 10 MHz. Readout may be made with any convenient dc measuring system such as a digital voltmeter, differential voltmeter or potentiometer and volt ratio box system. All the operator has to do to make connection to the ac input and dc output terminals is apply the unknown ac signal and read out in less than 10 seconds directly with an accurate dc measuring device.

CIRCLE NO. 313

Portable ohmeter reads to 50 k_{Ω}



Associated Research, Inc., 3777 W. Belmont Ave., Chicago. Phone: (312) 267-4040. P&A: \$139; stock.

This instrument is suited for determining resistance of leads, grounds, resistors, coils and similar electrical components.

The unit permits direct readings of resistances from 0.05 to 50,-000 Ω . It is calibrated in four ranges: 0 to 50; 0 to 500; 0 to 5000 and 0 to 50,000 Ω . The unit weighs 12 pounds and measures 8-7/8 x 6 x 8-1/4-inches.

CIRCLE NO. 314

Tracking filter ranges to 20 KHz



Agac-Derritron, Inc., 600 N. Henry St., Alexandria, Va., Phone: (703) 836-4641.

This automatic tracking filter is for use in combined sine-random vibration tests, resonant search and indentification studies, mechanical impedance investigations, and narrow band random signal analyses. The unit can be turned by any audio signal with an allowable amplitude variation of 40 dB over the frequency range from 3 Hz to 20 kHz. It has all solid-state circuitry, provision for five plug-in crystal filters. automatic bandwidth switching between all five filters, sine rejection output for performing automatic sine-random vibration testing, an internal 2 kHz calibration signal and a dc output proportional to the filter output. Power requirements are 100-125 V, 60 cps and 25 W. Size is $5-1/4 \times 19 \times 16$ -inches.

CIRCLE NO. 315

Transistor test sockets of Dupont resin

DuPont Co., Wilmington, Del., Phone: (302) 774-4315.

Transistor sockets, a part ma chined from DuPont's polyimide resin, allow testing of TO-5 type transistors IC's in extreme-temperature environments not previously possible. The test sockets operate continuously in a range of -60 to 575 ° F while maintaining good dimensional stability and wear resistance. The volume resistivity of the parts at 575 ° F is $10^{11} \Omega$ - cm., which provides exceptional electrical insulation. The parts are not brittle and can be machined to close tolerances which bring about smooth, hard surfaces.



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ANY SIZE ANY POWER RATING ANY COMBINATION ANY CONFIGURATION

For • RF1 Suppression • Arc Suppression

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Total Networks available in tolerances as close as \pm 1%.

All Networks are manufactured under rigid Quality Control to meet your specific requirements.

Send us your specifications for quotation. Prototype quantities furnished prior to production.

For Complete details write Dept. ED-9

Standard CONDENSER CORPORATION

1065 W. Addison Street, Chicago, Illinois 60613 ON READER-SERVICE CARD CIRCLE 185

LOW PROFILE _____ IC PACKAGING SOCKET



Directly interchangeable! Exclusive socket configuration, identical to I C package, saves time, simplifies mounting on P C board.

- Permits card stacking on 1/2" centers
- Accepts packages with flat or round leads
- Easy I C insertion with wiping type beryllium copper contacts
- Easy extraction, minimum lead damage — optional extractor tool available
- Available in diallyl phthalate or black phenolic with gold or tin-plated contacts
- Dimensions .79 L x .49 W x 31 H

Request Data Sheet 166.

Extractor tool

ON READER-SERVICE CARD CIRCLE 186

C. 31 PERRY AVE., ATTLEBORO, MASS. 02703



TEST EQUIPMENT

Solid-state oscilloscope has seven modes



Measurement Control Devices, Inc., 2445 Emerald St., Philadelphia. Phone: (215) 426-8602.

A solid-state oscilloscope can be switched to any of the seven operating modes by simply turning a knob. Incorporating complete dualchannel and high-gain differential circuitry, the unit makes possible selection of either channel alone; dual-trace with both channels displayed alternately or chopped to 100 kHz and both channels added algebraically. X-Y plotting or high-gain differential operation is also possible. In addition to the switching between modes without any plug-ins, the scope features fast warm-up, a dc-to-5-MHz bandwidth, and selectable low and high-speed automatic triggering. Polarity reversing switches permit 180° inversion of either channel in all dual modes, or inversion in the differential mode. Full triggering capability is provided for either positive or negative signals with either internal, external or line source, ac or dc-coupled. Automatic triggering provides a reference base line at 25 Hz or 1 kHz.

A construction arrangement has the model 0531's board assemblies edge-mounted for adequate cooling without fans or blowers.

Let Honeywell extend your EMI measuring capabilities with these off-the-shelf products.

6846 VHF RECEIVER – For EMI evaluation, countermeasures and surveillance applications. High sensitivity; all solid-state; bandwidth variable 20kHz, 200kHz, 5MHz; powered from AC line or batteries.



7870 METERING PANEL – To convert any general purpose receiver with an IF output from 10kHz to 65MHz into a tuned voltmeter for EMI measurements. Wide bandwidth, slideback detector, average or peak reading voltmeter, high level video out to 50 chm load, all solid-state.



PLT-1/PP REGULATED AC POW-ER SUPPLY – A solid-state, 60Hz, 115v rms supply for use in any application requiring extreme amplitude and phase stability. Low distortion; 1 KVA.



4881 TRANSIENT GENERATOR -

For making conducted transient susceptibility tests to interference specs, such as: MIL-STD-826 and A, MIL-E-55301(EL), MSFC-STD-279 and others. High peak pulse power, 60 and 400Hz synchronization, plus 0.5 to 500 PPS free running, 360° pulse positioning.



4857LOW FREQUENCY IMPULSE GENERATOR – Provides flat spectrum of calibrated amplitude signals in 120Hz – 250kHz range for signal substitution or calibration of receivers and field intensity meters. Solid-state electronics.



3858 – 3861 LOW FREQUENCY POWER LINE IMPEDANCE STA-BILIZATION NETWORKS – Used for conducted interference testing of equipment requiring high level input power line current. Frequency range: 14kHz-5MHz; 50ohm line impedance.



2880 MULTICOUPLER – Provides up to 20 outputs from 50 or 72 ohm input. Low noise, all solid-state, modular, 0 db insertion loss from 1 to 54 MHz.



\$980

AW-204 TRANSISTORIZED WIDEBAND AMPLIFIER – Used as a preamplifier with standard EMI meters and calibrated signal sources to provide rapid, remote measurement of extremely low level electric field signals in the 14kHz – 30MHz frequency range.



3862 HIGH FREQUENCY POWER LINE IMPEDANCE STABILIZA-TION NETWORK – Same as 3858 – 3861 networks, but for 4MHz – 1GHz frequency range; 80 amp capability.



Honeywell engineers sell solutions

The instruments shown here are more examples of how Honeywell's broad line, backed by local sales and service, can provide the *precise* solution to your instrumentation problems. For full details on any or all of these fine products, call your local Honeywell Representative, or write: Honeywell, Test Instruments Division, Annapolis Operation, Box 391, Annapolis, Md. 21404.

Digital db.

An Easy-to-Read Unit With Direct Dialing

Dial your way from 0 to 50 db attenuation in 1 db steps with the RA-54, 50 ohm, and RA-74, 75 ohm, miniature dual concentric, rotary attenuators. Easy to read and easy to install being only 1%" dia. by 3%" long and weighing 14 ounces. The RA-54 provides accuracy of better than ± 0.5 db at 500 MHZ, ± 1 db at 1000 MHZ and ± 1.5 db at 1500 MHZ, with a VSWR of less than 1.25 and insertion loss of less than 0.3 db at 1000 MHZ. The RA-54 can be supplied with BNC, TNC or Type N connectors. Lightweight, easy-to-read, easy-to-dial and only **\$165.00**.



Specialists In Electronic Instrumentation



198

TEST EQUIPMENT Ac-to-dc converter has zero impedance



Natel Engineering Co., 7129 Gerald Ave., Van Nuys. Phone: (213) 782-4161. P&A: \$570; 3 wks.

A miniature, solid-state ac-dc converter with zero output impedance is especially designed for airborne, ground support, and laboratory use. Automatic ranging of the input signal in the unit eliminates problems of range adjustment. A 5range and 11-range model are available with full scale signal inputs from 1 mV rms to 10 V rms for full scale output. Other features of the converter include input impedance of 500 K and isolation of 50 M Ω between input, output, reference, and power ground. Temperature range is -55° to $+71^{\circ}$ C.

CIRCLE NO. 318

Digital IC tester rises in 1 ns



Automated Measurements Corp. 638 University Ave., Los Gatos, Calif. Phone: (408) 354-6491.

This system performs every dynamic testing function, including toggle testing. The system's prime measurement instrument, the model 1000 waveform analyzer, can automatically deliver up to 200 measurements per second. It readily communicates with the computer and engineer through variable word-length CIRCLE NO. 319

programming language. The AMC 9001 is compatible with all devices up to 16-pins, low or high impedance. It can be programmed manually, or automatically by tape, magnetic disc, drum or computer. Risetime capability of the system is under 1 ns with less than 5% waveform distortion. The system can be converted from one device configuration to another. The model 9001 uses a field-coded, variableword length, serial-by-character method of programming, with information supplied to the system at the rate of 1,000 characters per second. Test results are presented visually on the system's control module, and automatically signaled to external data logging equipment. CIRCLE NO. 320

High frequency probe for dc voltmeters



Hewlett-Packard, Loveland Div., 1501 Page Mill Rd., Palo Alto, Calif. Phone: (415) 326-7000. P&A: \$45; 60 days.

A high frequency probe converts dc voltmeters into ac voltmeters capable of measuring signals in a range from 100 kHz to 500 MHz. Accuracy within this frequency range is better than 1 dB but probe response extends both above and below the 100 kHz-500 MHz range. The probe can be used for relative measurements from 1 kHz to 1 GHz. Probe input impedance is 4 $m\Omega$ shunted by 2 pF. The probe works with any voltmeter, either analog or digital, that has an input resistance of 10 m $\Omega \pm 10\%$ and there is no need to adjust the voltmeter calibration. The probe accommodates ac signals within an amplitude range between 0.25 and 30 V rms. Accessories provided include a straight tip adapter, a hook tip adapter, a ground clip, and a high frequency adapter.



AI-2 (actual size)

High torque, Self-shielded

When critical indicating and control is essential this versatile mechanism will fill your requirements. Available with "On-off", "+, -", "go-no go", null, left-right or scale indicators. High torque, self-shielded core magnet design permits multi-function groupings in small space. Moving coil is 100 mg lighter than most comparable mechanisms, yet provides 10% more torque. Choice of many sensitivities; synchro or standard mounting.



AMMON INSTRUMENTS, INC. 345 Kelley St., Manchester, N.H. 03105

ON READER-SERVICE CARD CIRCLE 190



1090 Springfield Rd., Union, N.J. 07083 • (201) 686-7870 Western Division: 15700 S. Garfield Ave., Paramount, Calif. 90723 444

Profitable programming with electronic timing and control



• All model dimensions are: 1¹³/4" deep: 2%" wide; 3%" high including knob • Standard 8 (Model TMA1)

 Standard 8 (Model TMA1) and 11-pin (Models TMR1 and TSV1) plug-in mounting

- Dust-tight housing
- Operating voltages; 24 vdc, 115 and 230 vac
- Load contacts: 10 amp.
 DPDT





External Cerrical Cerrical

CLARE-ELECTROSEAL Solid State Timers

Clare-Electroseal timers serve a wide variety of applications for industrial interval timing...actuating an electrical circuit (up to 10 amp.) after a pre-set time. Solid state circuitry eliminates mechanical maintenance costs ...assures reliable, trouble-free operation. For complete data, circle Reader Service Number. 231.

Model TMA1 TIME DELAY RELAY

Application of power starts preset timing period...load contacts transfer at the end of period. 100 ms removal of power resets timer. Time will repeat within 1%. For complete data, circle Reader Service Number 232.

Model TMR1 MONITOR TIMER

Used to monitor events occurring on a predictable time basis. If external contact is not interrupted within pre-set time, load contacts transfer. Remove power to reset relay. For complete data, circle Reader Service Number 233.

Model TSVI SUMMATION TIMER

Operates from external contact for a summing mode of operation. Timer actuates load contacts after pre-set time. Operate time will be the cumulative sum of closures of the external contact. Interruptions up to 15 minutes will not alter summing operation. For complete data, circle Reader Service Number 234.

Model TBR1 DELAY-ON-BREAK TIMER

Closure of external contact causes immediate transfer of the load contacts. The load contacts will remain in transferred position for an adjustable time after opening of external contact. Reclosure of external contact prior to timing out, initiates a new timing period. For complete data, circle Reader Service Number 235.

For complete information, circle Reader, Service Number—or write Group A9

CLARE-ELECTROSEAL CORP. Subsidiary of C. P. CLARE & CO.

946 North Ave., Des Plaines, III. 60016 ON READER-SERVICE CARD CIRCLE 192

MICROWAVES

Power klystron produces 500 kW



Eimac Div. of Varian, 301 Industrial Way, San Carlos, Calif. Phone: (415) 592-1221.

With a minimum output power of 450 kW, cw, this klystom features 55% efficiency and 55 dB gain, with 20-MHz instantaneous bandwidth at the 1-dB point. Beam voltage may be varied from 45 kV dc (130 kW) to 62 kV dc (500 kW) without returning. The klystron is tunable and op

erates in the 2.32-to-2.45-GHz band. The klystron has five cavities, and features a half-wave beryllium oxide window, protected by an arc detector. The klystron is equipped with a nonintercepting modulating anode permitting pulsed operation, in addition to the normal cw mode. The collector is rated at a MW allowing a-m of the drive level without overheating.

CIRCLE NO. 322

CIRCLE NO. 323

Telemetry amplifier responds at 7.5 kHz

Bourns, Inc., Instrument Division, 6135 Magnolia Ave., Riverside, Calif. Phone: (714) 684-1700.

This telemetry amplifier is designed for use with a thermocouple, strain gage and other sensor systems. The model, measuring $1.0 \ge 1.0 \ge 0.5$ -inches and weighing 0.6 oz., is capable of producing up to $0-5 \lor DC$ output with a 5 mv input. In addition, various input levels, gain values anywhere between 100 to 1,000, and parameters other than those of the standard configurations are available.

Broadband dipoles cover 150 to 2000 MHz



Nurad, Inc., 2165 Druid Park Dr., Baltimore. Phone: (301) 664-8300. P&A: \$400 to \$600; 45 days.

A complete series of octave bandwidth crossed dipoles covering 150 through 2000 MHz are made of aluminum. The antennas contain two electrically independent linearly polarized elements which can be used separately or arrayed to provide orthogonal circularly polarized operation. Isolation between each element exceeds 25 dB at all frequencies. The antennas can be used with or without a reflector.

CIRCLE NO. 324



"IT'S GOOD BUSINESS To hire the handicapped."

ISN'T THAT A GREAT IDEA, SNOOPY?



THE PRESIDENT'S COMMITTEE ON EMPLOYMENT OF THE HANDICAPPED, WASHINGTON, D. C.

This AE Type 44 Rotary Stepping Switch Thrives on Solitude.



So do all the rest of our hermetically sealed stepping switches. That's because we build switches so they can't bind, never overthrow.

Most of the secret's in our stepping mechanism. We don't use a pawl stop block. Instead, we use a unique "free-floating" pawl—with a set of stopping teeth on the end of the armature.

This way, the armature not only steps the wiper or cam assembly to the next position—it also *locks* the rotor in the correct position. Overthrow is impossible. So is pawl wear and bind against a pawl stop block—even at low temperatures.

Where can you use these sealed switches? Almost anywhere. Some people take them out

in the desert or down to the bottom of the ocean. Others fly them above 40,000 feet, where the mean temperature is -55 degrees Centigrade. You might want them for a particularly dusty location in your shop.

How can you use reliable, versatile rotary stepping switches? There's a lot of



helpful design information in our Circular #1698. It's yours for the asking. Just write the Director, Relay Control Equipment Sales, Automatic Electric Company, Northlake, Illinois 60164.





Specifying can be a challenging problem, and with this in mind, we put our experience at your disposal. Don't hesitate to call or write us when you're puzzled as to the right deflection yoke for your display.



INSTRUMENTS, INC. 100 Industrial Road, Addison, Illinois Phone: Area 312, 543-6444

MICROWAVES

Coherent synchronizer spans 500 MHz to 40 GHz



LFE Electronics, a division of Laboratory for Electronics, 1075 Commonwealth Ave., Boston. (617) 254-4233. P&A: \$4195; 60-90 days.

This microwave synchronizer spans the frequency range from 500 MHz to 40 GHz. The LFE model 246 will phase-lock any voltage tunable microwave source such as a klystron, BWO, VTM or sweep enerator to a quartz crystal. Long-term frequency drift of 1 part in 107 can be achieved over a period of 30 minutes. This stability can be improved through the use of an external ribidium or crystal standard. Two waveguide mixer-harmonic generators are supplied as external accessories. This allows generation of the IF at the source, and transmission to the synchronizer through a flexible cable. A coaxial fitting and mixer are supplied internally at frequencies between 0.5 and 18 GHz. A selection of nine phase-lag networks insures that the proper compensation for source modulation sensitivity will be achieved. Modulation sensitivities from 0.05 MHz/V to 38 MHz/V can be chosen. The lock-in range of the model 246 can be varied from 0 to ± 40 V providing a greater frequency hold-in range.

The 5 MHz crystal oscillator in the multiplier-harmonic generator is also used as the reference source for the phase detector to make the system coherent. A selection of sixteen crystals can be sequentially chosen and pulled until the proper lock-line is noted on the signal level and phase meters. Simply inserting the synchronizer output between the voltage control element of the source to be stabilized and its power supply is all that is necessary to place the system in operation.

CIRCLE NO. 325

ON READER-SERVICE CARD CIRCLE 196



USCC announces the highest capacitance density in a micro-miniature size

If you have problems obtaining high capacitance values in your microelectronic circuits, let USCC's C20 Series help you. These Ceramolithic® capacitors feature the best capacitance per unit volume available anywhere in a ceramic capacitor for filtering, bypass, coupling and blocking in microminiature circuits.

Here's what you get:

C20 SERIES Capacitance (pF)	Size (in.) Diameter Length
10 - 12,000	.080 x .150
15,000 - 27,000	.095 x 185
33,000 - 56,000	.095 x .250
68,000 - 120,000	.125 x .250
150,000 - 220,000	.140 x .250

Designed for "cordwood" applications, epoxy resin encapsulation offers outstanding insulation resistance, adhesion qualities and high temperature characteristics. Axial leads of .016" diameter Nickel per MIL-STD-1276 N-1 are excellent for soldering and welding. @Ceramolithic* capacitors of the C20 Series give you large size performance in a small size with no sacrifice in electrical characteristics.



Rising sun magnetron delivers 3.2 mm output



Amperex Electronic Corp., Hicksville, N. Y. Phone: (516) 433-9045.

A conventional magnetron that delivers 5 ns pulses of 95 GHz energy at 10 kW is designated the DX287. It has a 3.2mm wave-length output. This output figure lies squarely in the center of an atmospheric window where the attenuation is much lower than at other frequencies in the millemeter region.

The short pulses possible with this unit, down to 5 ns, give range resolution of better than a meter. The duty cycle of 0.0002 allows any practical repetition rate up to 40 KHz, producing bright, detailed PPI displays—or even TV-type rasters.

CIRCLE NO. 361

Voltage-tuned oscillator ranges 1.2 to 1.8 GHz



Microwave Products Group, 115 Old Country Rd., Carle Place, N. Y. Phone: (516) 741-1500.

Voltage tuned oscillators covering 1.2 to 1.8 GHz offer a linearity of within $\pm 1\%$ over 75% of the tuning range and within 7% of the full tuning range. The 1 x 1 x 2-1/4-inch device provides a power output of greater than 50 mW. CIRCLE NO. 326



You're right on top with pluggable/patchable Cambi-Cards®

This new idea in logic card make-up lets you go from original function patch-up to final system check-out without tear-down or breadboard change. What's more, your final system configuration built with Cambi-Cards is ready for production — no breaking down or production re-design is required. All three steps — initial IC logic function assembly to intermediate rack mounting to system incorporation — are possible with Cambi-Cards.



Because patching and IC pluggability are both on the same side of the card, you can see what you are doing — no mis-wiring. Find out how successfully and quickly your design and system ideas can prove their value with Cambi-Cards. Ask for a demonstration, contact: Cambridge Thermionic Corporation, Digital Products Division, 433 Concord Avenue, Cambridge, Massachusetts 02138. Phone: (617) 491-5400.



Standardize on CAMBION 21,541 guaranteed electronic components



ON READER-SERVICE CARD CIRCLE 198

ON READER-SERVICE CARD CIRCLE 197 ELECTRONIC DESIGN 19, September 13, 1967

MICROWAVES

Compare these two precision miniature bearings:



This one is a precision miniature ball bearing.

In every respect save one, the two are perfectly interchangeable. The Northfield bearing, precision made of sintered bronze (per MIL-B-5687 Type 1, Comp A), will meet your closest dimensional requirements. Only in performance at high speeds with heavy loads do they differ, and even there the Northfield bearings rate high. Their coefficient of friction is low, and they are vacuum impregnated with instrument oil, per MIL-L-6085A.

This one is a Northfield precision sintered bronze bearing.

Yet Northfield bearings cost only ¹/₃ as much as miniature ball bearings.

Northfield precision bearings come in a full range of miniature sizes – flanged and sleeve types. Call or write for complete information and prices.

Northfield Precision Instrument Corp., 4400 Austin Blvd., Island Park, L.I.. New York 11558. Tel.: (516) 431-1112.



Maybe you've been overspending for years.

ON READER-SERVICE CARD CIRCLE 199



Cw lag laser with 3 W output



Korad Corporation, 2520 Colorado Ave., Santa Monica. Phone: (213) 393-6737. P&A: \$7000; 30 days.

A portable yttrium aluminum garnet (YAG) laser with a typical continuous-wave output of 3 W can be used for trimming of resistors, interferometry, alignment of optical systems and real-time data display. The device also would be useful for Raman spectroscopy, infrared microscopy, and studies of nonlinear optics and parametric amplifiers. Emission is at 1.06 microns, with a half-angle beam divergence of 2.5 milliradians at threshold. The power supply is contained in a 10 x 10 x 20inch cabinet. The laser head measures $4 \times 4 \times 12$ inches, and the combined weight is 64 pounds. Input power is 117 V, 60 Hz, 1800 W. The unit is cooled by a closed-cycle, water recirculation system.

CIRCLE NO. 327

X-band power source delivers 100 Mw

RCA, 30 Rockefeller Plaza, New York. Phone: (212) 265-5900.

The type S198 power source is designed for use as a local oscillator, low-power relay transmitter, pump for parametric amplifier or low-power radar transmitter. This X-band device provides a power output of 100 mW over a mechanical tuning range of 100 MHz. It employs a transistor oscillator-multiplier driving a two-stage varactor frequency multiplier chain. The chain consists of a broadband bartype doubler circuit follower and a quadrupler output. The S198 can be adapted for electronic tuning.





A 1 photocen, especially designed fornumerousapplications in outside or inside lighting, flame control, and relay applications where the light source is incandescent. Proven by

hundreds of thousands of photocell years of service. Shown actual size—standard models available.



Our engineering department will work with you on any special application of photosensitive layers.

STANDARD MODELS

CDS Type No.	1 FC Simulated Daylight 50 V AC Mean* Output	Nominal Resistance 50 FC 2800° K Incand.	Max. Dark Curent** or Min. Dark Resistance	Max. Dissip.	Max. Volt Dark
701	1.5 ma		25 ua		500 V
702	3 ma		25 ua	all rated	500 V
703	6 ma		40 ua	1/4 watt	350 V
	1.			continuous	
710		1330 ohms	4 meg.	i watt	500 V
711		670 ohms	4 meg.	1 minute	500 V
712		330 ohms	2.5 meg.		350 V
901	1.5 ma		25 ua	All	1000 V
902	3 ma		25 ua	rated	1000 V
903	6 ma		40 ua	1/2 watt	700 V
904	12 ma		200 ua	contin-	500 V
910		1330 ohms	4 meg.	uous	1000 V
911		670 ohms	4 meg.	2 watts	1000 V
912		330 ohms	2.5 meg.	1 minute	700 V
913		165 ohms	0.5 meg.		500 V

•Range of values in any category equal to $\pm 33\%$ of mean. ••Measured at 100 V, 5 seconds after 50 FC light extinguished.



ON READER-SERVICE CARD CIRCLE 206 ELECTRONIC DESIGN 19, September 13, 1967

S-band multicouplers use stripline transistors



Micro State Electronics Corp., 152 Floral Ave., Murray Hill, N. J. Phone: (201) 464-3000.

Stripline transistors are used in these multicouplers. They cover the L and S-band telemetry channels. Isolation between outputs is 50 dB with an input-output VSWR of less than 2. The isolation is obtained by using power splitters of the hybrid N-way type with resistive networks included to terminate incident energy at the outputs. The units consist of a preamp, eight postamplifiers and power dividers. Gain is from 2 to 5 dB.

CIRCLE NO. 329

L-band circulator has Y and T styles



Raytheon Co., Special Microwave Devices Operation, 130 Second Ave., Waltham, Mass. Phone: (617) 899-8080.

A low-power coax L-band circulator has a maximum VSWR of 1.2, maximum insertion loss of 0.3 dB and minimum isolation of 20 dB. The unit comes with type N connectors in a Y configuration package. It can also be supplied with one port terminated for isolator applications, and in a T configuration. CIRCLE NO. 330



You've got a bigger logic selection with Cambion[®] IC Assemblies

We started with the idea of providing the widest selection of standardized integrated circuit assemblies anywhere in the industry. We're over 200 already and continuing to add.

You name the function you want and chances are we have a standard assembly for it, whether it's a counter, decoder, or register. We've even tried to anticipate your needs and have some complex functions available.



Up to 5 digit decade counters per card.

And we'll help you design digital logic assemblies into efficient, low cost systems for a variety of special applications, if you wish.

For complete information and specifics on integrated circuit assemblies, contact: Cambridge Thermionic Corporation, Digital Products Division, 453 Concord Avenue, Cambridge, Massachusetts 02138. Phone: (617) 491-5400.

Standardize on CAMBION



VICE CARD CIRCLE 207

205





Range: 0.10µh to 1,000µh in 49 stock values Size: 1/10 dia. by 1/4 lg. Inductance Tolerance: ±10%

This new "NANO-RED" offers the highest inductance to size ratio available in an axial shielded inductor. Exceptional "Q" and self-resonance characteristics. Max. coupling 2% units side by side. Non-flammable envelope. Designed to MIL-C-15305C. Operating temperature - 55°C to 125°C.

Other Lenox-Fugle Subminiature Shielded Inductors:



MICRO-RED The "Micro-Red" is a shielded inductor that offers the largest inductance range in its size: 0.10µh to 10,000µh. "Q" to "L" ratio unsurpassed, with excellent distributed capacity. Inductance tolerance $\pm 10\%$. Designed to MIL-C-15305C. Stocked in 61 predesigned values.

> The "Mini-Red" offers the highest "Q" to "L" ratio available over inductance range 0.10µh to 100,000µh in its size. Inductance tolerance $\pm 10\%$ measured per MIL-C-15305C. Stocked in 73 predesigned values.

0.395" =.020" DURA-RED The "Dura-Red" is designed to MS-90537 with inductance range 0.10μ h to $100,000\mu$ h with tolerance $\pm 10\%$. Stocked in 73



SYSTEMS Physiological monitor displays 8 channels



ITT Corp., 320 Park Ave., N. Y. Phone: (212) 752-6000.

A large-screen physiological monitor that displays simultaneously 8 channels of transudcer data such as ECG, EEG and a variety of similar signals is now available. Time sharing allows from one to eight channels of data to be displayed simultaneously on the 17-inch CRT screen area. A "chopped" communication method of multichannel display assures high resolution, uniform trace intensity, and faster recognition of short transients. Commutation rate is selected to assure faster continuous traces on all beams at sweep frequencies.

The aluminized CRT has 20 lines per centimeter resolution, 20 mV per centimeter sensitivity and 1% linearity. Drift-free performance without warm-up is accomplished because of its solid-state design. The unit may be rack-or benchmounted.

CIRCLE NO. 331

A to D converters are solid-state

RC-95, Inc., 9 E. 38th St. New York. Phone: (212) 689-9776.

Standard models in the 510 series provide any data-word length from 4 to 10 bits, at 2 to 4 μ s per bit, yielding corresponding conversion rates from 25,000 to over 125,000 conversions per second. These ADC modules, employing IC's and subminiature components, occupy less than 11 cubic inches of space, and weigh 9 ounces. All circuitry is p.c. board-mounted, fully accessible, field repairable and rigidly mounted in an internally-finned, one piece aluminum shell.

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They do.



They work for freedom. And more than seven out of ten of them are supporting freedom with their dollars, too through investment in U.S. Savings Bonds. When you buy Bonds, you can save up for a rainy day, a home, a free and comfortable future — and at the same time show these brave men you're on their side. Join the Payroll Savings Plan where you work or buy Bonds where you bank. You'll walk a bit taller.

New Freedom Shares

Now, when you join the Payroll Savings Plan or the Bond-a-Month Plan, you are eligible to purchase new Freedom Shares. They pay 4.74% when held to maturity of just four-and-a-half years (redeemable after one year), and are available on a one-for-one basis with Savings Bonds. Get the facts where you work or bank.

Join up. America needs your help.



Microwave receiver listens to 40 GHz



Scientific-Atlantic, Inc., Box 13654, Atlanta. Phone: (404) 938-2930. P&A: \$5800; Nov.

Suitable for field or laboratory use, the 1710 receiver has a range of 940 MHz to 40 GHz and will handle cw, sine-wave, square-wave, or pulse-modulated input signals. The unit has a dynamic range of 40 dB, three switch-selectable IF bandwidths, and uses the crystal-harmonic mixing technique that permits wide-range coverage without plug-in units. Options include a low-frequency converter that extends the frequency range down to 20 MHz, an extended dynamic range (60 dB), a signal level compensation a precision IF attenuator, and a battery inverter for field use. CIRCLE NO. 333

Digital replacement for cam programmer



Hafstrom Technical Products, Inc., 4616 Santa Fe, San Diego, Calif. Phone: (714) 274-8822.

Using digital techniques, this programmer provides one or more variable control resistances suitable for programing temperature, voltage and currents. It is possible to program functions with rates of change or durations outside the limits of the average cam program unit. Programs can be changed by simply rearranging a patch panel.

CIRCLE NO. 334



You're more productive with back-plane wiring

These new panels let you get the most out of wire-wrapping techniques. By combining specially drawn Wire-Wrap* terminals (for machine or hand-gun interconnection) with CAMBION®'s exclusive cage jack (for IC pluggability) you can have both packaging density and high production.





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ON READER-SERVICE CARD CIRCLE 211

ELECTRONIC DESIGN 19, September 13, 1967

New! RF Shielded New! Front-Mounting

Eldema's C-Lite Cartridge and D-Holder combination provides both incandescent or neon panel lites. Now D-Holders with the added reliability of RF shielding and the added flexibility of front-mounting. Eldema plug-in cartridge lites are inherently reliable, simple to in-

stall, and easy to replace. Available in a large range of lens shapes, styles, and colors. Matching push switches utilizing C-Lites are also available. Eldema cartridge lites and holders conform to MIL-L-3661. Write for complete brochure and free samples. or use reader service card.

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- Wakefield, Mass. (617)245-9359 Pa. (except Pitteburgh), So N.1 Eastern Components, Inc. Philadelphia, Pa (215)927-6262 New York (except N.Y.C.) Midatele Research Sales Co. Surgenze, New York
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- Scottadale, Arizona (602)947-4355

MATERIALS

Freon aerosol spray removes rosin flux



Sprayon Products, Inc., Industrial Supply Div., Bedford Heights, Ohio. Phone: (216) 292-7400.

For removal of rosin flux as used on printed circuits, relays or semiconductors, this aerosol Freon spray is gentle enough for use on plastics and elastomers. The spray is also used in cleaning computer parts. It is nonflammable and nontoxic and comes in a 16-oz can with an extension tube for pinpoint application.

CIRCLE NO. 335

Conductive adhesive for rf gaskets



Emerson & Cuming, Inc., Canton, Mass. Phone: (617) 828-3300. Price: \$10 up.

This silicon resin adhesive has a volume resistivity of approximately 0.001Ω -cm. It can be used continuously at 400°F. Eccoshield RVS contains solvent which makes application easy. As solvent evaporates, the adhesive develops tack. Gaskets are applied, or closure is made while in this condition. If tack is lost, it can be regained by surface application of solvent, such as toluene.

ON READER-SERVICE CARD CIRCLE 213



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Designed Especially for OEM Application. Two Lines Available. SPECIFICATIONS

	R-3100	R-3200
Type of Voltage Regulation	True RMS	Peak
Regulation Technique Type of Reference Input	Peak Clipping RMS Sensor 100-130 VAC 47-63 Hz	Peak Clipping Zener Diode 100-130 VAC 47-63 Hz
Output	115 VAC	115 VAC (RMS)
Line Regulation $(\pm 10\%)$ line variation	±0.5%	±1.0%
to Full Load)	±0.5%	±1.0%
(47-63 Hz)	±0.5%	$\pm 1.0\%$
tion ($+0.7$ to -0.7) Phase Shift	±0.5% None	±1.0% None
Response Time	10-50 µsec.	10.50 µsec.
Models Available	15-1000 va	15-1000 va

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Epoxy molding powder cures in 1¹/₂ minutes



Mitchell-Rand Manufacturing Corp., Torne Valley Rd., Hilburn, N. Y. Phone: (914) 357-2700.

A one-component epoxy molding powder is designed for encapsulating electronic components by transfer molding. A mineral molding powder yields a rigid and highly moisture resistant encapsulation with thermal stability at 155°C. After molding it is self-extinguishing and exhibits good electrical properties over a broad range of temperatures. Supplied in three flow ranges it insures optimum molding properties for individual molds, component configuration, and press designs. A cure time of $1 \ 1/2$ minutes in the mold is suggested for initial evaluations.

CIRCLE NO. 337

Fluorochemical liquid boils at 90°F



3M Co., 2501 Hudson Rd., St. Paul. Phone: (612) 733-1804.

The liquid, called FC-32 has many applications as a non-corrosive, low temperature refrigerant and as a coolant for electronic systems including computers. The dielectric strength this liquid at $77 \,^{\circ}$ F is 42 kV/0.1. Its dielectric constant is 1.72 at 1 KHz and its dissipation factor is less than 0.0003 at 1 kHz.

CIRCLE NO. 338



You've got it made with Cambion[®] Logic Assemblies

Difficult digital application? Noisy environment? Need high speed or a large variety of functions? Ask for CAMBION DTL, TTL or HTL logic assemblies. CAMBION gives you more functions per card and all cards are compatible.



Choose from over 200 assemblies with high package densities and 70-pin input/output capability. That's twice the number of any competitor, and CAMBION assures you the **lowest cost per function**. The complete line includes power supplies, card files, drawers . . . available off the shelf, in the widest variety.

For more information on CAMBION's IC assemblies, contact: Cambridge Thermionic Corporation, Digital Products Division, 457 Concord Avenue, Cambridge, Massachusetts 02138. Phone: (617) 491-5400.



21,541 guaranteed electronic components



ON READER-SERVICE CARD CIRCLE 216

ON READER-SERVICE CARD CIRCLE 215 ELECTRONIC DESIGN 19, September 13, 1967

HGHEST m_T/t_{SU}

We call your attention to a whole new parameter by which to compare IC testers: number-of-test-measurements-per-time-spent-setting-up. You get more of it from the Birtcher Model 800. \Box Not only can you make up to 50 separate measurements with one programming of the Model 800's matrix; you can also perform complete functional testing of digital-type IC's without reprogramming. Test speeds like 24 microcircuit parameters in 30 seconds are routine. And the Model 800 is a manual tester, with a manual tester's price tag. \Box It has five digitally-settable integral power supplies (one of them a constant current source), and provision for five more external inputs. The matrix is the convenient crossbar type, rather than a pin board. \Box Other features include push-button test sequencing; 1% accuracy of internal readout; and hook-up for external readout. A full complement of adapters is available, covering all types of IC's. \Box Construction is modular, and there are options on matrix size and accessory modules. \Box Price is in the \$2000-\$3000 range.

Write for catalog and applications data.

the BIRTCHER CORPORATION

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ON READER-SERVICE CARD CIRCLE 217

Design Aids



Design aids catalog

This 16-page, 1967-68 catalog contains full descriptions of slide and rotating type calculators, comparators, charts, handbooks, manuals, nomographs, curves, rules, converters, drawings, and other devices and reference works. The design aids are intended for electronics, fluid, mechanical, and structural designers, engineers, and draftsmen. These time-saving devices and reference works are designed to provide answers to a myriad of problems and questions. TAD Products Corp.

CIRCLE NO. 339

Semiconductor cooling

In addition to an established line of standard cooling products, the 20-page catalog contains new data on filled epoxy systems for bonding or encapsulating insulating wafers, clips for plastic transistors, thermal links for fused glass diodes, thermal equalizing links for dual TO-18 units, and two heat sink/retainers for TO-5S. Wakefield Engineering, Inc.

CIRCLE NO. 340

Guide for nut fastening

Basic information on nut assembly is contained in a pocket-size guide. Particularly helpful is the inclusion of the nut application index. The proper nut fastening for 14 different types of nut applications and requirements can be seen at a glance. In each instance, the index thoroughly explains the reasons for making the specific nut fastening selection. Shakeproof, a division of Illinois Tool Works, Inc.





Bomac Orthospan[®] Mixers with a power handling capability of 200 mW.

Frequency: any 1 GHz range between 12.4 and 18.6 GHz.

Noise, nominal:

At 1 GHz bandwidth: 8.5 dB At 4 GHz bandwidth: 10.0 dB

Warranty: 5000 hrs.

Bomac Orthospan Mixers are also available in X band and K band. For details, write: Varian Bomac Division, Salem Road, Beverly, Massachusetts 01915. In Europe: Varian A.G., Zug, Switzerland. In Canada: Varian Associates of Canada, Ltd., Georgetown, Ontario. In Australia: Varian Pty. Ltd., Crows Nest, Sydney, Australia.



ON READER-SERVICE CARD CIRCLE 218 ELECTRONIC DESIGN 19, September 13, 1967

TR-Limiters*

from Bomac



Frequency Coverage: 15.8 to 17.2 GHz. Operating Range: 0.5 GHz. Recovery Time: 0.5 μs. Insertion Loss, typical: 0.8 dB. Power Level, maximum: 10 kW. Warranty: 2000 hrs.

Bomac TR-Limiters are also available in X band; they will operate over any 0.5 GHz range between 8.5 and 9.6 GHz. For details, write: Varian Bomac Division, Salem Road, Beverly, Massachusetts, 01915. In Europe: Varian A.G., Zug, Switzerland. In Canada: Varian Associates of Canada, Ltd., Georgetown, Ontario. In Australia: Varian Pty. Ltd., Crows Nest, Sydney, Australia.

*Combined gas-switching TR tube and solid-state limiter.



ON READER-SERVICE CARD CIRCLE 219



Bomac Orthospan[®] Modulators With a power handling capability of 500 mW.

Carrier Frequency: any 1 GHz range between 12.4 and 18.6 GHz.

IF Frequency*: 5 to 250 MHz.

Conversion Loss, maximum: 10.0 dB.

Warranty: 5000 hrs.

Bomac Orthospan Modulators are also available in X band and K band. For details, write: Varian Bomac Division, Salem Road, Beverly, Massachusetts 01915. In Europe: Varian A.G., Zug, Switzerland. In Canada: Varian Associates of Canada, Ltd., Georgetown, Ontario. In Australia: Varian Pty. Ltd., Crows Nest, Sydney, Australia.

*Under special conditions, this can be extended to 1.5 GHz.



ON READER-SERVICE CARD CIRCLE 220 211

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Application Notes



DI/An Controls, Inc.

Magnetic logic cook book

A 90-page magnetic logic book covers logic fundamentals and circuit packaging and specifications. It is the purpose of this book to make it easy to design digital equipment using magnetic circuitry. It is a cook book because all the ingredients and techniques a designer will need to build such circuitry without resorting to a long dissertation on the chemistry of the processes are included. The real secret is simply to read the book, take the proper ingredients and assemble them in the prescribed way.

Chapter 5 is, for the practical designer, the heart of the book. It gives in detail the best and most useful circuit designs originated over the years. Di/An Controls, Inc. CIRCLE NO. 342

Uses unlimited

This new edition covers a dozen ideas in unique switch application in such areas as aero-space, diecasting, data processing, drill press applications, dentistry, vending machine cup dispensing, and parts production. Switch applications include the environment-proof aircraft switches, BZ basic switches, V3 low force, new terminal on the SM1 and SX1, KB switch/display matrix, toggle switches, rotary switches, limit switches and the PD pulse switch. Micro Switch, a division of Honeywell.

Here's why you should now be using Datavue^{*} Indicator Tubes



CK1904—Interchangeable with B59956 and NL809.

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8422—Interchangeable with B5991 and NL8422.



CK1903—Interchangeable with B5992 and NL5992.



CK1901—Interchangeable with B5016 and NL5016: CK1902— Interchangeable with B5032, NL5032, B50911 and NL50911.



8421—Interchangeable with B5092 and NL8421; 8037—Interchangeable with B5031 and NL8037. Also available: 6844A.

CK1905; CK1906 (right-

hand decimal point)

competitively-or lower. Most side-

view types, for example, cost less than \$5 each in lots of 500 or more.

Reliably trouble-free readouts. All

Datavue characters are fully formed

characters are brightly displayed

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They last for years. They're made better because of Raytheon's expe-

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-not segmented. The fully formed



8754—Also available with right- and/or lefthand decimal points; Interchangeable with NL840/8754. Decimal-point types Interchangeable with NL841, 842, 848.

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CK1900 (used with CK8650, CK1905, CK1906); CK1907 (used with 8754)—Interchangeable with NL843.



life types, for example, have dynamic life expectances of 200,000 hours or more.

They're readily available—in sample and production quantities. For samples, prices, or technical information, call your Raytheon regional sales office or distributor. Or write: Raytheon Company, Industrial Components Operation, Quincy, Mass. 02169. *Trademark of Raytheon Company



ELECTRONIC DESIGN 19, September 13, 1967

New Literature



Analog catalogue

The second issue of "Analog Dialog" with 16 pages of technical information about operational amplifier fundamentals, circuits, and applications is offered.

An eight-page treatment entitled "Operational Amplifier Principles," by C. V. Weden of Fairchild Instrumentation, covers basic op amp theory and describes the major circuits and their purposes.

A NEREM paper on varactor bridge amplifier fundamentals shows how these important but neglected amplifiers achieve ultralow drift and noise, and accomodate up to nine decades of signal variation. The last page summarizes some ten published papers and technical articles and invites readers to participate in two-way technical discussions. Analog Devices.

CIRCLE NO. 344

Shock control

A four-page, two-color monograph presents basic information on types of shock disturbances, how to analyze shock, principles of attenuation and methods for calculating the required protection. The monograph discusses force excitation, the two major types of shock, and provides examples of eight specific shock problems with their corresponding solutions. Shock is a phenomenon with many sources, thus shock protection is a requirement in good product design. Lord Manufacturing Co.

CIRCLE NO. 345

6,051,045 thermocouples

Con-O-Chart One utilizes a new coding system for thermocouples that allows you to order in six numbers and words instead of giving verbal description that can run to ten pages. It covers thermocouples to $2500 \degree$ F.

The form includes the "I" type of wire (3% rhenium/tungsten) and updates all data to accord with current technology. Incorporated are all the tables and data necessary to thermocouple specification: meltingpoints of metals, material selection by temperature, temperature mV scales, temperature conversion tables etc. Sixteen pages of data are presented as a letter size folder that opens to fit a desk or wall chart. A feature is continental sensing specification #3421, which gives the complete specification, including methods of process, tests and calibration. Con-O-Chart.

CIRCLE NO. 346

Electron tubes

A 32-page, short-form catalog describing Machlett's electron tubes is now available. Described are large power tubes (triodes, tetrodes, and pulse modulator tubes), uhf planar triodes, high-vacuum diodes, variable capacitors (vacuum) and vapor cooling systems. The large power tubes described cover the cw power spectrum from 3 to 440 kW and offer pulsed powers to 25 MW. Included is a description of Machlett's magnetically beamed electron tubes. which require 10 to 100 times less power than conventional tubes. Machlett Laboratories, Inc.

CIRCLE NO. 347

Magnetic shielding

A 16-page catalog covering EMI materials and magnetic shielding alloys has been announced. A large variety of wire mesh constructions, wire and elastomer combinations, RFI Honey comb ventillation panels, and specialized gasket formations are described. Magnetization curves on high permeability and high saturation shielding alloys are included with fabrications considerations. Primec Corporation.

CIRCLE NO. 348

Transformer handbook

A description of basic transformer construction and operation is covered in this handbook. Such subjects as insulating, isolating and shielded winding operation of 60-Hz transformers on 50-Hz frequency and paralleling transformer connections using single-phase transformers on 3-phase service are covered. A full range of connection diagrams shows how to use transformers for distributing power at high voltage, insulate circuits, eliminate separate high-voltage and low-voltage wiring and connect and insulate a 4-winding transformer for auto transformer service. Acme Corp.

CIRCLE NO. 349



Allied 1968 catalog

This 600-page book lists over 50,-000 separate stock items from over 500 manufacturers for research and development, production, communications, education, controls and entertainment. Listings show prices for purchase in various quantities of every type of component, including integrated circuit devices, semiconductors, vacuum tubes, relays, timers, transformers, resistors, capacitors, connectors, coils, chokes, sockets, plugs, jacks, switches, fuses, batteries, clips, lamps, wire and cable. Detailed specifications, descriptions and illustrations are provided for all products. Allied Electronics Corporation.

low cost Bolitron's 1 NPN SILICON TRANSISTORS



Initial customer response to Solitron's ISOLTAXIAL NPN Silicon Transistors has proven the wide acceptance of these devices for high reliability applications. Their special construction innovations and processing techniques have resulted in a combination of high secondary breakdown resistance and low leakages never before achieved. As shown in the cross-sectional drawing, the key is in the planar surface and the uniform base. Available in a TO-3 or TO-61 case, the ISOLTAXIAL may be used in power supplies, audio amplifiers, inverters, converters, relay drivers and series regulators.

ISOLTAXIAL (Oxide Passivated Single Diffused Transistor) **REFERENCE INDEX**

- Aluminum Leads
- 2. Aluminum Metalization for low contact resistance
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- Silicon Dioxide Passivated (low leakage) Uniform Base Concentration for high 4. 5.
- secondary breakdown
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All types include these specifications:

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				Min.	Min.	Min.	μA (Max.)	Volts
SDT9801	TO-3	SDT9901	TO-61	60	40	12	100	40
SDT9802	TO-3	SDT9902	TO-61	80	60	12	100	60
SDT9803	TO-3	SDT9903	TO-61	100	80	12	100	80
SDT9804	TO-3	SDT9904	TO-61	120	100	12	100	100

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Designed to provide reliable 360° RFI shield termination for the "new breed" of high-density microminiature circular connectors, these lightweight adapters from Glenair insure connector performance and integrity under the most restrictive weight limitations.

Available in environmental or nonenvironmental versions, these adapters come in a choice of cable entry sizes for each connector and accommodate overall shielded cables and harnesses, or shielded and jacketed cables alike.

These new adapters are easy to assemble and are available for all circular connectors including such high-density microminiatures as Amphenol Astro 348, Bendix JT-JTRE, Cannon Centi-K, Deutsch STK, Matrix Mini-Mate, Microdot Marc 53 and others. They are also available for MIL-C-26482, MIL-C-26500, MIL-C-38300, NAS 1599 and similar connectors.

For more information, write, wire or phone today.



ON READER-SERVICE CARD CIRCLE 228

NEW LITERATURE



Zener diode handbook

This publication supplies applications information for the product advances in zener diodes and zenerlike devices. It covers applications for temperature conpensated zeners, reference standards, current regulator diodes, and zener transient suppressors as well as the latest types of zener diodes.

The handbook is organized to give the circuit designer all the data necessary for the efficient use of zener components with the major emphasis on circuit design. Proven, basic circuits are also provided as take-off points for the designer's own requirements.

Available for \$2 from Motorola Semiconductor Products, Inc., Box 13408, Phoenix.

Microwave transistors in coaxial package

RCA, Electronic Components and Devices, Harrison, N. J. Phone: (201) 485-3900. P&A: \$90 ea.; samples.

This device should find applications as a microwave straightthrough amplifier or fundamental frequency oscillator in L- and Sband equipment designs including telemetry, radar, ECM equipment or as a driver for microwave tubes and varactors. The TA7003's microwave performance can be seen from the following specifications; as an amplifier (1 μ W power output at 2 GHz with 5 dB gain and 30% efficiency, as an amplifier) 2 W output at 1 GHz with 10 dB gain and 50% efficiency and as an oscillator (1.5 W power output at 1 GHz). Both modes of operation use a 28-V power source.

CIRCLE NO. 351

Quality evaluation

Guidelines for equipment and system manufacturers in evaluating quality assurance systems of vendors and for component vendors in assuring prospective customers of the quality of their products, have been published by the EIA. The guidelines include standard questionnaires which provide answers to the questions buyers want answered before they buy parts. The procedure sets forth in logical sequence questions about component manufacturers' quality and reliability assurance systems and their compliance with military specifications and standards, and provides a means of exchanging survey results.

Available for \$1 from Electronic Industries Association, 2001 Eye St., N.W., Washington, D. C.



Instruments catalog

The entire product line of the electronic instruments division of Beckman Instruments, Inc., as well as useful applications information and measurement techniques, are detailed in a new 224-page, hardbound catalog. The 1967/68 catalog features sections on data acquisition recorders and system elements, electronic instruments and medical recorders. Within each section are complete product specifications and pricing information. The catalog also devotes space to application information and data. Subjects include "Characteristics of Direct-Writing Oscillographs," "Frequency and Time-Interval Measurement," "Accuracy in Data Systems," and a "Physiological Measurement Guide." Electronic Instruments Division of Beckman Instruments, Inc. CIRCLE NO. 352

JUST CUT TO PATTERN

Netic & Co-Netic Magnetic Shields

HAND FORM **IN SECONDS**

A great convenience to design engineers, packaging engineers, R/D, etc. A fast inex-pensive empirical tool to determine and shield the necessary components of systems. Use multiple layers if needed. Thick-nesses from .002". Also widely used in automated or manual production line techniques.

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Wein Bridge Oscillator



DIVISION MAGNETIC SHIELD Perfection Mica Company

1322 N. ELSTON AVENUE, CHICAGO, ILLINOIS 60622 ORIGINATORS OF PERMANENTLY EFFECTIVE NETIC CONETIC MAGNETIC SHIELDING

ON READER-SERVICE CARD CIRCLE 229



New Plug-in Stepping Switch

Here's a printed-circuit stepping switch that can solve many of your remote control problems. You can use it for sequential circuit switching, pulse counting, and programming. It plugs into a standard printed circuit board connector so you can conveniently and easily mount the switch and make your electrical connections at the same time.

You can choose AC or DC operation, with or without homing. The step rate is up to 15 steps per second; switches are available with 3, 4, 5, 6, 10, or 12 positions. Life is over 6,000,000 steps at rated load. Send for technical data now.



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CIRCLE NO. 353

Plastic rectifiers

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CIRCLE NO. 354

Bulletin on thermocouples

This bulletin reviews the factors in selecting and specifying the primary temperature-measuring elements designed and manufactured by the Foxboro Company. A fivepage section includes a temperature guide for selection of protected thermocouples, data on time constants and limits of error and also includes a list of industrial applications. In addition, the illustrated 25 page bulletin describes the features of the spring-loaded Minox thermocouple assembly and the tube wall thermocouple. Foxboro Company.





Aerospace digital computers

A 24-page catalog describes nine different aerospace digital computers and their variations. With descriptive text, pictures and other features, the catalog details a variety of general-purpose and special purpose navigation and guidance computers designed for more than 25 different applications aboard aircraft, missiles, boosters, and spacecraft. General Precision Systems.

CIRCLE NO. 356

Filter definitions

A six-page glossary of terms most commonly used in describing filters and their characteristics is offered. The definitions given in the illustrated glossary are intended to assist those unfamiliar with filter terminology in specifying filters. More than 50 specialized terms are also defined. Electro-Mechanical Research, Inc.

CIRCLE NO. 357

Merrimac catalog

The 72-page catalog contains photographs, schematics, performance curves, drawings and detailed descriptions of Merrimac's quadrature (90°) hybrids, hybrid junctions, power dividers, directional couplers, attenuators, phase shifters, balanced mixers, monopulse comparators and terminations. An introduction to each product category covers characteristics, operation and applications. Of interest are details of Merrimac's new miniature components, including its ultraminiature quadrature hybrid and balanced mixer. Merrimac Research and Development Inc.



ON READER-SERVICE CARD CIRCLE 235 ELECTRONIC DESIGN 19, September 13, 1967

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Sept. 21-22

Microelectronics Applications Symposium (Garden City, N. Y.) Sponsor: IEEE; L. I. Kent, Executive Secretary, c/o Airborne Instruments Laboratory, Commack Road, Deer Park, N. Y. 11729. CIRCLE NO. 465

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Oct. 2-4

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CIRCLE NO. 467

Oct. 9-10

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Oct. 16-19

Users of Automatic Information Display Equipment Meeting (Washington, D. C.) Sponsor: UAIDE; G. E. Perez, UAIDE Program Chairman, P. O. Box 6749, Fort Davis Station, Washington, D. C. 20020.

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