SAFETY: A GROWING CONCERN IN INSTRUMENT DESIGN/117

Using optical isolators in linear applications/105
Reliable design with solid-state power devices, part 1/111

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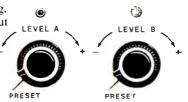


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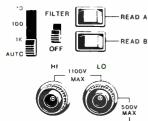
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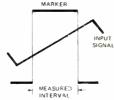


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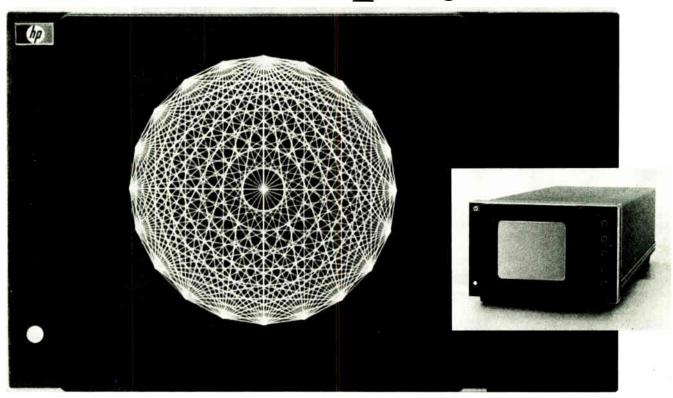
measurement problems. The counter goes to work when your command tells it to. Ideal for burst frequency or sweep generator linerarity measurements. Standard.

These are just a few things, of course. There are many more thoughtful engineering innovations that combine to give you everything you're likely to need in a general purpose, medium-priced counter for a long time to come. We talk about them in our 12 page booklet. Write for one or ask your nearby HP field engineer for a copy. We want you to find why we call this universal counter universal.



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The International Magazine of Electronics Technology

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Highlights

Cover: Citizens' band collects the crowds, 91

The popularity of CB radios is crowding the channels and causing interference between them—both problems that new regulations and new technology could alleviate. The Federal Communications Commission will soon add more channels, but most technical advances will be delayed till demand for CB gear stops outstripping supply.

Cartoonist Bob Clarke drew the cover.

Rosy employment forecasts may hurt engineers, 67 Predicted shortages of engineers have failed to come true and have damaged the profession by luring too many people into it, argues a member of the National Society of Professional Engineers.

Optical isolators have analog uses, too, 105

Despite their nonlinearity, several kinds of optically coupled isolators can confer noise immunity on analog as well as digital circuits. But the right circuit techniques must be used.

Dependable designing with power devices, 111

Even the newcomer to industrial design can build reliable power-semiconductor equipment if he follows a few simple precepts. Part 1 of this two-part series tells him how to deal with noise and control problems.

And in the next issue . . .

More about high reliability in power-semiconductor equipment: part 2 of a two-part series... the why and the how of true rms measurement... the unusual usefulness of resistive insulated gates.

Electronics

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Publisher's letter

There are few consumer-electronics success stories as dramatic as the takeoff in citizens' band radio sales. Spurred by the gas shortage and truck-driver need for information on gasoline supplies, CB sales have soared as the radios themselves have become indispensible companions for those quixotic contemporary American folk heros-drivers of the big highways rigs.

Dick Gundlach, our communications and microwave editor, says that he gave little thought to how widespread CB use was before he started to prepare the nine-page report that starts on page 91. "But while out on the road doing interviews, I started to do an 'ear' count, a tally of the number of CB antennas sported by the long-distance trucks. In one 90-minute drive through wintry Minnesota countryside, about 70%, or five out of every seven trucks, was CB-equipped."

Dick, by the way, took the plunge into CB himself, and wished he had taken his set with him so he could benefit from the truckers' tips on road conditions, traffic obstacles, and the like. But he could not have reciprocated-legally, at least. "My license still has not been sent to me by the FCC, and I sent in my application and \$4 fee some two-and-ahalf months ago. I've tried to find out when it will come, even though I realize there's a huge backlog, further compounded by some 550,000 new applications in January, up from 440,000 in December."

The next step in citizens' radio evolution is not firmed up yet, although the latest proposal before the FCC calls for 50 channels for a mix of a-m and single-sideband use. Says Dick: "Commissioner Robert E. Lee is slated as the dinner speaker for the big CB show-Personal Communications-'76-in Las Vegas this month. I think he may well take advantage of that forum to make the long-awaited announcement that the present 23 channels will become 50.

We have added another page to our continuing coverage of career-related developments. In the Probing the News section this issue, you'll find a thought-provoking article about engineering-manpower predictions and how poorly they have fit actual manpower demand over much of the past decade. The article, on page 67, not only details the discrepancies between prediction and fact, but raises the question of whether such faulty estimating is, in itself, a disservice to the engineering profession.

Part of our career coverage, by the way, caught the attention of the judges of the Jesse Neal Award. Sometimes called the Pulitzer Prizes of the business press, the Neal awards recognize achievements of individual editors. For the second year in a row, Electronics editors have been among the winners in the Neal Award competition. New York bureau manager Ron Schneiderman, associate editor Jerry Walker, and senior editor John Johnsrud received a certificate of merit for their contributions to a series of editorials on age discrimination, a career ill that hits EEs harder than it does those in other professions.

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Readers' comments

Fair play for creativity

To the Editor: The very fact that the writer of the letter about patent preassignment agreements [Dec. 25, p. 6] felt it necessary to withhold his or her name shows how repressive it is for a creative person to work for a company demanding such an agreement.

On the other hand, the creative person needs the backup of big business to have new ideas carried through development and production.

Rather than for management to stifle creativity, it could profit greatly by giving recognition and fair royalties to creative personnel.

It is ironic that enforcing such fair play would infringe upon the rights of management to the same degree that not enforcing fair play infringes upon the rights of the individual.

> Phyllis B. Chisholm Hi-Lab Frisco, Colo.

The time for PM

To the Editor: I disagree with the statement on preventive maintenance, "If it works, let it alone," in Mr. Kush's letter [Jan. 22, p. 6]. According to Murphy and his law, "If anything can go wrong, it will." This applies to any piece of equipment, mechanical or electrical. Therefore a time must be allotted for testing equipment for proper performance—for preventive maintenance, if you will.

The frequency of testing could be based on the mean time between failures for the system being tested. That is, if the system is rated to be reliable for 1,000 hours without a failure, then PM should be performed within this time frame; say, every 900 hours.

In this manner, excessive PM could be avoided, thus enhancing systems performance.

To put it in other words, preventive maintenance is necessary to insure that systems reliability is maintained.

D. D. Dempsey Fort Gordon, Ga.

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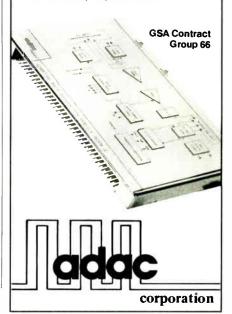
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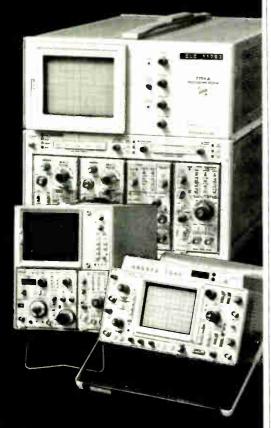
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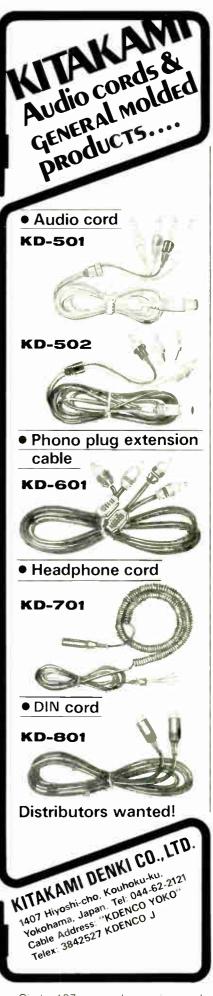
News update

■ The market for intermediate-frequency filters using surface-acoustic-wave technology has not picked up as fast as Crystal Technology Inc., Mountain View, Calif., anticipated in the year since it became the first U.S. company to announce plans to manufacture them [May 1, 1971, p. 25]. A company spokesman says the "market has just been devastated, and no one has been willing to go with any major innovative change." Robert Carlson, vice president for marketing, notes that the \$1.50 price tag on the filters is low enough to begin encouraging colortelevision makers to use the intermediate-frequency filters by the second half of 1976.

At Plessey Memories Inc., Santa Ana, Calif., however, demand for its i-f filters—using acoustic waves—is "phenomenal." U.S. color-television manufacturers are the prime customers, though the company is also looking for healthy sales in the citizens' band and video-game markets. Industry officials estimated the market for the filters will reach as high as 20 million units within the next three years.

■ The National Semiconductor Corp.'s plans to develop both a watch module and microprocessor using integrated injection logic [April 17, 1975 p. 25] have been shelved, says the company. National engineers found that incorporating minor changes into the design of C-MOS chips allowed those chips to drive the light-emitting diodes in watch modules just as effectively as would 12L. "If the advantages of 12L are low power consumption and high density," says an official, "the same thing is offered by n-channel MOS."

In microprocessors, the company says it "would mean a whole new technology." Instead, the company is developing a microprocessor using bipolar technology, but not the relatively slow t²L. In digital-to-analog conversion, however, National believes that this technology is the one to use, and it may soon introduce a related product.



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The need to be heard

A serious barrier to communication appears to be developing in the IEEE. A growing number of members of its professional activities committees believe that the IEEE leadership is trying to stifle their attempts to communicate—not only with other members, but with the institute's leadership itself. The tragedy is that what they have to say should precipitate the kind of debate that would make the institute a stronger force in engineering career matters.

Whether the causes for their belief are real or imagined is no longer the issue. The fact that accusations of "censorship" and "laundering" of controversial issues are being made should be enough to persuade the IEEE leaders to reconsider their stand on how to handle critical, even dissident, in-house communications.

Because the professional activities committee chairmen have tended to feel left out, they have begun to communicate informally among themselves and to exchange ideas on how to get the attention of the policy makers at national headquarters. Unfortunately, a "them-versus-us" attitude has developed along with these informal contacts, and they now view their relations with the board of directors as an adversary situation.

It's up to the board to take the initiative in improving communications. The knee-jerk reaction to this type of proposal has been that there are more than adequate opportunities for every member to be heard either through the local sections or through the technical specialty groups. It may be true in theory, but

does not work well in practice; otherwise there would not be the growing frustration among the most active sections about the drift in IEEE's professional activities.

What can be done? A good start would be for the board of directors to reestablish, through IEEE's publishing facilities, a professional activities committee newsletter that would tell all members what's going on at the local sections across the country. This publication, obviously would also do much to cultivate communications between headquarters and the locals.

A couple of conditions would have to be clarified, however. First, the local committees would have to be responsible for getting the material ready for publication, not the head-quarters staff. A previous IEEE newsletter was loaded with trivia and proved to be ineffective. Second, headquarters has to keep its hands off the material that is submitted, other than to process it for publication. One of the reasons that the grass-roots chairmen feel alienated is the belief that the New York staff wants to avoid controversy and therefore launders material to make it "acceptable."

Such a newsletter may not work, either because the sections fail to get behind it despite their complaints to date or because the head-quarters staff sabotages the content. But in view of all the changes that have buffeted members in recent years, it is worth a try. If IEEE is to gain the high ranking in professional activities that it has in technical information, it must not put a damper on ideas.

Nobody ever made a 4K static RAM family. Until today.

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People

Schneider named head of RCA Americom

The company is new, and the title is new, but it really won't be that much different to Philip Schneider, president of RCA American Communications Inc. He's still at RCA Corp., and he's still in the communications-satellite business.

RCA formed RCA Americom last month to operate the company's domestic communications-satellite system. The Federal Communications Commission had ruled that international message carriers should not allow their domestic and international communications operations to cross-subsidize one another, and they must be separated.

Till then, Schneider had been executive vice president of RCA Global Communications Inc., RCA's international communications carrier. Now, in his new capacity, he is back before the FCC, fighting for RCA's right to provide interstate telephone traffic to Alaska via its new Satcom I satellite system. He's up against American Telephone & Telegraph Co., which hasn't launched its own domestic communications satellite yet but recently requested the orbital location that RCA wants.

As the man responsible for the development, marketing, construction, and implementation of RCA's Satcom system while at RCA Globcom, Schneider says: "We would never have designed the satellite as it currently is without Alaska."

The 24-channel Satcom I was launched in December. The system will go into operation "some time in April," says Schneider, and will serve telephone, video and data customers from an operations center in New York City.

The lanky, easy-going executive, who joined RCA in 1961 after tours with Bell Telephone Laboratories and Teleregister Corp., says the RCA service has been carrying network TV and radio over Western Union Corp.'s Westar II domsat. Last September it also began furnishing transmission service for the Home Box Office pay-TV system in New

World Radio History



New launch. Philip Schneider sees RCA providing a "switchboard in the sky."

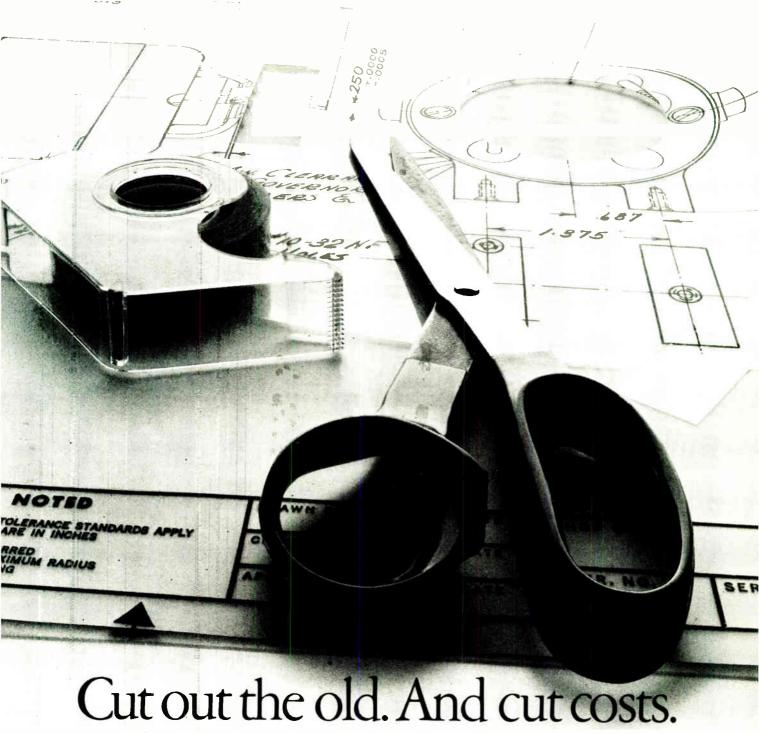
York City. The HBO-RCA network now covers cable-TV affiliates in 23 cities and is expected to grow with the installation of ground stations in as many as 40 cities by the end of this year.

In addition, as an alternative to terrestrial-communications service, Schneider expects this year to offer major private-line users a new "switchboard in the sky" service. Its main attraction: "You'll pay for it only when you use it," he says.

Abraham at Fairchild looks to combine l²L, Isoplanar

Yields of bipolar memory devices were a serious casualty at Fairchild Semiconductor during the last half of 1975 as production rates built up. And it can only be surmised that Richard Abraham, Fairchild's new vice president and general manager of bipolar memories, had much to do with correcting the problem.

In the corporate shakeup that came earlier this year [Electronics,

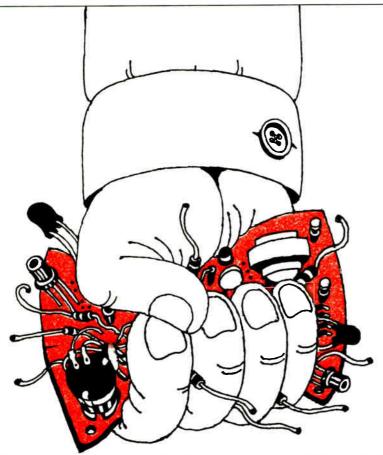


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People

Feb. 5, p. 36], the 46-year-old former head of Motorola's discrete transistor operation has come out a winner. Whereas before he was just another member of the middle-level corporate management in the now defunct Logic and Memory Group, the 6-foot, 2-inch executive now reports directly to Wilfred Corrigan, president and chief executive officer of the Sunnyvale, Calif., semiconductor company. The move, Abraham says, is indicative of "a renewed emphasis on bipolar technology" at Fairchild.

Isoplanar plus. Another indicator is Abraham's present concentration on combining integrated injection logic and the proprietary walled-oxide Isoplanar technology that has enabled Fairchild to dominate the market in 1,024-bit bipolar memories with its 93415 family.

Within the next six months, he says, the company should be in production with two Isoplanar integrated-injection-logic (I3L) products-1,024- and 4,096-bit static RAMs-with performance specifications comparable to Fairchild's present 45-to-120-nanosecond 1-k bipolar Isoplanar RAMs. Further down the line-early 1977, says Abraham—is the just-announced 4-k I³L bipolar dynamic RAM [Electronics, Feb. 19, p. 105].

MOS competitor. This will have an operating speed expected to fall below 100 ns and a power dissipation-400 milliwatts active and 80 mw standby—that is no greater than today's metal-oxide-semiconductor RAMs.

"The combination of oxide isolation and integrated injection logic," says the former manager of advanced integrated-circuit development at both Motorola and Texas Instruments, "opens up a number of new applications areas for bipolar memories. Oxide isolation gave bipolar memories a significant performance edge over MOS memories. 12L gives bipolar memories low power and high performance and while n-MOS will always have an edge in cost and density, Isoplanar integrated injection logic will narrow the gap considerably."

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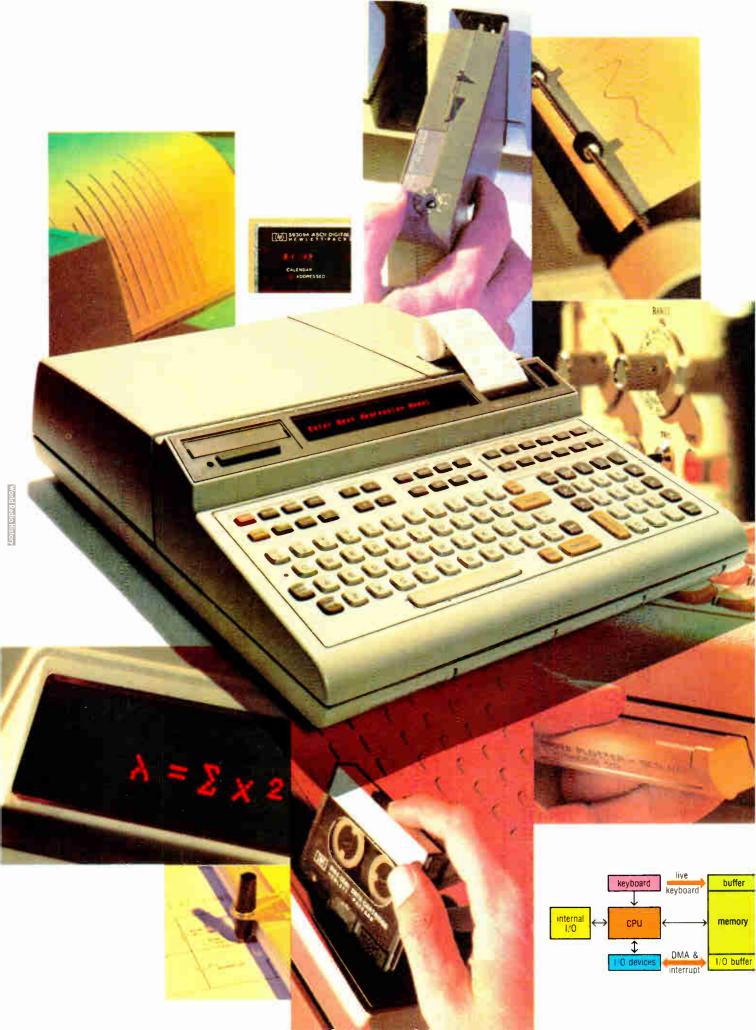
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Up to 400k transfers per second direct memory access provides minicomputer speeds which allow real-time data acquisition and data transfer with high-speed devices

Electronics/March 4, 1976

High-speed, **250k** byte tape cartridge with 6-second average access time permits rapid processing of data and loading of programs.

Multidimensional arrays allow you to organize data logically, thus saving program space and execution time. A 20 x 20 matrix can be inverted in 10 seconds.

Buffered I/O increases throughput by providing a programmable software buffer between the program and an external device.

Memory load and record allows you to suspend processing whenever you want and store the complete contents of memory on tape—including data and pointers—for continuation later on.

High level language (**HPL**) offers you power and efficiency for handling equations, data manipulation.

and input/output operations. Yet it is easy to learn and use.

Other features and capabilities enhance 9825 performance and versatility: for example, upper and lower case alphanumerics on both the display and printer; interfacing to any of eight HP calculator peripherals through three I/O slots, and up to 45 different instruments via HP Interface Buses.

Simultaneous processing of several diverse jobs.

Say you're using a 9825 to control an instrument test stand, and acquiring data from it at speeds in excess of 1000 bytes a second; then printing the results on the new HP 9866B Thermal Line Printer. At the same time, the same 9825 can also be processing and plotting a statistical problem. And through the 9825's live keyboard, you can check the

progress of either program and even change parameters if you desire. It seems the 9825 is doing all these operations simultaneously, thanks to its speed, buffered I/O, and interrupt capability.

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Meetings

Conference on Personal Communications, Electronic Industries Association, Las Vegas Hilton Hotel, Las Vegas, March 30-April 1.

CATV '76-National Cable Television Association Convention, NCTA (Washington, D.C.), Dallas Convention Center, Dallas, April 4-7.

Paris International Electronic Components Show, French Trade Shows (New York), Paris, France, April 5-10.

NBS Seminar on Frequency Standards and Clocks, National Bureau of Standards, Boulder, Colo., April 6-9.

Acoustics, Speech, and Signal-Processing International Conference, IEEE, Marriott Hotel, Philadelphia, April 12–14.

Computer Software Engineering: Reliability, Management, and Design, IEEE, Barbizon Plaza Hotel, New York, April 20–22.

Reliability Physics International Symposium, IEEE, Caesars Palace, Las Vegas, April 20–22.

Electronic Components Conference, IEEE, Jack Tar Hotel, San Francisco, April 26–28.

Optical Computing International Conference, IEEE, Capri, Italy, April 27–29.

Circuits and Systems International Symposium, IEEE, Technical University, Munich, Germany, April 27-29.

Offshore Technology Conference, IEEE, Astrohall, Houston, Texas, May 3-6.

Carnahan Conference on Crime Countermeasures, IEEE, University of Kentucky, Lexington, May 5-7.

Electro 76—IEEE International Convention, IEEE, Hynes Auditorium and Sheraton-Boston Hotel, Boston, May 11–14.

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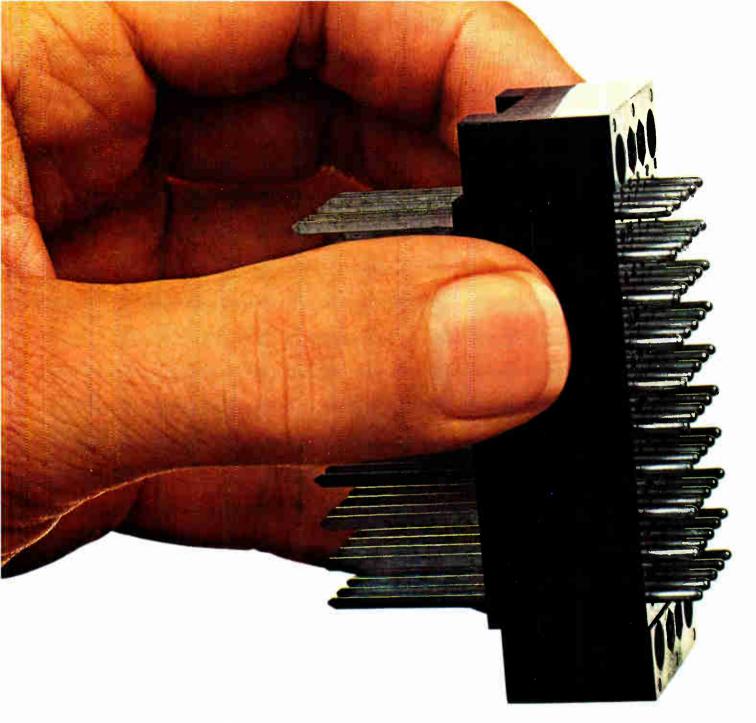
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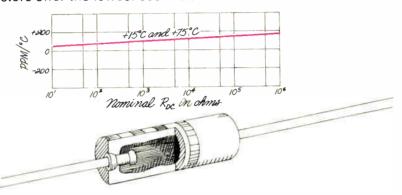
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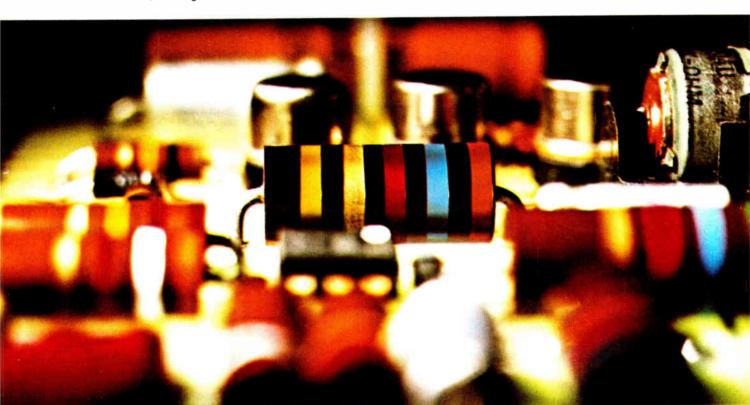
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EC113

Electronics newsletter

Motorola drops I²L in switch to enhanced MOS . . .

Although Texas Instruments and Fairchild Semiconductor are continuing to pursue injection logic for high-performance digital products, one major semiconductor maker, Motorola Semiconductor, has stopped its I²L work in memory and microprocessors in favor of enhanced MOS. According to Robert R. Heikes, assistant group executive for the Semiconductor division, Motorola has set up a special group, headed by Robert Jenkins, manager of LSI engineering, to concentrate the company's efforts in high-performance MOS—including enhanced silicon gate as well as the new MOS techniques, such as V-groove MOS and double-diffused MOS technology.

Recent organizational moves at Motorola also reflect the MOS emphasis. Albert Dulac, head of the MOS Austin, Texas, plant, has been named director of LSI operations, with Colin Crook, manager of LSI systems, and Jenkins reporting to him. Some immediate MOS projects: a 16-k, 16-pin dynamic RAM in sample quantities in a few months and a one-chip 8-bit microprocessor in its final design stages.

... as 6800 gains speed, lower prices by summer

By adding depletion loads to most parts in its M6800 microprocessor family, Motorola Semiconductor plans to double the system's throughput and reduce prices by the end of the summer. The MC6800D—to be sampled in July—will measure 160 mils on a side, down from 200 mils on the current part, "and could be as much as a factor of two faster," says an official at the firm's LSI operations in Austin, Texas. "Because of the enhanced process we expect the 6800D to go to the full military temperature range."

Clock specs on the micropressor will be relaxed and Motorola also is readying a monolithic Schottky driver for July sampling. Dubbed the MC6875, the linear part will replace hybrid or multi-package approaches used now, will cost less than \$2.50, and can use an inexpensive 3.58-megahertz TV crystal as its timing source. Other depletion-mode parts will include the system's 128-by-8-bit static random-access memory, 1,024-by-8-bit read-only memory, and peripheral interface adapter.

TI to add 4-, 8-bit processors to 9900 line

Besides full software compatability with Texas Instruments' 990 minicomputers, TI's recently announced 16-bit microprocessor will have smaller siblings as well. In development in Houston are follow-on microprocessors that are architecturally similar, and software-compatible with the n-channel 9900, but—at least externally—able to operate on 8-bit and 4-bit data buses. Samples of an 8-bit version will be available this year; a 4-bit processor, in 1977.

Also in the 4-bit realm, the firm plans to upgrade its p-MOS TMS 1000 microprocessor to n-MOS this year, yielding a part that requires a single 5-volt supply. Beyond that, "we're working on an electrically alterable ROM version of the TMS 1000," a spokesman says.

National, other firms use outside designers

Here's a new one, even for the semiconductor industry. Some major suppliers, feeling the pressure of a fiercely competitive memory market, have been getting help on key memory products from outside design shops staffed by well-known designers. For example, all of National

Electronics newsletter

Semiconductor's 4-k RAMs were designed by a task force that included a two-man Bay Area design shop called RAM Power.

RAM Power is headed by Joel Karp and John Reed, both former Intel employees who were responsible for the industry's first 4-k design in the early 1970s. According to a National spokesman, the deal with RAM Power has been so successful that "we've renewed our contract for designs on 16-k RAMs, as well as future bipolar and static memory products."

Another two-man design team, called MOS Aid, has recently been formed in Ottawa, Canada. Specializing in dynamic MOS RAM designs, it is headed by Richard Foss, formerly with the defunct Microsystems International Ltd.

500-V rectifiers due from Unitrode

By closely controlling the gold-doping process and optimizing package designs for low thermal resistance, Unitrode Corp. has extended the state of the art for high-speed rectifiers. Its products handle 500 volts at 50 amperes, with a recovery time of 50 nanoseconds, whereas today's best comparable devices, according to the Watertown, Mass., company, handle 450V at 12 A, with a recovery time of 75 ns. Unitrode's gold-doping process is proprietary; engineers will say only that their technique prevents over-doping, which would lead to undesirably high leakage currents. Unitrode has held leakage currents to a maximum of 1 milliampere in its 500-V, 50-A device, as against 25 mA for rectifiers on the market today.

The family is aimed directly at designers of power supplies and will be introduced later this month. It will include 200-, 300-, 400-, and 500-V rectifiers ranging in current ratings from axial-leaded units at 2 and 4 A all the way up to the DO-5-packaged 50-A entry.

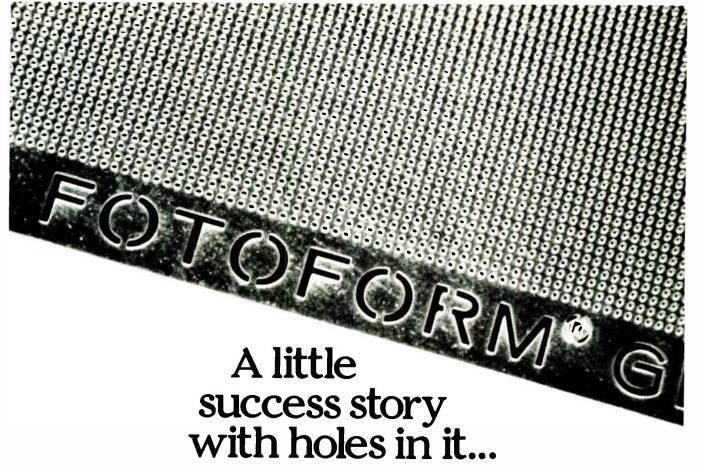
Tl's first 16-k RAM to have 16 pins

Although Texas Instruments chose not to join the group that formed around a 16-pin package for 4-k RAMs, its first 16-k versions will have 16 pins. The first samples, to be shipped late this month, will have a 16-k-by-1-bit configuration that is pin-compatible with Intel, Mostek, and Motorola parts.

Other pin-outs are in the works, however, and users eventually expect 22- and perhaps 20-pin devices from TI, but the parts probably will have different organizations that take more than 16 pins to implement.

Digital multimeter handles 1,000 conversions per second

Using an extension of dual-slope integration, Data Precision Corp. of Wakefield, Mass., has developed a 5½-digit multimeter that can do a conversion in a millisecond. The company calls the integrating technique to be used in the model 7500 "quadraphasic," and it entails the integration of the unknown input for 200 microseconds (slower for conversion requirements) to get the fast conversion speed. The instrument then integrates a large reference input for whatever period is required to get within 1% of full scale, at which time the comparator emits a pulse that changes the size of the reference, and slows the clock speed, each by a factor of 100. This greatly reduces the burden on the comparator for the final short period of integration. The model 7500 will cost less than \$3,000.



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Electronics review

Significant developments in technology and business

Full one-chip controller sells for under \$10

Four-bit Rockwell device aims at control applications for which today's multi-chip systems would be overkill

The first of a new class of one-chip controllers is here. Getting the jump on its competitors, Rockwell International has developed an under-\$10 microcontroller chip that contains enough on-chip registers, memory, and input/output capability to allow equipment designers to handle many stand-alone and peripheral control applications with a single device [Electronics, Feb. 19, p. 132].

Micro. The first of the new PPS-4/1 family from the Microelectronic Device division, Anaheim, Calif., is actually a small microcomputer chip, the A67XX, with 10,752 bits of read-only memory, 384 bits of random-access memory, and 31 input/output ports—more on-chip 1/0 capability than is available on the single-chip 4-bit controllers originally developed for calculators.

Also adding to the versatility of the PPS-4/1 is its large instruction set—50 instructions all told—and its ability to be used with Rockwell's older general-purpose PPS-4 and the recently introduced two-chip PPS-4/2 systems.

Clearly, the 4- and 8-bit programable microcontroller chips that are now or will soon be available provide the cheapest solution to a wide range of control applications—in appliances, low-cost instruments such as digital thermometers, in small games and toys, and in industrial

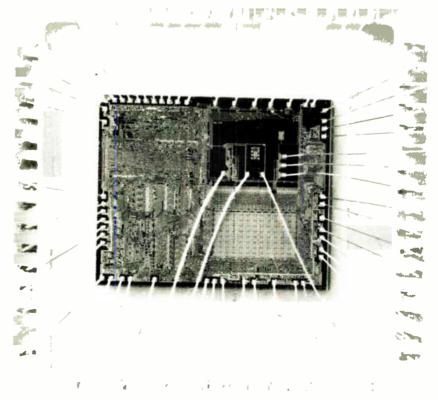
gear that requires a modest amount of data processing, such as gas pumps, scales, and servo gear.

These chips can also be used for peripheral controllers in large microprocessor-based systems, such as point-of-sale and communications terminals. Here they will take the load off the central processors (such as Rockwell's PPS-4 and PPS-8 multi-chip systems) and add capability and flexibility cheaply.

Small but powerful. The ROM, RAM, working registers and 1/0 ports of the PPS-4/1 chip are arranged to

De la contraction de la contra

Development. PPS-4/1 evaluation chip has address and memory lines bonded to pins so prototype programs can be stored in external PROM or ROM. Production version, in a 42-lead plastic package, will have program mask-encoded in an on-chip 10,752-bit ROM.



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provide a basic microcomputer system. A 1,344-by-8-bit mask-programable ROM functions as the program memory, while a 96-by-4-bit RAM provides data, parameter, and working storage. Data is processed by the chip's accumulator and fiveregister arithmetic/logic unit, which together with a carry register perform either binary or decimal arithmetic. Here the accumulator functions as the primary working register, receiving constants via ROM instructions and variable data supplied from or exchanged with the RAM under control of a data-address register. The accumulator also serves as the primary path for 4-bit parallel or serial data.

But what chiefly enables the PPS-4/1 chip to handle the widest variety of applications is its powerful instruction set and 1/0 structure. In addition to its two 4-bit 1/0 channels and 10 discrete 1/0 lines, it has two 4-bit input channels for use simultaneously in testing or comparing data.

What's more, interrupt capability is provided by means of two interrupt-request input lines, one of which can automatically trigger an echo signal or provide priority input and status capability. In addition, the PPS-4/1 is both TTL- and C-MOS-compatible, has a resistor-controlled clock generator, operates from a single 15-volt power supply, and comes in a 42-pin quad in-line plastic package.

Lowest cost. According to James Slager, who's responsible for the system design of the PPS families, "the A67XX chip—unlike others that were first designed as calculator chips—was conceived as the lowestcost microcomputer circuit element that would still be powerful enough for a wide range of system implementations. Not only can the A67XX be used as a compact standalone microcomputer, but it can also be used in conjunction with other A67XX chips in expanded system configurations, or as peripheral controllers with other PPS families, such as our PPS-4/2, PPS-4 and PPS-8 microcomputer families."

The 50-instruction set of the PPS-

4/1 is similar to Rockwell's other PPS-4 sets, enabling a PPS user to move quickly up and down the family, adding equipment features or updating performance. And as with all of Rockwell's microcomputer parts, full software support is provided, as well as a development system, called an assemulator. This sytem combines assembler and emulator functions and helps a user convert high-level assembler lan-

guage into complex machine code.

In addition, Rockwell is making available an evaluation 64-pin chip (P/N A6799), which has its address and memory lines bonded to external pins (see photographs on p. 31); it permits a user to store a program in an external PROM or ROM for real-time development and testing. The prototype device could even be used to implement low-quantity systems quickly.

Memory

Texas Instruments moves bubbles to pilot production for its products

Texas Instruments has moved magnetic bubble memory devices out of the laboratory and into pilot production in Dallas. It's now supplying samples to its own computer operations and has started talking to outside users to determine preferred organizations for the memory. "We hope to see our equipment incorporating our bubbles out in the market certainly no later than 1977," says C. Morris Chang, group vice president for semiconductor products.

The firm's Digital Systems division originally began working with a 256,000-bit module-sixteen 16,000bit chips wired together on a ceramic substrate that measured an inch and a half on a side, a configuration that was first operated with a TI minicomputer in 1974. It's since graduated to a single 100,000-bit chip in a ceramic dual-inline package, with the permanent magnets needed to bias the bubbles mounted inside the package. TI apparently held off moving the devices into pilot production until it perfected the 100-k package: "For bubbles to be attractive, the cost should be down in the 20 to 50 millicent/bit range," Chang says, "and to get to that kind of cost, the packing density needs to be at least 100,000 bits.

Few months away. The Digital Systems division has used the new memories in both computers and terminals "in tests in a lab environment until now," says Doss D. Dun-

lop, the vice president heading the division. "But we will put equipment using bubble memories into the hands of internal customers in the next few months. We're now in the stage of defining what products they'll go in."

A likely candidate is a portable data terminal, where their non-volatility and low power consumption make the bubble devices especially attractive. "We can do storage in a portable environment with bubbles. The alternative, a cassette drive, takes a fair amount of power to operate," Dunlop says.

A second application is a disk replacement. "In terms of speed, it fits right in between disk drives and semiconductor memories," Dunlop explains. "There are still some unanswered questions, but we might use it in the same manner as a floppy disk, although it's not a removeable medium like a floppy disk."

Besides shipping samples to internal users, the semiconductor group has engaged mainframe computer makers in discussions aimed at determining requirements for standard products. "We're asking their preferences for organization of the memory," Chang says. "From that consensus, we will try to develop a few standard products based on customer opinions, rather than develop one memory for each customer."

The firm has no plans in the im-

mediate future to offer bubble devices on the open market, however. "Rather than making a big splash in public, we will work with those customers who have indicated a strong interest," he says.

Large market. TI expects to see significant bubble memory business by 1979, with sales of the devices that year to top \$100 million, as bubbles begin penetrating slow-speed memory applications. "By 1985, I would expect to see a \$400 to \$500 million market, but that's just speculation at this point," Chang says. "What we're working toward is to capture a rather major share by 1979."

Consumer

Electric range holds cooking programs

A microcomputer controls baking, frying, grilling and cooking done on a new electric range from West Germany's AEG-Telefunken. On display last month at a home appliance show in Cologne, the range was developed at the firm's Household Appliance division in Nuremberg and is claimed to be the first European-built electric range to use a microcomputer.

Not surprisingly, AEG-Telefunken used its own recipe for the microcomputer chips—one central processing unit and two read-only memories. They are part of the CP3-F microcomputer system developed at the company's semiconductor facilities in Heilbronn [Electronics, Dec. 12, 1974, p. 33]. The two ROMs store some 120 baking and grilling programs that can be used with hundreds of different recipes.

Keyboard control. The three chips are the key components in an electronic unit mounted above the range. This unit, dubbed "Cookbit," has keyboards that replace all the knobs and controls of conventional electric ranges and trigger dozens of oven and cooking-plate functions. The unit also has displays for show-

ing the information that's important in food preparation. The displays are made up of flat-envelope gasdischarge tubes.

AEG-Telefunken's new Cookbit equipment is in a top-of-the-line model in the firm's "Regent" series of electric ranges. The range consists of a four-burner, glass-ceramic cooking section and a broiling/baking section with a pyrolytic self-cleaning facility. The range will go on sale in the spring of next year. No price has been set yet.

With its new range, AEG-Telefunken will be banking heavily on one big selling point—with 120 stored programs for baking, broilnumber would be used for, say, lemon pie, chocolate cake, or doughnuts, while another number may cook fish, sauerbraten, or duckling of a certain weight, says Klaus Fischer, who is in charge of developing electronic systems for household appliances at the Nuremberg facilities. Still other program numbers would be used to prepare, for example, rare, medium or well-done steaks of certain thicknesses.

However, a housewife can also try out "unprogramed" recipes by merely punching in temperatures and cooking times on the keyboards, Fischer adds.

For stove-top cooking, the range



Hausfrau's friend. Programs for cooking, baking and frying are selected via overhead push buttons in AEG-Telefunken's electric range. Microprocessor-based electronic unit stores 120 cooking programs for different disnes—anything from doughnuts to ducklings.

ing, and stovetop cooking, a house-wife need not be an accomplished cook or baker. All she need do is follow the cookbook instructions in selecting the ingredients and their amounts, put the food in the oven, and key in the program number given in the cookbook. The Cookbit unit takes care of the time-temperature profiles for each dish.

Of American manufacturers of electric ranges, only Frigidaire division of General Motors Corp. has introduced "computer control" [Electronics, Feb. 1, 1973, p. 44]. But in Frigidaire's approach the user programs times and temperatures from the front panel—no cooking programs are stored in memory.

In the AEG unit, one program

has nine cooking cycles for each of the four burners. Each cycle has a specific pair of boiling and simmering levels stored in a random-access memory that's part of the CP3-F microcomputer system's CPU. Electronic timers insure that the power levels of the burners are automatically changed at the right time.

Communications

AT&T wants laws to bar competition

The American Telephone & Telegraph Co. is lining up its Bell System affiliates and independent tele-

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phone companies for a drive to persuade Congress to legislate in favor of a single U.S. telecommunications system. Moreover, AT&T is proposing a rewrite of the 1934 Communications Act that would give state regulators, rather than the Federal Communications Commission, control over interconnection of customer-provided station equipment and terminal facilities, even though the equipment may also be used for interstate service.

The proposals are contained in a 14-page white paper, "The Crisis in Telecommunications: Discussion and Proposed Resolution," being circulated by the company among its affiliates and independent telephone companies. Upon request, it furnished *Electronics* with a copy of the document.

Dated Nov. 4, 1975, the white paper calls for legislation that would provide "indispensable guidance to the FCC and the courts in directing the future of the nation's telecommunications system." Industry sources say the paper is designed to serve as the basis for achieving industry agreement on legislation to be proposed to Congress later this year.

Reaction. Not too many outside the telephone side of the telecommunications industry have seen the document, although a number have heard about it. According to a ranking official at one telecommunications equipment vendor familiar with the plan, "The idea is crazy. Bell is normally very astute, but if they think this plan has a chance in Congress, they are living in the past."

Says Walter Hinchman, chief of the FCC Common Carrier Bureau: "I haven't seen the document but I'm familiar with its contents. To me it sounds suicidal." If the commission is called to testify at the introduction of the kind of legislation that AT&T wants, he is sure the FCC will oppose it.

In addition to calling for "an integrated system of common carrier telecommunications" the AT&T white paper says, "The Congress would find that authorizations to

foster a multisupplier environment for interstate services are contrary to the public interest."

Apparently distressed with earlier FCC actions rejecting as too low several proposed AT&T tariffs designed to compete with the specialized carriers, the company proposal wants Congress to "declare that no charge which is compensatory may be found unjust or unreasonable on the ground it is too low." Any charge that "equals or exceeds the incremental cost of providing the service-i.e., all the added direct costs that are incurred" would be acceptable, according to the white paper. Specialized carriers have contended that AT&T eliminates some of its indirect costs, such as that of equipment already in place, to price new and competitive services below

Takeovers. The legislation being sought by AT&T would also "require a means of achieving industry restructuring" by permitting unlimited corporate acquisitions. These acquisitions, it suggests, should be exempt from any contrary laws, such as those relating to antitrust.

As an additional roadblock to competition, the AT&T legislative plan "would establish binding standards to be met prior to the FCC's authorization of specialized common carriers." The bill would require a specialized carrier to prove before it is allowed to begin operation that it will not "result in increased charges for basic local telephone service; that its facilities will not wastefully duplicate the facilities of an established carrier; and that its authorization will not impair the technical integrity of the nationwide telephone network."

Industrial

Microprocessors smarten controllers

Introduced about seven years ago, programable controllers basically have been more reliable and easier-to-assemble solid-state substitutes



Easy fit. Elements of processor-based model 1084 programable controller from Modicon can be plugged into the unit.

for electromechanical relay logic. But the capabilities of the solid-state devices have gradually expanded until now Modicon Inc., Andover, Mass., is introducing a controller designed around microprocessors that behaves almost as if it had been designed around a minicomputer.

In fact, Modicon, which claims to have 60% of the \$20 million-plus annual programable-controller market, is out to tackle process-control applications that have been handled by dedicated minicomputers, according to product market manager Bruce Rusch. He is refering to control systems in such fields as petrochemicals and chemicals.

Up to now, programable controllers have been used to control sequential operations largely in automotive manufacturing and for machine tools. But even here, Modicon's new 1084 control will be able to handle larger and more complex control schemes.

Custom design. The new model relies on custom-designed micro-processors for its computer-like features, Rusch explains. But the unit

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retains the features that make the programable controller preferable to a minicomputer in many industrial applications, he is quick to point out. It can still be programed with relay ladder diagrams instead of computer programing languages with which many potential users are unfamiliar. Also, it will operate in industrial noise and temperature environments that minicomputers may find too tough.

Up to 10. The new controller has up to 10 Modicon-designed processors, using high-speed Schottky transistor-transistor logic, according to Rusch. The processors are microprogramed units, containing arithmetic and logic elements, storage registers for partially processed results, and a control read-only memory that contains the microprogram and constants.

Each processor operates independently so that the controller can handle simultaneously such tasks as process control, monitoring fault diagnostics and information storage and display. For example, the 1084 can accommodate ASCII peripherals like card and badge readers, cathode-ray-tube terminals, teletype-writers, and line printers; supervise the operation of other, simpler, programable controllers; exchange information among controllers, and collect data and provide hard-copy printouts.

Fast processor needed. "We needed an extremely fast processor to handle all the instructions," explains Rusch. "Off-the-shelf single-chip microprocessors were not fast enough, so we developed a custom unit that has a 150-nanosecond cycle time." Commercial microprocessors with a 200-nanosecond cycle time have only recently become available, he points out.

The typical price will range from \$15,000 to \$70,000 depending on features such as whether the controller operates in a stand-alone or supervisory mode. The 1084 has 10,240 input/output points, 10 times the number of any other programable controller, Rusch says.

In about six months Modicon will have another processor for the 1084

that will make it the first programable controller geared to trigonometric, logarithmic, and fast Fourier transforms—features that will dramatically increase its process control capabilities, Rusch says.

Military

Tactical R&D up in fiscal 1977 budget

The \$10.99 billion total sought for fiscal 1977 military research, development, test and engineering may be another record, but it is nevertheless \$1 billion below what Pentagon research chief Malcolm Currie believes is "a fully justifiable program." Convinced that the Soviet Union is driving its military R&D to achieve technological as well as numerical superiority in weapons, Currie and his staff are pushing hard for congressonal acceptance of their budget.

Priorities. Despite the 15% boost over last year in the request, Currie says he does "not apologize for it." And, says a Currie staffer, for the fiscal year beginning Oct. 1, "we won't have a lot of extra money because some of the programs moving into engineering development will require a great deal more money." He cites the Air Force and Navy cruise missiles as examples. But, as the Pentagon moves to replace inhouse with industry R&D and pushes for more competitive prototyping, the outlook for the electronics industries is improving.

To offset Soviet numerical superiority in weapons, Currie and his staff see improved command and control and communications as a kind of "force-level multiplier," one that can counter to a degree the Russian advantage in force. "Realtime surveillance, target acquisition, and battle control" provided by weapons such as the Airborne Warning and Control System (AWACS) or the Navy's E-2C aircraft with its look-down radar, are worth heavy investment "if they can multiply the individual capabilities of a

large number of less sophisticated and expensive weapons," says the Department of Defense source.

Tactical. Funds for tactical RDT&E are up sharply in the new budget to \$4.23 billion—a 20% jump over a year ago-while strategic systems R&D is up only 3% to \$2.41 billion. The emphasis on tactical systems becomes more evident with a look back at the fiscal 1975 budget, when tactical and strategic R&D outlays totaled a respective \$2.98 billion and \$2.13 billion. The gap between tactical and strategic spending will widen further, according to DDR&E, as projected fiscal 1978 tactical outlays total \$4.72 billion and strategic R&D drops to \$2.36 billion. "Electronics will benefit more than some other technologies like large missiles," observes the DOD source, "since we need more small battlefield weapons and better means to control them."

Remotely piloted vehicles for reconnaissance, target acquisition, or kamikaze-like weapons delivery are getting new emphasis, according to DDR&E. The Army, for example, wants \$7.5 million for RPV advanced development, including \$2 million for field tests of the new multimission Aquila mini-system.

For manned weapons, the Army wants \$26 million for advanced development of its Advanced Scout Helicopter, a highly maneuverable, all-weather vehicle. The Army will spend most of its new money on developing an antisubmarine warfare package for the Advanced Attack Helicopter. It will also include a laser target designator to guide such supporting weapons as the Hellfire and Maverick missiles and cannon-launched guided projectile.

Off-the-shelf. Complementing the RPV and helicopters needed to fulfill this mission are DDR&E programs like the stand-off target acquisition system, funded at \$8.4 million, and the remotely monitored battlefield sensor system known as Rembass, for which \$11.5 million is sought to continue full-scale development. Both systems exemplify DDR&E's interest in using off-the-shelf hard-

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ware when possible. Sotas, for example, uses moving-target-indicator radars like the AN/APS-94 for mounting in a helicopter.

Computers. Modification of commercial minicomputers for military use beginning in fiscal year 1977 is reflected in the more than \$30 million sought for new field electronic data-processing capabilities. The new battery computer system alone is budgeted for \$12.7 million, including \$10 million for engineering development.

The battery computer system evolved from the Army's unsatisfactory development in the 1960s of the field artillery digital automatic computer system known as Fadac. The new system will respond faster to forward observer fire requests from such new systems as the cannon-launched guided projectile—a weapon that the Defense Department hopes will achieve most kills on the first shot.

Reliability

Military too strict on microprocessors

Although the price of microprocessors bought off the shelf for commercial use keeps plummeting, their cost for military applications remains high—unnecessarily high, charges a top design engineer at Rockwell International Corp.'s Strategic Systems division. He claims that the specifications on which the military rely are outmoded and call for too many screening and test steps.

Marginal value. "The military should re-examine its mil-spec requirements and eliminate those of little or marginal value," proposes Jack Jurison, who, as project engineer for digital systems at Rockwell's Anaheim, Calif., facility has worked in the field for many years. He was speaking at the Session on Microprocessors at last month's IEEE Winter Convention on Aerospace and Electronic Systems in Los Angeles.

"Many of the screening techniques currently applied to microprocessors were developed more than 10 years ago for determining reliability of discrete components," he says. For example, visual screens can cause a good microprocessor to be rejected as bad when they throw out the device because of an unimportant surface flaw. Moreover, when applied at the manufacturing level, the screening techniques require special handling that often disrupts commercial production lines, adding further to costs, Jurison points out.

Cumulatively, the effect of the specifications and screens on device cost is disastrous, he says. A \$10 commercial processor has its cost doubled by the most preliminary screen, standard Class C; a class B screen roughly triples its cost, while the full-bore military qualification raises total cost five or six times.

Question of price. Asks Jurison: "How much are we prepared to pay for reliability in what is basically a cost/reliability tradeoff? If we are able to back off on the screens, we

can reduce the cost significantly."

On reliability, Jurison cites Rockwell's field experience of 1.5 statistical failures per million hours of operating Rockwell-designed pchannel off-the-shelf commercial devices. Since reliability attains such



Critic. Rockwell's Jack Jurison says the military could save lots of money by using already reliable commercial microprocessors.

Standard chips not likely

Another way to lower the cost of microprocessors to the military is by standardizing on a relatively few basic chips—at least in theory. But it will prove most difficult to achieve in practice, if the lack of agreement evident during a panel discussion at Wincon is anything to go by.

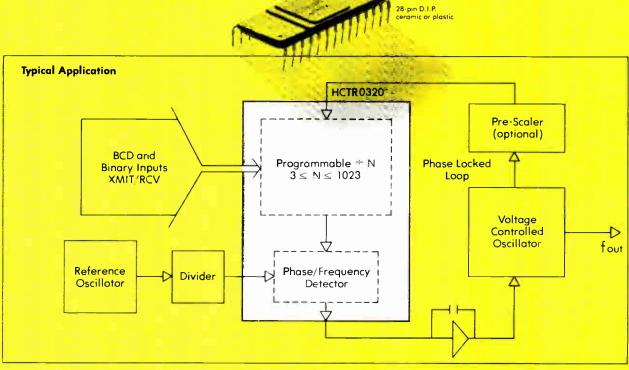
William C. Eppers Jr., acting deputy director of the Air Force's Avionics Laboratory, Wright-Patterson Air Force Base, stated the standardization case for the services. "Proliferation [of chips] is eating us alive costwise. The whole point is to try to tackle life-cycle costs." Maintenance, he says, represents 70% of the life-cycle cost of a typical electronic system. Standardizing on a few chips, or microcomputers using chips, would permit high production volume, lower unit cost, and allow the throwaway advantage that drastically reduces maintenance, Eppers explains.

Among the objections to this philosophy, one in particular was raised repeatedly. This stressed that "freezing" technology at a standard level closes out the rapid advances occuring in semiconductors. "There's a big difference between LSI a few years ago and what can be done now," one panelist said. Another drawback is the many different ways of configuring a chip, notes Donald Calhoun of Hughes Aircraft Co., who presented a paper on applications of a military microcomputer.

Other IEEE members also attacked the whole concept of standardization, which they say seeks to stop the flow of technology at a certain point. "This is not a healthy situation," one remarked," "since it dooms us to an inferior system."

Since the discussion failed to find common ground on the need to standardize, many participants concluded that a definitive program lies far in the future—and likely never will happen.

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high levels without military screening, "wouldn't it be more cost-effective to take our chances on letting microprocessing fail?" he asks.

In fact, the small size and versatility of the microprocessor device allows designers to "selectively tailor a high degree of redundancy" into a system and thus safeguard critical functions in a way they couldn't hope to do with discrete components, he claims.

While Jurison advocates taking a hard look at what he feels is marginally effective reliability screening, he takes pains to state that the military requirement for testing all devices at elevated temperatures is basic. "All I'm saying is that maybe we can buy cheaper microprocessors and still get reliability." This proposal has been put to microprocessor and microcomputer project people in the Air Force, Jurison says, and "they have shown interest."

Packaging & production

Nitride film seals at low temperature

For years, semiconductor manufacturers have protected silicon wafers by coating them with a final oxide layer of glass. But, unfortunately, the so-called Silox process does not seal the wafer hermetically. That job requires a material like silicon nitride, which, until now, has required too high a temperature to deposit it on most devices.

Now, LFE Corp., Waltham, Mass., has developed a low-temperature silicon-nitride process [Electronics, Feb. 19, p. 25] that it says can supplant Silox. Arthur W. Zafiropoulo, marketing manager for plasma systems, estimates that a typical deposition system would pay for itself within months by allowing manufacturers to substitute hermetically sealed plastic packages for more expensive ones. "One [high-volume] manufacturer of bipolar devices has estimated that he can save \$17 million a year by going to plastic instead of ceramic," he says, but

stresses that such a large saving applies only to a high-volume manufacturer who gets high yields from relatively small dice.

The low temperature is important, Zafiropoulo points out, because passivation, one of the last steps in wafer fabrication, must not disturb the metal and insulating layers deposited earlier. He claims that the process produces extremely pure nitride films at high average deposition rates and uniform thickness and density across the wafer.

Processing. This uniformity is achieved, Zafiropoulo says, not only from using nitrogen instead of ammonia as a reagent, but also because LFE does not premix its reagents as some others do before putting them in the reactor. It introduces silane, or silicon hydride, diluted with argon, separately from the nitrogen in vacuum inside a reactor, where the wafer is heated to about 300°C.

It uses reagents of silane gas and nitrogen, whereas previous processes use diluted silane and ammonia. The secret is in how LFE introduces these reagents into the vacuum-chamber reactor containing the silicon wafers, but Zafiropoulo is not telling precisely how it's done. He says only that nitrogen is dissociated and reacts with silane "by means of a complex mechanism" to form a silicon-nitride film on the heated wafer surface.

He points out that in conventional processes, silicon nitride is deposited at temperatures higher than 750°C—too high for passivation of an entire wafer. This limitation restricts the material to use as a gate dielectric for high-reliability devices and nonvolatile memories.

Fewer contaminants. The process also solves some of the problems that result when ammonia is ionized in the deposition process, Zafiropoulo continues. Chief among them is the introduction of contaminants including hydrogen and oxygen. These impurities may form water vapor with its potential for corroding interconnects or may produce porous nitride films that either don't protect the devices against leakage or have undesirably high etch rates.

LFE has processed "thousands of wafers"—both bipolar and MOS—to demonstrate its system to semiconductor manufacturers, says Zafiropoulo. Yield increases ranged from 5% to 30% through the wafer-probe and final-packaging steps and after burn-in and elevated temperature and humidity tests, he asserts.

He also claims the company already has "a number of orders." A single-reactor system that accommodates one wafer, designed to provide experience with the process, sells for \$20,000; an automated system with multiple vacuum-chamber reactors carries a \$70,000 price tag. The automated units accommodate wafers in cassettes. The wafers journey through an in-line system from one cassette to another to eliminate human handling. Throughput rate is 45 wafers per hour for a siliconnitride layer 4,000 angstroms thick; thinner layers increase the throughput.

Solid state

Read-only memory reaches 32-k bits

In the last stages of development at Electronic Arrays Inc. is what will be the largest commercially available n-channel semiconductor readonly memory to date: 32,768 bits. And it uses an amalgamation of techniques—clocking and sensing adapted from dynamic random-access memories and a metal-gate process that uses a self-aligning gate usually associated with silicon-gate techniques.

What this has produced, says the firm's manager of memory product development, Michael R. McCoy, is a static device with an access time in the 200-to-300-nanosecond range and a power consumption averaging 10 microwatts per bit—making it two to three times faster than ROMs a quarter to half the size, but requiring at least two thirds less power. Sample quantities will be available in the second quarter of the year.

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Electronics review

high density, low power, and high speed in the 158-by-255-mil, 28-pin EA3200, the Sunnyvale, Calif., firm's engineers decided that "in terms of cell size and speed it was important to retain metal gates because of their interconnect advantages," McCoy says. "In particular, no yield-reducing contacts are required."

Fewer steps. With silicon gates, such contacts are essential. But silicon gates, because of their selfaligning feature, offer the advantage of fewer masking and processing steps and thus lower costs. What the company's engineers came up with is a self-aligning metal-gate process achieved with a masked oxidation technique.

In the usual metal-gate process the source and drain regions are etched first, followed by a diffusion step. A thick oxide is then grown. The gate areas are etched through the oxide, following which the gate oxide is regrown. "Unlike silicon gate, this is not self-aligning," says McCoy. "The oxide often overlaps, and invariably it's necessary to etch over the source and drain to insure you have contact in those areas."

The source, drain, and gate areas are etched concurrently with the new technique. Then the gate area is masked with nitride, followed by a diffusion step and then the growth of an oxide layer. "Because the gate area has been specifically masked," says McCoy, "there is no overlapping into the source and drain, and only the channel has oxide."

The combination of metal-gate interconnects and self-aligning masked oxidation results in a cell size of only 0.42 square mil, one half the size of cells in 16,384-bit silicongate ROMs.

8980A speeds. With just these techniques alone, it would have been possible to build 32-k ROMs, which with 550-ns access times would be at least 200 to 300 ns faster than their 16-k counterparts. "But the main application for ROMs—microcomputer systems such as the Intel 8080A—are down in the 300-ns range," says McCoy.

To get the ROM speed to this

News briefs

Northrop wins Harpoon strapdown guidance

Northrop Corp.'s Electronics division last week received a \$2.5 million contract from McDonnell Douglas Co.'s Astronautics division for initial production of strapdown guidance assemblies for the Navy's Harpoon missile. Harpoon is an antiship missile that can be launched from beyond the horizon by aircraft, surface ships and submarines. Northrop will build the attitude reference assemblies at its Precision Products department, Norwood, Mass., with deliveries to start in January 1977. Each assembly consists of three subminiature rate-integrating gyros and three linear force-balance accelerometers, along with associated pulse rebalance loops to interface with the missile computer.

Shepherd, Bucy move up at Texas Instruments

Mark Shepherd, president of Texas Instruments, Dallas, will move up to board chairman on April 22, succeeding Patrick E. Haggerty who reaches the company's compulsory retirement age of 62 later this month. J. Fred Bucy, executive vice president, will succeed Shepherd as president and remain the chief operating officer of the company.

Marisat placed in orbit

The first maritime communications satellite (Marisat) has been launched and is scheduled to become operational by the end of March. A second Marisat is to be launched in May. Designed to provide ships at sea with full-time, quality communications, the first will serve an area extending eastward from the East Coast of the U.S. to the western portions of the Indian Ocean. The second satellite will cover the mid-Pacific. Shore stations will be interconnected with telephone land-line networks and linked with a control center in Washington, D.C. The Marisat system is jointly owned by Comsat General Corp. (the system manager), RCA Global Communications Inc., Western Union International, and ITT World Communications Inc.

DEC enters word-processing market

Digital Equipment Corp., Maynard, Mass., has entered the word-processing market for the first time. DEC's Datasystem 310W, built around the company's PDP-8/A minicomputer, will sell for \$22,600. It includes the computer, a 45-character-per-second letter-quality printer, a cathode-ray-tube terminal, two floppy disks and a software package that will handle tasks ranging from general correspondence to contract preparation. Jack Gilmore, manager of the word-processing product line, says the 310W is aimed at traditional company data-processing customers whose needs have begun to include word processing.

Upgraded IBM 370/168 coming this summer

IBM Corp. will begin shipments this summer of an upgraded 370/168, which it claims boosts internal computing performance as much as 1.5 to 1.8 times over that of a model 168-3 with a single instruction unit. With the new Attached Processor System (presumably developed by IBM in response to the inroads made into its high-end central processing market by Amdahl Corp.), all data input and output operations are handled by the "host" model 168-3 central processor. But main memory is shared by both the host and the attached instruction processors—increasing the computer's ability to process machine-language instructions.

Westinghouse replaces Xerox computers

Systems Engineering Laboratories Inc., Orlando, Fla., will provide Westinghouse Electric Corp. with up to \$10 million worth of 32-bit computers under a new three-year agreement. The contract replaces the agreement Westinghouse had with Xerox Corp., which recently dropped out of the computer business. The new computers will be part of the systems developed by Westinghouse for energy management by public utilities.

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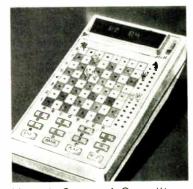
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Electronics review

range, McCoy borrows techniques developed for high-performance 4-k dynamic RAMs. "In previous ROM designs, for example, it was common to use a simple buffer amplifier to read out the signal level," he says, "but for a 32-k ROM this would take as much as 300 to 400 ns. This is reduced by half, using a 4-k dynamic sense-amplifier technique to look at the slope of the signal rather than its absolute level."

These dynamic techniques require three power supplies: -12 and ±15 volts, with the ROM clocked on or off via a chip-enable signal. Active power dissipation is 480 milliwatts; standby power is 10 mw.

Chess mate



Mostek Corp. of Carrollton, Texas, having won one kind of consumer with a checkbook calculator, is now pursuing a totally different breed-the chess player-with the electronic chess set drawn above.

Demonstrated in breadboard form last week at the 73rd annual American Toy Fair in New York City, the set is a hand-held calculator in which a chess algorithm is stored and into which a player feeds his moves. The calculator, after analyzing the position of the pieces, responds with countermoves indicated on an 8-digit diode display. Basic to the set is an F-8 microprocessor developed by Fairchild Camera & Instrument Corp. but secondsourced by Mostek. Cardinal Industries Inc., a New York City toy manufacturer, plans to hire an assembler for the unit and begin selling it in June at a suggested retail price of \$120.

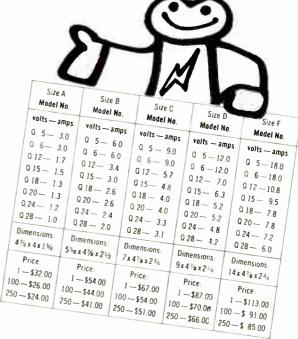
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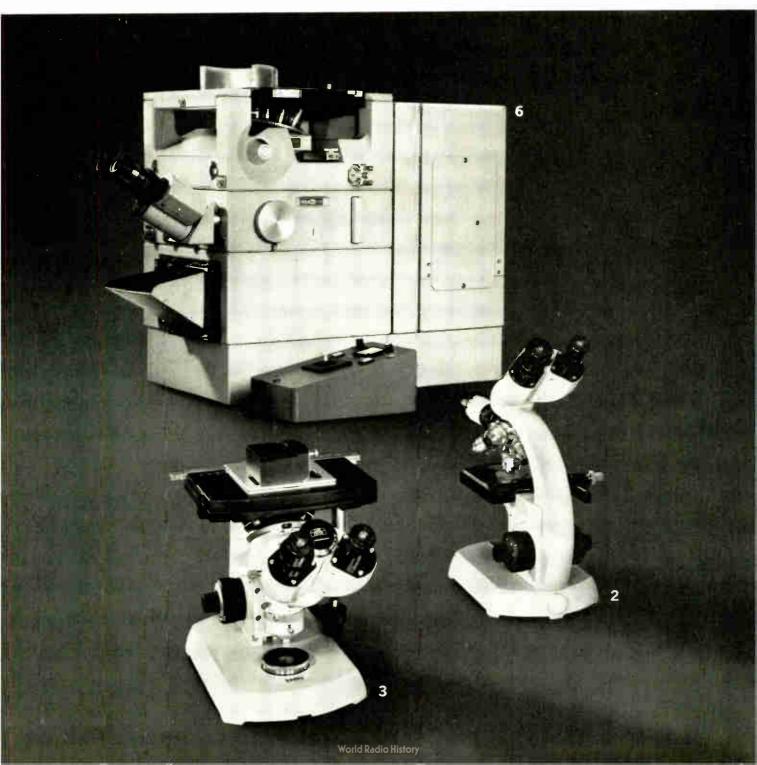
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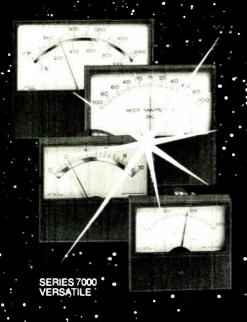
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Washington newsletter

Fixes for Phoenix to take 5 years, cost \$67.1 million . . .

Even apart from the new engine problems in Grumman's F-14 fighter, the Navy is distressed over the **poor performance of the plane's long-range AIM-54 Phoenix missile**, built by Hughes Aircraft. Yet the Navy hopes to salvage Phoenix with a new five-year R&D program costing \$67.1 million, of which the first \$10.7 million would be spent in fiscal 1977. Operating in a high-density environment, like that of the Mediterranean, the missile cannot discriminate between friend or foe, sources report.

Navigation problems, failure to respond to midcourse and terminal guidance, and a warhead that can't destroy targets unless it hits them directly—all need to be corrected, say Navy officials. First-year funds will be used for tradeoff studies and beginning development of a new on-board digital processor, digital autopilot, target detection sensor, and warhead.

. . . and faulty Sparrow will cost Navy \$28.6 million

Another Navy air-to-air missile, the old standby AIM-7F Sparrow, built by Raytheon Co., has also been having problems, chiefly in the reliability area and, to a less extent, in target detection. The Pentagon figures it will take \$28.6 million over three years to fix Sparrow, including \$16.3 million in fiscal 1977 for engineering development of an advanced seeker and to upgrade the missile's electronics complement with state-of-the-art components.

Opposition forms to rewrite of contract renegotiation

Military electronics and aerospace companies are gearing up for a hard battle to kill H.R. 10680 in the Senate this summer—the bill that rewrites the Renegotiation Act to require Government contractors to figure their profits by division and major product line within a division. At present it is done on the basis of a company's aggregate Federal business. Recently passed by the House, the new bill would permit the Renegotiation Board to require a contract to be renegotiated if it claimed an excessive profit within one division, or on one product, rather than on a company-wide basis.

Industry contends that the existing system of lumping a company's Federal contracts in a fiscal year permits it to compete for business with an uncertain or limited profit by offsetting it with higher profit margins elsewhere in the company. The new system, they say, would tend to make companies more selective in bidding for Federal jobs and increase their internal accounting costs—a cost that would be passed on the Government.

Broadcast radiation study set by EPA: controls possible

The Environmental Protection Agency is beginning a two-year study of radio and television broadcast and microwave radiation in major U.S. cities to determine whether environmental controls are required. Using a van packed with measuring instruments, a small computer for data storage and analysis, and three men, the EPA will survey from 14 to 18 sites in each city. Data acquired by the EPA mobile unit will be combined with research data on health effects being developed within the agency, and the package will then be presented to an inter-agency working group headed by the White House Office of Telecommunications Policy. The seven cities in the survey's first phase include Atlanta, Boston, Chicago, Miami, New York, and Philadelphia. Cities in western states will be surveyed between October 1976 and September 1977.

Washington newsletter

of international dataphone ruling

ITT World Communications Inc. has gone to a Washington Federal court to prevent development of what it believes will be an American Telephone & Telegraph Co. monopoly in the international record-communications market, if overseas telephone customers are allowed to use dataphone-type equipment. ITT Worldcom wants the U.S. Second Circuit Court of Appeals to set aside a recent Federal Communications Commission ruling that AT&T tariffs limiting international customers to voice-only communications are no longer appropriate and that dataphone-type hardware may be used. ITT Worldcom charged that the order would alter the structure of the international data communications market.

Zooming costs doom computerized traffic network

If the experience of the District of Columbia is any measure, the promising computer market for municipal traffic control systems is in serious trouble. The reason: inflated operating costs, largely the result of telephone rate increases, are making it impossible for cash-starved cities to continue system operations. The D.C. experimental system, built by Sperry Rand Corp. for the Federal Highway Administration, is being shut down for that reason.

Since D.C. doubled the system's capacity to control automatically traffic flow at 200 of the city's 1,150 intersections with signals, the monthly cost of the telephone lines linking the two computers to 600 traffic-monitoring sensors buried in the streets has climbed from \$675 to \$5,798. And if a pending Chesapeake & Potomac Telephone Co. rate increase is approved in March as expected, the monthly bill will soar to \$21,000. D.C. says it is going to look at new technology to see if it can find a cheaper communications system. The highway administration says that, because of the system's technological success, some 20 other U.S. cities are installing similar ones.

Two in competition for phase shifters for agile radar

Raytheon Co. and Microwave Applications Group are competing for a Westinghouse Electric Corp. subcontract to supply low-cost phase shifters for the multimode Electronically Agile Radar. Competitive tests are beginning at Westinghouse's Baltimore division. Each airborne BAR antenna, designed for Air Force use in strategic bombers like the B-1 and B-52, will contain 1,818 individual ferrite phase shifters and account for about 10% of the radar's cost. Westinghouse says the phase shifters are the target of a major cost-reduction effort under its design contract for the Air Force Avionics Laboratory to balance performance and costs.

FCC to speed citizens' band license action

In a move to alleviate a backlog of several hundred thousand citizens' radio license applications, the Federal Communications Commission has approved a procedure for issuing temporary permits until it can develop and implement a high-speed licensing system. At the same time, the commission approved a program by its Office of Plans and Policy for a series of planning studies of public need for personal radio communications. The FCC said the studies will not delay a decision on Docket 20120, which proposes to relieve CB frequency congestion by allocation of additional Class D channels.

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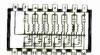
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Electronics international

Japanese work on self-coupled laser to play stylus-recorded video disks

Although many researchers have investigated the interaction produced by the coupling of two semiconductor lasers, a Japanese team is apparently the first to self-couple a single laser successfully. The effects they have discovered are likely to be used soon with video disks and other types of optical memories. The team at the Japanese government's Electrotechnical Laboratory is now extending its investigations to try to demonstrate that optical playback of video disks is feasible.

The team found that when the infrared laser's output is reflected back to the source, both the output and terminal voltage are appreciably increased—the latter by several millivolts. The experimental laser, called Scoop for self-coupled optical pickup, is a continuous-wave multiheterostructure gallium-aluminum-arsenide device with stripe geometry 7 micrometers long and 200 μm long.

Experimenting. Threshold current is about 180 milliamperes, and the near field pattern of light emission is a single spot over the working range of the pumping current. The beam path is about a meter long—several times the distance the beam can remain coherent.

The increase in beam output is greatest when the laser's pumping current is below the lasing-current threshold, but at this low pumping current, the return must be at least 10% for the effect to be observed. A mirror reflects about 30% of the infrared output back to the laser.

The results have convinced the researchers that the optical effects can be attributed solely to the returned light. In the experimental setup, they used a 20× microscopic objective, a beam splitter, a mirror-dynamic chopper arrangement, a variable attenuator, and a beam splitter to help them study and dramatize the effects.

In a practical application, the lens would be replaced by a length of Selfoc optical fiber, which transmits light and also has lens characteristics. The photodetector would be included only if used as a sensor with higher sensitivity than the terminal voltage of the laser could provide. The beam splitter and variable attenuator would be unnecessary.

The team now plans to use the la-

ser with the hill-and-dale-type video disks made by Germany's Tele-funken AG, which are normally recorded and played back with a stylus. However, for an effective demonstration, the team will have to develop a servo tracking system to keep the laser beam in the spiral groove of the rotating disk and may have to metalize the disks to provide enough reflectivity.

Around the world

UK study advocates proven bipolars in space

Designers of European spacecraft for the 1980s should stick to off-the-shelf Schottky transistor-transistor logic or other bipolar circuits and avoid custom large-scale integration. That's the conclusion of a European Space Agency (ESA) report on improving the reliability of electronic subsystems in space. Other conclusions to be drawn from the study, intended to govern the design of digital subsystems for command and control functions, are:

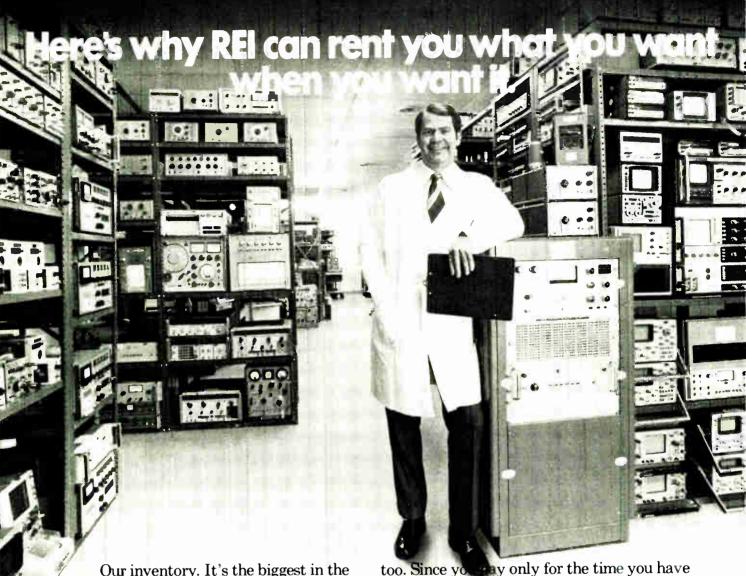
- Integrated injection logic looks promising because of its low power and high packing density, but not enough production experience or failure data has been generated yet.
- MOS technology, though production-proven, shouldn't be used because it is 10 times more likely to fail from radiation and several times more likely to pack up in high temperatures than bipolar logic.
- Custom LSI should be used only where absolutely necessary because one year or more is needed in production to prove its reliability, it is subject to inherent catastrophic failure, and it is too expensive if made reliable.
- Standard circuits designed into space subsystems should be sorted first by military-standard specifications and then by a failure-predicting standard similar to the British post office's stringent D-4000.

Cambridge Consultants Ltd. in the UK, which conducted the study, has designed and proposed a dual fail-safe (DFS) design for which it has jointly applied for a patent with ESA. The company claims that the DFS design is more economical and reliable than triple modular redundancy, used by NASA in the U.S.

Swedish load sensor weighs trucks on the road

A strain gage developed in Sweden can be welded in place. The device, called the Aton, is being welded to truck axles to weigh loads—front or rear axle, gross or net—at the press of a button while on the road. The weight is displayed on the dashboard.

The Aton will be manufactured by Amlab AB. The makers claim a maximum error of $\pm 1\%$. The sensor consists of a steel strip, 20 millimeters long, 0.15 mm thick, and 2 mm wide, that is bent under high pressure into a slight S in the horizontal plane. An electromagnet above the gage energizes it with electric pulses and in the off phase detects the vibrations they set up. The frequency, about 1.5 kHz, increases with the weight on the axle. The sensed signal is frequency-modulated and transmitted to an Intel 4040 microprocessor for conversion into units of weight that are shown on a Monsanto digital display.



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International newsletter

Japanese predict record electronic growth this year

Electronic production in Japan will set a record this year, and growth will continue steady, predicts the country's Electronic Industries Association. Production in 1976 will total \$14.187 billion, a 16.4% increase over the level last year and 5.2% higher than in 1974, the best previous year, says EIA-J. Most categories are growing, except receiving tubes, black-and-white television picture tubes, industrial instruments, and thyristors. The last two categories reflect lack of capital investment.

Components are growing fastest—up 25% over last year to \$4.253 billion. Passive components lead with an increase of 20% to \$2.321 billion. Integrated circuits will be 24.2% higher to \$491.2 million. Discretes will follow with an increase of 21.9% to \$633.5 million. And paced by color production, picture tubes will rise 17.3% to \$807.4 million.

Germans allocate \$1 billion to space from 1976 to 1979

A space program that will cost nearly \$1 billion from 1976 to 1979 has been approved by the West German government. Nearly two thirds of the money is earmarked for joint European ventures—among them, the German-headed Space-lab project. The program aims to push new technologies, to help improve long-range weather forecasting, and to expand the assortment of German and European space-related telecommunications hardware. More than previous space programs, this one emphasizes practical applications, but leaves room for basic research.

Peripherals to lead 1980 computer sales in Western Europe

Peripherals—including printers, keyboard terminals, and intelligent terminals—will account for more than \$5 billion of an \$8 billion Western European computer market in 1980, preducts a market study in the UK. However, the share of equipment from the U.S. will fall from 80% to 60%, forecasts Mackintosh Consultants Co. The reduction is attributed to government policies and new companies capitalizing on advance in microprocessor technology and custom large-scale integrated circuits.

The printer market will climb from \$337 million to \$1.4 billion, and keyboards and interactive intelligent terminals will account for \$425 million. As in the computer market, West Germany will continue to lead, but the United Kingdom, now third, is expected to replace France in second place by the end of the decade.

Danes to test advanced phones in Jutland

Several versions of an advanced telephone have been developed by ITT's Danish subsidiary, Kristian Kirks Telefonfabrikker, in cooperation with a regional Danish phone company, Jydsk Telefon A/S. Jydsk, which operates phone service in the Jutland Peninsula, plans to introduce 10,000 trial sets there early next year.

The most advanced phone, equipped with a small display, can operate as a computer terminal. A phone can also be programed with the 10 numbers called most frequently so that touching just two identifying buttons will put through a call. The called number can be flashed on the screen so that if it's either wrong or busy, the caller needn't pick up the phone. The keyboard of numbers and letters can also function as a miniature calculator, flashing the results on the screen. Although the phone was developed partially by the ITT subsidiary, the Danes hope to sell production licenses outside the country.

International newsletter

Minicomputer runs navigation system for small aircraft

A small minicomputer-based air-navigation system is being marketed worldwide by the UK's Marconi Elliott Avionic Systems for executive-type aircraft, military jet trainers, and military and civil transports. The market for the 19-pound AD620, priced at \$10,000, is estimated at 4,000 during the next 10 years. The company has already sold an undisclosed number to the West German air force.

Pilots can encode Tacan and VOR/DME beacons in the systems, as in larger conventional systems. In addition, if the aircraft gets out of range of a line-of-sight radio beacon, the AD620 automatically reverts to dead reckoning according to previous radio inputs and wind velocity. The cockpit control unit, which measures 1½ by 4 by 6 inches, handles the aircraft's sensor inputs and cockpit displays in addition to beacon inputs. The system's AD2770 minicomputer, developed for military airborne Tacan operation, uses transistor-transistor logic. One version contains 10 printed-circuit boards, and the other has 15, depending on the number of turning points or way points desired. Memory capacity is expandible from 2,000 digit words to 24,000 words. Instruction-cycle time is 8 microseconds.

Phased-locked loop is key to Motorola stereo decoder chip

Motorola Semiconductor plans next month to start sampling an improved stereo decoder chip based on a phase-locked loop. The device, designated the TCA 4500A, bests Motorola's current decoder chip, an industry standard, particularly for auto radios. The phase-locked loop rejects the 57-kilohertz and 114-kHz interference from European "roadside information" broadcasts. Transition from stereo to mono is linear, eliminating abrupt changes when signals are weak, and the chip provides some gain instead of the usual insertion loss.

Two firms rush electronic trend in teletypewriters

A trend toward electronic teletypewriters in Europe is picking up momentum. West Germany's Siemens AG has introduced its model 1000, and Philips in the Netherlands has announced plans to show its version next May. The Siemens model 1000, announced on the heels of the LO 2000 from ITT subsidiary Standard Elektrik Lorenz AG [Electronics, Feb. 5], operates almost noiselessly, unlike electromechanical systems.

In its printing unit, the symbols are mounted on 56 spokes of a rotating plastic disk that moves into the write position within a few milliseconds after the key is depressed. Large-scale integrated MOS circuits and other electronic devices help reduce the model 1000 to two thirds the size of a conventional machine. Like SEL's LO 2000, the Siemens model has no dialing disk, but signals other stations through figure keys.

Hitachi gets high speed/power ratio in 1-k bipolar RAM

High speed of a new 1,204-bit bipolar memory from Japan's Hitachi Ltd. is attributed to silicon-dioxide isolation and use of the 100 crystal plane. Sample shipments of the HM 2110, which is compatible with emitter-coupled logic, have already begun, and quantity shipments, including exports, are to begin in April.

The memory, only 56% as large as memories made by pn-diode isolation, is equivalent to two U.S. models: the F10415A recently introduced by Fairchild Semiconductor and the MC10146 soon to be marketed by Motorola. Oxide isolation is used only at the sides of the elements, and diode isolation is used at the bottom. The standard HM 2110 has maximum access and cycle times of 35 nanoseconds. The HM 2110-1, priced at a premium, also has a maximum cycle time of 35 ns, but its access time is 25 ns.



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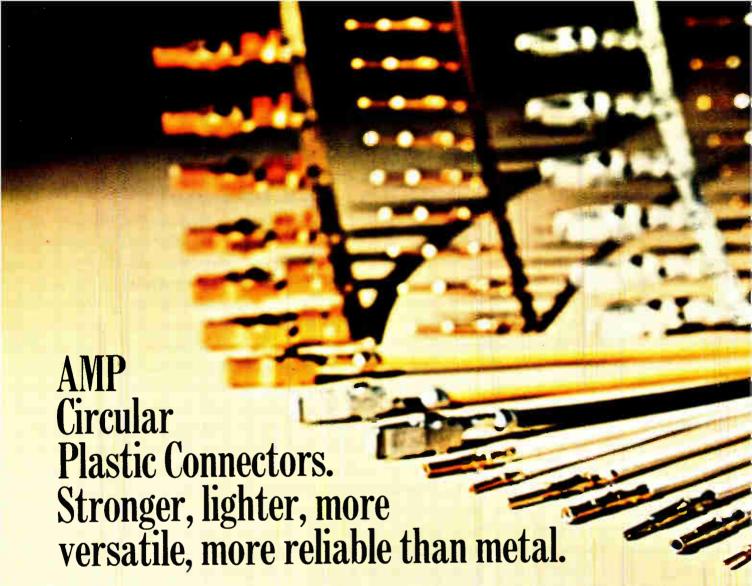
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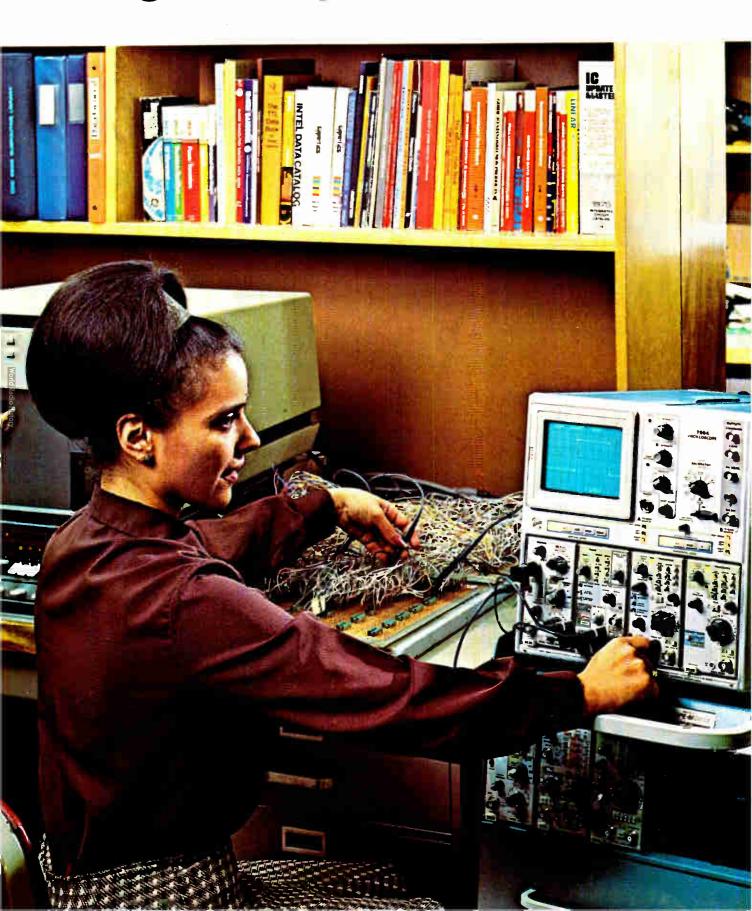
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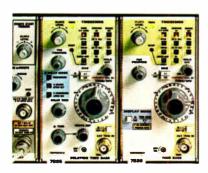
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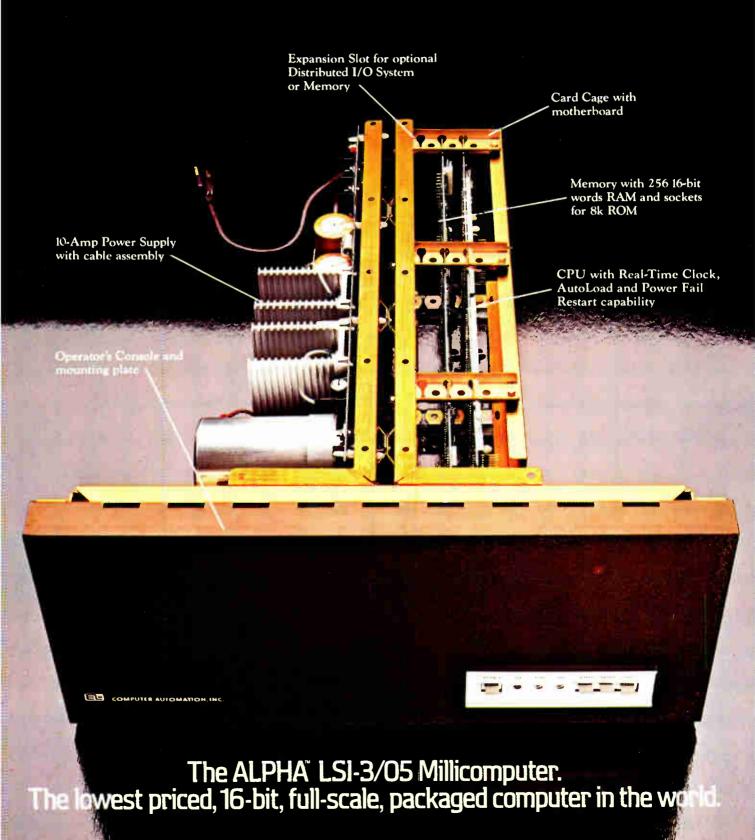
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ComputerAutomation will build thousands of ALPHA LSI-3/05 systems.

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The ALPHA LSI-3/05 is offered in three series featuring a choice of card cages, consoles, memories and power supplies.

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Which is why we invented the Distributed I/O System. An optional interfacing system

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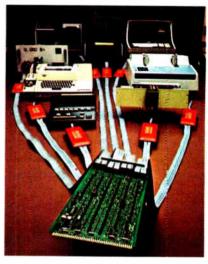
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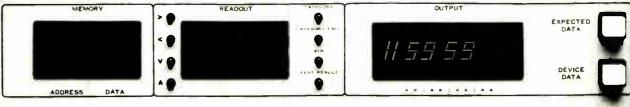


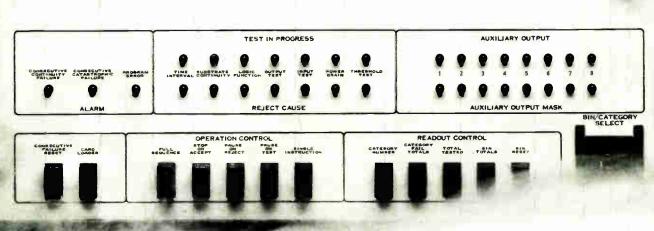
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THE LANGE !

Manpower predictions draw fire

Years of forecasting engineering shortages have played havoc by creating oversupply, job analysis indicates

by Gerald M. Walker, Associate Editor

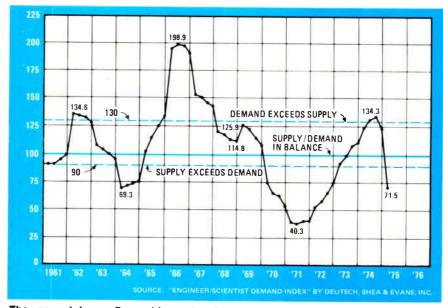
Are some manpower studies that depict a shortage of engineers incorrect? Yes, and they endanger engineering careers, says one engineer who has done extensive statistical research into those studies.

To improve their professional and economic standing, engineers have to put a stop to those supply and demand predictions that promote a belief in engineering shortages but in fact contribute to an oversupply. This conclusion was drawn by Milton Alpern, a consulting structural engineer in Massapequa, N.Y., following an analysis of engineering job status over the 10 years from 1963 to 1973.

Alpern has been virtually a lone voice in the National Society of Professional Engineers in trying to call a halt to what he sees as the erosion of all engineers' careers. He wrote his report as a member of the society's manpower task force, and since completing it in September 1974, he has been trying with only middling success to end what he labels the myth of shortages.

No existing studies break out separate figures for EEs. But experts believe that supply and demand statistics for all engineering disciplines for the most part follow the same trends.

Much of Alpern's thinking has been the result of his comparison of statements about demand made by the Engineering Manpower Commission, a research arm of the Engineering Joint Council, with actual measures of demand prepared by employment specialist Deutsch, Shea, and Evans Inc. When the predictions and the index were put side by side, the results were startling.



The ups and downs. Demand for engineering manpower, as measured by Deutsch, Shea, and Evans index, fell way below the supply-equals-demand range after 1969.

For 40% of the time studied, demand was less than "normal" as defined by Deutsch, Shea, and Evans, Alpern states. "Despite this, almost all of the writing that has appeared in the EMC documents regarding forecasts of engineer supply and demand have almost continuously stated outright that there will be a shortage of engineers in the near or far future, or have been heavily slanted to give that impression."

He points out that in reporting engineering shortages to high-school guidance counselors through its publications, the commission was wrong 77% of the time from 1963 to 1974. "I am convinced," Alpern charges, "that the commission has taken this tack to ensure an oversupply of engineers. All the evidence points to it."

Recently, he managed to con-

vince others that there was cause for doubt and concern. At the society's winter meeting in Albuquerque, N.M., in January, its board of directors passed a motion that it "publicly state that there is serious evidence to doubt the credibility of the Engineering Manpower Commission's repeated forecasts of engineering shortages in the past, and therefore in the future, and that any predictions of engineering demand are unwarranted and inappropriate for high-school guidance purposes. Further, all NSPE relevant policies should be revised accordingly."

What kind of evidence has Alpern been going on? The following is a brief summary of a few of the 32 statements that he analyzed. In July 1964, the commission predicted "... all sectors reported that their employment of engineers ...

Probing the news

would rise steadily during the decade ahead. . ." But the Deutsch, Shea index values showed demand during the decade actually fluctuating, with unemployment existing for at least half of the time.

A bulletin issued in November 1969 and entitled "The Prospects of Engineering Graduates, 1969,"

stated: "Every indication available to the Engineering Manpower Commission is that employment prospects for new engineering graduates continue to be excellent, at continuing rising salaries. No evidence of significant softening of demand was apparent in the June 1969 data."

The index at the time was about 110-close to the "normal" 100-and a recession in engineering demand lasting three and a half years

was about to begin. Thus, there was no accuracy in this prediction, comments Alpern. He points out that in 1970, when the engineering job market was becoming more and more dismal, the reports began to reflect only "a slowdown in the long-term growth" of engineering employment.

Then in 1971, when the index was at 42, a report labeled "Job Prospects of 1971 Graduates" stated, "... statistics compiled by the Engineering Manpower Commission from 196 schools indicate that engineers and engineering technicians at all levels ... were largely successful in finding jobs or carrying out other plans." It concluded, "... there is every indication that the U.S. economy will continue to generate a firm demand for new engineering graduates in the foreseeable future."

In the year and a half since he completed his study, Alpern reports, the commission has continued to cling to the general tenor of the statements above. Yet at a meeting last year of industrial and college leaders, John Alden, its executive secretary, admitted that the ability to project future manpower supply and demand is poor—so poor that it's not possible to predict whether total engineering employment is growing or contracting, let alone speculate on the hiring or firing of particular specialties or industries.

The simplest solution to the problem of predicting engineering supply and demand, Alpern believes, is to stop trying to forecast because the results have done more harm than good. And, he adds, engineers should challenge others who make these predictions to produce substantiation. "If somebody is good enough to predict engineering demand, they should be able to do the same for the stock market and make a fortune."

As for the future, he recommends that engineers follow the physicians of America in establishing a true profession—cutting out the subpar engineering schools, putting a "quality filter" on the incoming engineering students, policing the quality of present practitioners, and gaining preeminence in certifying the safety of products and engineering projects.

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Components

Thick films roll on

Sometimes considered a transitional technology, thick films are finding their niche in military, medical, and consumer products

by Lucinda Mattera, Components Editor

Having weathered a challenge from thin films, thick-film technology is finding no end of circuit jobs in its second decade of use. Thanks to hybrid applications with thin films, multilayering, improved resistor inks and other materials, and advances in laser scribing and trimming, thick films look as if they are here to stay.

Sales for thick-film resistor networks are growing at the healthy rate of 10% to 20% a year. While improvements in thick-film materials have paced much of this progress, Jack Cole, chief engineer of microcircuit engineering at the Helipot division of Beckman Instruments Inc.,

Fullerton, Calif., points out, "It's a maturing technology, and more and more people are learning what it can do." Lyle Pittroff, product-marketing manager for hybrid microcircuits at the Helipot division, agrees, noting that, although "hybrids are talked about as an interim technology, they just keep on going."

Control. Mixing thick and thin films on the same substrate is tricky, cautions Pittroff. "Everything on one chip depends on extremely good process control," he points out. Although thick-film resistors are only about half as accurate as thin-film ones, they offer the advantages

of quick turnaround and easy modification, making them substantially cheaper than thin-film devices, says Cole.

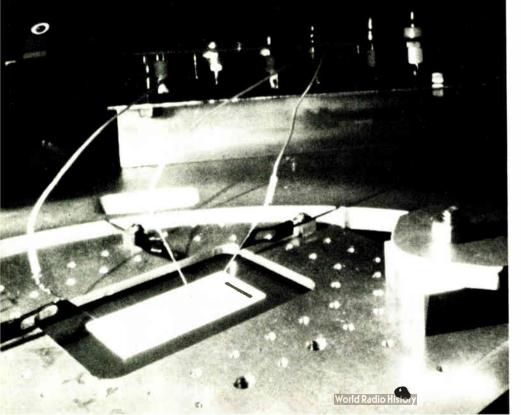
Later this year, Zenith Radio Corp., Chicago, which has been using thick film since mid-1972 in many of its television receivers, will start putting thick-film components in its audio products, as well as new black-and-white TV receivers, says Len Dietch, vice president for TV engineering at Zenith. "We're doing printed-circuit-board layouts now to favor concentrations of resistors—we're doing the layouts with thick films in mind," he notes.

Other potential future applications include electronic tuner-address systems, as well as substrates for potentiometers and switch-stator parts, says Dietch. Thick-film parts are "more stable than phenolics in the presence of humidity," he adds.

Cost advantages. For Burr-Brown Research Corp., Tucson, Ariz., the cost advantages of thick film become especially important for its operational amplifiers and data converters. When an accuracy within 50 parts per million per degree centigrade is not required, thick-film resistors are at least 50% cheaper than thin-film resistors, says William Olschewski, a designing engineer who is chairman of the company's hybrid-technology task force.

Along with new developments in resistor inks, the advent of laser techniques for scribing and breaking substrates, as well as for trimming resistors, have made thick films more competitive, says John Colglazier, president of CTS Microelectronics Inc., Lafayette, Ind. With the laser, tolerances of 0.5%, or even

Cut to size. Thick films, such as this high-voltage divider network, have moved into more and more products as processing steps, from laser trimming to materials, have evolved.



0.1%, are possible—tolerances that were not economically achievable with sand-trimming, he says.

Advantages of thin film have been narrowed to packing density alone, says Paul Schwartz, vice president and general manager of General Instrument Corp.'s Hybrid Microcircuits division, Hicksville, N.Y., "If you have to get extremely tight packing densities, in some cases—but only in some cases—there is an advantage to using thin-film resistor arrays, or thin-film technology."

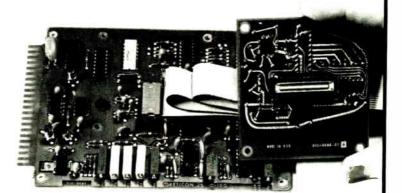
Opinions about multilayer hybrids are mixed. Multilayer hybrids are suitable for small-quantity jobs, like the space program, in which low yields are no problem, says Jack Heller, marketing manager for Circuit Technology, Farmingdale, N.Y. For good reliability and reasonable yields, it's best to limit the number of layers to three, he cautions.

Another manufacturer making multilayer hybrids—ILC Data Device Corp., Bohemia, N.Y.—has increased its business by more than 50% from 1974 to 1975. Customers who want very high packaging densities are turning to multilayer hybrids, says Bruce Beller, manager for custom hybrids.

Complications. On the other hand, Stanley F. Ribich, manager of microelectronics for Allen-Bradley's Electronics division, Milwaukee, doesn't foresee any growth in ceramic multilayer hybrids. Multilayer hybrids are "inherently expensive to produce—they require additional operations, added inspections, and a very tough electrical inspection."

However, significant improvements in thick-film materials are on the way. Sometime this year, David P. Anisfeld, product-marketing manager, for the Electronic Materials division, Photo Products department of the DuPont Co., Wilmington, Del., expects resistor materials that can be used with nonprecious metals. "Products are either on the market or being brought on the market that will provide twoto-four-fold improvement in printing speeds, processing capabilities down to 20 to 30 minutes in furnaces, and laser-trimming speeds approaching 2 to 3 inches per second," says Anisfeld.

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4-k RAM users check on reliability

Most problems have been worked out by manufacturers; pattern sensitivities, however, cause difficulties

by Larry Armstrong, Midwest bureau manager

Despite semiconductor manufacturers' best efforts to weed out faulty parts before they leave the factory, much of the burden of screening, testing, and preconditioning of 4,096-bit random-access memories has been assumed by the users. They've purchased sophisticated electrical testers, they're subjecting the parts to extensive power-on burn-ins for up to two weeks, and they've designed and redesigned printed-circuit-board layouts around 4-k vagaries.

On the whole, though, users are pleased with the performance of most semiconductor vendors and their parts. "But generally speaking, you must keep on top of suppliers relative to workmanship, or they have a tendency to back off," warns Harold Totty, principal design engineer at Control Data's Corp.'s Small Systems division, with facilities in La Jolla, Calif.

The main problems with 4-ks—which manufacturers and users agree have largely been solved—include too-long refresh times, full-range temperature specs, single-cell shorts caused by small or partial pinholes in the protective oxide layers, and sensitivity to noise.

Fixes. Based on Hewlett-Packard Co.'s experience with "almost every 4-k RAM around," Robert Frankenburg, responsible for testing memories used in the HP2000 minicomputer, believes many of the problems of reliability and workmanship have been solved by improvements in the manufacturers'

Hot box. Oven for burn-in tests at Intel Corp. can accommodate anywhere from 5,000 to 10.000 sockets at a time.

processing and testing, as well as by parallel improvements in user tests.

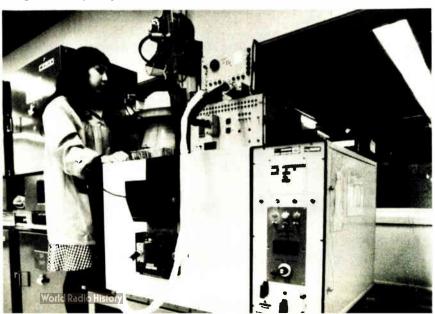
But most users are still grappling with pattern sensitivities. Some sequences of 1s and 0s tend to cause "soft" errors, errors that don't repeat. The problem also occurs in core-memory systems.

Says HP's Frankenberg on pattern sensitivity: "This is not just a onesupplier thing. It's true of almost every 4-k type we've tested. It's industry-wide. We've discovered a number of very effective tests, but unforunately the test sequences are rather long," he continues. As a result, it is difficult for a semiconductor manufacturer to do them on a production line. "So we've incorporated them into our testing process where we have more time to test, as well as test more parts at one time, than the manufacturer can," Frankenberg explains.

Each 4-k type, he says, has a "weakest link" relating to its design that reacts to particular patterns. But a weak link common to most designs, he says, appears to be the

use of polysilicon rather than metal fanouts from each individual memory cell. Thus far, only a few companies have opted for the metal-fanout approach [Electronics, Feb. 19, p. 32]. That's because metal sense lines have greater capacitance than equivalent polysilicon sense lines, and therefore reduce the signal to the sense amp. This either results in a poor signal-to-noise ratio and sensing errors over temperature or it requires a more sensitive, more costly, and less reliable sense amp that draws more power.

Metal fanouts with poly would seem to be a good design practice, Frankenberg says, not only because of the much lower resistivity generally, but fewer variations in resistivity along the line. "It's much more difficult to control resistivity in poly than in metal. Variations in poly resistivity in lines out to sense amps, address buffers or clock drivers where there are high currents usually manifest themselves as pattern sensitivities." In the newer 4-k RAMs, he says, many of the pattern-



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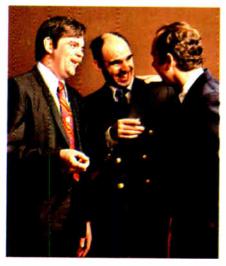
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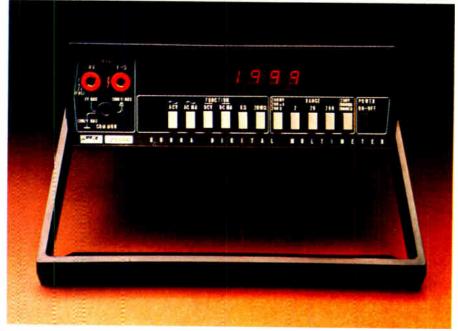
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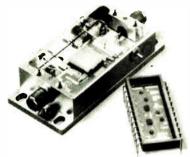
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Probing the news

sensitivity problems relating to design are being solved.

Incoming test procedures that users have devised are stringent. At minicomputer-maker Prime Computer Inc., for example, incoming 4-ks are screened for electrical faults by being inserted in a test board, heated to 50°C, and then exercised via a disk operating system with virtual memory in a way they would function in an actual system. Following that, the good RAMs undergo a two-week burn-in at 70°C in the refresh mode and are then cycled back to the 50°C electrical screen mode. "Naturally, we get some fallout with these techniques," says Robert P. Berkowitz, manufacturing vice president for the Framingham, Mass., minicomputer maker, "but it's in the same range as the other ICs we use.'

As for noise immunity, in some plants it is a problem while other users have taken the noise-immunity problems of many RAMs in their stride. Many have gone through several pc-board layout iterations, giving special care to ground networks, methods to maintain constant voltage distribution, and decoupling techniques. "The RAMs we use are rather unforgiving about noise," says one user. "They're much more sensitive than the TTL and Schottky [circuits] we use with

shipping both two-and four-layer 4-k populated boards in its computers for a year without noise-immunity problems. Multilayer boards with ground planes provide noise suppresion for the system, Berkowitz explains.

The 4-k RAM also turned out to be much less tolerant of supply-voltage variation than the 1-k predecessor. Mostek Corp., however, announced in January a 4-k part with a 200nanosecond access time that will tolerate power-supply variations of up to 10%—the first standard RAM to deviate from the traditional 5% spec. And TI solved its earlier problems with supply variations by adding a 25-volt, high-temperature stress test to its repertoire of outgoing tests.

Generally, users are loath to name names or talk numbers when they talk about reliability, particularly in the matter of 16-pin vs 22pin parts. Burroughs Corp. has found, however, that it's tested 300 16-pin 4-k pieces for 3,000 hours in controlled voltage and temperature reliability tests with absolutely no failures. According to J. Reese Brown, senior staff engineer in the Electronic Memory Systems organization, Piscataway, N.J., the firm has had less success with 22-pin 4-k devices, experiencing some infant mortality with these. He confirms that one of the problems was operation at full temperature, particularly when the devices are being refreshed, and says "we got together with the manufacturer, showed him what we found, and the problems which were design problems—have already been corrected."

Good efforts. Another user, Control Data's Small Systems division, though "reasonably pleased" with one supplier's devices, has had some "basic workmanship problems." These are of a "type easily prevented, and the supplier has taken appropriate corrective action," says Totty. Testing of a batch of 2,000 250-nanosecond 4-k RAMs caused "catastrophic" failures of five devices, he explains, with wire bonds actually lifting and foreign materials found floating.

Up to now, CDC has received 21,000 300-nanosecond 4-k RAMs and has compiled reliability records on them. Of 17,237 parts, a 3.5% rejection rate resulted, which is average, say industry sources. At the board level, 70 of 10,872 failed. And at the system level, 6 of 4,000 plus failed for a 0.16% rate. From the field, only one card failure has been reported.

Now, the CDC division is testing the 250-ns RAM that will go into all of its systems that are produced after April.

Of the 2,000 parts tested so far, 21 have failed, which includes the five "catastrophic" failures. Control Data tests all 4-k RAM at operating temperature, ac/dc, then burns them in for 96 hours at 125°C. Another ac/dc test is performed before a 72-hour burn-in, followed by still another.

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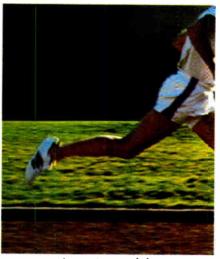
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THE INDUSTRY STANDARD. 8600A DVM. FLUKE

Communications

Britain to get wired city—via telephone

by William F. Arnold, London bureau manager

The "wired city," which a few years ago seemed to be the communications wave of the future, hasn't been plugged in yet. Interconnecting a cable-television system to provide a host of interactive services proved to be simply too costly.

However, British post office officials are confident that they have found a cheap and easy way to do the job. They hope to link the entire United Kingdom, and possibly the world, through existing telephone systems to an impressive array of information services.

Called Viewdata, their system ties a TV set equipped with a decoder to a telephone so that the user can access through an alphanumeric keyboard one of a virtually unlimited number of "pages" of information from remote data banks. Not only could a home viewer use a remote computer to work out mathematics problems, for example, but businessmen are being promised new classes of telephones and privatewire communications among farflung corporate offices—all with a hard-copy option.

Trials scheduled. Pilot trials with about 500 receivers are to be conducted next year, and public market trials are to be conducted later in the year. If those milestones are passed satisfactorily, public service would begin in 1978. Among the unknowns yet to be explored are the size of the market, prices, and the services offered, says Roy Bright, Viewdata commercial manager.

The post office is lining up "information providers" for each of the 19 categories to be used in the trials.

Among the services to be offered are news from Reuters and stock quotations. Other promising categories are education, money and insurance, houses and mortgages, jobs and careers, and holidays and travel. A husband could send a message through Viewdata to his wife, who, alerted by a red light, could switch on the TV set and read the message.

Cost to the home owner for 30 pages of information is expected to be about double the price of a local three-minute call, in addition to the price of a Viewdata-equipped TV set. Bright estimates that a black-and-white Viewdata terminal will be about \$200. However, in a business, a terminal might replace many telephones because a visual display unit is combined with a telephone.

A color model and a portable model are planned in a later stage. The hard-copy option, possibly through a cassette recorder, would follow later in the program. Post office officials now plan to sell the business terminals and let home viewers purchase Viewdata sets from retail outlets. The sets would be installed by post office personnel.

Business to benefit. Even though the outlook for the home service is bright, the business prospects are even better. Viewdata promises executives and professionals like doctors up-to-date information literally at their fingertips. What's more, Bright says, they could feed information into the system for their colleagues. Privacy could be restricted by using encoded cards similar to those used to draw money from



Screen test. British post office's Viewdata system links television terminals with telephone lines to display data for subscribers.

banks. Companies or institutions with in-house exchanges could set up their own Viewdata systems. This idea was shown in February with a post-office-sponsored link between ITT's European headquarters in Brussels and a British office.

Viewdata computers are to be arranged in networks so that each would serve about 300,000 subscribers in an area, explains Sam Fedida, who originated the Viewdata concept. He is manager of computer applications at the post office research center in Marthsham, Suffolk. A medium-size minicomputer could handle 200 simultaneous subscribers, and a 20-megabyte disk could store 20,000 pages, he says.

Hardware. Not unexpectedly, semiconductor houses readying Teletext decoders [Electronics, Feb. 5, p. 68] are leaving room to add chips for Viewdata. The post office considers the two services complementary. They share common standards of 960-character pages, each with 40 characters in 24 rows, but the Teletext "magazine of the air" is now limited to about 800 pages per TV channel, whereas Viewdata is constrained only by the amount of computer storage available.

A Viewdata decoder would use about a third as many large-scale integrated circuits as a Teletext decoder, including the random-access memory and the character generator. High-density integrated injection logic or conventional bipolar technology could be used in a chip for the rest of the circuitry, including the timing circuit, keypad, processor, row counter, and the like.

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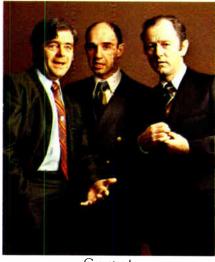
1250 MHz Prescaler

Covers 50 to 1250 MHz using a scaling ratio of 8. Sensitivity is 20 mV to 1000 MHz, increasing to 40 mV rms at 1250 MHz. Maximum input 5 V rms (fuse protected), and VSWR less than 2.5:1 for levels less than 1 V rms.

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Time Base Options							
	TCXO	Oven- Stabilized					
Frequency:	10.00 MHz	10.00 MHz					
Aging Rate: (constant temperature)	$<\pm 3 \times 10^{-7} / \text{mo}$.	$<\pm 1 \times 10^{-7} / \text{mo}$					
Temperature Stability:							
20°C-30°C	$\pm 2 \times 10^{-7}$ typ.	$\pm 3 \times 10^{-9} \text{ typ}$					
0°C-50°C	$<\pm 5 \times 10^{-7}$	$<\pm 1 \times 10^{-8}$					
Line Voltage: (±10% change	e)<±5x10 ⁻⁸	<±3x10-9					

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Memories

Static RAMs are on the move

New 16,384-bit devices may be grabbing the headlines, but novel design approaches are vastly upgrading their older, smaller cousins

by Bernard Cole, San Francisco bureau manager

All the shouting about the dynamic random-access memories is drowning out the quiet evolution in static 1-k and 4-k RAMs. The changes are opening a new range of markets—and, because the 4-k static is cheaper than its 1-k counterpart, the new parts not only excite the old markets but can penetrate some of the territory of dynamic and core memories.

All this means that life has become more complicated for the designer of memory systems. Up to now, if he wanted to go all out for speed, he used 1,024-bit static bipolar random-access memories, such as Fairchild Semiconductor's 93415, which got him down to 35 to 100 nanoseconds. If low cost and ease of use were more important, there were a number of 2102-type static n-channel metal-oxide-semiconductor RAMs, like those from Intel Corp., with speeds in the 500-600ns range. At the sacrifice of some ease of use, designers first had the choice of 1103-type 1-k n-MOS dynamic RAMs with access times around 200 ns, then the more recent 4,096-bit RAMs with similar access times and even lower costs. Now, 16,384-bit RAMs are on the way.

But before the 16-k RAMs get to the market in volume, system designers will be offered the fruits of new static 1-k and 4-k memory design, which will further blur the distinctions between high-speed bipolar and n-MOS and between static and dynamic devices.

Consider, for example, Intel Corp.'s new 2115. A 70-120-ns depletion-load n-MOS static 5-volt RAM, it is pin-for-pin and functionally interchangable with Fairchild's

bipolar 93415, with the additional advantage of requiring almost 300 milliwatts less power. According to Dave House, memory-applications manager at Intel, standard silicongate MOS processing was used to fabricate the 133-by-136-mil IC. The field regions on the 16-pin device were not implanted and a conventional diffused phosphorous sourcedrain was adopted. Only the depletion load used ion implantation.

"To achieve the static bipolar performance, we combined depletion load with on-chip substrate bias generation," he says. To reduce device-body effect and parasitic junction capacitance, a negative substrate voltage is generated on chip to eliminate the requirement for an additional pin and an external power supply, and the back-bias circuit consists of a self-starting oscillator driving a charge pump that is capacitively tied to the substrate.

Not far behind Intel are National Semiconductor, Fairchild Semiconductor, Signetics Corp., and Advanced Micro Devices, among others, with their own fast 1-k static n-MOS rams.

Static 4-k. Also in development and approaching the sampling stage at many of these companies are a number of static 4,096-bit n-MOS RAMS. Using many of the same techniques it developed for its 2115, Intel is already sampling a 1-k-by-4-bit RAM in an 18-pin package. Called the 2114, it is also a 5-V device, and access time from address will range from 200 ns up to 450 ns, with a power dissipation over that range of about 500 mw. While the main mission of the part is to open up the saturated 2102 market,

House believes that it will also have an impact on a portion of the dynamic 4-k RAM market, especially on users scared away by the apparent complexity of the dynamic memories.

Other semiconductor makers have even more ambitious plans for their new static 4-k RAM designs. National Semiconductor will be sampling three 5-V, single-power-supply device in June: the 4-k-by-l MM5257 and the 1-k-by-4 MM5255 in 18-pin packages and another 1-k-by-4, the MM5256, in a 22-pin package. According to Mauri Morin, National's director of memory products marketing, the speed distribution on these parts ranges from 150-ns to 250 ns.

Using similar static storage and pipelined peripheral-circuit techniques, engineers at Advanced Micro Devices have built 1-k-by-4-bit and 4-k-by-1-bit static 5-v RAMs in 22-pin packages. Access times are 150 to 200 ns and typical power dissipation is 350 mw. When deselected, says Joseph Kroeger, manager of AMD's MOS applications, the supply voltage can be dropped to 1.5 v reducing the power by over 80% to 70 mw.

What is really unusual about AMD's 192-by-197-mil static 4-k parts—theAM9140 and AM9130 [Electronics, Feb. 19, p. 105]—is the use of a novel memory static-output signal. It simplifies the timing and boosts the speed of some systems configurations using AMD 4-k statics down to about 100 ns. It means, Kroeger says, the memory can be used at its actual operating speed rather than at the worst-case access and cycle times.

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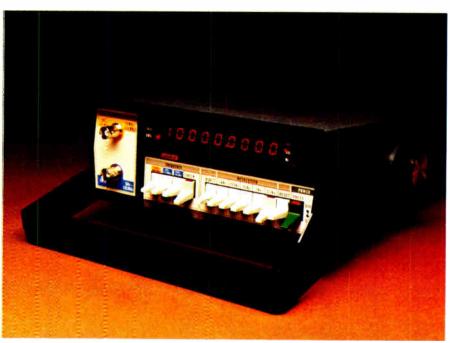
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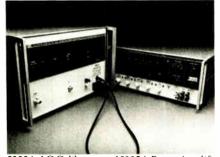
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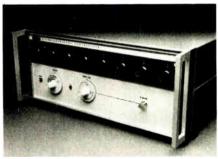
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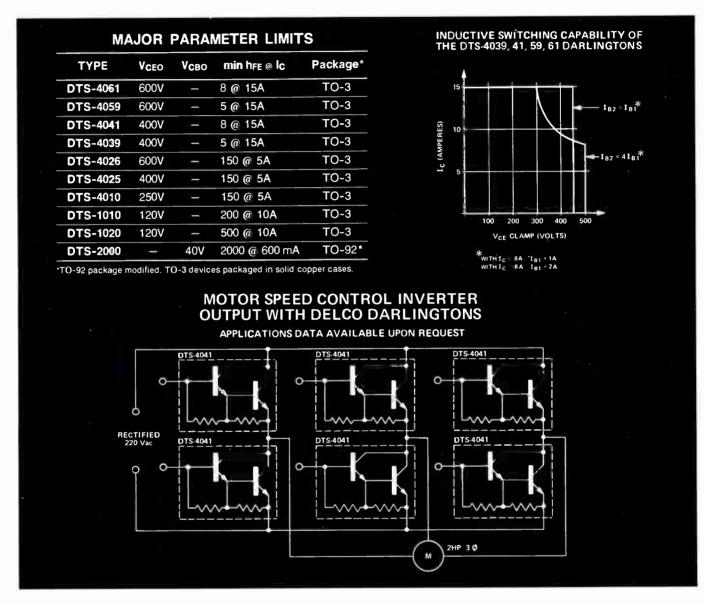
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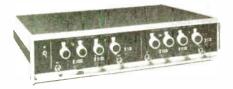
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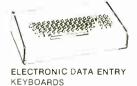
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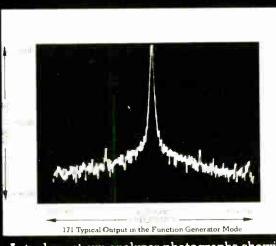
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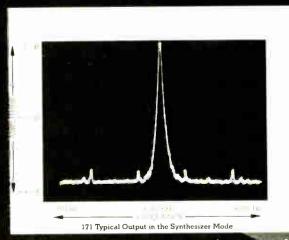
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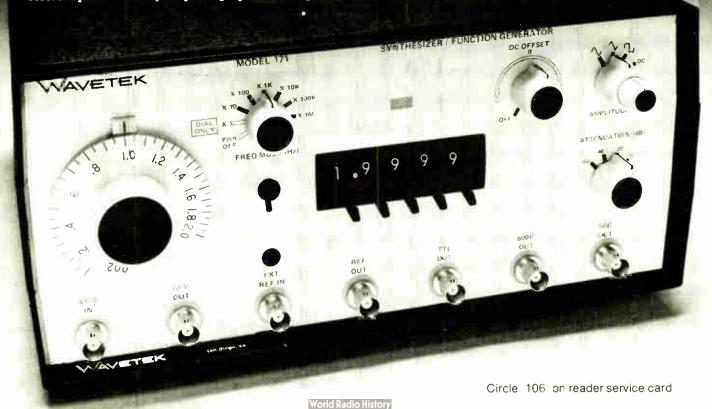
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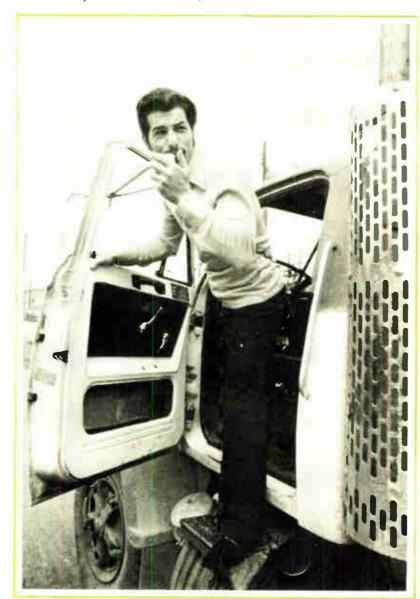


Actual spectrum analyzer photographs showing the improved waveform characteristics in the synthesizer mode.



Citizens' band clamor gives headaches to FCC, designers

by Richard Gundlach, Communications & Microwave editor



☐ It took 16 years from the inauguration of citizens' band radio to accumulate the first million licenses, the first eight months of 1975 for the next million, and only three more months for the third million. Today more than six million licensed and unlicensed users crowd the scant 23 channels allotted by the FCC. In another year, there may be double that number competing for space in the 27megahertz CB spec-

trum.

The staggering explosion in popularity has overwhelmed the 50-odd set makers and has caused a booming market in stolen sets. But the user who can hang onto his transceiver finds that popularity is creating its own headaches. Although a good many set owners treat their sets like auxiliary car radios and rarely transmit, there are enough citizen broadcasters for crowded channels and interference to have become major problems. The Federal Communications Commis-

sion probably will allocate more channels soon (see "CB and the FCC"), and interference may fall victim to the technological advances that are beginning to appear in the still developing field of CB-set manufacturing.

The boom started with the gasoline shortage of 1973-74. Longhaul truck drivers bought sets to keep each other informed of the whereabouts of gas supplies. When the highway speed

limit was cut to 55 miles per hour, the sets took on a new role: a warning system about speed traps manned by "Smokeys" or "bears." as highway troopers are called in the colorful CB lingo.

Set makers think the long-haul trucking market, which has been about 65% of the sales, is close to saturation. But the entertaining channel chatter has attracted a rather large following among motorists in automobiles and light trucks, and there is considerable

CB and the FCC

Commonly called citizens' band, Class D of the Citizens' Radio Service was established late in 1958, when the Federal Communications Commission reallocated a portion of the 11-meter amateur band for personal or business communications over distances of 15 miles or less. The FCC set up 23 channels in the 26.965-megahertz-to-27.255-MHz region. One of the channels is reserved for emergency calls, another for calling and the rest for general purpose.

Transmitter power is limited to 4 watts into the antenna for amplitude-modulation and 12-W peak envelope power for single sideband. The range of an a-m transmission can be as much as 25 miles in rural areas and as little as three miles in the noisier urban sprawl. SSB transmitters usually reach half as far again as their a-m counterparts. The FCC requires the transmitted frequency to be within 0.005% of the nominal frequency, and modulation not to exceed 100%.

Operators must have FCC licenses (no test required), but this is a requirement honored as much in the breach as in the observance. Estimates are that there is one unlicensed user for every licensed one—but the FCC really can't do anything to the nonlicensed offenders should its small enforcement staff happen to catch them. It can, however, move against licensed users who violate regulations, which is no inducement to applying for a license.

Right now, the biggest problem for the FCC (other than moving the millions of recent license applications) is finding a way to relieve the channel congestion created by the burgeoning army of CBers. There have been several proposals made:

■ Docket 20120 proposes an immediate expansion of the allocation of channels from 23 to 70, with increasing

emphasis on SSB and eventual elimination of a-m. Industry talk has it that the decision will be an expansion to 50 channels with continued emphasis on a-m, in an announcement timed for the Personal Communications Show of the Citizens' Radio section of the Electronic Industries Association, starting March 30 in Las Vegas.

- Docket 19759 proposes a new CB radio service in the 220-MHz region for fm transmission, but there is strong opposition. The armed forces use that frequency band to operate tracking stations and radar installations; Mexico and Canada fear interference with adjacent very-highfrequency TV transmissions, and members of the Amateur Radio Relay League don't want to share their domain with what most "ham" operators consider undisciplined users. "I do get upset when CB manufacturers push for occupancy of other frequency bands-like 220 MHz, which is now occupied by 'ham radio' operators and where the possibility of interference with TV signals is very great—and not really give a darn to look elsewhere,' says Harry Dannals, league president. "I don't see why the FCC doesn't make use of some of the old class-A frequencies up at 460 MHz for this type of Citizens' Radio Service." No commission action is expected before 1977, but there must be some decision before the world Radio Administrative Conference in 1979, which will discuss spectrum allocations.
- Docket 20351 proposes that all CB transmitters (and certain other radio services) have a selective calling system, known as the Automatic Transmitter Identification System. Transmissions would be specially encoded to activate only similarly equipped receivers, with each code probably identifying a different geographic area. It looks as though this proposal will lie in permanent limbo.

growth potential here, manufacturers think.

No matter what their vehicle, motorists use CB radios to break the boredom of their driving, to get weather and accident warnings, and to call for breakdown assistance and other emergency help. Commuters can call home when delayed by traffic (or, better yet, learn about the delays and how to avoid them). Farmers and other outdoor workers and sportsmen rely on CB to spring them from their isolation, and fleet owners use it to communicate with their vehicles.

Even with the limited power ordered by the FCC, the transceivers can be useful. The U.S. Department of Transportation and some safety and law-enforcement agencies think citizens' band may provide a nationwide highway communications network. The Missouri Highway Patrol already has sets in its patrol cars, and its troopers are learning of accidents much faster.

Such governmental support and the fact that auto makers are beginning to view CB radio as a highly saleable option may make the sets as common as windshield wipers in cars of the 1980s. But such a proliferation of users would cause even heavier channel crowding and interference.

About 90% of the sets sold under U.S. labels are assembled in the Far East, often with American components. Right now technology is relegated to providing what's needed to pass FCC tests and to anticipating the

changes needed for the projected increase in the number of channels. Prices for the current 23-channel amplitude-modulated sets range from \$125 to \$250, with another \$100 for single-sideband capability (see "SSB—a powerful option"). There is a whole shopping list of options to choose, and most CBers want them all, even though the sets will operate without some of them perfectly well. Set manufacturers commonly call their options "bells and whistles."

FCC approval of 27 additional channels would help relieve the crowding problem, as would the less likely decision to eventually eliminate a-m transmission in favor of the now-optional SSB mode. But regulatory decisions won't solve the biggest problem facing CB users: interference, both man-made and natural.

For example, noise pollution from automobiles has become so heavy that the FCC is studying the problem—which stems in part, paradoxically, from technological advance. High-efficiency alternator diodes and electronic ignition systems have magnified electromagnetic interference levels. These fast turn-on devices generate a much broader spectrum of spurious signals that interfere with the motorists' sets.

Noise caused by adjacent-channel interference is another serious problem, as is increasing solar storm activity. Every 11 years, sun-spot cycles peak. These—and less severe peaks within each cycle—cause CB trans-

SSB: a powerful option

The audio signal in amplitude modulation varies the amplitude of the carrier, producing two additional sidebands, one higher than the carrier frequency, and the other lower. Only the sidebands carry information, so the power put into the carrier is lost.

Single-sideband transmission eliminates the carrier and one sideband, although the carrier must be reinserted at the receiver to recover the message being transmitted. SSB has several advantages:

- Theoretically an SSB system can give an effective gain of up to 9 decibels over a-m, which is equivalent to increasing transmitter power eight times.
- Since no carrier is transmitted, heterodyning from nearby carrier frequencies and the accompanying noise are eliminated (in an SSB-only service).
- With the receiver bandwidth halved, the signal-tonoise ratio is improved.
- Two different stations can operate on the same channel frequency, one on the upper sideband and one on the lower. This doubles the channel capacity without increasing the spectrum.

However, since there are no exclusive channels for SSB, the many a-m transmissions cut into the advantages. Stability also is a problem with SSB sets. The re-

ceiver must be kept tuned within 50 hertz of the transmitter frequency if the message is to be understood word for word, although the meaning usually can be grasped if the receiver is within 100 Hz. That can be expensive, since a good crystal with a frequency drift of ± 10 parts per million from -30° C to 70° C would be needed for long-term stability.

However, some manufacturers think extra tolerance and stability is economically feasible in a transceiver with a single-crystal phase-locked-loop synthesizer chip. The savings in crystals would offset the cost of a better crystal with the extra tolerance and stability. Frank Rose, chief of the FCC's technical standards branch, feels such sets—which really would not need a clarifier control—would go a long way in making SSB more attractive to the average CBer.

Although the SSB-only service is unlikely in the U.S. in the near future, there is international precedent. The International Telecommunications Union, an arm of the United Nations in which all member countries participate, has proposed conversion to SSB-only in the maritime service by 1982. There is every reason to believe that this concept will be carried into point-to-point land communications as well

missions to skip thousands of miles with little attenuation. The resulting signal-to-noise degradation contributed to a decline in popularity during the high point of the last cycle in 1969, many manufacturers believe. There is no consensus on the market effect of the 1980 peak. Interest may be so great that the sun-spots won't dampen enthusiasm. But no one really knows.

Even at the low point of the sun-spot cycle there still is troublesome interference from adjacent channels. An extra-strong signal from a CBer on a nearby channel can overload a nearby receiver and severely densensitize it.

With the mushrooming number of users, on-channel noise may replace adjacent-channel interference as the number-one noise headache. The FCC allows a 0.005% frequency tolerance in transmissions on the same channel. Other transmissions usually are beyond the range of the user's rig, but the slightly varying frequencies will heterodyne together, raising numerous beat frequencies that take on the characteristic of white noise. This effectively raises the background noise level. White noise is random and can't be removed, so it sets the level of theoretical maximum receiver performance. But spikes of white noise and peaks of noise from other sources can degrade performance even more.

Getting rid of noise

There are three measures now in use against noise reduction: automatic noise limiting, noise blanking, and squelch. Noise-limiting circuits detect the threshold of incoming noise and prevent a series-diode clamp from passing the signal until it reaches the preset level. Noise-blanking circuits try to eliminate noise spikes before they reach high-gain stages, where stretching and nonlinear conditions can add to the problem. Many engineers feel more effective noise-blanking systems are

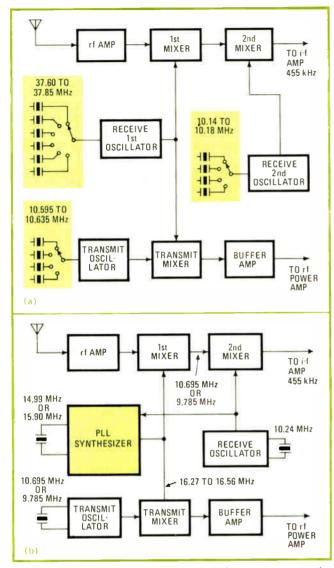
essential. Squelch circuitry opens up the audio path when no carrier is present. This silences the speaker and eliminates the annoying hiss of background noise.

Technology can provide a technique for the recognition, processing, and elimination of noise. Already proved in more expensive military equipment, such circuits automatically adjust noise thresholds, while signal-enhancement circuits are boosting the signal-to-noise ratio. Although such adaptive processing techniques will reduce sensitivity a bit, it's easily tolerated in the short-range CB communications.

Large-scale integrated-circuit technology can pack the circuitry for adaptive processing onto a single chip. IC designers can afford to use hundreds of transistors to accomplish the signal processing, which was not economically feasible with discrete components. Highspeed, high-density technologies like integrated injection logic, which combines analog and digital functions on the same chip, will help bring digital techniques to CB sets. And LSI makes it possible to apply adaptive circuits, using microprocessors, to improve noise performance under changing ambient conditions.

A major sign that technology will play an important role in the development of new CB sets is the growing interest in digital frequency synthesizers using phase-locked-loop techniques. These circuits can generate all the needed channel frequencies from a single crystal-controlled reference oscillator that determines the accuracy. They are essential if the almost-certain allocation of 27 more channels is to be covered inexpensively.

As it stands, set manufacturers are using all-crystal synthesizers, and they're having trouble getting enough crystals [*Electronics*, Feb. 19, p. 76]. A standard arrangement is a 14-crystal synthesizer, with six in a common bank and four each for receiver and transmitter



1. Cutting crystals. Phase-locked-loop synthesizers generate the 23 CB frequencies with a single crystal, whereas a typical crystal-synthesized CB transceiver would use a common bank of six crystals and two separate banks of four crystals each.

functions (Fig. 1). By heterodyning these carefully selected crystals, the frequencies for the 23 existing channels are generated. But a PLL synthesizer can lock in on any of the CB frequencies from a reference source controlled by a single crystal. Moreover, it requires no more crystals to generate all 50 of the anticipated expanded allocation of channel frequencies—in fact some models can generate upwards of 70 channel frequencies. The present all-crystal synthesizers can generate about 30 channels, but it will take more than 14 crystals to get all 50 channels.

Locking it up

Many versions of the PLL synthesizer are available, from one-chip LSI versions using one to three crystals to three-chip versions that need only one crystal (Figs. 2 and 3). The trend is towards one-chip LSI with all necessary functions, such as channel programing and a code converter that also drives a digital readout directly.

One of the first U.S. manufacturers on the scene was the Microelectronic Products division of Hughes Aircraft Co., in Newport Beach, Calif. It has adapted the complementary-metal-oxide-semiconductor PLL synthesizer originally designed for a military portable transceiver for use in CB sets [Electronics, Jan. 8, p. 31].

A number of Japanese firms plan to supply PLL synthesizer chips, including Matsushita Electric, Mitsubishi, Nippon Electric, Nippon Precision Circuits Ltd. (a subsidiary of watchmaker Seiko), Toshiba, and Hitachi. And several U.S. semiconductor houses have developed PLL synthesizer chips for the CB market.

E. F. Johnson, the Waseca, Minn., pioneer in CB radio, has designed its own custom PLL synthesizer chip, which is being produced by Motorola and National. Communications Power Inc., Mountain View, Calif., put together its PLL synthesizer with inexpensive off-the-shelf transistor-transistor-logic ICs and a multiple-sourced read-only memory (Fig. 4).

Some companies have gotten it all together. For example, Nitron of Cupertino, Calif., and set maker SBE of Watsonville, Calif., co-developed a chip with all the components necessary for frequency-generation and selection. In some of the other PLL synthesizers these functions must be performed outboard of the chip. Access to the SBE-Nitron chip is by channel number—the onboard memory can hold a total of 80 channels' worth of information.

Both Motorola and Signetics have put together several CB radio circuits to complement their PLL synthesizer chips for single- or dual-conversion transceivers. Motorola offers an audio-processor module, and a combined second mixer, intermediate-frequency amplifiers, and detector—along with automatic-gain-control amplifiers for both rf and intermediate-frequency stages.

Multipurpose chips

Signetics used double-diffused MoS technology in its 16-pin, 100-channel-frequency synthesizer because D-MOS has a 20-megahertz capability not achievable with n-channel MOS or C-MOS. The firm also has developed several other ICS (Fig. 5). These include a D-MOS field-effect-transistor switch for use as a voltage-controlled oscillator and a dual-gate D-MOS FET that can be used as a radio-frequency amplifier or an oscillator/mixer.

It also has a 7-watt audio power amplifier with thermal shutdown, and a monolithic IC, designed for a-m receivers up to 50 MHz, that includes an on-chip rf prestage with agc, a balanced mixer, a separate oscillator, and an i-f amplifier with agc. Half of a dual low-noise preamplifier is used in a loop filter circuit, and the other half as a microphone preamplifier.

Signetics also is offering a bipolar monolithic compandor IC [Electronics, Feb. 19, p. 153] that compresses the audio before modulating the carrier and expands the demodulated audio, if necessary. This makes it possible to raise the average modulation without affecting the limit. To boost the range effectively, Signetics engineers see two approaches to compression. One is a standard amount of compression that gives some increase in "talk power," without requiring an expander in the receiving set to understand the message. The other

switches the compandor to a mode with even greater compression, which requires a receiver with similar companding capability.

Present modulation devices in CB transceivers are diode limiters and other techniques that limit the average modulation to at least 12 decibels below peak. Signetics engineers believe it will be possible to boost the average modulation by at least 6 dB with their new compandor, effectively giving the user that much more transmitting power. But the FCC must approve use of the compandor, and there may be questions about overmodulation producing spurious signals on the band.

Selecting the channel

An expansion from 23 to 50 channels requires rethinking of channel-selection techniques. There are a number of possibilities, and most CB manufacturers are hedging their bets because they are concerned about making the right final selection.

The standard device right now is a 23-position rotary switch, which clicks each time a user moves into a new channel—so counting the clicks takes the place of looking at the channel indicator when switching. It would be difficult to keep track of the clicks with a 50-position switch, but a 25-position switch with an upper/lower mode is a possibility, as is a two-switch arrangement, one for the 10s and one for the 1s.

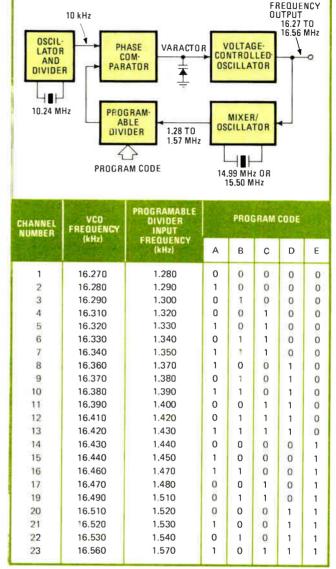
A different approach is continuous automatic incrementing with some memory added so that the last-selected channel will return when the set is turned back on. Automatic stepping with a depress-and-hold arrangement could step up or down to each channel frequency at a preset rate. A speed control would allow a varying step-rate, so that moving from channel 1 to channel 50 needn't take too long.

Another possibility is keyboard entry, along the lines of a model developed by SBE (Fig. 6). "Our keyboard entry system can't produce any channel frequency that is programed into our ROM," says Steve Snell, the firm's chief engineer. Users can rewire switches to get access to illegal frequencies—and they have been known to do so with CB radios.

The SBE model uses a 3-by-4 matrix of numbers, each of which is scanned for key closures. To prevent mistaken recognition of a keybounce as two key strokes, the circuit is scanned several times. The key must be closed at least 5 milliseconds, and if the closure still registers after several scan cycles, that number is entered in the address decoder, which selects the proper programing for the channel from the read-only memory after two numbers are entered.

Of course, with a keyboard, motorists must take their eyes off the road to hit the right key. And they must divert their attention after entering the two numbers to verify the channel selection on a display. Some CB manufacturers think this will militate against acceptance by most users.

But before a channel-selecting technique can vie for consumer approval, it must gain acceptance by the FCC—which now says "the frequency selector shall be limited to a single control." Queries from set manufacturers have led to reevaluation of the rule, and it may



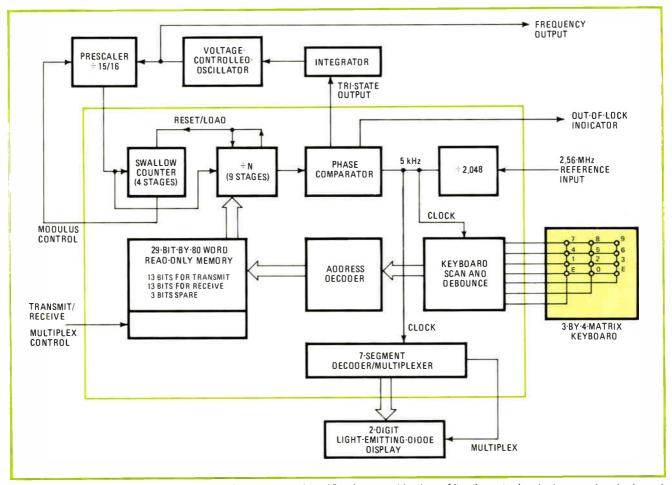
2. BCD data programs synthesizer. Toshiba's three-chip PLL synthesizer uses the new clocked complementary-MOS technology. The 8-bit programable divider accepts a program code in BCD format from the channel selector. The reference oscillator and divider will accept an input frequency of either 5.12 MHz or 10.24 MHz.

be modified to specify only one location for the controls. The commission's main concern is that only the correct frequencies can be produced by users.

The first generation of expanded-channel CB radios will have many clever ways to display the selected channel—from magnified dial numbers to digital displays using rotating tapes, light-emitting diodes, or liquid-crystal displays. However, LED displays may be difficult to read in daylight in vehicles. Although LC displays are more visible in bright light, they're also more expensive.

Some manufacturers think that building a keyboard, a display, or both into the microphone is the way to go (Fig. 7). Other firms, however, are quick to point out that many CBers do more listening than talking, so they don't have the microphones in use much of the time.

Technological attention may be fixed on PLL synthesi-



3. Keyboard entry. The 3-by-4 arrangement of contacts provides 12 unique combinations. After the enter key is depressed and released, information contained in the last two digit keys will be entered and displayed. Keyboard logic will accept closures that last longer than 16 milliseconds, thereby eliminating the effects of contact bounce and noise. The ROM stores frequency information for up to 80 channels.

zers, but, with so many CB users on the air, interest in receivers (see "Typical specs") with good adjacent-channel rejection, good overload characteristics, and low cross-modulation products is bound to grow. It's difficult to pinpoint how much of a problem cross-modulation is, but as the CB spectrum becomes even more congested—especially in urban areas—it will become one of the limiting factors on a set's usefulness.

Land-mobile techniques apply

Some of the techniques used in land-mobile radios are beginning to be applied to citizens' band. Eliminating the rf preamplifier, for example, eliminates a cross-modulation source. High-gain rf stages cause the mixer to overload more easily, producing nonlinear effects and thus increasing cross-modulation products.

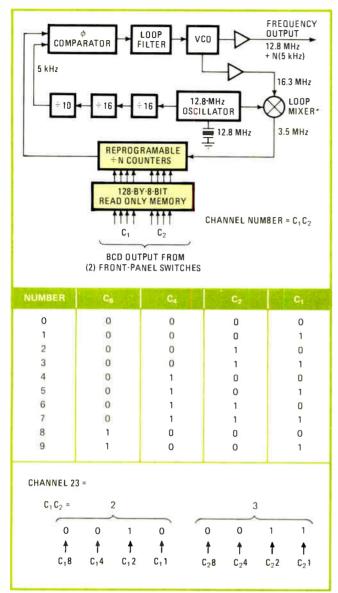
There are other technological advances that point to the demise of the rf stage. With junction-FET mixer gains of 10 dB and noise of less than 5 dB, an rf stage is no longer needed to mask higher mixer noise. And to-day's J-FETs have more than 70 dB of spurious rejection, so no rf stage is needed for additional local oscillator isolation. Also, the J-FET mixer is capable of taking a lot more rf signal before cross-modulation becomes a real problem. Another reason for eliminating the rf stage is that 1-microvolt sensitivity is not needed for the short

range of citizens' band communications.

Communications Power Inc. has gotten rid of the rf stage, put in a J-FET mixer, and added a p-i-n diode attenuator that doubles as a receive/transmit switch. Dave Wisherd, the firm's vice president, says that cross-modulation is at least 80 dB down instead of the 50 dB or so expected with conventional techniques and standard mixers. The firm uses an eight-pole filter right after the mixer for extremely good rejection of adjacent-channel interference. "It costs more to do this, but using the same filter for both a-m and SSB is a good trade, economically," Wisherd says.

Conversion: double vs single

Many of today's CB transceivers use double conversion techniques—two mixer stages and two separate intermediate frequencies—although multiple mixing creates more spurious frequencies and reduces the signal-to-noise ratio. "Usually, when manufacturers opt for double conversion over single conversion, they are simply realizing that a higher frequency provides an additional measure of image-frequency rejection," Wisherd says. But, as he points out, image frequency is predictable—twice the frequency away from the carrier frequency. If there is no one using the image frequency, image rejection becomes a minor problem.



4. Off-the-shelf synthesizer. Communications Power Inc.'s synthesizer uses multiple-sourced TTL ICs and a MOS RAM. Two front-panel switches provide binary-coded-decimal outputs corresponding to each digit of the channel number selected. Each decimal is binary-coded, not the entire number. C_11 , C_12 , etc. are the "bits" of C_1 , the first digit in the channel number C_21 , C_22 , etc. are the "bits" of C_2 , the second channel number as shown. The actual radio operating frequency is equal to N(5 kHz) + 12.8 MHz, where 12.8 MHz is a fixed heterodyne offset and N is a BCD number that determines the divide ratio for the programable counter.

Although single-conversion with a higher i-f has advantages, Hugh Barnes, engineering manager for E. F. Johnson's CB products, says his firm's top-of-the line models use single conversion with 4.3 MHz because "it's difficult to do effective noise blanking at lower frequencies." It's not as easy to achieve the necessary coincidence between noise pulse and activation of the series gate that blanks it, at the lower frequencies.

The transmitter is the only part of the CB set that matters for FCC type-acceptance. Transmitter technology has remained fairly conventional, but Commu-

nications Power Inc. has beefed up the radio-frequency power-output stage.

Aiming at the top-of-the-line a-m/SSB market, CPI uses a 10-watt emitter-ballasted rf transistor. Although it's much larger than needed, it's rated to withstand second breakdown into infinite voltage-standing-wave ratios caused by a broken or disconnected antenna. To satisfy the FCC's dissipation limit for the output transistors, the firm has designed the rf-output stage to work into a fixed-load line.

The transmitter also has an unusual modulation scheme (Fig. 8). Most CB sets use high-level modulation, in which a transformer modulates the voltage to the final rf and driver stages with diode shaping to get linear modulation. Since this model needs a highly linear transmitter capable of good peak envelope power for the SSB portion, CPI uses the same circuitry for a-m. "We just turn on a diode to reinsert the carrier for double sideband," says company vice president Wisherd. "This way, it's a full a-m modulator, not just a compatible a-m modulator that reinserts the carrier after the filter to produce carrier and one sideband."

The switchable-mode, eight-pole crystal filter also minimizes spurious frequencies. Should over-modulations occur, the filter suppresses any frequencies generated that lie outside of the a-m passband.

What's ahead

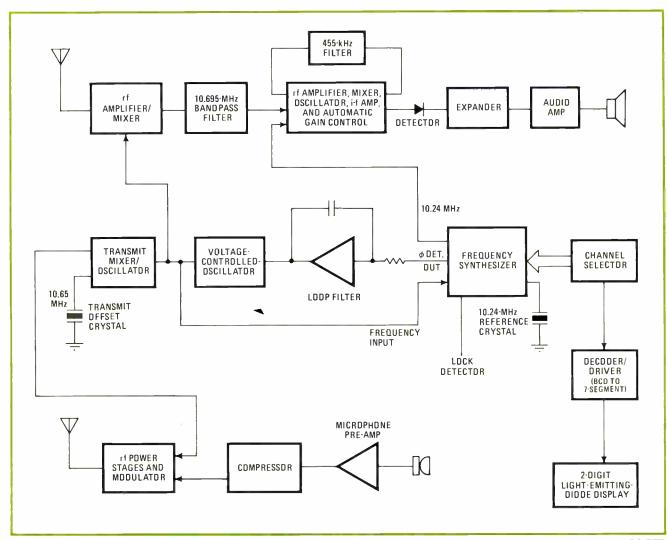
Although technology can advance the state of the art in citizens' band, much of what happens in the next few years depends on how the FCC decides to relieve the channel crowding. That it will decide to relieve it, with an immediate expansion of the present range, is the only prediction industry observers make confidently. Other possibilities, including fm transmission and restriction of the a-m to SSB transceivers, are up in the air at the moment.

Electronics pegs the 1976 market at a conservative \$500 million in U.S. factory sales—and manufacturers are reminded every day that it would be bigger if they could produce or import enough sets. The sellers' market has a dampening effect on technological change, so the sets will remain pretty much the same until competition forces innovation.

However, many CB-set manufacturers are speculating about what is ahead. Hugh Barnes of E. F. Johnson pretty much sums up present thinking of the manufacturers: "CB sets will reflect a good, better, still better, and best pricing structure in the near future, with prices starting about \$129.95 and ending about \$259.95 for some time to come. As competition gets heavier, manufacturers will try to put more and more features they think the consumer wants into the sets, but prices won't come down much."

The firm's Washington, D. C., representative, Stuart Meyer, thinks when change does come it will be in the form of a dual-mode transceiver. "A-m at 27 MHz will work well in the rural areas," he says, "but a service using fm transmission in the vhf band will be needed in congested urban areas."

CPI's Dave Wisherd doesn't see any ultra-high-frequency CB sets in the future: "They'll be very expensive,

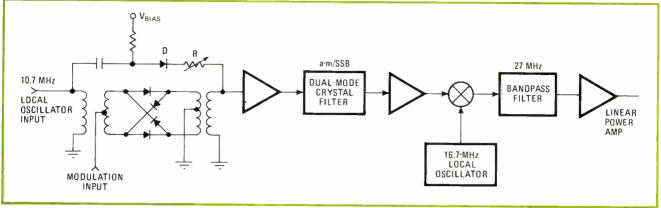


5. CB circuits on chips. Signetics has put together several circuits including a 16-pin dual in-line D-MOS PLL synthesizer, a D-MOS FET used as the VCO, and a D-MOS IC that functions as an rf amp mixer or oscillator/mixer. It has introduced a compandor circuit, half functioning as an expander, the other half as a compressor. Another dual IC is used as a microphone preamp and loop filter amp.



- **6. Selecting channels with push-button ease.** SBE's phase-locked-loop synthesizer accepts keyboard inputs at a random and asynchronous rate, where all inputs are neglected except for the last two digits punched in before the enter button is pressed.
- 7. More than a mike. Several CB-set manufacturers are contemplating putting a channel display using a LED or LCD readout along with an up/down channel selection switch into the microphone. Next step may be to include a keyboard for selecting the channels.





8. Double Duty. The modulation scheme Communications Power Inc. uses in its a-m/SSB CB sets doesn't require an extra crystal filter. For a-m, the carrier is reinserted around the balanced modulator when diode D₁ is turned on. R₁ adjusts the modulation percentage.

since the fm uhf radios haven't much commonality with a 27-MHz a-m rig, and it's so different to use." He does think there is a good possibility of restricting the 27-MHz frequency channels to SSB operation for an effective doubling of channels with the same frequency allocations. The PLL synthesizer and a better crystal will make economically feasible 100-hertz stability, which he thinks will be adequate. However, not all set makers agree with him that a received transmission 100 Hz away from the transmitting frequency will be adequate for understandability.

CB manufacturers do generally agree that sets are going toward in-dash installations, as they move into the largely untapped automobile market. Detroit will play a big part in what happens. Several U.S. CB-set makers have already held discussions with car makers, and two Japanese companies—Japan Industry Ltd. and Panasonic—have in-dash units that include a cassette stereo player, an a-m/fm multiplex radio, and a 23-channel citizens' band transceiver.

In-dash mounts can reduce theft rates, but there may be installation problems in the already limited space behind the dashboard. One CB-set manufacturer thinks installation cost will soar to close to the cost of the set. The solution may be in technology, most set makers think.

"I see putting the main part of the radio in the trunk, with a control head (which could be something like the Princess telephone handset) with channel selector and microphone up front convenient to the driver," says Ray Edwards, chief engineer of the Pathcom's Pace Communications division in Harbor City, Calif. "The handset could be detached and stowed in the glove compartment for further theft protection."

Dave Wisherd agrees. He sees the microprocessor as the key to doing this inexpensively. In the control head, the microprocessor would organize all channel information and other data, which could then be sent serially over a single wire to the shift register, which would be in the trunk module.

This module would provide a parallel format to control all necessary radio functions. "It'll provide the future CBer with a whole host of functions performed digitally such as monitoring preset channels periodically and reading out VSWR, power output, etcetera—simply

with keyboard control," he says.

The promise of a volume market should provide the impetus to produce an LSI chip with just about all the functions for a CB radio. With semiconductor manufacturers already eyeing the volatile market, plus the interest of such high-technology set manufacturers with land-mobile, military and commercial communications expertise as GE, Motorola, and RCA, there is always the possibility that technological innovations may occur faster than anticipated.

There are certain to be many casualties among the present 50 set manufacturers before the marketplace shakes out. Consumer preference is a big question mark, because of radically different operating conditions. In low-density rural areas with little channel crowding or interference, the lower-cost 23-channel sets should be adequate. On the other hand, for the high-density urban areas, higher-performance 50-channel sets are a must. What may finally emerge to meet the needs of mobile America are both types, with technological advances bringing them close together both in performance and in price.

Typical specs

The FCC only checks the transmitter portion of CB radio before type acceptance. However, the receiver portion often determines how well the set will function in today's crowded, noisy environment. Although cost is a pretty good indicator of a set's performance, the many options offered boost the price without a corresponding increase in performance. A sampling of CB manufacturers' literature gives an indication of standard receiver specifications.

Sensitivity (for a 10-dB signal-to-noise ratio)

a-m 0.5 to 1 microvolt SSB 0.25 to 0.5 μ V Image rejection μ greater than 60 dB Adjacent channel/reflection Selectivity

a-m -50 dB min. at ± 10 kHz -50 dB at ± 2.3 kHz

Spurious and cross modulation rejection

650 dB min.

Audio output 2–5 W, with under 10% distortion

Designer's casebook

Counter inverts period to measure low frequency

by Matthew L. Fichtenbaum General Radio Co., Concord, Mass.

Measuring the frequency of a low-frequency signal directly is a slow process, since enough signal cycles must be counted to give the needed resolution. Measuring the signal's period instead can give the needed information in only one period, but computation or circuitry to convert period to frequency is necessary. The circuit described here finds the reciprocal of the measured period by means of standard binary and binary-coded-decimal counters.

The concept underlying the technique is illustrated in Fig. 1. Four counters are required. Counter A measures the period of the unknown signal by counting the number of clock pulses, N, during a cycle. The number N programs counter B, which is a programable divide-by-N unit. Counter C creates a burst with a fixed number of pulses K.

This burst is applied to counter B, which computes a number K/N, thus taking the reciprocal of the period, N, and producing a number of pulses proportional to the unknown signal frequency. Finally, counter D accumulates these pulses to display the frequency.

The application determines the clock frequency and the counter lengths.

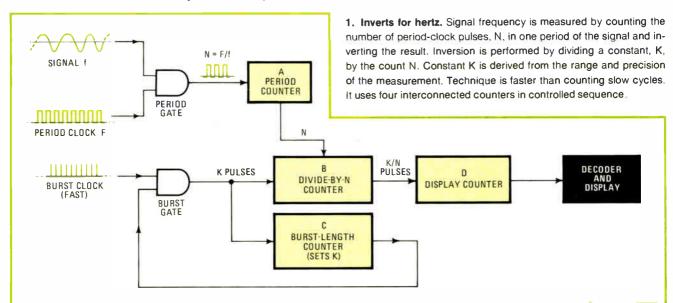
1. Clock frequency is defined by the highest frequency to be measured, and the resolution with which it must be measured. For example, if 1 kilohertz must be measured to 1% accuracy, 100 clock pulses must

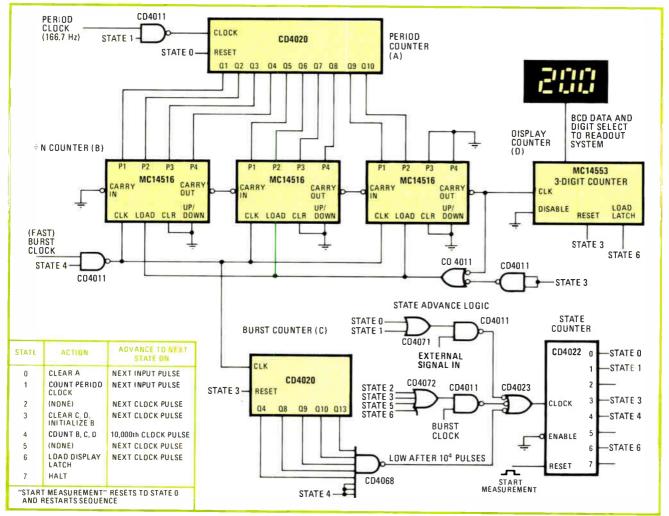
- occur in 1 millisecond (one signal period). This defines a clock frequency of 100 kHz.
- 2. The lowest frequency to be measured has the longest period, which defines the lengths of counters A and B. In this example, a low-frequency limit of 10 hertz would require a counter capable of counting 0.1 second (period of 10 Hz) × 100 kHz, or 10⁴ clock pulses. Fourteen bits would suffice.
- 3. The size of counter C depends on the length of the pulse burst. The period of a 10-Hz signal results in a count of 10⁴ in counter A. If K/N is to be 10, then K, the number of pulses in the burst, must be 10⁵. A 17-bit counter is required. The actual frequency of the burst does not enter into the end result; to speed calculation, the burst frequency should be as high as is convenient.
- 4. Finally, counter D must count to the highest frequency measured, 1,000. This requires 10 bits, or 3 BCD decades if the frequency is to be displayed.

Control logic, to implement the sequencing required, must of course be provided.

Figure 2 shows how the technique may be used in a tachometer capable of measuring shaft speed from 10 to 200 revolutions per minute, with 2% resolution at 200 rpm. The four steps above give the circuit parameters.

- 1. At 200 revolutions per minute, the signal period is 0.3 second. To measure the speed to 2%, 50 clock pulses must be counted in this time, so the clock frequency must be 166.7 Hz.
- 2. At 10 rpm, the signal period is 6 seconds. Counting clock pulses for this time results in 6 × 166.7 = 1,000 counts, so counters A and B are each 10 bits.
- 3. The pulse burst must give a quotient of 10 with 1,000 counts, so it must be 10,000 counts long. Therefore K is 10⁴, and counter C thus requires 14 bits.
- 4. Counter D, the display register, must count up to the





2. Measuring frequency. Implementation of arrangement in Fig. 1 uses C-MOS devices. A fifth counter, the CD4022 divide-by-8, sequences the steps of the procedure, as shown in the table. This measurement system is fast because it counts clock pulses for only one period of the signal frequency, instead of counting many cycles of signal. Precision of measurement is high because clock rate is high.

maximum of 200. Three BCD decades will suffice. For the low frequencies involved, complementary-MOS devices are adequate. The control logic consists of a CD4022 divide-by-8 counter with individually decoded outputs. Each output corresponds to a step in the control sequence as follows:

- Clear period counter A.
- On next signal transition, start counting clock pulses in counter A.
- On next signal transition, stop counting A.
- Clear counters C and D.
- Enable counters B, C, D to compute frequency.

- After 10,000 pulses, stop counting.
- Load output latch to update BCD display.

A network of gates selects the proper signals to advance the sequencer from state to state.

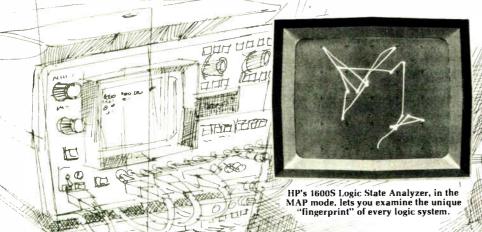
For easier implementation, the length of the pulse burst may be chosen to be an exact power of 2. This simplifies the logic around counter C.

The final display is equal to K/FT, where K is the length of the burst, F is the period-clock frequency, and T is the signal period. Scaling either K or F changes the displayed result accordingly and provides an easy way to change the units of the displayed answer.

Hexadecimal encoder debounces keyboard

by Ralph Tenny Texas Instruments Inc., Dallas, Texas Programs and instructions for microprocessors are commonly written in hexadecimal machine code (0, 1, ... 9, A, B, ... F), but must be fed into memory in binary code. In the 16-key encoding keyboard circuit described here, the unique application of two eight-input priority encoders provides the user with key lockout—if he accidentally presses two keys to one encoder simultaneously, only the higher value is encoded, and oper-

Let's talk about the easy way for you to spot microprocessor hardware problems.



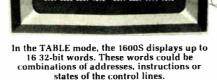


We've probably both spent hours at the simulator to prove we had good software and then discov-

ered the hardware won't play—what do we do? You know the traditional answer. Dig out the scope, get out the program printout, and brace yourself for hours of grinding, point-by-point checks. But I can tell you that doesn't have to be the case. Especially now that HP has introduced some new tools that can really cut down your troubleshooting time.

HP's Logic State Analyzers can really take a lot of pain out of your troubleshooting procedures. You'll find wiring errors, defective components, and even solder splashes; and you'll find them a lot more quickly than ever before.

Let me give you an example. We had an eight-bit micro-processor system with start-up problems. The clocks were running and phased right, and the address lines toggled, but the machine didn't function. So, we



set up an HP 1600S Logic State Analyzer to look at both the Address and Data buses. It was then we noticed that only "zeros" were being fetched from memory. Knowing the ROM was good, we then added several control lines to the display and the problem showed up immediately. The "Enable" line never went high. A quick look at the "Enable" driver showed the input was ok, but no output. Obviously, the gate was defective.

I don't know how long it would have taken to find that one without HP's Logic State Analyzers, but I know it would have taken us a lot longer.

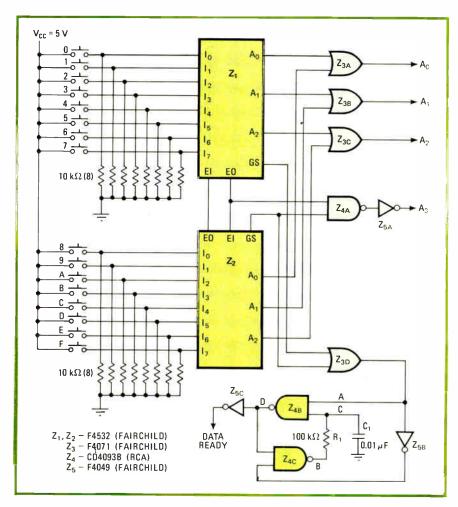
Call your local HP field engineer. He'll give you all the details on the 1600S (priced at \$6800*) including spec sheets and application notes detailing the use of mapping for troubleshooting minicomputer and microprocessor systems. He'll tell you about the seminars that HP has arranged around the country and tell you when one will be held in your area and how you can attend. You ought to go to one, because you'll discover an exciting new concept in digital troubleshooting.

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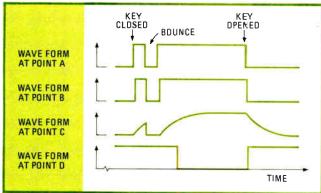




Sales and service from 172 offices in 65 countries.



1. Encodes and debounces. Pressing any one of the 16 hexadecimal-digit keys produces the corresponding 4-bit binary output through the two priority encoders Z_1 and Z_2 . Key debounce is provided by the Schmitt trigger delay latch Z_{4B}/Z_{4C} , as illustrated in Fig. 2. This keyboard is a convenient interface to microprocessors or other binary devices for which instructions may be written in hexadecimal code.



2. Less bounce to the ounce. Wave forms at four points in delay latch illustrate how R_1C_1 delay and Schmitt trigger action prevent key-bounce from generating multiple outputs.

ation of one encoder automatically disables the other.

Another unique feature of the circuit is a delay latch, which debounces the keys. Only when the key contacts have settled down to a steady closed or open position can the microprocessor read the code.

As Fig. 1 shows, the circuit accepts the hexadecimal input over 16 keys and translates it into 4-bit binary outputs for parallel feed to a computer. The priority encoders provide the basic encoding.

The keys are arranged in two groups, 0 through 7 and 8 through F. Each subgroup is encoded in 3-bit binary

code; if two keys in the same subgroup are pressed, the code output corresponds to the highest-priority key depressed. Lockout between subgroups is accomplished by cross-coupling the enable-input (EI) and enable-output (EO) pins, so that the output code is decided by which subgroup is accessed first. Encoding for the fourth bit is accomplished by an AND gate between the low-order enable-output and the high-order group-select (GS). The composite output code then consists of the subgroup output bits ORed together for the lower three bits, and the fourth bit output.

The keys are debounced by delay latch Z_{4B}/Z_{4C} . The basic ingredients of the latch are the CD4093B Schmitt trigger elements and an RC delay (R_1 and C_1) in the feedback loop of the latch. The debounce wave forms in Fig. 2 show that a key must stop bouncing before the latch feedback, delayed by a time 0.85 R_1C_1 , locks in the key action. Release of a key immediately resets the latch, with the input bypassing the delay loop. It is imperative that a Schmitt trigger be incorporated in the delay latch, but the use of C-MOS holds down the size and cost of the capacitor. The values of C_1 and C_1 are determined by the bounce characteristics of the pushbutton keys. The 0.01 microfarad and 100 kilohms shown in Fig. 1 will debounce almost any key.

Designer's casebook is a regular feature in Electronics. We invite readers to submit original and unpublished circuit ideas and solutions to design problems. Explain briefly but thoroughly the circuit's operating principle and purpose. We'll pay \$50 for each item published.

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Optical isolators yield benefits in many linear circuits

by Mark Hodapp, Hewlett-Packard Co., Optoelectronics division, Palo Alto, Calif.

□ For eliminating noise and breaking unwanted ground loops in digital circuits, optically coupled isolators have earned a well deserved reputation. It is perhaps less well known that optical isolators also can do a fine job in analog circuits. Although the optical isolator is not strictly a linear device—the output is not necessarily linearly proportional to the input—with the right circuit techniques, the isolator's advantages can be applied to such linear tasks as sensing circuits, patient-monitoring equipment, adaptive controls, power supplies, and audio or video amplifiers.

A number of isolators are suitable

Four kinds of isolators are appropriate for linear work—the phototransistor coupler, the photodiode coupler, the transistor-amplifier coupler, and the logic-gate coupler. All of these have an infrared or near-infrared light-emitting diode as their input stage, but their outputs are different, as reflected by their names. Both the transistor-amplifer and logic-gate coupler employ photodiodes as their photodetectors, which are followed by amplifying and/or conditioning circuitry.

For strictly analog circuits, the photodiode and transistor-amplifier isolators are most effective because they are faster and more linear than phototransistor types. In the phototransistor coupler, the collector-base junction of the phototransistor serves as the photodetector, and the capacitance of this junction impairs the rise time of the signal at the collector. Also, amplified photocurrent flows in the collector-base junction and modulates the photoresponse.

In a transistor-amplifier coupler, however, the photodetector and the amplifier are separately integrated devices, so that the coupler's photoresponse is not affected by amplified photocurrent. Also, so long as the bias voltage for the photodiode remains constant, the photodiode's capacitance will not hamper isolator speed. Similarly, the photodiode coupler, which has a very small current gain, usually operates with a separate, though external, amplifier. In contrast, although the logic-gate coupler has a separate photodetector, it is difficult to bias at a stable quiescent point for analog applications its output is usually either on or off.

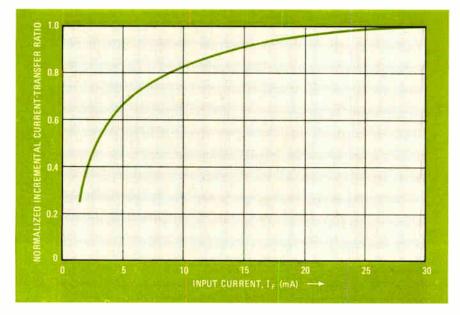
However, the logic-gate coupler is ideal for linear circuits in which analog signals are converted to digital at very high speeds. For slower rates, the phototransistor and transistor-amplifier couplers are suitable—and they usually can provide greater noise immunity. Photodiode isolators are seldom used in these linearized conversion circuits because of the output buffer they require.

Examining isolator linearity

To evaluate the linearity of an optical coupler, its transfer function can be expressed as:

$$I_0 = K(I_{\rm F}/I_{\rm F}')^{\rm n}$$

where I_o is the isolator's output current, I_F is the input current to the isolator (the forward current through its LED), $I_{F'}$ is the input current (other than zero) at which K is measured, K is the output current at $I_{F'}$, and n is the linearity factor. For a transistor-amplifier coupler, I_o is the collector current of the output transistor, whereas



1. Current characteristic. Linearity of optical coupler depends on where its input LED is biased. At low currents, its response is quite nonlinear. But at values of 10 mA or higher, the device's response, depicted here as $\delta I_{\rm o}/\delta I_{\rm F}$ becomes more nearly linear.

Electronics/March 4, 1976

for a photodiode coupler, I_o is the current flowing into the cathode of the output photodiode.

The linearity factor, n, can also be thought of as the slope of the log-log curve of output current I_o vs. input current I_F . When n=1, the coupler's transfer function is linear, and its current-transfer ratio, which is the current gain expressed as a percentage ($I_o/I_F \times 100\%$), becomes constant. Similarly, when n=1, the device's incremental current-transfer ratio, or the ratio of the change in output current to the change in input current ($\delta I_o/\delta I_F$), is constant and independent of the input current level, as shown in Fig. 1.

A LED's emission is nonlinear, particularly at low input-current levels. For low values of I_F, say, 5 milliamperes or less, n is usually equal to or greater than 2. At higher input currents, the response of a LED becomes essentially linear. Usually, at an I_F value of 10 mA or more, n approximately equals 1.

More than one coupler may be needed

An analog isolation amplifier is used to transfer a low-level ac or dc signal from one ground reference to another in the presence of a large potential difference or induced noise. Since a dc reference must be maintained in a direct-coupled isolation amplifier, the quiescent input current to the optical coupler is normally set at some low level to minimize the device's thermal drift. As a result, two couplers are usually needed for a direct-coupled isolation amplifier, so that each balances out the other's nonlinearity. This balance can be achieved by means of either servo or differential techniques.

On the other hand, an ac-coupled isolation amplifier needs only a single coupler if it is biased properly because there is no need to maintain a dc reference. The isolator's input LED can be biased at some high current level where the isolator's transfer function is more nearly linear.

For the servo-type dc isolation amplifier of Fig. 2, the input current of one isolator is made to track the input

current of a second isolator. To do this, the couplers must be matched—that is, their linearity factors must be equal. Here, a single dual-channel transistor-amplifier isolator is used, and the matching is good, since both couplers are fabricated at the same time.

Building a servo amplifier

The input signal is applied to the noninverting input of amplifier A_1 , which drives the LED of the upper isolator. The LED of the lower isolator is driven by amplifier A_2 , which compares the outputs of the two couplers and forces the forward current through each LED to be equal. The output current of the upper isolator can be written as:

$$I_{\rm C1} = K_1 (I_{\rm F1}/I_{\rm F1}')^{\rm n_1}$$

where I_{C1} is the collector current of the isolator's output transistor, I_{F1} is the forward current through the input LED, K_1 is the output current for an input current of I_{F1} , and n_1 is the linearity factor. Similarly, the output current of the lower isolator is:

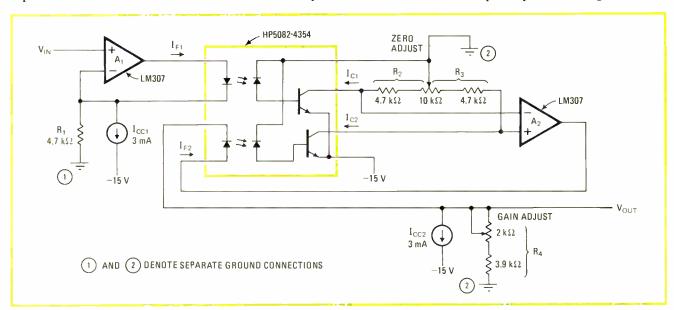
$$I_{\rm C2} = K_2 (I_{\rm F2}/I_{\rm F2}')^{\rm n_2}$$

where I_{C2} is the collector current of the isolator's output transistor, I_{F2} is the forward current through the input LED, K_2 is the output current for an input current of I_{F2} , and n_2 is the linearity factor.

The transfer function for the circuit is:

$$\begin{split} V_{\rm OUT} = \, R_4 \Bigg[I_{\rm F2}' (I_{\rm F1}')^{-n_1/n_2} \! \Bigg(\frac{K_1 R_2}{K_2 R_3} \Bigg)^{1/n_2} \\ & \left(I_{\rm CC1} + \frac{V_{\rm IN}}{R_1} \right)^{n_1/n_2} - I_{\rm CC2} \Bigg] \end{split}$$

where $V_{\rm OUT}$ is the circuit's output voltage, $V_{\rm IN}$ is the circuit's input voltage, $I_{\rm CC1}$ is the value of the constant-current source for the upper isolator's LED, and $I_{\rm CC2}$ is the value of the constant-current source for the lower isolator's LED. Some simple ways of realizing these con-



2. Dc servo amplifier. Dual-channel transistor-amplifier coupler provides input-to-output isolation of 500 V for servo-type amplifier. Isolator input currents are forced to track each other, and since the couplers are matched, nonlinearity is virtually eliminated.

stant current sources are shown in "Some biasing basics," p. 110. After the circuit's null point has been adjusted, this transfer function can be expanded:

$$\begin{split} V_{\rm OUT} &= R_4 I_{\rm CC2} \bigg[\frac{n_1}{n_2} \bigg(\frac{V_{\rm IN}}{R_1 I_{\rm CC1}} \bigg) \\ &+ \frac{n_1}{n_2} \bigg(\frac{n_1}{n_2} - 1 \bigg) \bigg(\frac{V_{\rm IN}}{R_1 I_{\rm CC1}} \bigg)^2 \bigg/ 2! + \dots \end{split}$$

When n_1 and n_2 are approximately equal as in these matched couplers, all the higher-order terms of this expression become very small, so that the transfer function is essentially linear.

The output voltage, V_{OUT}, for the servo amplifier can also be written as a generalized proportionality:

$$(1+x)^{n_1/n_2}-1$$

where x is the ratio of modulation current to quiescent current for the upper coupler's input LED:

$$x = (V_{\rm IN}/R_1)/I_{\rm CC1}$$

The nonlinearity, or linearity error, of the entire circuit can then be expressed as:

$$[(l+x)^{n_1/n_2}-(n_1/n_2)x-l]/(n_1/n_2)x$$

For this circuit, x is less than or equal to 10.351 and $n_1/n_2 = 1.05$, so that the linearity error can be computed as 0.99%.

Resistor R_1 determines the input range of the circuit. For the R_1 value of 4.7 kilohms given here, current $I_{\rm F1}$ varies between 2 and 4 mA as the input voltage ranges between -5 and +5 volts. The circuit's null point is zeroed by adjusting the ratio of resistor R_2 to resistor R_3 . For unity-gain operation, R_4 is varied until $V_{\rm OUT}/V_{\rm IN} = 1$ when $V_{\rm IN}$ is at some value other than zero. Both resistors R_2 and R_3 should be selected to accommodate the worst-case spread between isolator current-transfer ratios.

The bandwidth of the circuit is limited by its oper-

ational amplifiers to about 25 kilohertz. Common-mode rejection is 46 decibels at an input frequency of 1 kHz, and temperature stability is good—gain drift is held to -0.03%/°C, offset drift to ±1 millivolt/°C. Insulation of the dual-channel coupler is limited by the spacing between its package pins. If separate couplers are used instead, the insulation can be increased to around 2,500 v.

Handling differential dc signals

For a differential-type dc isolation amplifier like the one shown in Fig. 3, the input current of one isolator is increased by the same amount that the input current of a second isolator decreases. In other words, the rise of current gain in one isolator is approximately balanced by a gain reduction in the other. Of course, the couplers must be matched with the same linearity factors, so that their gains change by an equal amount over the same operating range. Again, a dual-channel transistor-amplifer isolator is used.

Amplifiers A_1 and A_2 act as a differential-input pair, while amplifiers A_3 and A_4 form a differential current amplifier. The circuit's output voltage can be written as:

$$V_{\text{OUT}} = R_5 \left[(R_3/R_4)I_{\text{C1}} - I_{\text{C2}} \right]$$

where:

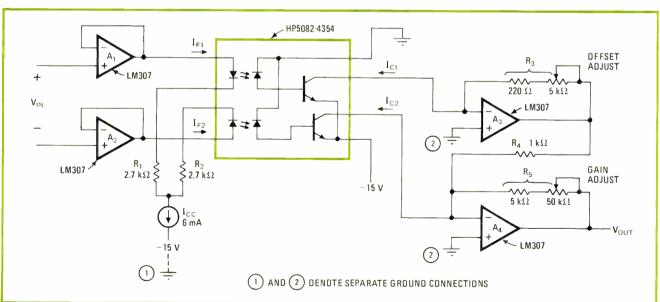
$$I_{\rm C1} = K_1 (I_{\rm F1}/I_{\rm F1}')^{\rm n_1}$$

for the upper isolator, and:

$$I_{\rm C2} = K_2 (I_{\rm F2}/I_{\rm F2}')^{\rm n_2}$$

for the lower isolator. Since the values of resistors R_1 and R_2 are the same ($R_1 = R_2 = R$), then the transfer function for the circuit can be expressed as:

$$V_{\text{out}} = R_5 \left[\frac{R_3 K_A}{R_4} \left(1 + \frac{V_{\text{IN}}}{R I_{\text{CC}}} \right)^{n_1} - K_B \left(1 - \frac{V_{\text{IN}}}{R I_{\text{CC}}} \right)^{n_2} \right]$$



3. Dc differential amplifier. Because of matched characteristics of dual-channel transistor-amplifier coupler, an increase in current gain of one isolator is balanced by an identical gain reduction in the other. Linearity error is held to around 3% over 10-V input range.

where:

$$K_{\rm A} = K_1 (I_{\rm CC}/2I_{\rm F1}')^{\rm n_1}$$

and:

$$K_{\rm B} = K_2 (I_{\rm CC}/2I_{\rm F2}')^{\rm n_2}$$

and $I_{\rm CC}$ is the value of the constant-current source biasing the input LEDs of the coupler. After the circuit's zero has been adjusted, this transfer function can be written as:

$$\begin{split} V_{\rm OUT} &= R_5 K_B \bigg[(n_1 \, + \, n_2) \bigg(\frac{V_{\rm IN}}{R I_{\rm CC}} \bigg) \\ &+ [n_1 (n_1 - 1) - n_2 \, (n_2 - 1)] \bigg(\frac{V_{\rm IN}}{R I_{\rm CC}} \bigg)^2 \Big/ 2! \, + \ldots \end{split}$$

Since the couplers are matched, n₁ is approximately equal to n₂, the higher-order terms of this expression become very small, and the transfer function can be regarded as linear.

When expressed as a generalized proportionality, the output voltage, $V_{\rm OUT}$, for the differential amplifier is proportional to:

$$(1+x)^{n_1}-(1-x)^{n_2}$$

where x is the ratio of modulation current to the quiescent current of the isolators' input LEDs:

$$x = (V_{\rm IN}/R)/I_{\rm CC}$$

The linearity error of the circuit can be computed from:

$$[(1+x)^{n_1}-(1-x)^{n_2}-(n_1+n_2)x]/(n_1+n_2)x$$

For the circuit of Fig. 3, x is less than or equal to |0.35|, $n_1 = 1.9$, and $n_2 = 1.7$. The linearity error, then, is around 2.8% when the linearity factors of the isolators are matched to about 12%. If the linearity factors are within 5% of each other, the circuit's linearity error can be reduced to less than 1.5%.

Resistors R₁ and R₂, which set the circuit's input

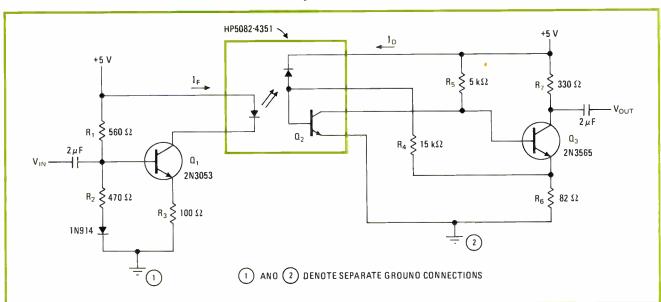
range, must have the same resistance value. Here, they limit the current to the isolator's LEDs to between 2 and 4 ma over the input range of -5 to +5 V. Resistors R_3 , R_4 , and R_5 are selected to handle the worst-case spread in isolator current-transfer ratios and to set the circuit's total gain at unity.

As an alternative to a dual-channel transistor-amplifier coupler, a pair of individual transistor-amplifier or photodiode isolators can implement either a servo or differential isolation amplifier. Although it is essential for the couplers to have the same linearity factor (n), they need not have identical output currents (K) for the same input current (I_F). However, when the K or current-transfer ratio of each coupler is equal, it is easier to zero the isolation amplifier.

In addition, both couplers should exhibit similar behavior as the temperature changes. In the servo amplifier, changes in the current-transfer ratios of the isolators have only a small effect on total gain and offset, so long as the ratio of the isolators' current gains stays the same when the temperature changes.

However, in the differential amplifier, a change in either isolator's current-transfer ratio caused by a temperature change will cause the circuit's gain to change. Such a change in gain can be compensated for by placing a thermistor in the circuit's output differential current amplifier. Output offset, referenced to the input, will be stable as long as the ratio of the isolators' current gains remains constant during temperature changes.

To prevent the capacitance of the photodiode detector in either a photodiode or transistor-amplifier coupler from impeding isolator speed, the photodiode should always be biased on a constant voltage. In a direct-coupled isolation amplifier, the speed of a transistor-amplifier coupler is also limited by the capacitance of the base-collector junction of its output transistor. Similarly, the speed of a photodiode coupler is also limited by how quickly it can operate into its external circuit.



4. Isolating ac signals. For ac-coupled isolation amplifier, optical isolator is biased at high input level, where its transfer function is essentially linear. Here, with single transistor-amplifier coupler, linearity error is typically 2% over 1-V peak-to-peak input range.

For an ac-coupled isolation amplifier, the optical coupler can be biased at a current level higher than that for a dc isolation amplifier. The isolator's incremental current-transfer ratio, therefore, remains fairly constant. The isolator's transfer function is essentially linear (n = 1), so that only a single coupler is needed.

Working with the ac analog signal

The ac isolation amplifier of Fig. 4, is built with a transistor-amplifier coupler. The transfer function for this circuit is simply:

$$V_{\rm OUT}/V_{\rm IN} = (I/R_3)(R_4R_7/R_6)(\delta I_{\rm D}/\delta I_{\rm F})$$

where I_D is the photodiode current, and I_F is the forward current though the LED. As long as $\delta I_D/\delta I_F$ is constant, this transfer function is linear.

Transistor Q_1 —along with resistors R_1 , R_2 , and R_3 —bias the isolator's LED at a quiescent current of 20 mA. Resistor R_3 determines the circuit's input range. Here, the LED current varies from 15 to 25 mA over an input range of 1 v pk-pk. Transistors Q_2 and Q_3 form a cascade amplifier having feedback paths through resistors R_4 and R_6 . The circuit's closed-loop gain can be adjusted with resistor R_4 , while resistor R_6 should permit transistor Q_3 to operate at its maximum gain-bandwidth product. Resistor R_5 simply provides a dc bias path for transistor Q_3 , and resistor R_7 limits the maximum excursions of the circuit's output voltage without clipping it.

Linearity error for this circuit is typically 2% over its l-v pk-pk input range. At the expense of signal-to-noise ratio, the linearity can be improved by reducing the excursions of the isolator's input current. To do this, the value of the resistor R_3 is increased, and an additional resistor is connected between the collector of transistor Q_1 and ground. Its value should return the LED bias current to 20 mA quiescent.

A photodiode coupler is also suitable for building an ac isolation amplifier. But whether the coupler is a

photodiode or transistor-amplifier type, its photodiode detector must be biased at a constant voltage for optimum isolator speed.

In the ac-coupled amplifier, the input diode of the optical isolator is biased at a fixed quiescent current and the circuit's output amplifier has negative feedback for stable gain. Since this circuit has no feedback around the isolator, any parameter that causes the coupler's incremental current-transfer ratio to vary will cause a change in the gain of the circuit. Because the photon emission of the coupler's input diode varies with temperature, the coupler's current gain will also vary with temperature. However, a thermistor can be used in the circuit's output amplifier to compensate for this change in gain.

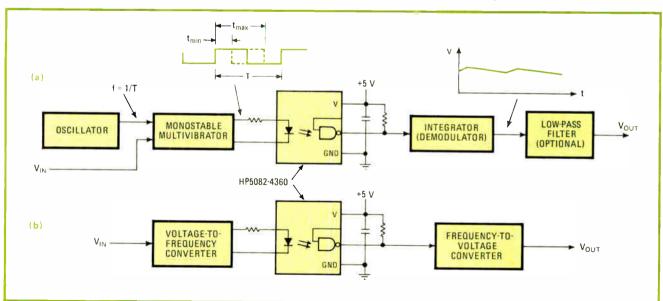
Exploring various digital techniques

With conventional ac and dc analog-isolation techniques such as those discussed above, the least linearity error that can be achieved with optical couplers is limited to 0.5% to 1%. For better linearity, as well as improved temperature stability, digital techniques are needed.

The analog signal is converted to a digital one, transmitted through an optical isolator, and then converted back again. Since the isolator acts only as a switch, the total linearity of circuit depends primarily on how accurately the analog-to-digital and digital-to-analog conversions are made. The circuit's bandwidth, however, is limited by the isolator's propagation delay.

Figure 5 shows a pulse-width-modulation technique for isolating an analog signal with a logic-gate coupler. The oscillator operates at a fixed frequency (f), and the monostable multivibrator varies the duty factor of the square wave from the oscillator, producing a signal that is proportional to the input $(V_{\rm IN})$.

The maximum frequency of the oscillator is determined by the required linearity of the circuit and the propagation delay of the optical isolator: $(t_{max} - t_{min}) \times$



5. Digital yet analog. Logic-gate coupler can be used to transfer analog data that has been converted to digital form. Analog signal can be pulse-width modulated, as in a, or converted into train of pulses whose frequency is proportional to input level, as in b.

Some biasing basics

When an optically coupled isolator is used in a direct-coupled isolation amplifier, its input light-emitting diode must be biased by a constant-current source. Although the regulation of this source is important, the circuit need not be an elaborate design—a simple one will work just as well.

If the supply voltage is stable, the current source can be a mirror-type circuit, built with a pair of npn or pnp transistors, as shown in a. The output current is approximately:

$$I_{\rm o} = (V_{\rm CC} + V_{\rm EE} - V_{\rm BE})/R$$

where V_{CC} is the positive supply voltage, V_{EE} the negative supply voltage, and V_{BE} the base-emitter voltage of transistor Q_1 .

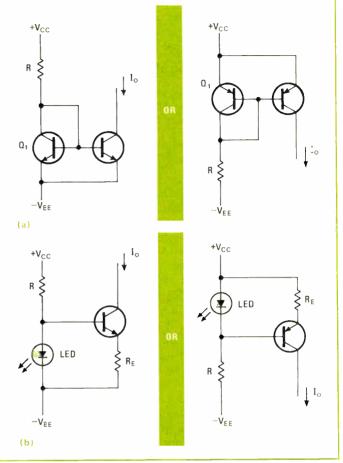
If the supply voltage is not stable, an LED can be used as an effective, yet simple and inexpensive, voltage regulator. As illustrated in b, the current source can be built with either an npn or pnp transistor. The LED compensates the transistor for changes in temperature because the temperature coefficient of the LED's forward-voltage drop is approximately equal to that of the transistor's base-emitter voltage. Usually, this junction-voltage drift with temperature changes is about -2 millivolts/°C. The value of resistor R can be computed from:

$$R = (V_{\rm CC} + V_{\rm EE} - V_{\rm F})/I_{\rm F}$$

where $V_{\bf F}$ is the forward-voltage drop of the LED, and $I_{\bf F}$ is its forward current. The circuit's output current can simply be expressed as:

$$I_{\rm o} = (V_{\rm F} - V_{\rm BE})/R_{\rm E}$$

where V_{BE} is the base-emitter voltage of the transistor.



(required linearity) must be greater than or equal to $|t_{PLH} - t_{PHL}|$, where t_{max} is the maximum pulse width of the modulated signal, t_{min} is the minimum pulse width of the modulated signal, t_{PLH} is the propagation delay of the isolator as its output switches from logic low to logic high, and t_{PHL} is the isolator propagation delay for an output transition from logic high to logic low.

After the modulated signal passes through the isolator, it is converted back to the original analog signal by means of a demodulator. Here, the demodulator is the integrator and optional low-pass filter.

Voltage-to-frequency conversion can also be used to isolate an analog signal, as shown in Fig. 5b for a logic-gate coupler. The voltage-to-frequency converter produces an output-pulse train having a frequency proportional to the level of the analog input. The maximum rate at which the pulse train can be transmitted through the optical isolator is limited by the coupler's propagation delay:

$$f_{\text{max}}$$
 approximately equals $[1/(t_{\text{PLH}} + t_{\text{PHL}})]$

The output signal from the isolator is then converted back to an analog voltage by a frequency-to-voltage converter. The linearity of the complete system is mainly determined by the linearity of the converter circuits.

Frequency modulation, which is similar to voltage-to-

frequency conversion, is yet another technique for transferring an analog signal through an optical isolator. By this method, a carrier frequency (f_0) is modulated by some change in frequency (Δf_0) so that the f_0 $\pm \Delta f_0$ is proportional to the analog input. The original signal can then be reconstructed at the output of the isolator with a phase-locked-loop or similar circuit.

Analog-to-digital converters also enable optical couplers to isolate analog signals. The binary output from an a-d converter drives an optical coupler, and a digital-to-analog converter changes the output from the coupler back to analog form. If the a-d converter has a parallel output format, it can be changed to a serial format by using a parallel-in/serial-out shift register to drive the isolator. When high resolution is needed, this method is useful, as well as economical, since it saves the expense of several isolators.

The rate at which data must be transmitted through the isolator more or less determines which type of coupler is best for a given application. Logic-gate couplers can operate at the highest speeds, attaining frequencies as high as 10 MHz. Transistor-amplifier isolators can achieve data rates as fast as 1 MHz, while phototransistor devices have a top speed of around 100 kHz. Usually, noise immunity must be traded off against speed—the faster the coupler operates, the lower its noise immunity will be.

Noise and control problems

Designing reliability into equipment having power semiconductors, part I

by Alexander Kusko, Thorleif Knutrud, and John J. Cain, Alexander Kusko Inc., Needham Heights Mass.

Dower semiconductor equipment faces a tough customer in industrial applications, which demand high reliability despite noisy, dirty, hot surroundings. But it is equal to the demand so long as its circuits are designed to cope with the factors affecting industrial reliability.

The electronic-design engineer inexperienced in this field is likely to fall into a number of traps that can seriously downgrade the performance of his design. And more and more EEs are facing these reliability problems as solid-state power equipment tackles more and more industrial applications. Still, our extensive consulting experience suggests that the newcomer can easily avoid many reliability traps by paying attention to some elementary design precepts. We will review these precepts in this two-part series.

This first part of the series begins by dealing with a problem that invades every aspect of electronic power-circuit design: electrical noise. Then the article concentrates on reliability in the control and logic portion of rectifiers, inverters and choppers that make up most

power semiconductor equipment. Particular points of discussion are feedback control circuits, firing-circuit control, abnormalities in control signals, and the effects of firing circuits on thyristors. The second article, which will appear in the March 18 issue, will address the problems of the power semiconductor circuits themselves.

To be considered highly reliable, industrial power semiconductor equipment must operate without failure or malfunction for 10,000 hours. That means five years of eight-hours-a day operation or one year of continuous operation.

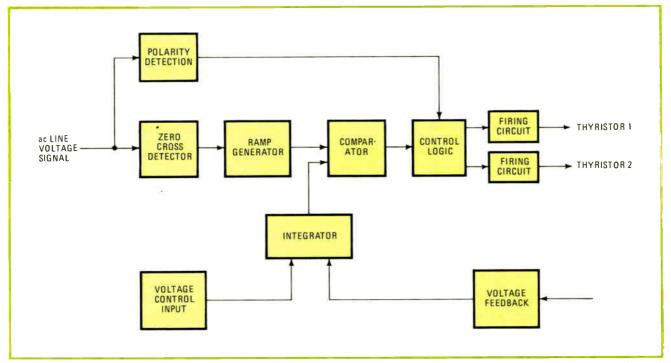
Very high reliability is the classification given to equipment that lasts 100,000 hours without failure. But the equipment must include redundant subsystems wherever a single subsystem turns out to have a lower reliability rating.

Threat from noise

One of the chief threats to reliability is noise, particularly noise in control and logic circuits. As the number

Rapid transit. A 3-phase, 575-volt phase-delay rectifier supplies power to a 70-horsepower dc motor to drive this 21-passenger, 8,700-pound vehicle at 30 miles an hour on a dedicated guideway. It was part of a West Virginia experiment in personal rapid transit.





1. Controlling power semiconductors. In this ac voltage controller, the ramp generator is synchronized to the line, and the feedback circuit at the comparator crossover point controls the firing angle. A gate network picks one thyristor on the basis of line-voltage polarity.

of power devices, the power level, the operating frequency, and system complexity increase, so does the need for high noise-immunity devices and noise suppression measures.

In control and logic circuits, noise is created chiefly by inductive and capacitance coupling between the power and control circuits. Inductively coupled noise is proportional to the power-current magnitude and the rate of current change (di/dt). It is inversely proportional to the distance between the power and control circuits. Capacitively coupled noise is proportional to the stray capacitance, the rate of voltage change (dv/dt) and the voltage magnitude.

When combined with the less significant noise generated by control-circuit operation, these two noise sources produce a cumulative error signal. The effects can be erratic operation, random equipment shutdowns, repeated blowing of fuses and tripping of circuit breakers, and even destruction of the power semiconductors.

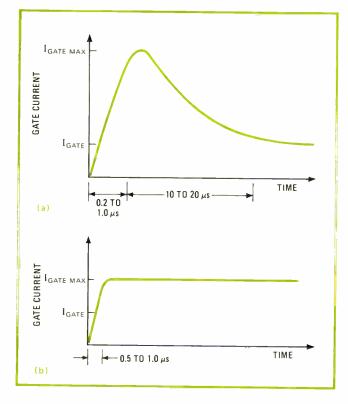
To minimize the effects of noise on control, there are some basic design guidelines:

- High-threshold transistor-transistor logic (HTTL) circuits provide the highest noise immunity, but the most cost-effective choice usually is complementary-metal-oxide-semiconductor logic operated at 12 or 15 volts. C-Mos logic will provide adequate noise immunity when moderate external suppression circuits are included. TTL should be avoided if possible. It has the least noise immunity and requires extensive bypassing, shielding, and a solid-ground system.
- The power lead for each integrated circuit should be bypassed separately with a ceramic or Mylar capacitor. For digital ICs, a capacitor between 0.01 and 0.1 microfarad is adequate.
- Power for analog devices should be supplied through

- a resistor-capacitor decoupling network, typically a 27-ohm resistor and a $0.05-\mu F$ capacitor.
- All relay coils should be shunted with suppressors. Ac relays require a metal oxide varistor and/or an RC snubber network, while their dc counterparts need an inversely connected diode.
- Wherever possible, a single-point ground system should tie all devices, subsystems and cable shields together.
- Power and control circuits should be kept as far apart as possible, and there should be some kind of barrier between them, preferably one of steel. Where the interconnection between them runs close to the power circuit, it should be twisted-pair shielded cable.
- Signal transformers should include an electrostatic shield where there is high dv/dt between windings.
- Lastly, circuits must be designed to be inherently immune to noise. For example, level detectors or precision comparators should provide enough hysteresis to eliminate multiple switching, while transistor switches subject to high noise levels should be reverse-biased to hold them in the off state. Differential amplifiers should be used to minimize the noise that affects two different parts of the same piece of equipment (common-mode noise)

Typically power-semiconductor equipment is controlled by a feedback control circuit. The circuit usually contains an operational amplifier integrator that compares transducer feedbacks of voltage, current, frequency, speed, and temperature against some references and uses the resultant output signals to regulate the power circuits.

Three factors are important in determining the reliability of these circuits: selecting the proper transducer, preventing transducer failure from creating unsafe op-



erating conditions, and minimizing control circuit influence on power circuits during transient conditions.

In selecting a transducer, the most important criteria are high noise immunity and dc isolation from the power circuit. High noise immunity generally demands that a transducer produce a relatively high output voltage from a relatively low source impedance. So transducers with a millivolt output, such as thermocouples and resistance shunts, are to be avoided unless they are extensively filtered and shielded and can tolerate the longer response time that filtering produces. Complex electronic transducers may require external bypassing and shielding to reduce the noise sensitivity of their internal circuits.

Transducers must be isolated from noise, but current and voltage transducers as well as de transductors are inherently isolated by virtue of their internal transformers. For transducers without built-in isolation, optical couplers can provide the necessary protection against voltage transients.

A transducer failure may result in a maximum output command due to loss of feedback to the regulated circuit. This can lead to unsafe operating conditions such as overspeed of a motor or heater burnout. For maximum safety, therefore, transducer circuits must be designed to be inherently fail-safe. The only alternative is to provide circuits that detect the loss of feedback signal or failure of the transducer and then shut down the equipment.

Transient conditions such as equipment startup and ac power interruptions can produce erroneous signals from the control circuit that result in excessive current through the power semiconductor devices. To prevent damage, it is usually necessary to incorporate circuits that disable the integrator and control logic outputs

2. Fire away. Hard-firing circuit (a) for a thyristor delivers 3 to 5 times the rated gate current to the device in less than a microsecond. Soft firing (b) is used with the new amplifying or dynamic gate devices. It delivers 1½ to 2 times the rated gate current.

during energization of the equipment and keep them disabled until the internal voltage levels are stabilized.

The circuits must then slowly ramp up the output, thereby limiting the inrush to the load. Quick-acting current-limiting protection also should be provided so that the power current devices can't be overloaded.

Inside the control logic

On response to feedback and operating controls, the control logic supplies switching commands to the firing circuits and to the power semiconductor devices. The logic must be designed to handle all possible states of equipment operation and must be coordinated with the load characteristics.

Generally, equipment using phase control circuits, such as ac voltage controllers and rectifiers, require fairly simple logic because the thyristors are line commutated. The frequency of operation is relatively low, and so when the ac current reverses, it supplies a reverse bias that lasts long enough to turn the thyristors off. But forced commutated equipment, such as choppers and inverters, operate at high frequencies and need an external circuit to supply the reverse bias. In addition, extra care must be taken to prevent commutation failure at the power circuit limits.

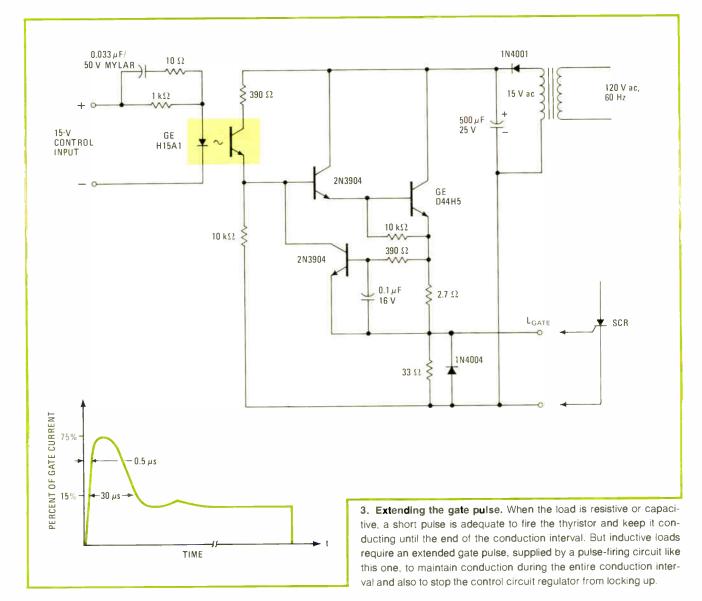
Not only must the logic components be selected to perform properly in noisy environments, but they also must include the necessary internal coordination to prevent erroneous logic states and output conditions. The actual organization of the logic and the selection of components will depend upon the type of power semiconductor circuits, the control complexity and the input/output interface requirements.

Simple control systems, such as the ac voltage controller of Fig. 1, typically use analog circuits for most functions. The analog functions in this circuit can be built with op amps and discrete components. The output logic is best handled by HTTL circuits, which have a high output drive suitable for operating the firing circuits.

More complex systems rely heavily on digital circuits. For example, where a phase-locked loop is used to regulate motor speed, a read-only memory and an adder typically will generate the switching patterns necessary for pulse-width modulation of the ac drive inverter. The control logic for cycloconverters, the most complex ac drive system, may even use a small microprocessor-based system.

For all complex systems, the designer must determine the optimum tradeoff among component cost, availability, noise sensitivity, circuit isolation requirements, and the output drive capability.

Usually, C-MOS logic proves to be the best choice for most digital signal processing or pattern-generation applications. But HTTL is better as a buffer between control-logic inputs and outputs requiring high drive capability. In addition, optical couplers can provide ohmic



isolation for the inputs and the firing circuit outputs.

With this component mix. the system receives maximum noise immunity on the control inputs and outputs, plus protection against C-MOS failures during testing due to parasitic latch-up. The mix also provides simple interfaces to the input control and output circuits that require high drive currents. It offers the simplest and most cost-effective implementation of the complex logic functions, eliminates ground loops, and permits simple signal interfacing between different dc levels.

Taking care of abnormal signals

To prevent their failure, power semiconductors must receive the proper control signals, no matter what the operating conditions.

Common problems in which control-signal abnormalities can produce power-device failure are: equipment startup and shutdown, supply line interruptions, overloads, and voltage dissymmetry of the power circuit. Proper design can avoid all of these problems.

With equipment startup and shutdown, all power device control states must be maintained in the off state

until voltage levels stabilize. Also, power circuit sequencing must be finished before the actual startup of the power circuit or shutdown of the equipment.

Sequencing is usually required before startup and shutdown for most inverter and chopper circuits. The startup sequence includes a series of commands that charge the appropriate commutation capacitors so that the first thyristors to conduct are the first to be turned off. In the shutdown sequence, all power devices are returned to the off state before deenergization of the power. If any device continues conducting, a fault can develop from the energy left in the power supply capacitor bank.

With supply-line interruptions, the control logic must protect the equipment as long as the interruption lasts. For example, in ac voltage controllers, loss of line synchronization followed by an inrush of current to the transformer or load can cause the power semiconductor to fail. So all firing commands must be locked out until line synchronization is reestablished, and then load power must be returned in a controlled manner.

Inverters present a more difficult problem. After a

supply-line interruption, all conducting thyristors must be switched off and all subsequent firing commands inhibited before the dc supply voltage can drop below the level necessary for commutation.

During overloads, ac voltage controllers and phasecontrolled rectifiers are protected by rapidly reducing the time into the cycle at which the thyristor is being fired (that is, the firing angle). Under fault conditions, all firing commands must be inhibited to limit the fault duration to half a cycle.

Limiting overloads and faults in forced commutated inverters and choppers is more difficult. The problem must be sensed and cleared before the current exceeds the capability of the commutating circuit. When a high rate of rise in output current indicates a fault, all conducting devices are switched off as soon as practical.

Voltage dissymmetry is a problem when transformers and motors are driven with power semiconductors. If it becomes significant, dissymmetry will produce a devoltage component that will saturate the magnetic circuit and thereby overload the power-semiconductor circuit.

To avoid dissymmetry in ac equipment, control signal processing—phase delay, pulse width modulation, pattern generation, and the like—must be symmetric. This requires a common circuit or subsystem for both the positive and negative portions of the ac wave. Also the circuit must have the appropriate logic for controlling the individual power devices. This approach prevents dissymmetry regardless of component tolerance variations, timing differences, and drift during the life of the equipment. It also is economical.

Two kinds of firing up

The firing circuit must provide a gate signal appropriate for the thyristor type and the power circuit. It usually must provide electrical isolation between its control circuit and the thyristor gate terminal. Depending on the thyristor, the gate signal will be hard or soft firing. A hard-firing circuit will supply a peak current equal to 4 to 6 times the rated gate current, while a soft-firing circuit supplies a peak current equal to $1\frac{1}{2}$ to 2 times the rated gate current. Figure 2 shows the two types of firing pulses.

Hard firing is required for the older, center-fire thyristors in order to achieve their di/dt rating. It also insures uniform conduction by the entire device during the initial conduction interval.

Soft firing is used with centerfire thyristors where di/dt is not critical and with the newer amplifying or dynamic gate devices that don't require hard firing to achieve the rated di/dt.

The thyristor circuit and its load determine the time during which the gate signal must be applied. For resistive and capacitive loads, a short pulse fires the device. The thyristor current starts almost immediately and continues until the end of the conduction interval.

For an inductive load, an extended gate pulse (Fig. 3) is necessary to insure that conduction occurs for the required interval and to prevent the control circuit regulator from losing control due to the lagging load power factor. An extended gate-pulse network provides hard



4. Medium-power rectifiers. Typical power semiconductor components are these rectifiers manufactured by Westinghouse Electric Corp., which are available in ratings of 3-70 A continuous current, 50-1,000 V blocking voltage, and 25-1,200 A surge current.

firing followed by a reduced gate current that continues for the duration of the selected interval.

Circuit-induced firing failures

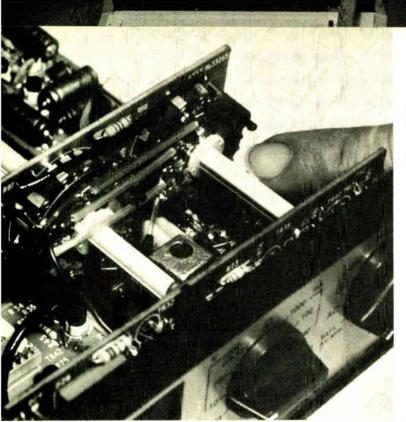
An improperly designed firing circuit can degrade reliability in many ways. If it is near the power circuit, it can become very susceptible to noise. Therefore it must be adequately shielded, bypassed, and grounded, as well as being designed as immune to noise as possible.

When the firing circuit provides de isolation between the control and power circuits, the switching action of the power circuit produces a high dv/dt condition between the input and output terminals of the firing circuit. The high dv/dt can produce erroneous gate pulses by acting on the interwinding capacitance of the gatepulse transformer or the feed-through capacitance of an optical coupler (including a printed circuit board).

To minimize the effect, electrostatic shields may be required on gate-pulse transformers. Optical couplers should have a moderate transfer ratio and be immune to capacitance feed-through effects.

Unless the thyristor di/dt capability matches the di/dt imposed by the power circuit, the thyristor may fail. An inadequate gate drive reduces the di/dt capability of the thyristor because conduction is initially limited to a small portion of the thyristor chip. To achieve rated di/dt, the gate drive must be adequate.

Where high reliability is required in severe environments, magnetic-amplifier firing circuits can be used for phase control applications. The amplifier control windings provide ohmic isolation and adequate gain for most regulator and compensation requirements.



2. Shaft shocks. Front-panel knobs, which often fall off instruments or may be pulled off, are inadequate insulation against shock hazards. This Ballantine digital multimeter has control shafts of plastic instead of metal to provide extra protection.

and other localities that now require a demonstration of a product's safety before it can be sold within their boundaries.

But there is more to providing safety than simply gaining UL approval. "That's just getting a passing grade," observes David Nurse, president of Heath Co., Benton Harbor, Mich. Suppliers must do everything possible to protect users against fire, shock, and casualty hazards, says Henry Littlejohn, product-safety engineer at GenRad Inc., Concord, Mass., and that requires a corporate commitment to safety.

Most major instrument suppliers have set up procedures to be followed in the design of each new product so that it will achieve a high level of safety. But to help instrument designers become more aware of the importance of safety, corporate managers have expressed their interest in adding safety as an independent design parameter. Such direction "pretty well has to come from the top," says Marshall Johnson, corporate safety officer at Hewlett-Packard Co., Palo Alto. Calif. "It has to filter down from a corporate objective," Ballantine's Katzmann adds.

Once this objective is set, "it's important to maintain a good, close, continuous contact with the project engineer," says LFE's Gubisch. Then, safety can be incorporated into designs at the earliest stages—and at the lowest cost.

Once a product is in the field, says Richard Nute, manager of product safety engineering at Tektronix, Inc., Beaverton, Ore., "there's a certain amount of responsibility on the user," both to use the product safely and to consider safety in product selection. Says Donald Mader, engineering group leader in the Electrical department at UL in Melville, N.Y., "today's test-equip-

ment designers and users seem to carefully weigh the many and varied performance specifications of a particular instrument prior to designing or purchasing it. But safety, one of the most important aspects of an instrument, is frequently ignored or taken for granted. Yet if the instrument user takes safety for granted, who will watch out for him?"

The instrument buyer has to perform that function. There are specific, safety-related design features he can look for when evaluating an instrument. Most of them may seem pretty obvious, but almost every instrument now in use lacks at least one.

For example, most instruments today are strong and rigid enough not to collapse under normal operating conditions. But are they all strong enough to withstand foreseeable abuse? In evaluating an instrument, especially one with a plastic cabinet or intended for field service, the engineer should check that the instrument can withstand dropping, connection to high voltages, and exposure to high temperatures or humidity without failing in a way that creates hazards.

Within the instrument case, printed-circuit boards may downgrade its safety unless really firmly mounted. Otherwise they may flex enough to cause a short circuit or mechanical failure when subject to abuse or even normal use, including the plugging in or unplugging of components.

Isolating live parts

An instrument should also be constructed so that no conductors carrying potentially hazardous voltages are accessible to the user while he or she is operating or servicing the unit in the normal course of events. But it isn't always as simple as it seems to provide this kind of protection.

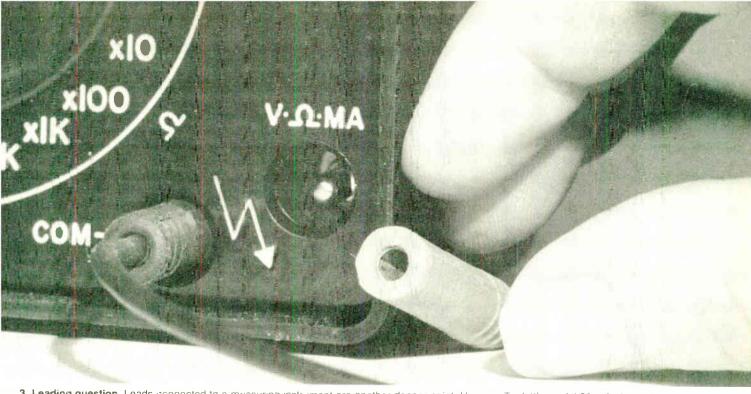
For example, the controls or switches on many instruments have metal shafts. Usually, the plastic knob mounted on the shaft provides sufficient insulation. But slip-on, friction-fit knobs can be removed, accidentally or on purpose, exposing a conductive shaft that passes through a panel to the inside of the instrument, where it may come in contact with current-carrying components.

One way to prevent contact with a potentially live control shaft, used by Tektronix in its model 213 digital multimeter/oscilloscope, is to cover the shaft with a second layer of insulation—a kind of plastic cap that is held captive inside the instrument by the control panel. An alternative preferred by many instrument makers is to use an insulating plastic material for the control shaft itself (Fig. 2).

Nor should electrically live parts be accessible during normal user servicing of the instrument—changing paper in a printer, for example (Fig. 1), or replacing batteries. In evaluating an instrument, the engineer should remember to check out such activities, since they may be performed by a technically unsophisticated user.

Dangerous connections

A hazardous location commonly found on the front panel of test instruments is the input or output connector. Signal sources and voltage or current standards, in particular, are often capable of producing voltages and



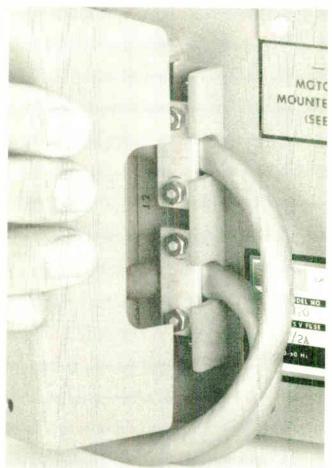
3. Leading question. Leads connected to a measuring instrument are another danger point. Here, on Triplett's model 60 volt-ohmmeter, a recessed imale plug mates with an insulated female connector on the test lead, protecting the user even if the lead pulls out of the instrument.

currents at this spot that are high enough to shock an unlucky user.

The connector used must therefore be designed so that the operator cannot contact current-carrying parts. This may sound trivial, but many common connector types—including BNCs—do not meet this requirement. The usual way to provide connector insulation is to use banana jacks or five-way binding posts in place of BNC connectors.

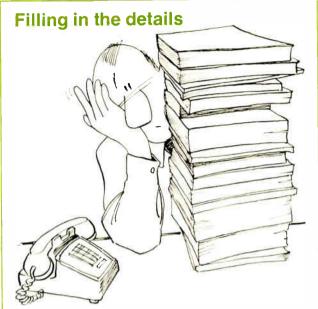
This works well for signal sources that do not require the impedance-matching capability offered by BNC-type connectors, but has a major failing when applied to instruments for measuring, as opposed to generating, signals. In a volt-ohmmeter or digital multimeter, for example, there is little chance of a high voltage appearing at the front-panel connectors. But when the instrument is connected to a circuit under test, high voltages may appear on the leads and mating connector plugged into the unit. If, as often happens, the mating plug pulls out of the meter, a hazardous level of energy may appear on the plug. This is especially dangerous when banana plugs are used because of the relatively large uninsulated surface that then becomes accessible.

There is as yet no widely used method of solving this problem. Locking-type connectors, which would prevent mating units from being disengaged, are generally too expensive and cumbersome to be used in relatively simple and low-cost testers like VOMs. However, an approach adopted by Triplett Corp. in its model 60 VOM has much to recommend it. A banana plug is recessed in the front panel of the instrument, and a mating banana jack is attached to the instrument end of the test lead (Fig. 3). If the jack pulls out of the instrument's front panel, a large rubber boot protects the user from any possible electric shock. While no other manufacturer as yet uses this configuration, its low cost and simplicity



4. Relieved. All wires or cables entering an instrument should have some form of strain relief and push-in relief as shown here, to prevent the cable from transmitting external forces to the internal wiring.

World Radio History



In-depth criteria for designing or choosing safer instruments—or just about any other product—are available in the form of standards published by American National Standards Institute Inc. (1430 Broadway, New York, N.Y. 10018) and Underwriters Laboratories Inc. (Publication Stock, 333 Pfingsten Rd., Northbrook, III. 60062).

Besides issuing ANSI C39.5-1974, which covers electrical and electronic measuring and controlling instrumentation, and ANSI C1-1975, the National Electrical Code, ANSI is the source for international standards from the International Organization for Standardization (ISO) and the International Electrotechnical Commission (IEC). All are listed in ANSI's free catalog. The institute will also order any national standards from foreign countries and even carries some of them in stock.

The proposed instrument safety standard, UL 1244, is only one of hundreds of publications available from UL. UL has also proposed standards in related areas—laboratory equipment, UL 1262, and power supplies, UL 1012. Published standards include printed-circuit boards, UL 796, and plastics flammability, UL 94. The UL catalog also lists product directories to parts and equipment that have met UL requirements.

may soon lead to its adoption as an industry standard.

The instrument buyer should also scrutinize the other end of common test leads—the probe end—for safety. The common straight-shaft, needle-point probe supplied with most voltmeters has a dangerously sharp tip, but this cannot be avoided if the probe is to function properly. However, the instrument maker should provide such a probe with some sort of cap, to prevent injury when the unit is not in use.

Straight-shaft probes also present a shock hazard. It is quite common for a user's hand to slide down the shaft of such a probe and come into contact with the energized probe tip or the circuitry under test. A simple fix for this condition is a shield, or some device like the raised ring often found on soldering irons, to restrain a user's hand from inadvertent contact with the probe tip.

The cables and connectors often used to tie instruments together into systems may also create hazards. As before, no high voltages should be accessible to the user on an interface connector or at the unconnected end of an interface cable. In addition, cables should be tied to the instrument case through some form of strain relief, to prevent forces on them from being transmitted into the instrument where they could damage internal wiring or terminals (Fig. 4). What's more, interface connectors should be designed so that plugging a cable into the wrong connector or misaligning it in the right connector will hurt neither instrument nor user.

Line-cord precautions

Such considerations are even more important where line voltages appear. The power cord itself should have a large enough capacity to handle any foreseeable load from the equipment, including accessories that may be connected to convenience outlets. Moreover, it should be mechanically restrained from being pulled out of or pushed into the instrument case. And, to prevent one wire from being mistaken for another, no low-voltage cable should be made to pass through the same bushing as the line cord.

The plug connected to the power cord should be the proper type for the maximum current and rated voltage of the instrument or, where it can operate at more than one voltage, for the voltage set at the factory prior to shipment. In addition, the plug should conform to any accessory outlets on the instrument if these outlets are either two-wire polarized or three-wire grounded types. In this way, the user will not be deluded into believing that, say, an accessory is grounded when plugged into a three-wire outlet if, in fact, the instrument itself has no provision for grounding.

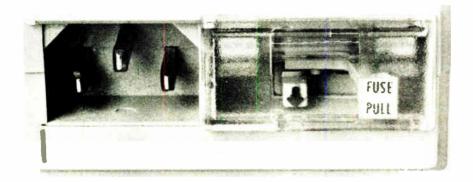
The design of accessory outlets, too, should prevent a user from coming into accidental contact with a high voltage. They should be mounted so that there is enough clearance around them to permit full insertion of standard plugs. Of course, the connectors used for accessory high-voltage outlets should never be of the same type as those used for low-level signals.

A life-and-death matter

It could literally be fatal if the engineer failed to check out the grounding scheme of an instrument he is considering purchasing. And the first thing he should look at is any connection to earth ground such as a three-wire line cord. This cord should tie every accessible conductive part in the instrument to ground, unless the part is well insulated from other conductive parts and cannot become live as a result of a component failure. If the means for grounding the equipment is through the line cord, the ground contact should be made first and broken last whenever the plug is inserted into or pulled from an outlet.

To maintain the grounding integrity of a supply system, any switch, fuse or circuit breaker that can interrupt the ground lead between the instrument and earth must also simultaneously interrupt all conductors of the supply circuit. And, in a system containing more than one instrument, each unit having a separate supply cord

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5. Orderly approach. This Hewlett-Packard-designed power module has a power connector that conforms to International Electrotechnical Commission standards and also ensures the ground connection is made first. The line voltage selector switch can be actuated only after removal of the power cord.

should have a grounding-type cord, and all units should be tied to a common ground.

Another set of safety requirements is imposed on instruments that employ line-voltage selectors. The switch that selects different supply-voltage inputs should be constructed so that the voltage setting cannot be changed accidentally, and if the setting can be changed by the user, then the action of changing the voltage selector setting should also change the supply-circuit voltage indicator. The switch that is most often used to meet these demands has a recessed actuator that can be moved only with the aid of a tool like a screwdriver, and the supply voltage is printed on the actuator so that the switch housing covers the unselected voltage level.

Another approach to tap switching, developed by Hewlett-Packard Co., Palo Alto, Calif., is called the power module (Fig. 5). The device has been licensed by HP to Corcom Inc., Chicago, for manufacture and sale to other firms.

The power module consists of a power-input connector, a fuseholder, and a voltage selector in a single housing. The construction of the device is such that a user must remove the line cord before he can change the fuse or the voltage selector. In turn, he must remove the fuse before he can change the voltage selector, as a reminder to him to check for the proper fuse rating for the new voltage setting. In addition, changing the voltage setting permits the proper voltage indication to show through a window, while masking all others.

Care in labeling

Aside from the proper voltage setting, an instrument should have a great deal of other information marked on it. The name of the manufacturer or other organization responsible for the equipment, along with a catalog number or other description, should be plainly visible. This, plus a clearly marked month and year of manufacture—even if in code—helps a technician make repairs or adjustments safely.

Complete operating instructions, a reference to the proper test leads if a set is not provided, and a statement about the proper battery charger or separate power supply to use, if any, should be marked on the



6. Red light. Wherever hazards exist, an instrument manufacturer is responsible for giving customers clear warning. Test instruments should be marked like this Keithley current source to remind the user that output terminals can produce dangerous voltages and currents.

equipment or in a booklet provided with the instrument. The correct fuses and other user-replaceable parts should also be clearly listed.

If any risk of fire, shock, or casualty exists in the use of the instrument, clear warnings to that effect should become visible whenever the hazard is present (Fig. 6). These warnings, even in combination with all of the other safety features discussed, may not make an instrument perfectly safe, but will markedly reduce its chances of injuring anyone.

Engineer's notebook

Ribbon cable makes coils for printed-circuit boards

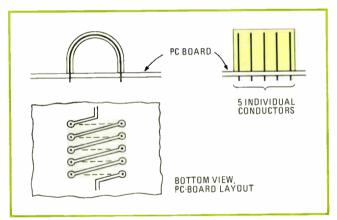
by Jim Edrington and F.E. Hinkle Jr.

Applied Research Laboratories, University of Texas at Austin

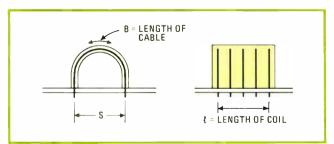
A microhenry inductor loop made from flexible ribbon cable is easy to mount on printed-circuit boards, and its inductance value is easy to control. This loop is a cross between the familiar wire-coil inductor and a printed-circuit inductor. Since the ribbon-cable loop is three-dimensional, a larger amount of inductance is possible for a given area than a printed-circuit coil can provide. Moreover, the ribbon inductor is much easier to manufacture and control than the usual wire-coil inductor.

Figure 1 illustrates the technique of bending the flexible cable into a semicircle and soldering the ends into a pc board so that the conductors interconnect to form a multiturn coil with a "D" cross section.

The inductance of a "D" cross-section coil may be calculated from the formula for a circular coil, with the



1. Loops good. Flexible ribbon cable is mounted on printed-circuit board, with individual conductors cross-connected in series, to form an inductance coil. One loop can be nested inside another to form a transformer, and individual turns of the loop can be tapped. These coils have the amounts of inductance needed for rf tuning.



2. Sizing it up. Definitions of dimensions used in text are illustrated. A given ribbon has a fixed number of conductors per inch of ribbon width (coil length), so n and I are equivalent quantities.

effective radius of the "D" coil substituted for the actual radius of a circle. For a single-layer n-turn circular coil with radius a and length l, the inductance in microhenries is

$$L = n^2 a^2 / (9a + 10l)$$

where a and I are in inches.

For a "D" coil with the dimensions shown in Fig. 2, the effective circumference, C, is the length of the ribbon cable, B, plus the pc-board spacing, S. The effective radius can be calculated from

$$r_{\rm eff} = C/2\pi = (B+S)/2\pi$$

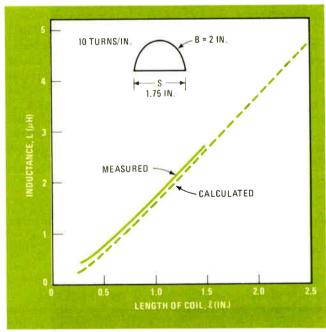
Thus, the inductance in microhenries of the coil made with ribbon cable is

$$L = n^2[(B+S)/2\pi]^2/[9(B+S)/2\pi + 10l]$$

where all dimensions are in inches.

The measured and calculated inductances of an actual "D" coil are plotted versus the length (1) of the coil in Fig. 3. The coil for this test was made of a 2-inch length of Ansley Flexstrip with a pc-board spacing of 1.75 in. With 10 conductive strips per inch, the Flexstrip forms a coil with 10 turns per inch. The calculated values of inductance proved to be close to the measured values over a range of coil lengths from 0.5 to 3.0 in. (n = 5 to n = 30).

Since flexible cable of this type can be purchased in precut sizes, the manufacture of highly repeatable inductors can be simplified. An impedance transformer can be made by adding a second coil inside the first. A



3. Microhenries. Inductance of ribbon-cable inductor is shown as a function of coil length, I. Since ribbon has 10 conductors per inch of width (coil length), abscissa represents 0 to 25 turns.

multiple tapped inductor is easily formed by bringing out any of the turns on the printed-circuit board. Since every turn is terminated on the board, other electrical components may be wired with the coil. For example, capacitors may be paralleled with the coil to form tuned rf circuits.

Microprocessor converts pot position to digits

by John M. Schulein
Aeronutronic Ford Corp., Palo Alto, Calif.

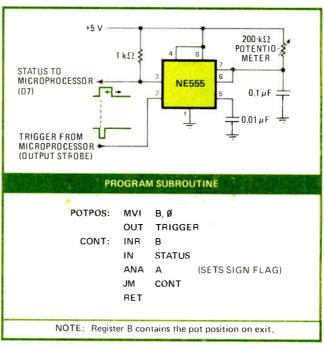
A few bytes of program in an 8008/8080 microprocessor, plus a 555 integrated-circuit timer, can convert the position of a potentiometer into a digital value. The arrangement is both economical and convenient when the position data is an input to a system already using the microprocessor, such as an industrial control system or a video game.

As the figure shows, a strobe pulse from the microprocessor triggers a 555 connected as a one-shot multivibrator. The output from the 555 stays high for a period of time that is proportional to the resistance of the pot. To measure this time period, the processor increments an internal register for as long as its input (D7) from the 555 remains high.

When data on the pot position is required, the microprocessor program calls up the POTPOS subroutine, which uses four flags, the accumulator, and the B register. In this subroutine, as the table shows, the processor:

- 1. Sets register B to 0.
- 2. Triggers the 555.
- 3. Increments register B.
- 4. Inputs the status of the 555 to bit D7 of the accumulator.
- 5. Sets a sign flag minus if status is high.
- 6. Jumps back to step 3 if flag is minus.
- 7. Returns to main program if flag is not minus.

Upon return to the main program, register B contains a number that measures the 555 output pulse duration and hence is a digital representation of the pot position.



Where is the pot? Potentiometer position is digitized by one-shot multivibrator and subroutine for the 8008/8080 microprocessors. When program calls subroutine, processor triggers one-shot and measures output pulse duration (which is proportional to resistance of pot). Register B stores this value for use in computation of next step in a TV game, process control, etc.

When the hardware and software are used on an 8008 system with a 2.5-microsecond clock, the B register digital output varies from 2 to 65 Hex, i.e., has 100 different values, as the potentiometer is varied across its range. The values of the pot and the timing capacitor can be modified to suit the speed of the processor and the desired range of the digitized output.

Op amp complements a-d converter output code

by Eugene L. Zuch
Datel Systems Inc., Canton, Mass.

In many cases where an analog-to-digital converter has data outputs that are complementary-coded (highest level represented by 00 . . . 0), the interfacing requires uncomplemented coding (highest level represented by 11 . . . 1), and vice versa. Conversion from one form to the other can of course be done with logic inverters, but

if the input analog signal is bipolar, a single operational amplifier can perform all of the inversions.

The approach also works with minicomputers, some of which require complementary input coding while others require uncomplemented coding.

Uncomplemented coding is shown in column 3 of the table, which illustrates the offset binary coding of a ± 5 -volt bipolar analog signal. A 12-bit a-d converter is assumed here, so the least significant bit is $10 \text{ V} \div 4,096$. or 0.0024 V. In this code the all-0s level (000000000000) corresponds to an analog value of minus full scale, or -5 V; the all-1s level (1111111111111) corresponds to plus full scale less 1 LSB or +4.9976 V.

In the case of complementary coding, shown in column 1 of the table, the reverse is true. All-0s corre-

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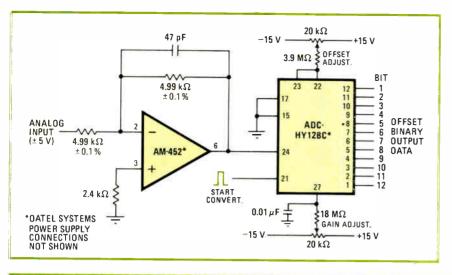
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Exchanging complements. Inversion of bipolar analog signal by op amp, prior to analog-to-digital conversion, yields uncomplemented offset binary coding of output data. This technique is useful in interfacing hybrid data converters to microprocessors, minicomputers, and the like.

Scale	CODING TABLE Column 1		Column 2	Column 3	
	Input voltage (± 5 V full scale)	Complementary offset binary coding	Complementary offset binary with inverted analog	Offset binary coding	
+ Full scale + Full scale - 1 LSB + 1 LSB 0 - Full scale + 1 LSB - Full scale	+ 5.0000 V + 4.9976 V + 0.0024 V 0.0000 V - 4.9976 V - 5.0000 V	0000 0000 0000 0111 1111 1110 0111 1111 1111 1111 1111 1119 1111 1111	1111 1111 1111 1111 1111 1110 1000 0000 0000 0111 1111 1111 0000 0000 0000	1111 1111 1111 1000 0000 0001 1000 0000 0000 0000 0000 0001	

sponds to +4.9976 v while all-1s corresponds to -5 v. Thus the codes in column 1 and 3 are simply the logic complements of one another.

The obvious way to go from one code to the other is to add a logic inverter to each output line. This is relatively inexpensive, requiring only two hex inverters for 12 output data lines, but it may often be quite inconvenient because 12 data lines must be connected to 12 dual in-line pins on a circuit board. And if the data output from the converter is tri-state, two strobe connections must be handled in addition to the data lines.

For bipolar operation, the inverting-op-amp method requires connection to only one pin of the a-d converter, the analog input. The gain of the inverting amplifier must be set close to -1, although small variations can be taken care of by the a-d converter gain adjustment. The circuit of Fig. 1 shows a fast integrated-circuit op amp connected to the input of a fast hybrid 12-bit a-d converter. After the amplifier is connected, the converter is calibrated for offset and gain, as is normally done for the coding of a bipolar signal (i.e., offset binary coding). Because the ADC-HY12BC converter normally has complementary offset binary coding, use of the inverting amplifier in this case results in offset binary coding.

The table shows how the coding transformation works. Starting with the complementary offset binary coding in column 1, inversion of the analog input in effect rotates the coding about the analog 0 axis, as shown in column 2. Now, column 2 is almost identical with column 3, except that column 2 is 1 LSB higher in analog value than column 3. In other words, 00 . . . 0 in col-

umn 2 corresponds to minus full scale plus 1 LSB, while 00...0 in column 3 corresponds to minus full scale. But this difference in analog value is easily taken care of by the offset adjustment of the a-d converter.

The technique works in both directions, from offset binary to complementary offset binary or the reverse. It also works between two's-complement coding, in which the most significant bit is complemented from its value in offset binary coding, and complementary two's-complement coding. (Most newer modular or hybrid a-d converters have an output that is the complement of the MSB output for use in two's complement output coding.)

In applications of the op-amp inversion technique, the settling time of the inverting amplifier must be added to the conversion time of the a-d converter. In the circuit shown, the settling time of the AM-452 amplifier is 3 microseconds to 0.01%. Added to the 8- μ s conversion time of the ADC-HY12BC, this gives a total conversion time of 11 μ s.

The conversion technique is useful in interfacing a-d converters to microprocessors, minicomputers, or other digital-input devices. Most of the earlier modular a-d converters used uncomplemented coding, but many of the newer converters, such as the new low-priced hybrid units, use complementary coding. The change derives from the popularity of the quad current switch technique and the use of a monolithic successive-approximation register inside the converters.

Engineer's Notebook is a regular feature in Electronics. We invite readers to submit original design shortcuts, calculation aids, measurement and test techniques, and other ideas for saving engineering time or cost. We'll pay \$50 for each item published.

Engineer's newsletter

dazzling

possibilities . . .

Designers foresee No sooner was the trend to single-chip microcontrollers spotted here last month than digital-system designers began writing in to say why they can't wait to get hold of them. Remember, these single-chip controllers are complete little computers. Unlike the more general-purpose microprocessors, they contain just enough logic, memory and input/output ports to handle a limited number of control functions by themselves, both in consumer products like appliances and in some industrial gear like gas pumps.

> But the real importance of the low-cost microcontroller, at least according to the designers we've been hearing from, may be in a totally different type of application—as peripheral controllers in large microprocessor-based systems, providing cheap and flexible local processing while taking the load off the central processor.

. . . in combining microcontrollers and microprocessors

A point-of-sale system is the perfect example of how a special-purpose microcontroller would work with a general-purpose microprocessor. The microprocessor would perform the main processing functions—the central bookkeeping, inventory, pricing, merchandise management, and so on—while a host of microcontrollers would handle the peripheral processing right at the various terminals—verifying credit cards, and handling price recognition and cash-register transactions. Not only is a great load removed from the central microprocessor, allowing it to operate faster and more efficiently, but now a system can be updated by simply changing a controller program instead of having to tamper with the more complex central program.

A penny-wise guide to IC design

At less than 3 cents a design, the best IC design aid may be a new little book called "101 Analog IC Designs," by the staff of Interdesign Inc., Sunnyvale, Calif., an IC house headed by Hans R. Camenzind. The \$3 book gives circuit diagrams for amplifiers, oscillators, phase-locked loops, timers, voltage regulators, comparators, and so on. It's aimed primarily at potential users of Interdesign's Monochip ICs, but would be useful to anyone considering setting up a small custom-IC development program.

have enough drive?

Does your SCR Firing a silicon controlled rectifier doesn't depend only on the voltage and current levels of the trigger pulse—the pulse width matters, too, points out J.D. Balenovich, who is with the reliability, quality, and applications engineering group of Westinghouse Power Semiconductor division, Youngwood, Pa. In high-frequency or high-current applications, when the trigger pulse is at most 20 microseconds wide, the gate drive amplitude must be increased possibly three to five times over its nominal value. Generally, the gate trigger current required at a junction temperature of -40°C is around twice that needed at 25°C, whereas at 125°C it is about half the 25°C level. So, before you assume that your gate drive is ample, make sure you're aware of all the nominal firing conditions, including the junction temperature that corresponds to the holding current. —Laurence Altman

NI 2001 Programmable Calculating Oscilloscope



Requires No Special Programming Language — or Experience.

It's no surprise that industrial and scientific users of test and measurement instrumentation are acclaiming the NI 2001. More than just a better instrument for the acquisition, processing and manipulation of electrical data, its innovations make it an entirely new generation of instrumentation. It ushers in a new era of ease and accuracy in analyzing signal data because it combines all the capability of a digital oscilloscope and microprocessor in a single mainframe. It eliminates interfacing requirements, computer instructions and requires no programming experience.

Only a demonstration can prove how unbelievably versatile the NI 2001 really is. No other instrument gives you the freedom and convenience of programmability. With the exception of time base, all front panel controls for mode and display, as well as the entire 81-key interactive keyboard can be programmed. The 11 keys of the keyboard's PROGRAMMING section, shown here, let you automate complex signal analysis. You merely press the PROGRAM key, perform the desired sequence once and press the END key. The NI 2001 will then repeat the sequence whenever the RUN key is pushed. You can store up to 200 instructions with complete editing freedom for additions or corrections. You can pause and review during any



data sequence or perform an operation a step at a time, editing as you go. And, as special-purpose plug-ins become available. like FFT for instance, you can include them in your programs too.

The programmability of the NI 2001 combined with its capability for conditional branching further emphasizes its versatility. Predetermined conditions can be programmed, which when met will allow the NI 2001 to perform decision-making functions. And with options, it can output a control pulse to extend its application as a controller for an experiment or ongoing process.

Programming is just one of a host of features you'll find only in the NI 2001. Get complete details and discover how you can make waveform analysis easier than you dreamed possible. Send the reader service card or write Norland Instruments Dept. E-1.



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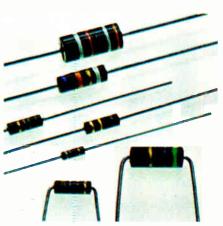
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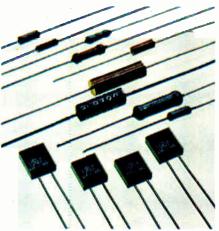
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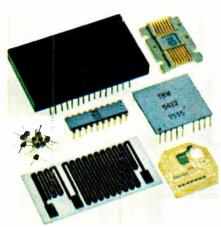
Billions used in consumer, industrial, military applications. TRW/IRC carbon comp. capability ranges from standard commercial types through established reliability RCR's, to ultra-high range (1011 ohms). Where you're using carbon comp.'s in automated assembly, TRW/IRC packaging options can help cut the cost of interfacing with your machines. Card packs, lead tape reels, cut and formed leads...we'll be glad to explore the potential economies with you.



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Metal Glaze

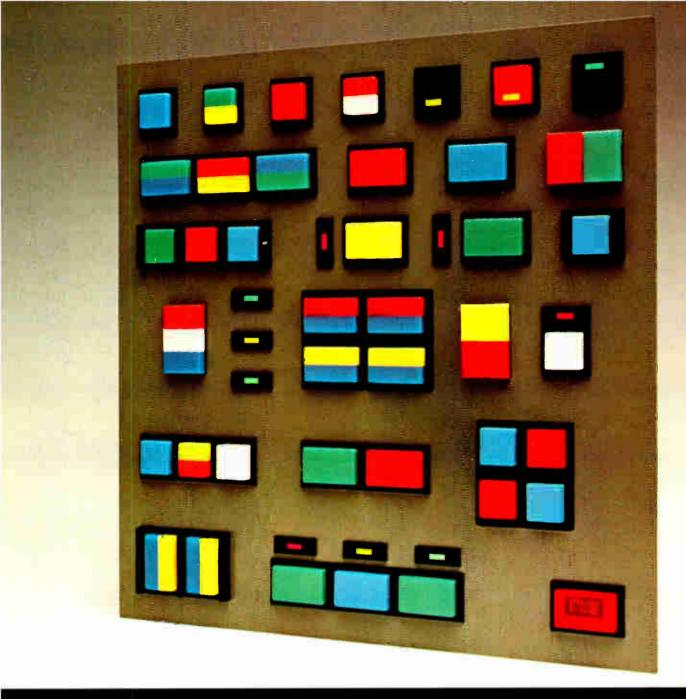
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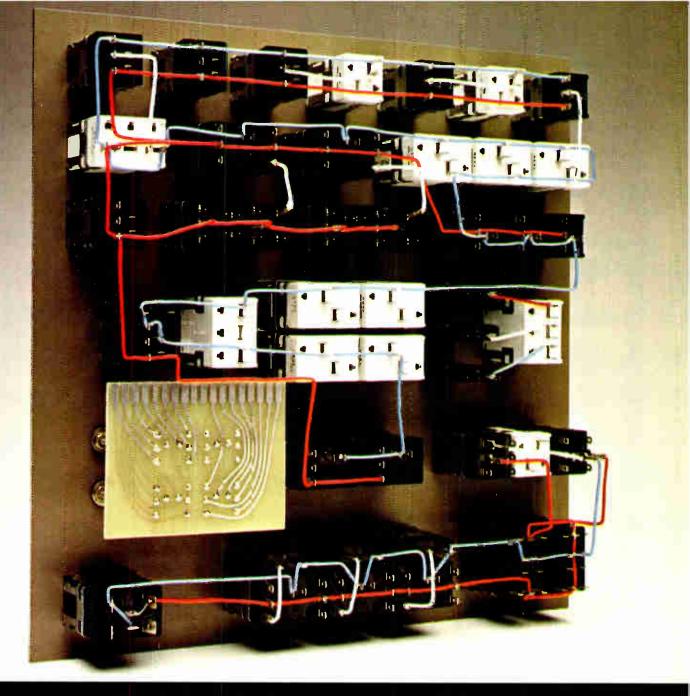
LED

The AML units have been designed to look good to electrical engineers, too.

Particularly in flexibility. Three different electrically rated switches in the same size

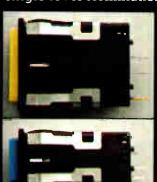
housing. You can choose solid state pushbuttons that operate at 5V or 6-16V with a built-in regulator, sink (TTL) and source (CMOS). Electronic control from logic switching to 3 amps, 120 VAC. And power control up to 10 amps at 120 VAC.

All AML units have been designed to offer the same shallow depth, to provide a unique



just as good when it's time to wire it.

single level termination. Which means easier



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But we believe there's more to building better panels than just offering better pushbuttons

and indicators. That's why we have MICRO



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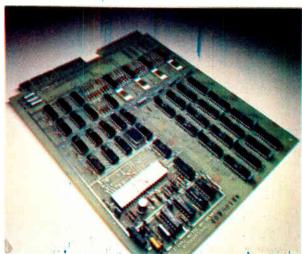
Electronics/March 4, 1976

Data General coup: chip to chassis

by Lawrence Curran, Boston bureau manager



Minicomputer maker markets a chip set, single-board microcomputer, and a system complete with software from the NOVA family



All the way. In addition to a chip set that includes its own microprocessor, Data General is marketing a single-board microcomputer (right) as well as a full-chassis system in its microNOVA line. The system is shown connected to a data-entry console.

Ever since some of the more sophisticated microprocessors have invaded the minicomputer's OEM stronghold, semiconductor makers and minicomputer vendors have been eyeing each other warily. But now for the first time a minicomputer maker has confronted the challenge head on. Data General Corp. has not only designed and built its own semiconductor microprocessor but has also made it the basis for a full family of microcomputers.

Called the microNOVA, the family consists of a chip set, a single-board microcomputer, and a full-chassis system—not to mention the substantial software support that a full 16-bit microcomputer requires and that comes from the company's seven years of experience with more than 20,000 installed Nova systems.

Edson de Castro, president of the Southboro, Mass., company, has been convinced for some time that integration downward was essential if minicomputer makers were to

keep their low-end prices competitive. Data General had to make its own microprocessor, he says, "because we were faced with a choice of doing that or effectively withdrawing from the low end of the OEM business."

The 16-bit microprocessor chip, which measures 225 by 244 mils, is being produced in the company's Sunnyvale, Calif., semiconductor facility. But even though Gardner Hendrie, Data General's manager of systems engineering, savs the

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In the beginning there was the Op Amp. And it was good. Unfortunately, it had high bias current and was slow. So, we created the LM108, with

Super Beta. Super Beta reduced

the high bias current. But it was still slow. So we developed the LM118. A beautiful thing, with feedforward, large BW and slew. Well, that made it faster. But it left us with the bias current problem all over again.

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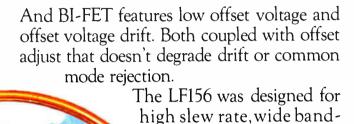
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New products

What's on the chip

Data General had two main objectives in designing its 40-pin, 16-bit CPU: to come up with a single chip that would use the latest MOS technology to implement the Nova instruction set, and do it in a way that made use of the company's experience as a minicomputer builder.

Gardner Hendrie, manager of systems engineering, elaborates on them this way: "We wanted to optimize an MOS process to implement this system, not simply use available TTL or adapt someone else's MOS process."

Accordingly, the chip has a classical register file and makes use of programable logic arrays that are dedicated to run Nova software. The chip is microprogramable with masks. Further, the chip is optimized to work with dynamic RAMs, with their need for refreshing, so all refresh circuitry is built into the CPU chip, as is all the RAM timing circuitry and a real-time clock.

The CPU chip also includes registers housing a hardware stack pointer and frame pointer. The latter has appeared previously only in higher-performance machines such as Data General's Eclipse series and the Hewlett-Packard 3000, Hendrie says.

And even though Data General has yet to earn its reputation as a micro-processor producer, Hendrie is confident the chip is producible in volume because it uses the same silicon-gate n-channel process with which the company has been successfully building its 4-k RAMs in Sunnyvale.

chip uses only loose design rules, the company still has to demonstrate that it can economically build a large, complex chip with good yields (see "What's on the chip," above).

The chip set includes the 16-bit silicon-gate MOS central processing unit in a 40-pin package and either 4,096-bit dynamic RAMS (also made in-house) or 2,048-bit programable read-only memories, or a combination of both, plus associated support chips. For example, there's a bipolar bus-driver chip to drive the heavy loads encountered in the larger memory of the board-level microcomputer. An input-output control chip designed and made by Data General is offered separately.

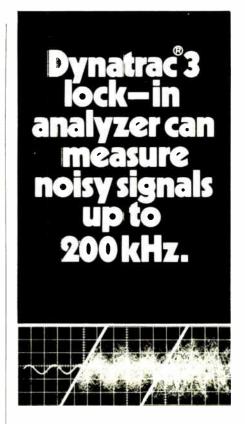
John Scanlon, a marketing specialist, maintains the chip set puts Data General "at the high end of the chip market, and we're bundling it with a lot of software support." The chip set incorporates the full Nova instruction set.

The input-output control chip represents a significant design undertaking. Scanlon says it contains functions that required some 50 TTL chips in the earlier Nova lines, and they're all incorporated in the single chip in a 40-pin package. It includes such functions as: 16-level program interrupt, 64-device program addressability, full busy/done inter-

rupt synchronous logic, data-channel-bus handshaking, full 15-bit count data-address registers, and all power-down and power-up circuitry.

At the board level, Data General is offering a 7½-by-9½-inch microcomputer that Scanlon thinks will compete with General Automation's 110 and 220, Computer Automation's 3/05, and above all Digital Equipment Corp.'s LSI-11. It has the basic chip set, including 4,096 words of memory (16 chips of 4 kilobits each), on-board buffering, and interfaces to printers and CRT displays, plus an interface to Data General's diskette subsystem. Additional RAM storage is available in 4,096- or 8,192-word boards.

The full-chassis system can handle traditional OEM minicomputer functions, or it can serve as a program or interface development system similar to Intel's Intellec or Motorola's Exorciser, but with full 16-bit capability. It's available with 9 or 18 board slots and with an optional calculator-sized hand-held programer's console for remote data entry. A typical boxed chassis will contain 16,384 words of memory, a dual-diskette subsystem, and an asynchronous terminal, such as a teletypewriter. The chassis is expandable to 32,768 words of memory





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June '75 Miniature lamp catalog features 40 pages and 500 data changes for complete 500-lamp line.

Feb. '75 Sub-miniature lamp catalog features 24 pages and 91 changes for more than 210 lamps.

Dec. '74 Glow Lamp catalog features 8 pages and 50 changes for 83 Glow Lamp Indicator and Circuit Component lamps.

For up-to-date technical information on any of these items write: General Electric Company, Miniature Lamp Products Department #3382-M, Nela Park, Cleveland, Ohio 44112.



New products

and, says Scanlon, will include Data General's disk operating system, standard software editors, a macro assembler, Fortran IV compiler and a relocatable loader. "In essence," he says, "the user will have minicomputer software."

Don McDougall, manager of product marketing, points out that a major question voiced by OEMs has been where they can grow with Data General in complexity, either up or down. "This line should answer that clearly," he says. McDougall notes that OEMs are "much concerned" about any company's commitment to all levels of its business and about its breadth of product line. The microNOVA line should erase any doubts about Data General's full-line commitment, he maintains.

The microNOVA family's key specifications include these instruction execution times: load accumulator, store accumulator, each 2.9 microseconds; add, 2.4 μ s; subtract, 2.4 μ s; multiply, 41.3 μ s, and divide, 59.1 μs. The 4-k RAM has an access time of 160 nanoseconds, which Hendrie describes as conservative. Those specifications are slightly faster than the same execution times in the Nova 1200, and company officials stress that the chip set accommodates the full 16-bit multifunction instruction set for any machine in the Nova line.

Field support. Scanlon feels the closest any other company comes to the microNOVA line is Texas Instruments Inc., with its model 990 minicomputers that use a 16-bit TI microprocessor [Electronics, Nov. 13, 1975, p. 36]. But he stresses that Data General has more minicomputers installed, an important factor in field service support and software familiarity.

Scanlon doesn't expect Data General to be slugging it out for chip-set orders with semiconductor microprocessor manufacturers. He thinks most customers for the microNOVA line, including the chip sets, will be current Data General customers.

Data General Corp., Route 9, Southboro, Mass. 01772. Phone James Buchanan at (617) 485-9100 [338]

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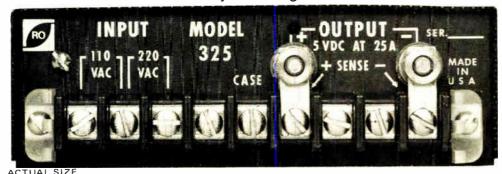
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Logic probe has pulse memory

First in family of digital troubleshooting instruments, priced at \$125, speeds design, testing of systems that have a variety of logic types

by Andy Santoni, Instrumentation Editor

Digital logic systems often combine a variety of logic families on a single board to take advantage of the distinctive features each has to offer. A system may be built with complementary-MOS circuits to minimize power consumption and contain transistor-transistor-logic chips to perform functions requiring higher speed, or it may contain some combination of TTL, C-MOS, and high-threshold logic.

The model 545A logic probe from Hewlett-Packard Co., the first in a series of new digital troubleshooting instruments from the firm [Electronics, Feb. 5, p. 26], can speed testing and designing systems containing most positive logic families: TTL. DTL. RTL. C-MOS. HTL. or MOS. Its price, \$125, is only slightly higher than that of similar instruments that can test only one logic form.

Two modes. The model 545A provides in-circuit functional indications in either of two modes. When the logic-level switch on the instrument's barrel is set to TTL, the logic thresholds are 2.0 volts +0.4, -0.2 v for a logic high and 0.8 v +0.2, -0.3 v for a logic low.

When the switch is set for the C-MOS mode, logic levels depend on the supply voltage: a logic high is greater than 0.7 times the supply voltage and a logic low is less than 0.3 times the supply voltage, and each of these levels has a tolerance of ± 0.5 v dc.

Power supply voltage can fall between 4 and 15 V dc in the TTL mode and between 3 and 18 V dc in the C-MOS mode. The unit is protected against overloads up to ± 25 V dc for one minute, and maximum current drain is 70 milliamperes.



The probe also has an independent built-in pulse memory and display to catch intermittent pulses. When a logic change occurs after the probe tip has been placed on a circuit point and the memory has been reset, the memory circuit retains and displays the pulse until the reset button is again pressed. Use of the pulse memory has no effect on normal operation of the probe.

The single-lamp indicator near the probe tip shows the state of the circuit under test and can be seen from almost any angle. Its interpretation is straightforward. A bright lamp indicates a logic high, a logic low is indicated by a lamp off, and mid-level brightness indicates an open or a bad logic level.

Pulse-stretching is provided so that short, fast pulses are slowed down enough for the operator to see. The minimum input pulse width is 10 nanoseconds when the probe's ground lead is connected to the ground of the system under test,

and typically 20 ns without a ground-lead connection. Maximum pulse repetition rate of the input signal is 80 megahertz in the TTL mode and half that—40 MHz—in the C-MOS mode.

When pulse trains are present at the probe tip, the model 545A's indicator blinks at a constant rate of approximately 10 hertz. This simplifies the detection of a high-repetition-rate signal. If the logic probe were to track the pulse train's level changes, the indication would appear the same as that of a bad level or an open: mid-level brightness.

The probe requires input currents of 15 microamperes or less, source or sink, and has an input capacitance of 15 picofarads or less. Overload protection is provided to ±120 v continuous from dc to 1 kHz and ±250 v for 15 seconds over the same frequency range.

Inquiries Manager, Hewlett-Packard Co., 1501 Page Mill Rd., Palo Alto, Calif. 94304 [339]

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New products

Instruments

Supply has three floating outputs

Low-cost unit combines fixed, variable sources for breadboarding circuits

Every engineer would like to equip his or her own work bench with a couple of power supplies just to avoid the time and trouble involved in searching for the right instrument or checking one out of the stock-



room. A combination of a 5-volt supply for digital circuits and a bipolar supply adjustable up to 20 v for analog circuits would cover most breadboarding and troubleshooting

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The fixed supply provides 5 v ±5% at up to 1.5 amperes. The supply is regulated for less than 2% voltage variation from no load to full load and less than 0.15% output-voltage variation for line voltage changes as large as 10 volts.

The variable supplies are continuously adjustable from 0 to 20 v at up to 500 milliamperes. There is less than 0.1% variation in output voltage from no load to full load and less than 0.2% variation for line voltage changes of 10 v.

The two variable supplies can be made to track each other so that one will follow the other at any specified voltage difference. When the difference is set to zero and the low side of one output is connected to the high side of the other, the level of a bipolar supply can be set on a ganged pair of controls.

Ripple and noise are less than 5 mV rms on any output.

All outputs are protected against short circuits and are current-limited. A switchable front-panel meter can monitor the voltage or current at any of the outputs.

The supply operates from 100 to 135 or 200 to 270 v lines, 50 or 60 Hz. It measures 4½ inches high, 10¾ inches wide, and 9 inches deep. Shipping weight for either the kit or wired version is 12 pounds.

Heath Co., Benton Harbor, Mich. 49022

Synthesizer system has low per-channel price

While many fairly inexpensive frequency synthesizers have come onto the market over the past few years, these precision instruments are still far from cheap. So if an application, such as a frequency-agile system or an advanced frequency-division multiplexer requires many synthesizers, the price is still quite high. One way to reduce the cost of these units is to use a multichannel synthesizer system such as the system 51. Covering the frequency range from dc to 3 megahertz with a constant resolution of 1 hertz, the system 51 consists of a controller and from one to four mainframes, each of which holds up to 12 synthesizer channels. The channels employ a direct synthesis technique which allows them to respond to programing changes

Low-cost (under \$1500) IC testers



Madel 1248 Digital IC Tester.
Devices tested: 14 and 16
pins. TTL, DTL and
CMOS @ 5V. Tests performed: Fixed pattern
functional test.
Remarks: Performs
22º inspections per test
in from 1 to 5 seconds. No
comparison with a "good"
IC is necessary. 4-digit display
gives absolute test results. Can
also be used to check continuity
of resistor network.



Model 1249
Digital IC Tester.
Devices tested:
TTL, DTL @ 5V, HTL @
15V, CMOS @ 5V.
10V, 15V.

Tests performed: Same as 1248. Remarks: Interfaces with voltages for CMOS.

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New products

in only 1 microsecond, but a time delay in the controller increases the system response time to 5 μ s. Each mainframe includes a power supply and a crystal-controlled master clock. Provision is also made for using an external clock such as a cesium-beam standard.

Each channel has three outputs: a sine wave and a complementary pair of TTL-level square waves. The sine wave has a fixed amplitude of I volt rms at a source impedance of 50 ohms. Harmonic components are at least 40 decibels below the fundamental, while nonharmonic components are at least 50 dB down through 2 MHz and 40 dB down from 2 to 3 MHz. Phase noise in a 30-kilohertz band excluding 1 Hz centered on the carrier does not exceed -50 dB from 1 Hz to 2 MHz, and -40 dB from 2 to 3 MHz. The square waves have the same phase-noise specification as the sine wave.

The controller can be operated locally, by means of front-panel switches, or remotely—by a computer, for example—using 25 TTL-compatible lines which are brought out to a rear-panel connector.

Pricing on the system 51 starts at \$27,500 for a 12-channel system. Delivery time is 120 days.

Rockland Systems Corp., 230 West Nyack Rd., West Nyack, N. Y. 10994. Phone Dave Kohn at (914) 623-6666 [353]

Logic analyzer handles up to eight channels

The model 80 logic analyzer is an eight-channel instrument that can be used with an oscilloscope to display the logic and timing relationships of up to eight synchronous or asynchronous signals. Any of its



eight inputs can be used to trigger the analyzer. Features include a glitch-detector mode, an adjustable display rate, and provision for external clock input (up to 5 megahertz). The model 80 sells for \$595 including a set of probes.

Digital Broadcast Systems Inc., Brentwood Lane, Madison, Ala. 35758. Phone (205) 837-2183 [354]

Digital capacitance meter measures up to 199.99 μ F

Able to measure capacitances up to 199.99 microfarads in seven ranges, with a resolution of 0.01 picofarad on the lowest range, the model 475 capacitance meter also measures dissipation factor from 0.0000 to 1.9999 to within an accuracy of 0.0005. Basic capacitance-measuring accuracy is within 0.1%. The instrument makes four-terminal measure-



ments at a test frequency of 1 kilohertz. Provision is made for the application of a dc bias voltage through rear-panel terminals to test diodes or electrolytic and tantalum capacitors. The model 475 is priced at \$2,200.

Electro Scientific Industries Inc., 13900 N. W. Science Park Dr., Portland, Ore. 97229. Phone (503) 646-4141 [355]

Meters measure inductance at 1-MHz test frequency

Two series inductance meters—one analog, the other digital—are noteworthy for their ability to make

15UNS 4K RAMS.

low in volume production.

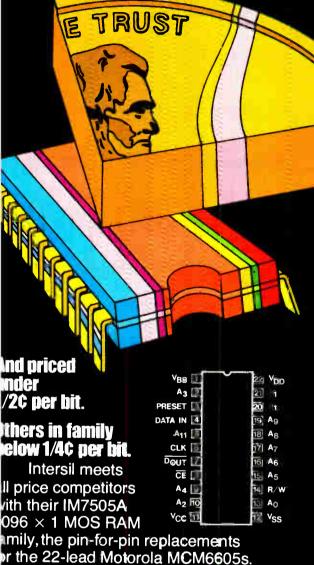
Like to know more?

If your prototype or production system uses 4K MOS RAMs, call your nearest Intersil sales office for data sheets and/or samples. Intersil, 10900 North Tantau Ave., Cupertino, CA 95014

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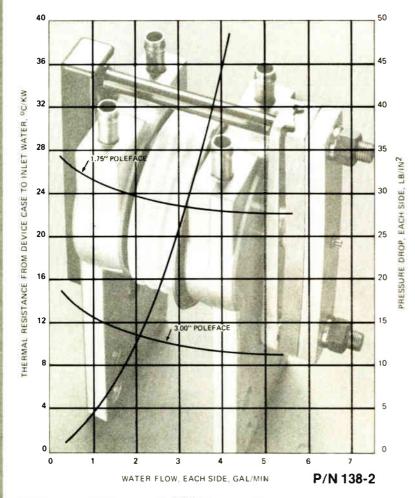
4096 × 1	Max.	Delivery ARO		Price	
MOS RAM	Access	100 pcs 5K/ma		100+	
M7505A	300nS	OTS	2 wks	\$ 9.99	
M7505A-1	150nS	OTS	8 wks	14.99	
M7505A-2	200nS	OTS	4 wks	10.23	

wer in higher volume. Comparable price eductions on the "-1" and "-2" versions

ake this truly the best all-round 4K

AM family you can find.

he IM7505A-1, with its 150nS access peed, is the highest performance dynamic K memory in volume production at any rice. Cost of the 300nS IM7505A is down 1/4¢ per bit in 100+ quantities...even



The Best Way to Cool Compression Type SCR's and Diodes

Wakefield's new Series 138 is a super efficient liquid-cooled bus block for use with up to 3 inch diameter compression type SCR's and diodes. Its unique design allows dissipation of 1 KW with less than 10° C temperature rise — performance unmatched by anything else available.

Accommodating Wakefield clamp #144 (6,000 lbs.) or #145 (10,000 lbs.), the Series 138 features low thermal resistance, low water flow and a massive area for mounting to a bus bar. The units are cast aluminum around copper tubes, combining the economy of aluminum with the compatibility of water and copper.

Call or write for full details on the latest of Wakefield's semiconductor coolers.



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New products



two-terminal inductance measurements at low test currents and at a frequency of 1 megahertz. The analog model 62A has full-scale manual ranges from 1 microhenry to 3,000 μ H arranged in a 1-3-10 sequence, while the digital model 62AD has four decade ranges from 2 μ H to 2,000 μ H full scale. The digital unit has both manual and autoranging modes.

The test current for both meters varies from 160 microamperes on the most sensitive ranges to 160 nanoamperes on the top ranges. The digital 62AD has both data outputs and programing inputs so that it can easily be incorporated into an automatic test system. Pricing on the meters is \$1,050 for the 62A and \$1,400 for the 62AD. Delivery time is two weeks.

Boonton Electronics Corp., Parsippany, N. J. Phone Wallace F. White at (201) 887-5110 [356]

120-MHz counter/timer sells for \$279 in kit form

Priced at only \$279 in kit form, the Unic 2001 is a universal counter/timer that can measure frequencies from 1 hertz to 120 megahertz, time intervals from 10 nanoseconds to 5 seconds, and events up to 99,999,999. In its standard form, the counter has a temperature-compensated crystal-controlled time base with an aging rate of 1 ppm per year. Various options



45nS 1K Schottky RAMs.

15 D_{IN}

14 WE

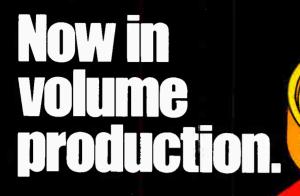
3 Ag

12 A₈

11 A7

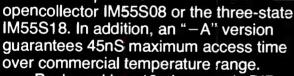
10 A6

9 A5



Prices down to 10 per bit.

Intersil's IM55S08/18 is a replacement for the 93415/93425 and the 82S08. The bipolar Schottky IM55S08/18 combines PNP inputs, 45nS typical access times and your DOUT 7 GND 8 choice of output: the



CS 1

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A₁ 3

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IM55S18C	0 to +75°C	70nS	OTS	7 wks	9.99
IM55S08M IM55S18M	-55 to +125°C -55 to +125°C		OTS OTS	8 wks	19.99 19.99

Like to know more?

If your prototype or production system uses 1K bipolar RAMs, call your nearest Intersil sales office for data sheets and/or samples. Intersil, 10900 North Tantauworld Radio History

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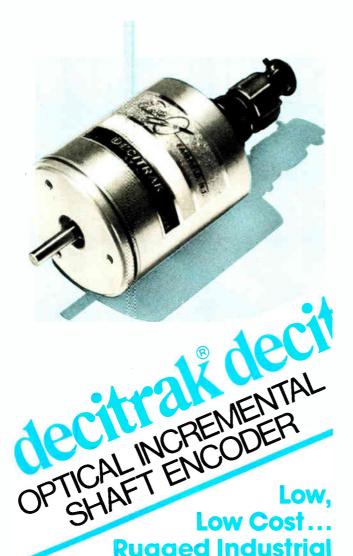
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range from the elimination of the time base (at a saving of \$35) to the inclusion of a double-cavity ovenized crystal oscillator (at a price increase of \$125). A wide variety of other options, including specialfunction cards for the direct display of rpm or gph, is also available. The instrument sells for \$399.95 in wired form.

Beco Inc., P. O. Box 67, Wirtz, Va. 24184. Phone (703) 483-9258 [357]

Signal generator provides two tones for SSB tests

Covering the range from 250 kilohertz to 80 megahertz, an rf signal generator, model 315, provides two-tone information for testing single-sideband equipment. It is capable of delivering single- or twotone signals at levels of 0.1 μ V (-127) dBm) to 2 V (+19 dBm) per tone. Signal spacing can be adjusted from 100 Hz to almost 80 MHz.

LogiMetrics Inc., 121-03 Dupont St., Plainview, N.Y. 11803. Phone Murray Feigenbaum at (516) 681-4700 [358]

TOPICS'

Instruments

Astro-Med, West Warwick, R. I., has announced a lifetime guarantee on the heated styluses

used in all of its chart recorders.

Exact Electronics Inc., Hillsboro, Ore., has developed an ASCII interface for its model 605 programable function generator. The new interface is compatible with IEEE standard 488.

Keithley Instruments Inc., Cleveland, Ohio, has announced its model 7802-ISB interface card, which is designed to allow instruments that meet IEEE standard 488 to plug into Keithley's system 1 calculatorbased automatic test system.

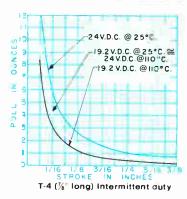
Weinschel Engineering,

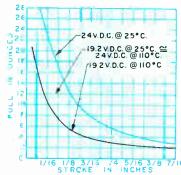
Gaithersberg, Md., has expanded its solid-state sweeper line with the addition of the model 436A plug-in unit which covers the range from 18 to 26.5 gigahertz.

MORE PULL in a smaller package?

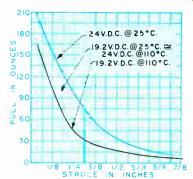


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Guardian Tubulars work in any position. Close tolerance between plunger and bobbin means no possibility of double seating. So they work in your product just the way you want them to work

Mount them directly into panel by inserting threaded bushing thru installation hole and tightening nut on lock washer. Or, mount with standard bracket.

Either way, Guardian Tubulars install without damage to the solenoid. Look how the





notched tube-steel shell mates with notched end plate Result? A stronger assembly that takes more torque when installing...with no chance of damage. The leads emerge thru a notch in the steel shell, so they will not, can not be sheared by rotation during installation.

Once you put a Guardian Tubular in your product...forget it. Typical mechanical life is 20 million. That's probably longer than your product's life expectancy...due primarily to the unique Valox* 420 molded bobbin.

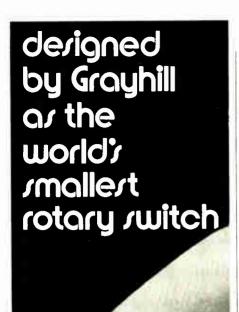
Variations and specials? Guardian's got 'em. Any DC voltage from 6 to 240. Push type or pull type operation. Return springs, silencers, termination variations, special mountings you name it and we'll deliver it with the high quality craftsmanship and low prices that have made Guardian Number 1 in Solenoids—and that keeps us here on top.

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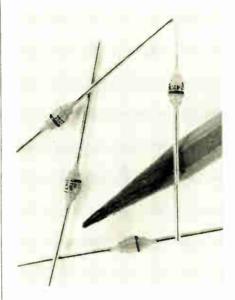
New products

Semiconductors

Rectifier diodes switch in 100 ns

High speed holds down temperature, aids efficiency in radar, other systems

Credit goes to improved production techniques, rather than design changes, for the development of a line of rectifier diodes that can switch 1,000 volts in as little as 100 nanoseconds.



Developed by TRW Semiconductors, the 500X series is an advance on units sold to date by TRW and others, which require up to 700 ns at 1,000 v, says Arthur Forbes, product manager.

Because the basic operating properties of this type of mesa-junction device (and thus its speed) depend on an even deposition of gold on the switch mechanism, the objective was to do a better diffusing job, Forbes says. In the past, the gold doping was diffused onto the junction only at high temperature and "often diffused out so thinly that in operation it migrated and allowed the underlying nickel to come up and lose the device its switching

properties," he adds. To correct this condition, the TRW researchers found new techniques to diffuse the gold doping and seal the completed switch at temperatures under 400°C.

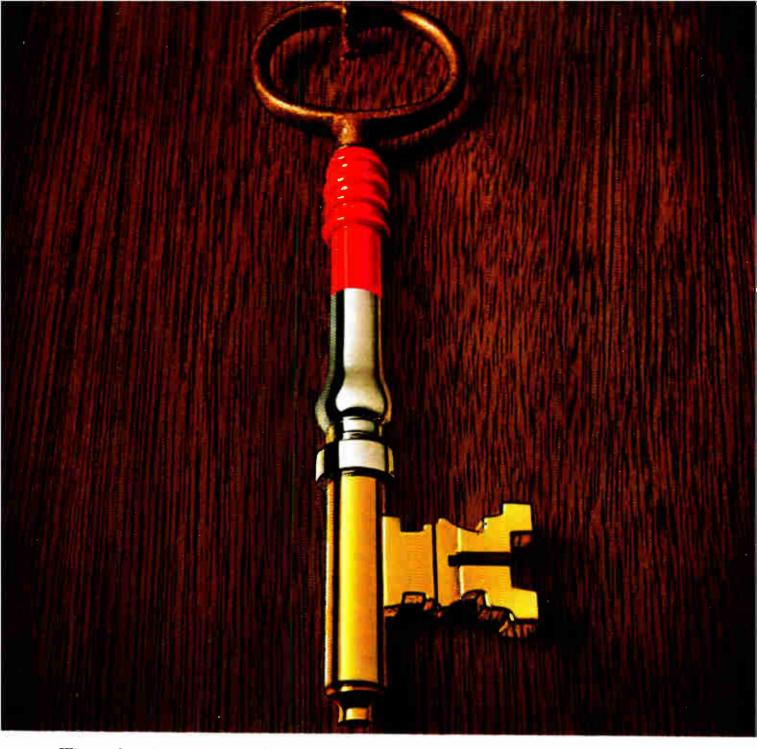
The need for even faster rectifier switching exists because a high speed holds down operating temperatures and contributes to overall system efficiency. The devices have already been tested in radar equipment, for example, where the fast rectifiers operating with a traveling-wave tube increase system efficiency by a critical 2% or 3%, Forbes says. A continuing program to attain comparable operation at 1,600 v should show results by year end, he predicts.

Other specifications for the rectifiers include: 100-v switching in 5 to 7.5 ns, and 500-v in 20 to 30 ns. Maximum current at 25°C is 10 microamperes and at 100° C, 350 μ A. The maximum recommended junction temperature is 160° C.

The rectifiers are packaged in a voidless monolithic glass-metal structure, using no organic materials. Designed in either 3- or 5-watt versions, parts are currently available for sampling. Volume production is scheduled for April 1. The 5-w unit will sell for \$7.50 in lots of 1,000, and the 3-w rectifier for \$4. TRW Semiconductors, Division of TRW Inc., 14520 Aviation Blvd., Lawndale, Calif. 90260. Call Arthur Forbes at (213) 679-4561 [411]

4-k static RAM has 100-ns access time

The 4402B 4,096-bit fully static random-access memory has an access time of 100 nanoseconds and a cycle time of 300 ns. Believed to be the fastest 4-k static RAM available today, the unit is aimed at communications switching systems, fast mainframe memory systems, and similar demanding applications. The memory requires two power supplies: a V_{DD} supply of +12 volts $\pm 5\%$, and a V_{SS} supply of -5 V $\pm 10\%$. It will retain all of its data even if V_{DD} drops as low as +4 v.



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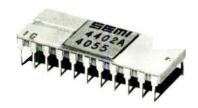
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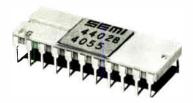
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New products





Typical operating power consumption is 450 milliwatts.

A companion memory, the 4402A, is slightly slower—its access time is 150 ns and its cycle time is 350 ns. Both metal-gate n-Mos devices are housed in 22-pin dual inline packages with pinouts compatible with those of 22-pin dynamic 4-k RAMs. Two other 4-k memories—one that requires only a single 5-v power supply and one organized as 1,024 four-bit words (to make it easier to use in microprocessor systems)—are a few months down the line.

The 4402A sells for \$15.25 in hundreds, while the 4402B is priced at \$22 in similar quantities.

SEMI Inc., 3883 North 28 Ave., Phoenix, Ariz. 85017. Phone Fran Krch (pronounced Kirch) at (602) 263-0202 [415]

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Intended for use in digital instruments, which often employ decade counters, data latches, and sevensegment output buffers. MM74C925 combines all of these functions in a single package. Consisting of a four-digit counter, data latches, and a multiplexed sevensegment output capable of driving a four-digit LED display, the IC contains its own free-running oscillator, and thus requires no external clock for the multiplexing circuit. Built with National's high-noise-immun-

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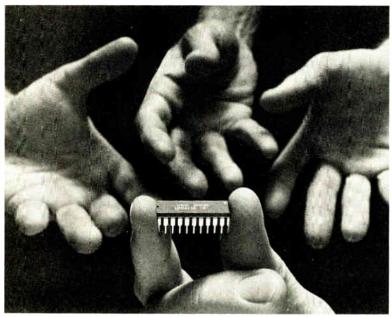
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Intermark Elect. Seattle, WA 206/767-3160 Santa Ana, CA 714/540-1322 213/436-5275 San Diego, CA 714/279-5200 714/453-9005 Sunnyvale, CA 408/738-1111

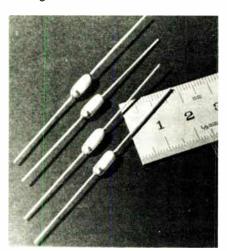
New products

ity C-MOS process, the circuit has a guaranteed noise margin of 1 volt, and can operate from supply voltages from 3 to 6 v dc. Several variations on the basic circuit are also available. For example, the MM74C927 divides the second most significant digit by six instead of by 10; thus, with a 10-Hz input frequency its display will read in minutes, tens of seconds, and seconds. The, IC sells for \$8 in hundreds and is available from stock.

National Semiconductor Corp., 2900 Semiconductor Dr., Santa Clara, Calif. 95051. Phone (408) 732-5000 [413]

10-kV rectifier is only 0.420 inch in length

Measuring only 0.420 inch long and 0.180 in. in diameter, the Semtech type SM 100 silicon rectifier is rated at 100 milliamperes forward current at 55°C and at 10,000 v peak inverse voltage. Other similar units have PIV



ratings of 4,000, 5,000, and 7,500 v. Made with Semtech's proprietary Metoxilite process, the rectifiers' outer cases consist of metal oxides which are fused directly to the rectifier junction assembly. Originally developed for critical military/aerospace applications, the rectifiers are also suitable for industrial applications and even some consumer products in which very high reliability is essential even in the face of high voltages and extreme

thermal shock. Delivery of the rectifier is from stock.

Semtech Corp., 652 Mitchell Rd., Newbury Park, Calif. 91320. Phone William B. Krause at (805) 498-2111 or (213) 628-5392 [414]

Four-bit ALU

is extremely fast

A four-bit arithmetic logic unit made by a low-power Schottky process is capable of performing a full 16-bit add or subtract operation in only 56 nanoseconds. Designated the Am25LS181, the ALU is a plugin replacement for the 54LS/ 74LS181. It can perform 16 binary arithmetic operations on two fourbit words, including add, subtract, double, and compare. The IC is offered in a wide variety of forms and in both commercial and military temperature ranges. Pricing varies from as little as \$4.50 each to as much as \$15.55 each in hundreds. Advanced Micro Devices Inc., 901 Thompson Pl., Sunnyvale, Calif. 94086. Phone (408) 732-2400 [416]

TOPICS Semiconductors

Motorola Semiconductor Products Inc., Austin, Texas, is into full production on its 4,096-bit MOS dynamic RAM. Housed in a 16-pin DIP, the MCM6604 is now available from stock. ... AMS Semiconductor Div. of Advanced Memory Systems Inc., Sunnyvale, Calif., has begun shipping production quantities of a new 4-k dynamic RAM that is housed in a 22-pin DIP. The AMS 7280 is TTL-compatible. Signetics, Sunnyvale, Calif., announces that nine of its lowpower Schottky devices have been qualified to MIL-M-38510A. The units are the JB54LS00, -03, -04, -05, -10, -12, -20, -22, and -30 ... United Detector Technology, Santa Monica, Calif., is offering a kit containing six different types of silicon photodetectors. Priced at \$245, the kit includes a design manual and a special silicon photodetector slide rule.

Data handling

Memory boards plug into LSI-11

A substantial number of accessory products are being designed for compatible operation with Digital Equipment Corp.'s widely used LSI-11 minicomputer [Electronics, Nov. 13, 1975, p. 190], and among the latest are add-on memory boards from Memory Systems Inc. of Hawthorne, Calif.

Andrew Huson, president, says the company will have by mid-March sample quantities of two boards, one with 8,192 words of 16 bits each and the other with 16,384 words. Offering access time of 500 nanoseconds and cycle time of 800 ns, the units are totally compatible with the LSI-11, Huson says. Tentative prices are \$1,450 for the 16-k board and \$875 for the 8-k version in single quantities.

The dynamic-memory modules were designed by Perry Persons and Joseph McGrath of Technical Marketing Inc., Costa Mesa, Calif. For the add-on boards, they chose the same Mos chip that is used in the LSI-11 itself—the Mostek 4096P-11, a 4-k device in a 16-pin package. The add-on memory has 64 of these chips in the 16-k board and 32 in the 8-k version.

For internal timing, four Fairchild Schottky driver ICs are used. No other functions are needed on the boards. They plug directly into the LSI-11 chassis. Power required is either 12 volts at 0.5 ampere or 5 V at 2 A.

Memory Systems Inc., 3341 W. El Segundo Blvd., Hawthorne, Calif. 90250. Phone Andrew Huson at (213) 772-4220 [361]

Serial impact printers can run at 340 lines/minute

Two serial impact printers, the model 103 and the model 503 (shown), can perform bidirectional printing at rates from 70 to 340 lines

per minute. In addition to their high throughput, the printers have a paper-saving high-density mode of printing which, at the flick of a switch, can increase page density by 33%. The result is 88 lines on an 11-inch form instead of the standard 66 lines.

Basically the two printers are very similar, with the 103 aimed at present users of 100 series printers who require complete compatibility with earlier features along with the speed advantage of the new units.



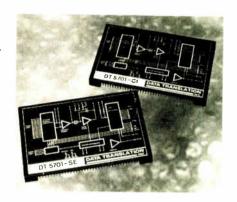
The 503 is configured as a low-cost basic system with all of the features of the 103 available as options. The 103 and 503 sell for \$4,560 and \$3,795, respectively. Delivery time is 45 days.

Centronics Data Computer Corp., 1 Wall St., Hudson, N. H. Phone (603) 883-0111 [367]

Prices slashed for data-acquisition modules

Believing that microcomputers eventually will become as available as op amps. Fred Molinari, president of Data Translation Inc., wants his company to price its data-acquisition modules so that the cost of these essential ingredients in microcomputer-controlled systems won't deter potential users. That's why the DT5701 16-channel module sells for about half the price of its predecessors in Data Translation's Datax II, series. The DT5701 will sell for \$175 in quantities of 100.

The key element in the module is a 12-bit analog-to-digital converter, a hybrid device with a settling time of 2 to 3 microseconds—not especially speedy, but sufficient, says



Molinari, to serve 75% of microcomputer needs for data-acquisition modules. In addition, the unit contains a 16-channel multiplexer, a buffer amplifier, a high-speed sample-and-hold amplifier, and all control and programing logic.

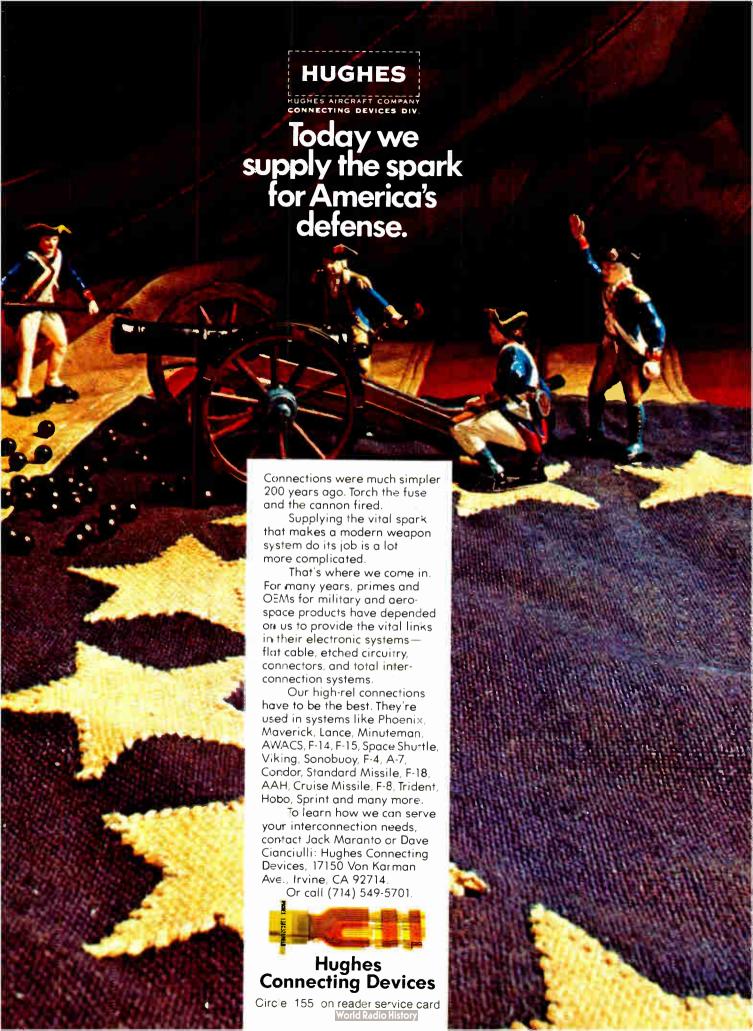
The DT5701 is linear to within half a least significant bit from 0°C to 70°C. Accuracy is within 0.03% of full-scale range, and maximum throughput rate is 35 kilohertz. The module is designed to plug into a board, which, in turn, plugs in to the microcomputer system.

Data Translation Inc., 109 Concord St., Framingham, Mass. 01701. Phone Fred Molinari at (617) 879-3595 [362]

8-color CRT terminal sells for \$1,995

The Intecolor 8001 is an eight-color CRT terminal with a keyboard, a 19-inch CRT, four kilobytes of random-access memory, and a 100-piece price of \$1,995. Controlled by a built-in Intel 8080 microprocessor, the terminal has a 25-line-by-80-character format and an RS-232 communications port that can





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Dana Laboratories, Inc., 2401 Campus Drive, Irvine, California 92664, 714/833-1234.



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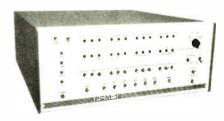
New products

handle data rates up to 9,600 bauds. A wide variety of hardware and software options, including extra memory and 64 special programable characters, is available for the terminal. Pricing starts at \$2,495 in unit quantities, and drops to as little as \$1,695 for large OEM orders. Delivery time is 60 to 90 days.

Intelligent Systems Corp., 2405 Pine Forrest Dr., Norcross, Ga. 30071. Phone (404) 449-5961 [363]

Kit computer can execute most PDP-8 software

Unlike most kit computers, which are supported by a minimum of software, the PCM-12 is designed around the Intersil IM6100 microprocessor—a 12-bit static C-MOS device that is software-compatible with the Digital Equipment Corp. PDP-8E minicomputer. Thus the assembled kit can execute most PDP-8 software including assemblers, edi-

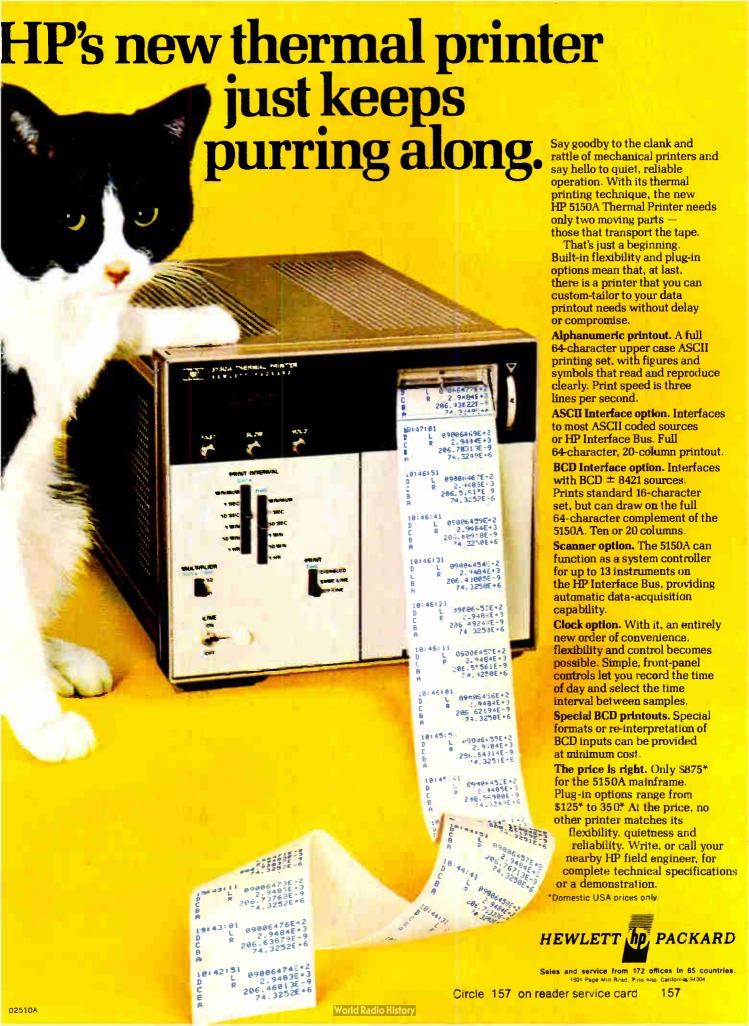


tors, and advanced languages like BASIC and Fortran. Supplied with 4,096 words of 12-bit memory, a control panel, a power supply, interfaces for teletypewriters and cassette recorders, and a cabinet, the kit sells for \$400. Other interfacing modules can raise the price to \$600.

PCM, P.O. Box 215, San Ramon, Calif. 94583. Phone (415) 837-5400 [364]

Graphic plotter draws stepless lines and curves

The 930 series graphics systems are a family of computer/plotters that work in a continuous, rather than incremental, fashion to avoid step



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discontinuities in the straight lines and curves that they draw. The systems include a computer for the creation and maintenance of a data base and for the control of such peripherals as teletypewriters, tape cassettes, tape and card readers, digitizers, and display devices. The system 930/100 has a usable plotting area that measures 31 by 36 inches and sells for \$59,000. The 930/200 has a 54-by-76-inch area and a price of \$75,800. Both units have a resolution of 0.001 inch, a repeatability of 0.003 in., and a maximum plotting speed of 16 inches per second.

Broomall Industries Inc., 682 Parkway, Broomall, Pa. 19008. Phone Richard Stover at (215) 353-4610 [365]

Tape punch runs at 20 characters/second

The model PM-820 is a rugged OEM tape punch that will perforate both roll and fan-fold tape at 20 characters per second. Supplied complete with drive motor, punch cover, and chad container, the PM-820 sells for \$590 in large quantities. Options include drive electronics, spooling, and circuitry to convert binary-coded decimal to ASCII. Delivery time is 30 days.

Data Specialties Inc., 3455 Commercial, Northbrook, III. 60062. Phone (312) 564-1800 [368]

Core memories are aimed at microprocessor applications

Stressing its cost-effectiveness for those applications in which loss of data from volatility will present operational problems and increased expense, Ampex has introduced the first in a family of magnetic-core memories designed for use with microprocessors, peripherals, and terminals. The MCM-1000 comes in three sizes—1,024, 2,048, and 4,096 bits—each of which has an access time of 450 nanoseconds and a cycle time of 1,300 ns. Each memory module is completely self-contained

O.K., you guys, back to the old drawing board.

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Model Number	Color	Size	Luminous Intensity	Viewing Angle
5174B*	Orange	T-1	5.0 mcd	90°
5274B*	Green	T-1	1.0 mcd	90°
5374B*	Yellow	T-1	4.0 mcd	90°
5774B*	Red	Ť-1	5.0 mcd	90°
5152**	Orange	T-13/4	40.0 mcd	28°
5252**	Green	T-13/4	15.0 mcd	28°
5352**	Yellow	T-13/4	45.0 mcd	28°
5752**	Red	T-13/4	40.0 mcd	28°

*Also available with $1^{\prime\prime}$ lead lengths, low profile (.138 $^{\prime\prime}$ high) lens, and 180° viewing angle.

Last year there were some sockets that demanded filament lamps, despite their inherent failure-and-replacement problems. Bright was needed, and damn the torpedos.

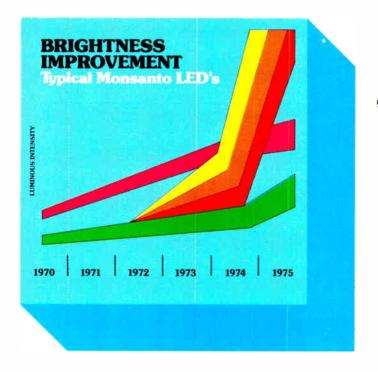
This year you just might find the bright you need in a shake-rattle-and-roll-proof LED lamp. Come and see.

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Expandability—Proto-Board units can be instantly interconnected for greater capacity

Utility—Models are available with or without built-in regulated power supplies (fixed or adjustable).

Variety — A wide variety of models are available with capacities ranging from 630 to 3060 solderless tie-points (6 to 32 14-pin DIP's), to fit every technical and budget requirement.

Speed—Assemble, test and modify circuits as fast as you can push in or pull out a lead. Save hours on every project.

Accessibility—All parts are instantly and easily accessible, for quick signal tracing, circuit modifications, etc.

Durability—All Proto-Board models are carefully constructed of premium materials, designed and tested tor long, troublefree service.

Economy — Eliminate heat and mechanical damage to expensive parts. Save money by re-using components.

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Whatever type of electronic circuits you work with, you can do more in less time with CSC's solderless Proto-Board™ systems. As fast and easy as pushing in or pulling out a lead, you can design, test and modify circuits at will. Components plug into rugged 5-point terminals, and jumpers, where needed, are lengths of #22 AWG solid wire. In the same time you took to read this ad, you could be well on your way to assembling a new circuit. For more information, see your CSC dealer, or write for our catalog and distributor list.

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New products

and includes the timing and control circuits, data and address registers, decoding and drive circuits, a TTL-level interface, and a data-save feature. This prevents loss of data under all possible modes of power turn-on, turn-off, or failure. The MCM-1000 is priced below \$500 in OEM quantities.

Ampex Corp., P.O. Box 33, Marina del Rey, Calif. 90291. Phone (213) 821-8933 [369]

Cassette data recorder designed for systems use

The model JK970 high-speed cassette data recorder is a highly reliable device intended for use in a wide variety of OEM applications. Using a Philips-type digital cassette tape (ISO and JIS standard), the new recorder offers a tape speed of 15 inches per second and start and stop times of 30 milliseconds and 40 ms, respectively. The unit derives its

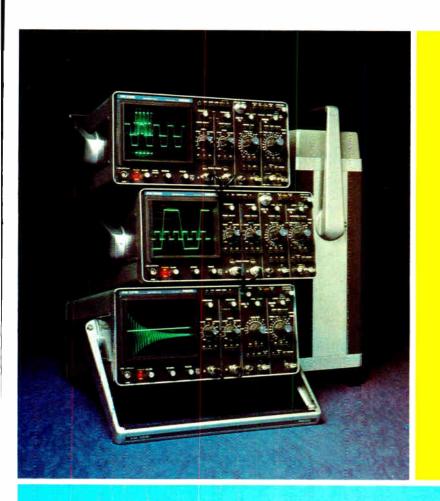


high reliability from the elimination of many mechanical parts. Its two reel motors and the cassette-setting parts are the only mechanical components. The recorder's drive system reduces the chance of tapes twisting around the capstan and pinch roller, and it minimizes dust accumulation on the tape by bringing only the magnetic head in contact with the tape during operation.

Panasonic, One Panasonic Way, Seacaucus, N. J. 07094 [370]

Portable data terminal weighs only 13 pounds

The latest addition to Texas Instruments' "Silent 700" line of data terminals is a portable unit that uses a microprocessor to keep its weight







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The 50 MHz model, PM3240, for example, weighs in at a mere 18 1/2 pounds. And because it's used as much outside the laboratory as in, we've given it a logical front panel layout where every control falls naturally to hand. The PM3240 has also been designed to operate from almost any supply, including DC; to have a bright 8 x 10 cm display and good circuit access for short service down-times on the oscilloscope itself.

MOVING UP TO 120 MHz

For higher bandwidth applications, the 120 MHz model, PM3260 is available. This 19.8 pounds light instrument therefore keeps you well ahead of Schottky TTL speeds and at the same time goes on triggering to over 200 MHz to meet the great majority of ECL applications.

Like the 50 MHz model, the PM3260 includes layout features such as clear separation of the main and delayed timebase, as well as operation from DC plus 100 - 240 V supply having frequencies from 46 - 440 Hz.

MULTIPLYING AT 100 MHz

The PM3265 extends all the previous benefits to a bandwidth of 150 MHz and also adds a unique built-in 100 MHz analog multiplying facility. Only with this instrument can you therefore make transient power and dynamic phase measurements on high speed components and circuitry. Moreover, this facility like the rest of the instrument, is extremely easy to use. You simply push the 'A x B' button to obtain the product which can also be displayed together with the B input signal.

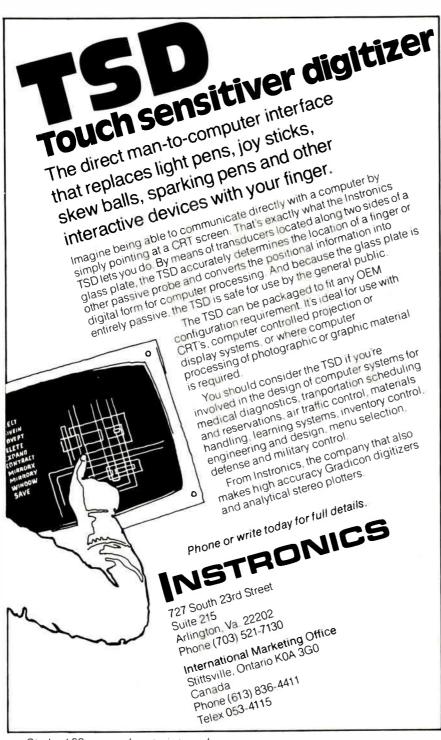
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down to just 13 pounds. The model 745 is a full-capability terminal which gives travelers, like salesman, instant access to a central computer by means of a built-in acoustic cou-



pler and any telephone. Priced at \$1,995 in small quantities, the model 745 includes an ANSI keyboard with a calculator-style numeric key pad, automatic paper loading, and thermal printing at 30 characters per second.

Texas Instruments Inc., P.O. Box 1444, M/S 784, Houston, Texas 77001. Phone (713) 494-5115 Ext. 2126 [366]

TOPICS Data Handling

Texas Instruments Inc., Dallas, Texas, has reduced the prices of six of its handheld calculators. Among the affected units are the SR-51A scientific/statistical calculator, which has been cut from \$149.95 to \$119.95 and the SR-50A scientific machine, which has been reduced from \$99.95 to \$79.95. Hewlett-Packard Co., Palo Alto, Calif., is conducting a special promotion of its HP-65 fully programable pocket calculator. People who buy the \$795 calculator before April 30 will receive \$195 worth of applications software at no additional cost. . . Sycor Inc., Ann Arbor, Mich., has introduced two new intelligent dataentry terminals that are compatible with European (ECMA) standards. The terminals, designated the 310 and 320, have been approved for use with the Honeywell series 62 and Sweda International's 1300 small business systems.

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frames, drawers, and rain

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socket complements, and are compatible with other boards for hybrid installations. We also offer automated wiring service. We're ready for you.

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sequence makes as many

as 50 simultaneous multiple connections in seconds, without stripping, soldering or

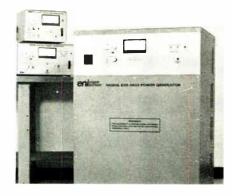
"Scotchflex" assembly

Industrial

8.4-kW generator is transistorized

Ac power unit also offers variable frequency over range from 9 to 250 kHz

While solid-state logic makes quantum jumps in technology almost as a matter of routine, its cousin in the industrial power arena has been a laggard. For example, bulky motor generators or vacuum tubes are still



the chief sources of ac power generation for induction furnaces, large ultrasonic transducers, and other industrial applications.

ENI Power Systems Inc., Rochester, N.Y., hopes to change that situation with its transistorized 8.4-kilowatt ac power generator. Not only is the EGB 8400 the first transistorized unit rated at more than 1 kw-it is the first of its size to offer variable frequency, operating over the range of 9 to 250 kilohertz. It is also the first to operate without water-cooling, a feature that has condensed the EGB 8400, together with its power supply and air cooling system, into a 44-by-29-by-30-inch package that weighs only 300 pounds—one third less in weight and size than comparable units.

The variable-frequency feature has proven a major plus. For example, during ultrasonic welding the optimum frequency for maximum power transfer will change as the metal or plastic melts. Most power generators are single-frequency and cannot adjust for changing conditions. But it's possible to tune the EGB 8400 to the process by adjusting the variable-frequency oscillator while reading the built-in true-average-power meter.

The power meter, which is designed around an analog multiplier and directional coupler circuit, offers accuracy to within ±1.5%. With a flick of the switch, the power meter display changes from power leaving the EGB 8400 (forward power) to power absorbed by the generator (reflected power).

The heart of the EGB 8400 is a broadband linear transistor power amplifier that uses more than 90 hybrid-coupled power transistors in the output stage alone. The use of hybrid coupling isolates the output stages; should one transistor fail, the remaining units will continue to supply their outputs. This would not be the case if the transistors were in a standard parallel or series arrangement.

The entire amplifier is driven by a low-level, highly stable voltage-controlled oscillator. Frequency is directly controlled and read out by a two-range oscillator control dial mounted on the front panel. A level control varies the output from zero to full power.

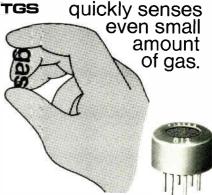
The generator provides maximum power transfer to a 50-ohm load. However, any load impedance (from an open to a short circuit) can be connected without fear of damage or failure.

High-voltage regulators eliminate any voltage spikes or hash on industrial power lines and prevent radiation from reaching the ac line. Input power-line voltage is 120/208 v ac, three-phase, with a maximum draw of 16 kw.

Available with a complete line of accessories that will match it to any induction heating coil, high-voltage corona generator or ultrasonic transducer, the EGB 8400 also has such optional features as frequency lock-loop, watt-second (joule) readout and control, and remote power

Gas Sensing Semiconductor

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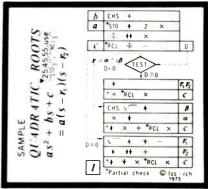
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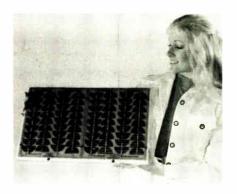
New products

control. Base price is \$11,900; delivery time is 90 days.

ENI Power Systems Inc., 3000 Winton Road South, Rochester, N.Y. 14623 [371]

Solar panel delivers 6 watts per square foot

Made of 3-inch silicon solar cells encapsulated in clear silicone rubber and mounted on an epoxy fiberglass board, the Unipanel is a solar-cell array capable of power densities as high as 6 watts per square foot. Unipanels may be interconnected in various series and/or parallel combinations to meet a variety of depower requirements. In particular,

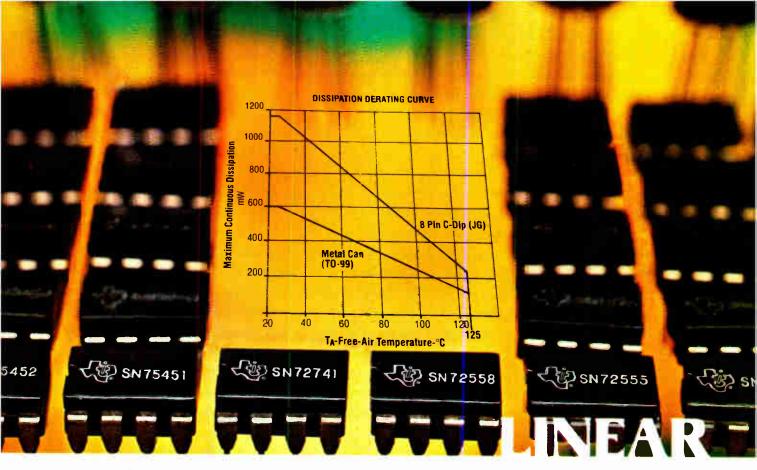


two of them can be combined to form a system with a nominal voltage of 14 volts—an ideal voltage for charging 12-v batteries. The two-panel combination is rated at 16 w and is claimed to be able to deliver a minimum yearly average power of 520 watt-hours per week at an average U.S. location. The system sells for \$760 in unit quantities and \$550 each in lots of from two to nine.

Solarex Corp., 1335 Piccard Dr., Rockville, Md. 20850. Phone Theodore Blumenstock at (301) 948-0202 [373]

Data-acquisition system works in harsh environments

Designed to withstand unusually harsh electrical and physical environments, the series 6000 data-acquisition and control terminal is compatible with standard mini-



New hermetic dual-in-line package offers superior thermal characteristics... automatic insertion...improves reliability.

Say good-bye to linear circuits in TO-99 cans. Welcome a new packaging milestone. TI's new hermetic 8-pin ceramic dual-in-line linear package.

The innovative JG-package with superior thermal characteristics provides substantially improved device reliability over the old TO-99 can, as the derating curve illustrates. Under identical operating conditions, chip junction temperature in the new 8-pin C-Dip may be as much as 50 C cooler than in a

As with all dual-in-line packages, the JG-package can be automatically inserted in p.c. boards which will cut

DADAMETED				
PARAMETER	TO-99 PA	CKAGE	JG-PACKAG	E UNIT
Thermal Impedance ()J-A	210		106 5	C/W
Max. Dissipation @ 70 C	384		744	mW
Max. Dissipation @ 125 C	120		232	mW
Operating Junction Temp.				
T _J @ 500 mW & T _A = 25 C	130		79	С
T _J @ 300 mW & T _A = 70 C	133		102	С
Typical fun	ctions avail	able in JO	a package	
High Performance Op Amp		SNC	2101AJG/SN	72301AJG
Differential Comparator		SNG	2111JG/SN72	2311JG
Universal Timer	SNC52555JG/SN72555JG			2555JG
QmA cO faud		SNC52558JG/SN72558JG		
General Purpose Op Amp		SN€52741JG/SN72741JG		
Dual Peripheral Driver		SNC	5451JG/SN75	5451JG
Dual Peripheral Driver		SNC	5452JG/SN75	5452JG

All TI linear products offered in 8 pin Plastic are also available in 8-pin C-Dip-JG

metal can (see box for more details). your installation costs. Another saving: The non-conducting ceramic base eliminates the need for insulators.

But you pay no more for all these advantages. TI's linear devices in the JG-package are priced the same as equivalent metal-can functions. Hermeticity and space requirements are the same. The JG-package has also passed all military requirements of Mil-Std-883 and data is available on request

For more information on the linear circuits available in the new dual-inline package, contact your nearest authorized TI dis tributor. Or write Texas

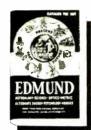
Instruments Incorporated, P. O. Box 5012. M/S 964, Dallas, Texas 75222



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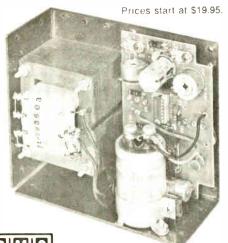
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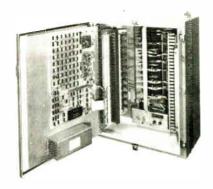
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New products



computer UART (universal asynchronous receiver/transmitter) interfaces such as the Digital Equipment Corp. model DL-11D. the Data General model 4023, and the Hewlett-Packard model 12587B. Requiring only a two-wire interconnection between the central minicomputer and each of up to 64 remote terminals, the series 6000 is expected to offer the advantage of very low cabling costs. In addition to direct wiring, the unit can be connected to the computer over voicegrade telephone lines. Prices start at \$2.850, and delivery time is 90 days. LFE Control System Industries, 2920 San Ysidro Way, Santa Clara, Calif. 95051. Phone D. Ridgeway at (408) 732-9400 [374]

Rugged optical encoder resolves 2,500 counts/turn

Offered with resolutions of up to 2,500 codes per revolution (or 10,000, with optional times-four multipliers), the RI/27 family of rotary optical encoders is a line of rugged devices intended for use in the OEM industrial market. The encoders have a maximum operating



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speed of 5,000 rpm and a bearing life in excess of a billion revolutions. They can operate over the temperature range from 0°C to 50°C at relative humidities up to 98%. The units have been shock-tested to 50 g and vibration-tested to 10 g.

Itek Corp., Measurement Systems Div., 27 Christina St., Newton, Mass. 02161. Phone Clyde Barnett at (617) 969-7300 [375]

Eight-digit counter can run at 12,000 counts per minute

Intended to replace conventional electromechanical counters, the Minicounter II is an all-electronic device with an eight-digit light-emitting-diode display and a maximum counting rate of 12,000 counts per minute. Available with



or without a reset function, the counter can be activated by switch closures, voltage pulses of from 4 to 50 volts peak amplitude, or pulses from standard TTL, DTL, or C-MOS logic. The model 428 Minicounter II sells for as little as \$45 in hundreds. Waugh Controls Corp., 9001 Fullbright Ave., Chatsworth, Calif. 91311. Phone (213) 998-8281 for bulletin PB 428.1 [376]

Power-line monitor logs fast ac transients

Just plug the model W115 Power Guard into the same ac line as a computer, and the unit will warn you of possible computer errors caused by transients on the ac line. Whenever a fast transient occurs.

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more than promises,

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Fairchild CCD TV camera

IN PRODUCTION



Size:

2"H x 2-1/2"W x 3-3/4"L

Weight:

12 ounces

Price:

\$4,500, including camera, lens and power converter

FEATURES

- Completely solid state, including CCD sensor (244 x 190 array)
- EIA Compatible Output
- Records on any EIA/JAL Videotape Recorder
- Uses "C" Mount Lenses
- Low Power Consumption (±12V, 4 watts)
- Gamma Correction
- Crystal Controlled Clock Circuits
- Automatic Gain Control (AGC)
- Low Light Performance

mv-201 television camera is the most advanced in a series of cameras developed by Fairchild Imaging Systems using charge-coupled device (CCD) sensors. Fairchild developed charge-coupled devices bring the inherent advantages of solid-state sensors — small size, light weight, low power, and long life — out of the laboratory into the real world by replacing the conventional tube type imaging sensors.

mv-201 television camera operates with standard closed-circuit television receivers and video recorders.

mv-201 television camera's advanced features make it possible to serve a wide range of applications in the military, commercial and industrial sectors.



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L BAND: 1KW 1-1 5GHz. 1DC: 500KW 1.2-1.35GHz 2usec 400PPS. Many more. Phone or write

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X BAND: 100W 9.2-9.5GHz Susce 1000PPS, 1 KW 8.9-9.4GHz 001DC; 65KW 8.5-9.6GHz 001DC; 250K W 8.5-9.6GHz 0013DC; 400KW 9.1 GHz 1.8µsec 450PPS, Many more, Phone or write.

Ku-K BAND: 50KW 16.4-16.6GHz 001DC: 135KW 15.5-17.5GHz 0006DC: 40KW 24GHz 0007DC: 40KW 35GHz 0004DC. Many more. Phone or write.

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245 KW LINE Output 16 KV 16 A. .25 μs 4000 PPS. 405 KW FLOATING DECK Output 20 KV 20 A 1 μs to

10 millesec pulse 500 KW LINE Output 22 KV 28 A. .4/1.75/2.25 µs 2500/500/300 PPS 1 MW HARDTUBE MIT MODEL 9 Output 25 KV at 40

A. 25 2 μs .002 D.C. 2.0 MW LINE 30 KV 70 A. 1/2 μs 600/300 PPS 3 MW LINE Output 39 KV 75 A. 25/1 μs 500 PPS

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X BAND FIRE CONTROL 250KW M-33
X BAND WEATHER/SEARCH 250KW AN/CPS-9
X BAND AIRBORNE TRACKER 50KW B-47
X BAND MAIBORNE TRACKER 50KW B-47
X BAND MAIT-INTRUSION 7 KW AN/TPS-21
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C BAND TRANSPONDER 100W AN/DPN-62
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DRONE CONTROL SYSTEMS
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UHF SEARCH 1MW TPS-28

60 FT. DISH

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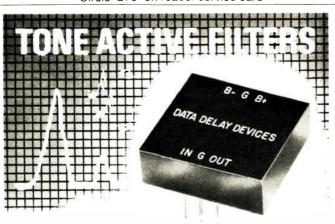
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Radio Research Instrument Co., Inc.

(203) 853-2600

Circle 270 on reader service card



A new family of tone active filters series TAF24 has been developed by DATA DELAY DEVICES.

They have the following specifications:

- 3db (BW) $-\pm 1.5\%$ to $\pm 3\%$ of fc.
- 20db (BW) $-\pm 10.5\%$ of fc.
- Gain 10db ± 1db.
- Gain stability ±.4db from 0°c. to 700 c.
- Power supply ± 15 Vdc.
- Size 1.5 x 1.5 x .375

Send for bulletin TAF24



New products



the W115 sounds an alarm, records the amplitude and duration of the pulse, and stops a digital clock at the exact time of the transient detection. The Power Guard includes a voltmeter which continuously monitors the ac line, and a strip chart recorder which operates only when transients are detected. Price at \$1,485, the units is available from

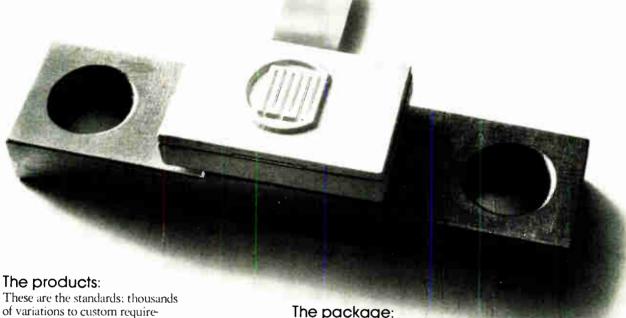
Holland Electronics Inc., 970 East 92 St., Brooklyn, N.Y. 11236. Phone (212) 649-7330 [377]

Six-digit stopclock's outputs can be bused

A panel-mounting six-digit stopclock, the DSC-8200 is similar to a digital stopwatch except that it has a full set of logic outputs and controls that allow it to interface with computers, data-acquisition systems, and process controllers. By moving some jumpers on its rear connector, the user can program the instrument to count in hours, minutes, and seconds; or in hours, minutes, and tenths of minutes, or in a variety of other scales including microseconds. Although it contains its own crystal-



Introducing RF resistors and terminations the likes of which you've never seen before.

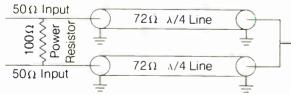


ments are easy.

Resistor or Termination	Package	Power Dissipation Watts	Resilitance Ohms	Frequency 'GHz	VSWR
T	Α	150	50	DC-2.0	1 25:1
T	В	200	50	DC-2.0	1 25.1
T	С	250	50	DC-1.0	1 25 1
T	D	300	50	DC-1 0	1 25:1
R	E	100	100	DC-2 0	1.25 1
R	F	125	100	DC-2.0	1.25:1
T	G	150	50	DC-20	1.25 1
R	Н	100	100	DC-2.0	1 25 1
R	J	150	50	DC-2.0	1 25 1
R	K	200	50	DC-2 0	1 25 1
R	L	150	50	DC-2.0	1.25:1

^{&#}x27;For 100"C case

2-Way Wilkinson Combiner



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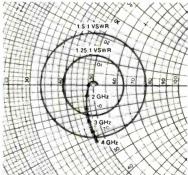
The poop:

No one else offers data sheets like ours: full characterization and all the information you need for easy design. Applications notes and schematics from a staff of expert circuit designers, available to assist you in special problems.

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Circle 173 on reader service card

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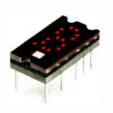
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745-0007 LED hexidecimal display with on-board logic operates from 5 to 6 volt supply, low power consumption. Integral TTL MSI chip provides latch, decoder and drive functions. 0.270" character display has wide angle visibility and mounts into standard 14-pin DIP socket.



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See Dialight.

New products

controlled timebase, the counter can also be slaved to the power line for especially good long-term accuracy. The DSC-8200 has front-panel start, stop, and reset controls, but these functions can also be controlled by switch closures or TTL-level signals applied through the rear connector. Visual data output is by means of large (0.6-inch) LED displays, while electrical data output is available in parallel binary-coded-decimal form. The counter has a small-quantity price of \$299 for the ac-power version and \$269 for the model that runs on ±5 v dc.

Datel Systems Inc., 1020 Turnpike St., Canton, Mass. 02021. Phone (617) 828-8000 [378]

TOPICS Industrial

Nova Electric Manufacturing Co., Nutley, N.J., has improved the frequency stability of all of its inverters, frequency changers, and uninterruptible power systems from 0.15% to 0.005% with no concomitant increase in cost. Jordan Controls Inc., Milwaukee, Wis., has added a new model-the AD-8800-to its line of servo amplifiers for remote positioning control systems. The amplifier is offered in 6- and 12ampere versions. Allen-Bradley, Milwaukee, Wis., is broadening its line of small, oiltight pushbutton switches. The

new devices include two- and

three-position illuminated and nonilluminated devices with pull-

to-start and push-to-stop action. Analogic, Wakefield, Mass., has introduced its Measurometer line of panel-mounting instruments for measurement and control. A key feature of the line is easy programability to read in any desired engineering units ... The Telemotive Division of Dynascan Corp., Chicago, has developed a digital telemetry and control system that makes it possible to communicate with and remotely control complex materials-handling and/or weighing equipment. System data is suitable for computer input.

New products/materials

Rare-earth magnet powders, ready to be blended, pressed, and sintered into magnetic components, are being offered to users who want to fabricate their own magnetic components and subassemblies. Hicorex brand sumerium-cobalt powder sells for \$50 per pound in small quantities, dropping as low as \$14.50/lb in lots of 10,000 lb. Applications areas include instruments, motors, electron tubes, electronic watches, and microphones.

Hitachi Magnetics Corp., Edmore, Mich. [476]

RTV silicone resins can be cured in thick sections and can also be dug out for repair and resealed. Three formulations are available: Eccosil 5122 is the general-purpose resin, 5850 has the best thermal conduc-



tivity (0.0043 calories per second-centimeter-°C), and 5640 has the lowest density (specific gravity is 0.75). At 200°F all three pourable resins cure in about half an hour. Emerson & Cuming Inc., Canton, Mass. 02021 [477]

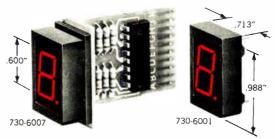
Beryllium oxide components for use in high-power traveling-wave tubes and klystrons are diamond-ground, metalized pieces fabricated to tolerances of ± 0.0001 inch. The BeO collectors, insulators, and support elements sell for from \$5 to \$75.

Ceradyne Inc., P. O. Box 11030, Santa Ana, Calif. 92705 [478]

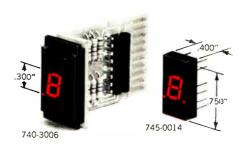
A temporary adhesive used to hold optical components and semicon-

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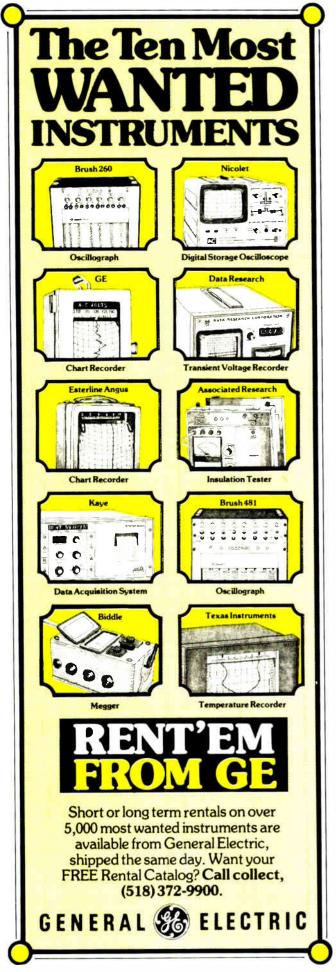
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New products/materials

ductors for dicing, slicing, and grinding is extremely resistant to acid attack. Crystalbond 557 adheres readily to porous and non-porous metals, glasses, and ceramics. Although it will resist attack by hydrochloric, hydrofluoric, and nitric acids, and by aqua regia, it is readily removed by a solution of acetone and methyl ethyl ketone. Priced at \$45 per pound in small quantities, Crystalbond 557 sells for \$30/lb in hundred-pound lots.

Aremco Products Inc., P. O. Box 429, Ossining, N. Y. 10562 [479]

A highly adsorbent desiccant material that changes color as it collects moisture is said to be particularly helpful when the desiccant is used as a drying insert in delicate measuring instruments, for the air drying of transformers, and in similar applications. Natrasorb indicating beads change from blue to pink as they adsorb water. They can hold as much as 40% of their weight in water at which point they can be reactivated by heating. The beads are nondeliquescent and noncorrosive and look and feel dry even when they are saturated.

Multiform Desiccant Products Inc., 1418 Niagara St., Buffalo, N. Y. 14213. Phone John E. Brewer at (716) 881-0100 [480]

Soldering aluminum and aluminum alloys is greatly eased by the use of multicore Alu-Sol 45D solder. The patented material is made of silverloaded tin/lead solder and four peripherally located cores of a special flux. It contains no bismuth, zinc, cadmium, or aluminum and therefore is very resistant to corrosion. It is also compatible with standard tin/lead solders. Alu-Sol 45D solves the problem of soldering such hardto-wet aluminum alloys as 6061 and 5052. It also works very well with tin plate, copper, brass, nickel, nickelsilver, and, of course, pure aluminum. It works somewhat less well with steel, stainless steel, and zincalloy die castings. Prices are similar to those of standard tin/lead solders. Delivery is from stock.

Multicore Solders, Westbury, N. Y. 11590 [440]

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New literature

Digital instrumentation. A videotape program and textbook, entitled "Digital Troubleshooting," provides training in digital instrumentation. Described as equivalent in coverage to a two-day seminar, the program includes 14 videotapes with a total running time of five hours and 39 minutes and includes a 180-page textbook.

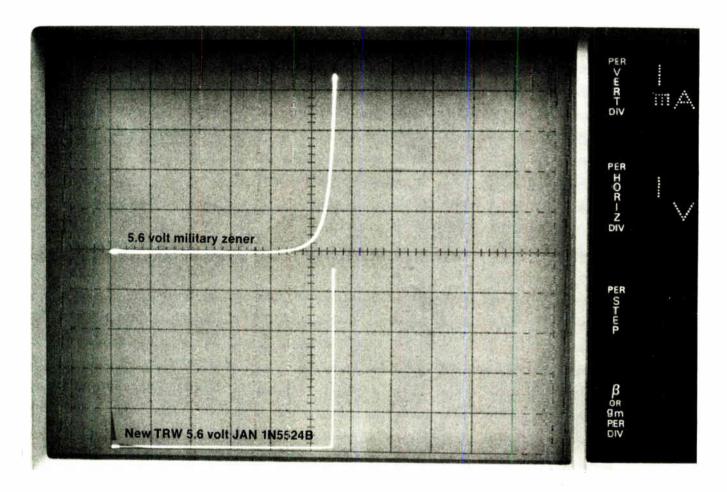
Videotapes and textbooks may be purchased individually. The entire program (14 tapes and one textbook) is available for \$2,200 plus taxes and handling charges. A descriptive brochure and order form is available without charge from Inquiries Manager, Hewlett-Packard Co., 1501 Page Mill Rd., Palo Alto, Calif. 94304. Circle 421 on reader service card.

Proximity switches. Product brochure FY describes two lines of selfcontained industrial proximity switches. One is the all-metals-sensitive FY series, which uses the eddycurrent-killed-oscillator (ECKO) principle, and the second is the 50FY, which is a magnetically operated Hall-effect device. The 16-page illustrated brochure lists features and advantages of the dc and ac products. It contains two pages of application drawings, simplified ordering guides, and detailed specifications. Write to Micro Switch, division of Honeywell, 11 W. Spring St., Freeport, Ill. 61032 [422]

Panel meters. Electrical-indicating analog panel meters are described in a catalog that covers complete lines for commercial, industrial, military, and aircraft applications. Dixson Instruments, Attn.: Meter Sales, P.O. Box 1449, Grand Junction, Colo. 81501. Phone (303) 242-8863 [423]

Toggle switches. Catalog No. 20 from Machine Components Corp. describes and illustrates a line of X-and X-Y-axis-motion joystick toggle switches. The switches are available with momentary or maintained contacts. Applications include motor controls, X-Y readers, microscope tables, telescopes, and aircraft-pilot

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New literature

controls. Machine Components Corp., 70 Newtown Rd., Plainview, Long Island, N.Y. 11803 [424]

Using NBS broadcasts. Published by the National Bureau of Standards, NBS Technical Note 668 is a guide intended to help users get the maximum benefit from NBS short-wave transmissions. The guide, which is entitled "The Use of NBS High Frequency Broadcasts for Time and Frequency Calibrations," places special emphasis on the use of wwv and WWVH signals to measure time or set clocks with a resolution of 100 microseconds, and to calibrate frequency to within a few parts in 10¹⁰. wwv broadcasts from Fort Collins, Colo., and WWVH from Kekaha, Kauai, Hawaii. Copies of Tech Note 668 may be ordered from the Superintendent of Documents, U.S. Government Printing Office, Washington, D. C. 20402. The price is \$1.05.

Patent information. A variety of online patent-information services that give vital data on patents from 24 countries is offered by Derwent Publications Ltd., Rochdale House, 128 Theobalds Rd., London, WC1X 8RP, England. The computer-based services are described in brochures which are available from the company. [426]

Drafting practices. A comprehensive manual of universal drafting techniques entitled "Manual of Modern Drafting Practices" is a 580-page loose-leaf manual being offered for sale by General Electric for \$95 plus \$2.95 for shipping. Used for more than 40 years by GE, the manual is updated every six months. The semiannual updating service is available for \$40 per year. Quantity discounts are offered. The manual and further information can be obtained from Technology Marketing Operation, General Electric Co., Bldg. 5, Rm. 311, One River Rd., Schenectady, N. Y. 12345 [427]

Solid-state relays. An application note entitled "Solid-state Relays: They're Great When You Use Them Right" discusses the relative

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Electronics/March 4, 1976

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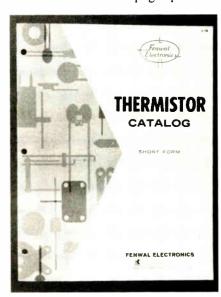
New literature

merits of these relays and conventional electromechanical relays. It suggests when to use each type and tells how to prevent certain common problems in applying SSRs. Copies of Ideafile No. 8 are offered by Heinemann Electric Co., Magnetic Dr., Trenton, N. J. 08602 [428]

Fluke catalog. A 24-page product directory and short-form catalog from John Fluke contains brief descriptions of the company's lines of test and measuring equipment. Included, among other things, are digital multimeters, digital thermometers, ac and dc standards, frequency synthesizers, electronic counters, logic-test equipment, and automated test equipment. Copies are available from John Fluke Mfg. Co. Inc., P.O. Box 43210, Mountlake Terrace, Wash. 98043 [429]

High-voltage interconnections. An expanded family of nearly 200 interconnecting leads and hermetic connectors for high-voltage applications is described in Catalog No. 73-218 from the Capitron division, AMP Inc., Elizabethtown, Pa. 17022. The 48-page catalog includes electrical, mechanical, and materials specifications. [430]

Thermistors. Catalog L-1B includes both product data and applications information on a wide variety of thermistors. The 12-page publica-



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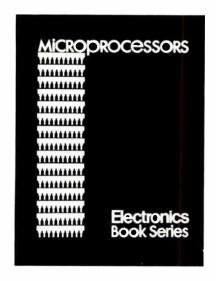
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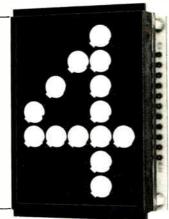
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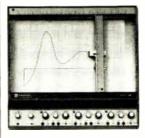
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ELECTRONICS DIVISION, 121 Industry Street, Toronto, Ontario, M6M 4M3, Canada Telephone: (416) 762-3661 Telex: 06-22007. 7501



MaX-Y line-up





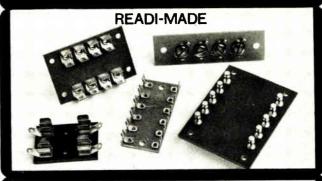
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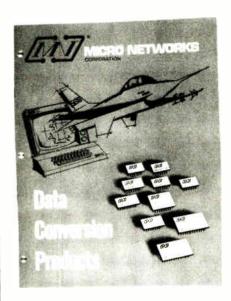


Color coating: 1.filter, 2.paint or 3.plastic?

New literature

tion, which can be obtained from Fenwal Electronics, 63 Fountain St., Framingham, Mass. 01701, includes 13 circuit diagrams. [431]

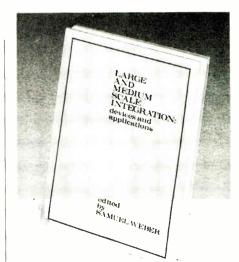
Data converters. More than 100 data-conversion products, such as analog-to-digital and digital-to-



analog converters, and sample-and-hold amplifiers, are covered in a 16-page catalog from Micro Networks Corp., 324 Clark St., Worcester, Mass. 01606 [432]

Interface circuits. The word "interface" has been applied to a variety of functions that are not easily classified. Motorola, therefore, has chosen to put all of its interface integrated circuits into a single. 36-page "Linear Interface" brochure, which contains data on memory interfaces, bus interfaces, instrumentation interfaces, communications interfaces, and computer and terminal interfaces. Copies may be obtained from the Technical Information Center, Motorola Inc., Semiconductor Products division, P.O. Box 20924, Phoenix, Ariz. 85036 [433]

Cylindrical connectors. A line of cylindrical multicontact, coaxial, high-voltage connectors, all of which have a Quick-Lok feature, is described in a short form catalog put out by Lemo U.S.A. Inc., 2015 Second St., Berkeley, Calif. 94710. [434]



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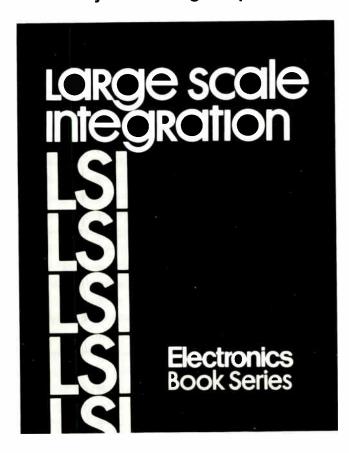


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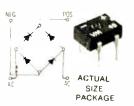
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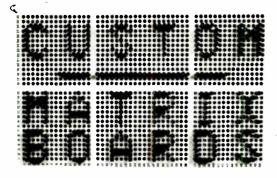
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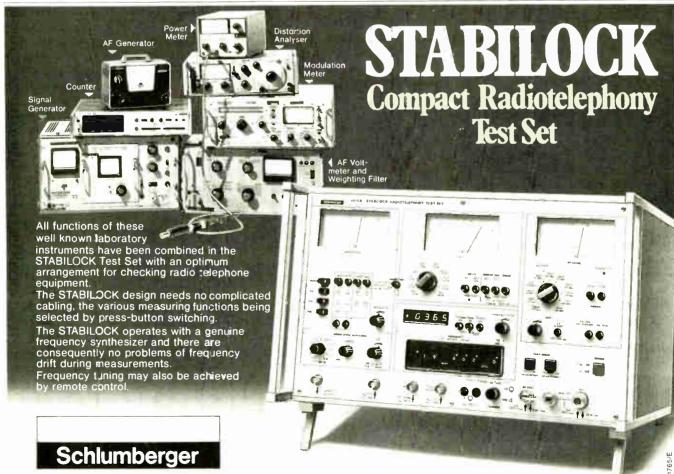
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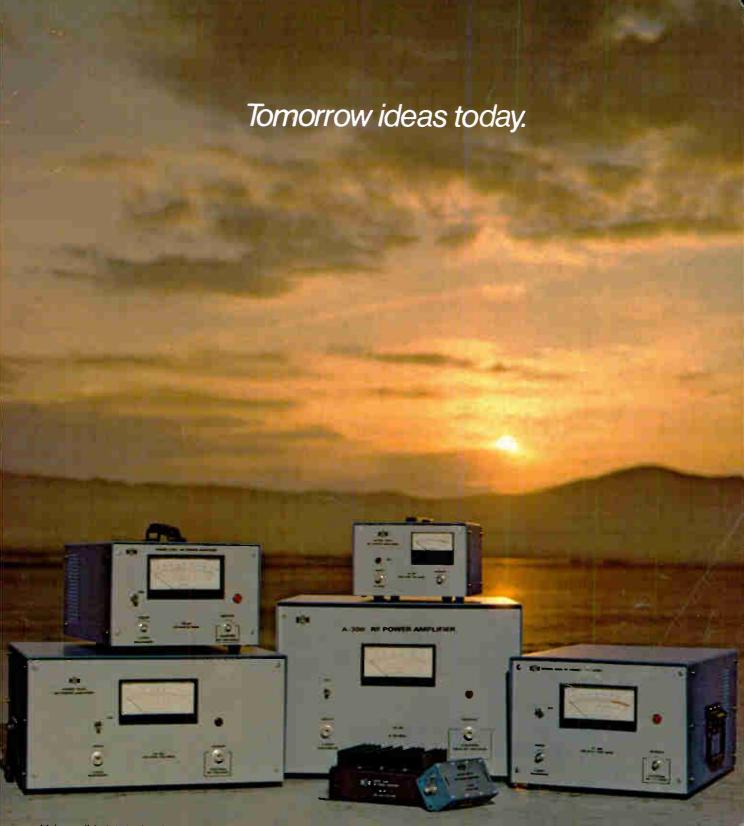
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A wise man once said, "A chain is only as strong as its weakest link".

That phrase says as much for electronic circuitry today . . . as it originally did for the value of the individual quality of man. For example, the failure of a single tiny printed conductor path in a resistor network can cause the failure of an entire circuit . . . or system.

Bourns doesn't want that to happen to one of your circuits. For that reason, we want to share some "inside" information about the design and manufacture of thick-film networks . . . so that you can be a more knowledgeable and more selective specifier



Lead Termination **Failure**



During Bourns initial design program, customer interviews indicated that commonly used "lap joint" and "butt joint" lead termination designs were subject to failure due to weakening of the solder termination during PC board wave soldering operations, and in-circuit heat cycling and vibration. These design-types depend heavily on solder alone for both mechanical and electrical bonding of leads to the substrate.

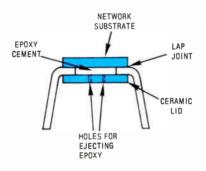




With this in mind, Bourns engineers developed the "Krimp-Joint^{IM}" lead frame termination design to protect customers from this

Bourns Krimp-Joint leads are firmly crimped onto the network element, much like a vise grasps a piece of lumber. To "cinch" the electrical connection, a special high temperature, reflow resistant solder is also used

3. The Packaging



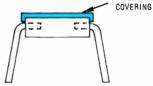
Various types of DIP packaging are utilized of which the molded and "sandwich" types seem most common. One problem that frequently occurs with the sandwich types is delaminating. This happens when air in tiny voids remaining in the epoxy filler (bonds the substrate to the sandwich "lid") expands in hot operating environments to the extent that the package comes apart and fails.

Bourns Krimp-Joint networks are encased in a homogenuous molded thermoset plastic package, which is highly heat resistant. Both 14- and 16-pin DIP models are machine insertable, and are available in handy cartridge packages.

Power

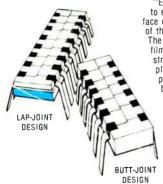
Bourns uses a high-copper alloy lead material to enhance power dissipation capacity. Other materials - ferrous and brass alloys -- do not have comparable performance. Furthermore, there is potential for rust with the ferrous alloy material. The highcopper alloy costs us more . . . but we think your satisfaction is worth it.

5. A Good Coat Is **Important** THIN ORGANIC



Our little network package must "weather" the homo sapien as well as the electrical environment. Example? Some users report that marking the top of example? Some users lepth that making the top of thinly coated networks actually changed internal resistor values. With the tight board spacing found in most equipment cabinets, components occasionally get scraped when boards are inserted and/or removed. Customers report that some thinly protected networks have shorted-out or opened under these conditions. Bourns networks wear a heavy coat of molded plastic to weather the homo sapien climate

2. Krimp-Joint Eliminates "Edge-Arounds"



EDGE-AROUND CONDUCTOR PATHS

"Edge-around" thick-film printing techniques are required by some designs to electrically connect the network circuit — printed on the horizontal surface of the substrate — to pin leads which are always "butted" to the **edge** of the substrate, or are "lap-jointed" to the opposite side of the substrate. The latter condition exists with lap-joint designs when more complex thick film circuits are executed which require printing on both sides of the substrate (such as resistor/capacitor networks, dual terminators, special application circuits, etc.). Edge-around printing leaves a natural conductor path weakness on the fine edges of the substrate, resulting in the possibility of a very "tenuous" connection. Such connections are subject to failure after exposure to heat cycling, shock, vibration, etc., and can result in an open circuit condition. Sometimes an intermittent condition results, which makes fault diagnosis more difficult.

Since most packages are not tested at full rated power during manufacturing QC, weak edge-arounds sometimes pass final tests... and then burn-out (like a fuse), when subjected to full power in an operating

Bourns Krimp-Joint mechanically contacts both top and bottom surfaces of the resistor network substrate, resulting in a strong, positive connection between pin lead and both sides of a network circuit. No edge-around paths are required.

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