HOW TO DESIGN DIGITAL CIRCUITS FROM SCRATCH

S1 25 **DEC**. 1978

THE MAGAZINE FOR NEW IDEAS IN ELECTRONICS

COVER STORY SOLAR ENERGY CONTROL

A guide to interfacing and controlling solar energy panels. Story starts on page 35.

HI-FI SPEAKER SYSTEM

State-of-the-art time-compensated design you can build yourself for true hi-fi sound. Construction starts on page 38.

REMOTE TELEPHONE EAR

Easy to build telephone accessory lets you monitor the sounds in your home from a remote location. Turn to page 67.

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Math board for 1802-based microcomputers speeds execution time and saves memory. Construction details start on page 45.

TO THE RESCUE

ions for the PROM make digital er. Story starts on page 43.

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GERNSBACK

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DESIGNING DIGITAL CIRCUITS from scratch. The step-by-step approach starts on page 63.

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As a service to readers, Radio-Electronics publishes available plans or information relating to newsworthy products, techniques and scientific and technological developments. Because of possible variances in the quality and condition of materials and workmanship used by readers, Radio-Electronics disclaims any responsibility for the safe and proper functioning of reader-built projects based upon or from plans or information published in this magazine. looking ahead

TV and hi-fi: Important developments are in the wind in the field of TV sound—a chain of events set in motion by the very limited use of satellites for domestic networking and followed by the telephone company's conversion of its intercity relay systems to diplexed sound (**Radio-Electronics**, July, 1978). Both relay systems now accommodate sound channels capable of a frequency response up to 15 kHz, compared with 5 kHz under AT&T's old system of transmitting TV sound by separate telephone line. Since last January, most network broadcasting (except for an occasional 16-mm movie) has been accompanied by noticeably better sound. But that's not all—AT&T's network lines now are capable of handling two discrete multiplexed sound channels, each with a frequency response out to 15 kHz.

This two-channel sound will be available with the TV signal as soon as a rate schedule is worked out by AT&T and approved by the FCC. There are no stereo TV sets, of course, and TV stations aren't allowed to transmit stereo sound signals—even if they were equipped to do so—so the first use of two-channel sound is likely to be for the simulcast FM-station sound accompanying TV music broadcasts. The diplexed signal along with the TV picture eliminates the complicated synchronizing and phasing processes that are needed when the stereo sound is networked separately from the picture, as traditional in network stereo simulcasts.

So now we have an interesting situation: Most TV broadcasting is accompanied by true hi-fi sound, but virtually no TV receivers are capable of passing it on to viewers. Network television is capable of transmitting stereo or other types of dual-sound signals, but stations aren't permitted to broadcast with dual sound. However, there are signs that this impasse will be broken. Most TV set manufacturers are now working on improved sound systems to take advantage of the better vibes coming from the stations. Don't expect hi-fi perfection, but starting with next spring's lines of sets, some models will offer wider frequency response, higher-powered amplifiers, bigger speakers and better baffling.

For those who can't wait for better TV sets to provide higher-quality sound, there will be a growing number of hi-fi video tuners and receivers, such as those now being developed by Pioneer and Wintec, both in direct response to the better sound offered by stations.

Will the next step be stereo sound? The whole subject is highly controversial, and many broadcasters, as well as some TV set manufacturers, will tell you at the drop of a decibel that stereo isn't suitable for TV—there's not enough music being broadcast, the picture is too small, and so forth. But there are exceptions in both ranks. Projection TV manufacturers are gung ho for stereo sound; as is the Public Broadcasting Service, whose associated stations originate many musical programs. Sylvania also hopes to take leadership among set manufacturers in pushing for stereo.

Even if broadcasters and manufacturers collectively don't want stereo, this doesn't mean they're against twochannel sound. Many approve of a system that was used in Japan in 1970 (and about to be revived there). This system provides two completely discrete channels, and can be used for language translations, stereo or any other application in which two sound tracks can be used. The broadcasters—led by ABC-TV—see the dual-sound track concept as a winner in bilingual areas such as New York, southern California and Miami, where viewership is low among those whose primary language isn't English. Manufacturers certainly wouldn't mind producing TV sets with special circuitry for selecting sound Channel A, Channel B or stereo.

The next step is expected to be the formation of an industry committee to develop and test various two-track proposals before any proceeding by the FCC. It could take from three to five years before definitive standards are set for stereo and other dual-sound TV broadcasts but they do finally seem to be on the way. And the pressure for stereo won't be lessened in any way when videodiscs come on the market. Many of these discs will have stereo sound, and all videodisc recorders will have jacks for stereo inputs.

New VCR's: Home videocassette recorders are proliferating, with new, more versatile step-up units reaching the market this fall. Perhaps the most fascinating is a programmable unit made by Matsushita Electric and being marketed in two slightly different versions under the Magnavox and RCA brandnames, with other similar units to come. The new VCR takes maximum advantage of the four-hour-percassette recording mode of the VHS format. Using a builtin microprocessor, a fluorescent digital display and a 14pushbutton varactor tuner, the new VCR may be programmed up to one week in advance to record four different shows, automatically turning on and off and switching channels. An optional mode of programming permits the recording of the same show every day of the week.

An interesting feature of the programmable VCR is "electronic indexing." A special electronic cue is placed on the tape at the start of each recording, whether the machine is in the manual or the programmed mode. The beginning of any program then may be located automatically by pushing the fast-forward button.

New VCR's in the Beta and VHS formats have been introduced for outdoor recording. The Sony and JVC units each weighs about 20 pounds. A rechargeable battery will operate the VCR and its associated color camera for one hour on a charge. Accessory tuners and timers are available for recording TV programs off the air.

A new VHS-format recorder by JVC lets you double your television viewing without increasing the amount of time you watch—that is, if you don't mind speeded-up action. A special remote-control switch plays tapes at double speed. Digital encoding keeps the sound at the proper pitch and comprehensible. The same unit provides a freeze-frame picture when the pause button is pressed. A new Hitachi VHS recorder also has the freeze-frame feature. With a new recording head, Hitachi claims a picture signal-to-noise ratio of 46 dB, which it says is the best of any home VCR.

> DAVID LACHENBRUCH CONTRIBUTING EDITOR



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Quartz analog watches tells time with "electronic hands"

Texas Instruments' completely electronic *Time Indicator* watch is an antimagnetic, shock-resistant LCD quartz analog watch that tells time without any moving parts, using "electronic hands." TI believes this design may provide the answer for those who want a watch without gears or other moving parts to wear out, yet need to see a visual time relationship.



TEXAS INSTRUMENTS new *Time Indicator* watches combine the advantages of both mechanical and digital watches.

The liquid crystal display that sweeps the face of the watch (in similar fashion to mechanical hands) is driven by an IC. Several timekeeping modes are availatile: hours/minutes; minutes/seconds; hours/minutes in another time zone; plus day and date; and a stopwatch gives the elapsed time in hours/minutes, minutes/seconds and seconds/10ths of seconds.

The watch is powered by a lithiummanganese dioxide battery and comes in two styles: round, in either white-gold or yellow-gold tone metal or square in either stainless steel or gold-tone metal; the strap can either be of leather or matching metal clasp bracelet. The watches range in price from \$275 to \$325.

50 Radio Shack Computer Centers to open in 1978-1979

Lewis Kornfeld, president of Tandy Corporation's Radio Shack Division, has announced that 50 new computer sales and service stores will open in 1978-1979; the new ventures will be called Radio Shack Computer Centers.

Although some of the Computer Centers will be located in new or existing Radio Shack stores, most will be separate operations in major market areas. According to Mr. Kornfeld, "their purpose will be to assist area Radio Shack stores in answering computer questions, closing sales and developing quantity sales (particularly of Radio Shack's TRS-80 system), and peripheral systems to businesses and institutions."

The Centers will provide classroom areas to teach computer use and programming to customers. In addition to servicing Radio Shack computer products, the Centers will sell a variety of components, software and some hardware of brands other than the TRS-80 system.

Video inventions use liquid-crystal switches

Two new inventions use liquid-crystal optical transmission switches that can help reduce the size, price and power requirements of video cameras, screens and projectors.

The "Flying Hole Video Camera" and "Flying Hole Display and Projector" use a two-dimensional array of liquid-crystal switches operated so that all but one are opaque. This "hole" is moved around in a scanning pattern.

In the camera, the light that is transmitted through the hole is converted to electrical signals by a photo detector or colorsensitive photo detectors. In the projector and display, the light is projected through the hole by a light source or a group of color-sensitive light sources. Since the video projector/display does not require a cathode-ray tube, it is small and flat enough to be either wall-mounted or worn on the wrist. The inventions were announced by an independent Canadian inventor, Donald L. Orr.

Wrist device prints messages, aids handicapped

Canon, U.S.A., a subsidiary of Canon, Inc., Tokyo, has developed a small batteryoperated wrist device that prints messages



WRIST DEVICE, the *Communicator*, keyboard contains 26 letters, and shift, back and space keys to print messages at a 10 character-persecond speed. The device is a special fast communication aid for nonverbal, deaf and other handicapped persons.

on paper tape designed to provide fast communications for persons suffering from a variety of verbal or motor disabilities.

The device called the Communicator is

distributed by Telesensory Systems, Inc., 3408 Hillside Avenue, Palo Alto, CA 94304. It is as small as a pocket dictionary, weighs 11 ounces and can also be worn around the neck. Messages are printed on paper tape at a speed of 10 characters-per-second, with a tape storage capacity of 12,500 characters. The keyboard contains 26 alphabetical letters arranged according to frequency of use, plus it contains shift, back and space keys. Pressing the shift pushbutton allows numbers and symbols to be used, and vowels and consonants are differentiated by color. The *Communicator* sells for \$549.

Computerized information system is developed for Canadian TV viewers

Three Canadian firms, Bell Canada, Southam Press, Ltd., and Torstar Corporation have agreed to cosponsor a pilot demonstration of a data system that will provide information stored at a central computer and transmitted via telephone lines to TV viewers whose sets are connected to the computer center.

The data system (known generically as videotex) consists of a computer connected to a single TV set with an attached keyboard. Using the keyboard a viewer can retrieve preprogrammed data, which is then displayed on the screen either as words or graphically. A Bell Canada spokesman said that this pilot demonstration will be followed by a more extensive market study later in 1980, in which the computer will be connected to several home TV terminals. Suggested future applications for the system are in providing weather news, travel data, general news programs, entertainment and even some advertising.

NESDA/ISCET choose officers and distribute awards at 1978 NESC

Approximately 600 persons attended the August 1978 National Electronics Service Convention (NESC) in Portland, OR. Among the many scheduled events, including a trade show, seminars and many social functions, were both the NESDA and ISCET conventions.

The ISCET and NESDA agenda included the election of officers for 1978–1979, as follows:

For NESDA—president, Robert A. Villont; vice president, Warren Baker; secretary, West Correll; treasurer, George Simpson; and regional officers Ted Stackhouse, Dorothy Cicchetti, Joe Gately, Billy Williams, Art Nelson, Bill Abernathy, Keith Knos, Jack Kelly, Bill Lawler, and Dick Scott.

For ISCET—president, Jesse Leach (also serving ex-officio on the NESDA executive committee and council); vice chairman, *continued on page 12*

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DECEMBER 1978

new & timely

continued from page 6

Forest Belt; secretary, Leon Howland; and treasurer, George Sopocko.

Those present at the ISCET convention voted unanimously to return ISCET headquarters to NESDA facilities in Indianapolis. A Joint Internal Affairs Committee was appointed by ISCET and NESDA presidents to study and consider the question of ISCET/NESDA autonomy. A report, incorporating the results of an opinion survey taken among the respective bodies, will be presented at a joint ISCET/NESDA membership meeting in 1979. The committee is composed of ISCET members Herschel B. Lawhorn, Dorman L. McDonald and Larry Steckler (chairman), and NESDA members Warren Baker, Jack Kelly and Dick Scott.

Among other scheduled events was a NESDA awards banquet at which LeRoy Ragsdale, outgoing president, received both the "Man of the Year" award and the "NESDA Outstanding Officer" award. Other winners were Don Surrette, Leo Cloutier, Nolan Boone, Fred Schuneman, Warren Baker, Gene Dillingham, Ray McAllister, Keith Knos, and Morris Finneburgh, Sr.

Another NESC highlight was the induction into the Electronics Hall of Fame of two new members: Enos Rice (CES/CET) and (posthumously) the late Ralph Johonnot. Mr. Rice, who is 74 years old, was honored for his 44½ years in the service industry and for his long involvement in and support of NESC activities. The late Mr. Johonnot was cited for his long career in the service industry; for his many years as an officer with the California State Electronics Association; and for his part in the inception of the CET program and the Western States Conference.

The 1979 NESC will be held in Tucson, AZ, concurrently with the Arizona State Electronics Association Convention.

Integrated optical device has multiple applications

A small integrated optical device that can have multiple applications has been developed by Bell Labs. It can be used as a logic element in optical memories; a pulse shaper and limiter; an optical switch; a difference amplifier; and as an "optical triode."

Operating at extremely low power levels over a wide range of wavelengths, the device is an optical waveguide version of a nonlinear Fabry-Perot resonator whose nonlinear characteristics are produced by using a photodetector output to drive the resonator's electro-optical elements. Other features include acceptance of electrical or optical outputs; its nonlinearity can be modified using a nonlinear circuit; acceptance of multiple logic inputs; and multilevel operation.

The device is processed using standard IC techniques by diffusing titanium ions

onto an electro-optic lithium-niobate substrate. Incoming light rays are reflected back and forth between dielectric mirrors affixed to the cleaved ends of the substrate material. A beam splitter transmits a portion of this light to the detector whose output is used to create an electric field



BELL LABS integrated optical device is Fabry-Perot resonator formed by dielectric mirrors on the ends of the electro-optic lithium-niobate substrate material.

between the electrodes on the crystal that modulates the refractive index of the crystal and produces the nonlinear characteristics.

In multilevel operation or A/D conversion, a high gain is required in the feedback loop, providing as many as 15 transmission levels. With less feedback and the resonator tuned for transmission showing a hysteresis characteristic, the device functions as a memory element. When operated in a high-transmission state, its constant power output lets it operate as an optical limiter. In the "optical triode" mode, when the resonator is tuned to transmit an S-shaped waveform, power transmitted through the waveguide changes rapidly according to the output. A small degree of light at the detector produces a vast change in transmitted light; a weak light signal falling on the detector controls the transmission of a power light beam on the device.

Morizono, Leonard, Staes win SMPTE awards

The Society of Motion Picture and Television Engineers (SMPTE) awarded the 1978 David Sarnoff Gold Medal to Masahiko Morizono of Sony Corporation. Mr. Morizono, general manager of Sony's Video Products Division, was cited for his leadership and outstanding engineering accomplishments in developing TV electronic news gathering (ENG) equipment. Mr. Morizono's other achievements include the development of portable helical-scan VTR systems exhibiting high-calibre editing capabilities; plus the design and development of audio and instrumentation recorders, Umatic cassette recorders, time-base correctors, cameras and accessories.

SMPTE also presented its 1978 Journal award to Eugene Leonard, Da Vinci Systems Group, with honorable mention to K. Staes, Agfa-Gevaert N.V., for their paper published in the October and August 1977 SMPTE Journal, respectively.

The award was presented at the Society's annual Awards Presentation in October, 1978, at the Americana Hotel, New York City.

Zenith releases three-hour VCR, color cameras, tape

A three-hour video cassette recorder, two color cameras and a three-hour-format cassette tape are all part of Zenith Radio Corporation's new fall line.

The model KR9000W is a single-speed VCR that can record up to three hours of programming using Zenith's three-hour tape. The remote PAUSE switch lets you stop the tape in either playback or record modes, so that you can edit during recording or stop playback if you must leave the room. Other controls include a six-pushbutton keyboard, a clock/timer, UHF/VHF tuners, automatic fine tuning control, and a special tracking feature to handle variations in prerecorded tapes.



THREE-HOUR VIDEO CASSETTE RECORDER (model KR9000W) released by Zenith is a single-speed unit with built-in UHF/VHF tuners; timer; standard pushbutton controls; tracking switch; remote PAUSE control; and tape counter with memory.

The two color cameras for use with the VCR (models KC1000 and KC1250) offer a number of significant features: the model KC1000 has a pop-up viewfinder that matches the view angle of the 25-mm-long barrel lens designed to be used with a trielectrode vidicon tube; a built-in mike; a remote PAUSE switch; plus adjustable temperature and brightness controls and an automatic light-level switch. The model KC1250 (which incorporates all the features of the model KC1000 color camera) also has a 6:1 Canon zoom lens incorporating a viewfinder with a 11/2-inch black-andwhite monitor screen. Suggested retail price range: \$2095-\$1395. R-E



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editorial

Radar Speed Traps, Detectors and Countermeasures

Technology is a wonderful thing. It starts off by doing a job. We can use radar to identify speeders on the highways . . . those people who endanger the lives of others using American highways. It can also help increase the monies received by a municipality in traffic fines. And it's been with us for a long time now.

Recently, electronic countermeasures have become available to the public in the form of detectors that warn you when you near a radar trap. Government agencies have fought these devices in several ways-made them illegal in some states, used portable units, different frequencies. Here's the latest development . police departments are buying miniature low-power radar transmitters to sprinkle along the sides of the road to trigger those radar detectors. Maybe they hope that after you've run through three or four false alarms you'll ignore the real radar and contribute some dollars to the local budget. By the way, those transmitters are frequently made by the same people who make your radar detector.

Then comes the latest countermeasure, a nifty little radar transmitter for the motorist. According to the outfit offering it, you simply dial in the speed you want the radar trap to clock you at. Your transmitter then issues a signal that tells the police you're only going 30 MPH or whatever you select. We're deliberately not listing the name or address of the manufacturer since we don't think such a device is legal under FCC rules. Please don't write us for that information because we won't respond. But what we would like to know is what you think of such devices and can you dream up any that are even better.

Electronic countermeasures aren't new, but what a funny world it is when so many dollars are being spent in a battle with the traffic laws. Maybe some serious thought to raising the speed limits to a reasonable level would make more sense.

dany Stee

LARRY STECKLER Editor

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WHAT IS AN ELECTRONIC ENGINEER?

As an engineer and head of a growing organization, I often meet people who don't really know what an electronic engineer does. All too often these people visualize an engineer as "someone who sits in a cave and designs things." Nothing could be farther from the truth, and I'd like to dispel this myth.

As I see it, the electronic engineer has the job of creating new electronic equipment or devices using the latest technology and to be manufactured at the lowest cost. In short, his job is to make money for the company that employs him. Besides circuit design, you'll find the engineer assisting in all areas from design to final product. And he will often be there when the design is being upgraded!

To be more specific, a typical engineer can spend as little as 10% of his time on actual circuit design. (This depends on the project, of course!) The rest of his time is

Double

spent assisting draftsmen in circuit-board layout, assisting in package design, writing specifications for the product, selecting components and materials for the production models—not to mention the building of the prototypes!

After the device is built on the production line, there are process-control problems to resolve, quality-control problems and more. Ever see 20,000 pieces of equipment roll off a production line—defective? This same engineer may step in and solve the problem—saving the company money. This is engineering in its finest hour, and an important distinction. You'll find most engineers in industry doing at least most of these things, and often much more. The engineer is rewarded with a sense of accomplishment, a modest salary and, if he's good, a chance at getting a management level job.

Now that you know what an engineer does, let's look at working with IC's. Actually, working with IC building blocks on

paper isn't too hard. The computer fans do this often, as can anyone with the IC handbook. I believe this sort of work should be called designing. It is different from engineering because the designer generally does not become involved with all the areas mentioned previously; at least this has been true of the "designers" I know. Also, an engineer designs a circuit by either selecting components known to best fit the job from past experience, or by using components of the latest design. This process requires an extensive knowledge of electronic circuitry, electronic components, plus manufacturing processes. All this expertise is necessary to build a project easily at the lowest cost . . . and cost is the name of the game in electronics today. None of the designers I know have the knowledge to optimize their circuitry. Also an extensive knowledge of electronics is important when the production line shuts down-an engineer would know how to start it up, a designer might not. And that's what sepa-

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rates the designers from the engineers-knowledge and experience!

If there are any doubters among the designers, let's see them design an 8-bit A/D converter for under \$5. It's been done-but not by designers. Cost is the reason; an IC block approach costs too much

To sum up, I propose that the people who string IC's together should be called "designers," and people who do the same but have extensive knowledge about their product should be called "engineers." GARY MCCLELLAN

S-100 BUS COMPANIES

I recently received from you a list of companies dealing with the S-100 bus.

I was somewhat surprised to see that the list was only two pages long. And it didn't even include the manufacturers of the equipment I have running in my system. Therefore, I decided to do a bit of research on a small scale. I looked through just the February 1978 issue of Interface Age and the February 1978 issue of Kilobaud, and the only company I knew about that I couldn't find was PolyMorphic Systems, the manufacturer of my computer.

Therefore, I am including a list of the companies I found less the ones on your list, plus phone numbers. I've listed them in the order I found them:

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continued on page 22



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I realize this list isn't quite complete, but I thought you should update your list. I would like to add that Vandenberg Data Products has the only 16K RAM board available for the S-100 bus for \$330, fully static (kit) and rated at 250 μ s, quite suitable for a Z-80. Anybody else's static RAM costs roughly double that. I am currently running 32K of their memory and am thinking hard about another 16K. All components in my system (except the TV) were built from kits. MARCUS S. LEWIS Omaha. NE

S-100 BUS ADDITIONS

I enjoyed the S-100 article (April 1978 issue, page 48) and S-100 bus listing. In the listing you asked to be informed of other S-100 products not listed.

CGRS Microtech manufactures three boards for the S-100:

1. The 6502 MPU Board—to our knowledge, this is the only S-100/6502 board for the S-100. It has been in production for 18 months and is working very well.

2. The T.I.M. II I/O Board—this is a "system monitor board" that provides ROM, RAM, and parallel and serial I/O ports.

3. The Multi-I/O Board—provides I/O for the Persci 1070 floppy-disc controller card as well as 4K of ROM, two parallel ports and two serial ports.

WILLIAM M. GOBLE CGRS Microtech P.O. Box 368 Southampton, PA (215) 757-0284

> WHERE ARE THE ELECTRONIC DESIGNERS?

I wish to respond to your June 1978 editorial entitled "Where Have All The Designers Gone?"

Electronic designers have always been wiring up "black boxes" to form new devices. What are resistors, transistors, capacitors, inductors, transformers, etc., but "black boxes"? A black box is simply a device with known characteristics.

The designer today uses IC's to reduce the amount of drudgery and cost in his designs. How would you like to redesign the NAND gate hundreds of times for each digital project? If every general-purpose IC (i.e., gates, op-amps, latches, etc.) had to be built from discrete components, a digital clock, stereo, CB radio and any other electronic device would cost much more than it now does.

In answer to the question, "What does an electronic designer do?" I think I am qualified to respond since I am an electronics engineer with IBM. My job is to design, oversee construction and debug electronic circuits. It also requires taking sometimes as many as 100 separate inputs and creating the proper responses at the proper time. I use digital IC's to create these circuits and do consider each circuit to be a new one.

This *is* designing, just as engineers in the 1950's designed using only transistors, tubes and other less-complicated devices.

I don't think it has slowed either the development or development time of new devices; it has simply made it economically *continued on page 24*

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LETTERS continued from page 22

feasible to develop more complicated products. For example, with IBM's Series III copier (yes, I know I'm biased), would so many functions have been possible without IC's? I think not.

Finally, an electronic designer is someone who extracts information from electrical signals (inputs) with electronic components, and who controls (via outputs) other devices with that information. The process of creating the device between the inputs and outputs (which, by the way, are usually defined before the design is begun) is electronic design.

DENNIS A. ROWE Boulder, CO

With respect to your June 1978 editorial, I think that persons who use and apply integrated circuits are, indeed, designers. They may be using available devices to design something that is totally new, unique, and perhaps useful, too. I think that the IC has freed us to concentrate upon the task at hand without having to craft the tools that are necessary to accomplish it. Remember the days of the tube-based op-amp, the tube-based counters and flip-flops, etc.? They are gone, luckily.

If IC users aren't really designers, then neither are the solid-state physicists and semiconductor engineers, since all they are doing is moving around the same atoms in different ways. The silicon, gallium and arsenic remain the same . . . the designers just do different things with them, JONATHAN TITUS

In reply to your June editorial, I feel that the designer is still with us.

It seems to me that the designer's role has taken three different paths—on one path he devises all the IC's we use. On another path, he is a systems designer, responsible for the design of the complete complex system. And the third path is that of designing the new instrumentation used to service complex devices.

Today, the engineer can take the IC's and connect them together to make a circuit or simple electronic device.

I really don't envy the job of the instrumentation designer for he has to design instruments that are to be used repeatedly by people not in the electronics field. For example, automotive mechanics are finding more and more electronics in our modern cars. They cannot just exchange one module for another; because of increasing complexity, they must use instruments to troubleshoot the entire system to find the malfunction.

Twenty years ago, when I graduated from electronics school, we did not have very many test instruments. We could trouble-shoot and repair just about everything electronic with an OS-8 oscilloscope, TS-352 VOM, TV-7 tube tester and BC-221 frequency meter.

I would hate to troubleshoot and repair

some of today's complex electronics systems with those Stone Age instruments. If you look at some modern instrumentation, you can see the designer has been busy. And there is always something new being released to make our jobs easier.

I feel that economics, not the integrated circuit, has slowed down the development of new electronic devices. The question is now when a new product is developed, is it to be made inexpensive enough to be disposable when it malfunctions or is it to be made repairable? And who will repair it? There are a few products on the market that are disposable because of their low cost—radios, LED watches and calculators. But now I feel that many manufacturers are taking a wait-and-see attitude before producing any more low-cost products.

JERRY W. CLARK New Richmond, IN

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HOME COMPUTERS

I have a hobby computer. I have not yet and probably never will wire it up to "run the house." Most household applications can be controlled just fine with a motor and cam for several operations per day. If I did "control the house" with my computer, I couldn't experiment with new programs since I might erase the household program.

What I really do with my computer is write game programs. After spending many hours writing and debugging the program, I

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play the game a couple of times and start writing a new program.

I agree with you that the "Home Computer" is nonsense. The only application I might have for it is to balance my checkbook. Even then I would pull out my pocket calculator.

Many people ask, "Well then, what good is your computer?" I guess the best answer is, "What good is your home TV set?" E. MORRIS Midland, MI

I do not own a hobby computer. I do have three hi-fi sets, a VCR, model railroad, darkroom and studio to support; so when I received several estimates of the cost of a computer system (\$8000) with enough memory and "crunch" to cross-reference my collection of 1000 LP records and songs into several categories, I bailed out.

I do know about computers since we use one to roll our film and tapes at the TV studio I work at. I also can remember anytime, anyplace that next month the real estate tax is due. I know how much I have in several savings and checking accounts. I don't need an expensive computer to remind me (if I happen to be near it), but if the price were \$200 for the system described above with a cassette, easily interchangeable programs, a video (or TV) monitor or hardcopy at additional cost, I might buy it.

I guess what I mean is I don't like to "play games" just for the sake of playing games, and if it takes possibly more time to program the computer to do a job than it might take to do the job the usual way, why bother? But if the cost of computers drops as it did with small calculators, we will have a different ball game. DICK WARTENBERG Brooklyn, NY

DARKROOM TIMER

With respect to my article in the July and August 1978 issues ("Build A Digital Timer for Your Darkroom"), the accuracy of the schematic was excellent. However, there were six errors that must be noted:

1. In the table shown in Fig. 3 that lists the power and ground connections for the IC's, I wrongly listed pins 14 and 7 as power and ground for IC13. The correct pins are 16 for power and 8 for ground.

2. In Fig. 3 again, an LED is missing between pins 10 and 1 of DIS4, but this is an internal LED contained within DIS4 as are the others.

3. The point at which S4 connects to the main PC board should be labeled "D."

4. The Parts List did not have numbers for the transistors. They can be any NPN silicon transistors whose beta is between 50 and 150, such as 2N2222, 2N957, etc.

5. The part number given for S1-S3 is incompatible with the PC board/front panel combination. The correct UID Electronics part numbers are: S1: RSW-0622-SD-BB-S-B1-BK; S2 and S3: RSW-0022-SD-BB-S-B1-BK.

6. One of the key cap part numbers should be 42-3100-03, not 40-3100-03, The "42" indicates double-width, which is what the "0" key cap is. RAYMOND G. KOSTANTY R-F

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Super Sleuth Descrambler



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AS MORE AND MORE LAW-ENFORCEMENT CHANnels are being monitored by unauthorized listeners using scanners, many agencies have attempted to improve voice security by using "scramblers." A scrambler is an audio frequency inverter. High frequencies become low and low frequencies, high; the result is unintelligible.

The Super Sleuth descrambler is designed to reinvert audio signals to normal. It plugs into the external speaker jack of a scanner and is usually left switched to the normal mode. When conventional transmissions are received, the descrambler's internal speaker is connected directly to the scanner's speaker jack, and the descrambler circuitry is off. When a scrambled message is received, the Super Sleuth is switched to the scrambled mode, and an internal ring-demodulator circuit rearranges the inverted speech to normal speech.

The Super Sleuth is entirely self-contained, including eight internal AA batteries (not provided), and its three adjustable decoding controls are switch-selectable.

Many law-enforcement agencies use more than one audio code, switching codes at different times; the three switch-selectable controls allows the listener to individually adjust each control to correspond to a given code, and then leave it set so that rapid switch-selectable decoding of the scrambled message is possible the next time it is encountered.

A FINE TUNE control allows an occasional adjustment of the audio for natural voice quality (however, it is seldom needed).

Since descramblers (and scramblers) inject an audio signal into their circuitry, that signal should be removed from the resultant audio output so that it doesn't cause annoying interference. The *Super Sleuth* has a CARRIER/ BALANCE null control knob to accomplish this.

Finally, the volume control permits you to adjust the audio output to a comfortable listening level.

The Super Sleuth looks impressive, functions well and its audio quality is very natural. Although the top-mounted internal speaker would be inconvenient for under-dash mobile mounting, most listeners would probably use the unit next to a scanner in the home or office.

A 741 op-amp audio oscillator injects an continued on page 32



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Mode WTCPN: Plug-in pencil soldering iron with comfort-cool heat shield, 17 available styles and sizes of interchangeable tips for 600, 700, and 800°F ten perature contrct, integral tip storage tray. 120V, 60Hz, 60W. Optional de-scidering adaptor.

Mode MP: For precision soldering of miniaturized electronics, especially PC work. Plug-in, true penpil shape 650 or 750°F ron with 1/8" diam. tips available in 8 sizes and styles. 120V, 60/40Hz, 22W. Molei DS100: De-soldering and re-soldering unit designed for PC board repair and re-work lines. Utilizes interchangeable 600, 700, and 800°F soldering tips and interchangeable 700 or 800°F de-soldering heads. Complete with footswitch, twin safety "ool holder, vacuum adjustment gauge, and cleanable, replaceable see-thru solder collector. 8 de-soldering tiplet sizes and 17 soldering tip styles (in 3 temps) available. Operates from factory air and line voltage.

Yes, of course, the entire family is UL listed and OSHA compliant. Now that you've been introduced, any leading electronic distributor will display the units and fill you in on all the features. For technical information and consultation, write the factory on your company letterhead.

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y father always told me that tages to putting all your eggs in one basket. "John," he said, "learn to do one important thing better than anyone else, and you'll always be in demand."

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Consider doctors. You wouldn't expect your family doctor to perform open heart surgery or your dentist to set a broken bone, either. Would you?

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I'll tell it to you straight. If you think electronics would make a nice hobby, check with other schools.

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EQUIPMENT REPORTS

continued from page 27

adjustable tone into a diode-bridge ring modulator (mixer), where it heterodynes with the inverted audio from the scanner's speaker-jack output. The difference frequency (in this case, normal unscrambled speech) is fed to an LM38ON amplifier IC for up to two-watts audio output.

The Super Sleuth weighs only 2 lb. and measures 10 W \times 3¹/₂ H \times 5 inches D. Its power drain is 12 VDC at 25 to 300 mA depending upon audio-output level. The unit is available for \$79.95 from Krystal Kits, Box 445, Bentonville, AR 72712. **R-E**

Continental Specialties LP-2 and DP-2 Logic Probes

ALTHOUGH USING AN OSCILLOSCOPE AND EXpensive logic analyzers to solve knotty troubleshooting problems is really best, logic probes save time in most situations. Using very few controls, the models LP-2 and DP-1 probes manufactured by Continental Specialties offer a combination of simple "go, no-go" testing and a high degree of diagnostic sophistication.

The model LP-2 logic probe is a hand-held instrument that uses three LED indicators to display logic levels and pulse transitions via a dual-threshold window comparator and bipolar edge detector. The model LP-2 probe is a less expensive version of the model DP-1; it has reduced frequency response, higher input impedance, and it lacks a pulse-memory feature.

In the model LP-2, a single switch selects either DTL/TTL or CMOS/HTL levels. For static tests, the upper LED illuminates when the voltage at the probe tip is within the selected logic family's high-level range. The TTL and DTL logic 1 threshold is 2.25 volts ± 0.15 volt, and for CMOS and HTL, the threshold is 70% of the supply (V_{∞}) or higher. Similarly, the lower LED indicates logic levels within the low range. Logic 0 levels are 0.8 volt ± 0.1 volt for TTL/DTL, and 30% or less for CMOS/ TTL. No LED illumination means there is an open circuit. Observing LED's near the probe tip is much simpler and less tiring than reading a voltmeter or oscilloscope screen, and then having to mentally translate that reading to



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acceptable or abnormal limits.

The pulse indicator flashes for 0.1 second every time the input signal makes a 0 to 1, or a 1 to 0 logic-level transition. A repetitive signal causes the indicator to illuminate constantly with an intensity that depends on duty cycle and frequency. Duty cycle is determined by observing the upper and lower LED's when the pulse light is lit. For example, if the upper LED illuminates, the signal is high most of the time and therefore consists of negative-going pulses. If both the upper and lower LED's have equal intensity, the duty cycle approaches 50% (or a squarewave).

Signals greater than 10 Hz but less than 100 kHz cause the LED to flash at a 10-Hz rate determined by the probe's 0.1-second pulse stretcher. With signals greater than 100 kHz and near 50% duty cycle, only the pulse LED is illuminated. The upper or lower LED's light as the waveform duty cycle deviates from symmetrical squarewave pulses at the higher frequencies.

The model LP-2 operates with pulses as narrow as 300 ns and a maximum frequency of 1.5 MHz. Overload protection is effective up to 50 volts DC, or 117 volts AC, for 15 seconds. To use the probe, power must be provided through black and red clip leads that are connected to the power source supplying the circuit under test. The probe's drain is 30 mA at 5 volts and 40 mA at 15 volts; and the input impedance is 300,000 ohms.

Several accessories are available, including a 2.5-inch probe tip, hooks and adapters, and ground clips. The *model LP-2* can be used for troubleshooting an inoperative divider chain by quickly finding the defective binary stage. It could also be useful to determine the static and pulse logic conditions on IC terminals. Using this probe to examine a microprocessor's ad-

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dress leads could help pinpoint a grounded bus run when compared with the other active bus lines.



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The model DP-1 is a digital pulser probe with tristate output. It resembles the model LP-2 physically, except it has only one LED pulse indicator plus a pulse-control pushbutton. A TTL/CMOS switch chooses output logic levels. The generator produces a single output pulse for each pushbutton operation. Depressing the pushbutton transmits a 100 pulse-per-second pulse train. Depending on the present logic level of the circuit area being probed, this produces differently formed logic pulses. If the probe senses a low logic level, it generates a positive-going pulse in an attempt to switch the logic-level state high. Conversely, if the logic level is high, the probe generates narrow negative-going pulses. Connecting the

probe to a variable-state point, such as a crossconnected feedback lead of a gate-wired flipflop, generates a 50% duty cycle (continuous waveform).

In the TTL mode, the *model DP-1* produces a $1.5-\mu s$ pulse with 100-ns risetime, and 500-ns storage and falltimes with a single TTL load. The storage and falltimes decrease with increased loading. The probe drives outputs as well as inputs, as long as the combined load is within the probe's 100-mA source and heatsink capability.

The CMOS mode produces wider pulses for the higher-impedance (and generally slower) logic family. Pulse width is 10 μ s with a risetime under 100 ns, 8- μ s storage and falltimes with a 100K load, and sink and load capacity of 50 mA.

In both TTL and CMOS, the probe output is current-limited and generates continuous safety pulses into a short circuit. The LED indicator displays pulse-output states by flashing once for single pulse operation, and by constant illumination for continuous pulseoutput trains.

If used with other logic probes such as the model LP-2 or with a logic monitor that simultaneously displays all IC outputs, the model DP-1 can test various types of logic devices. The probe is very useful in checking circuits that are only occasionally, or never activated. Using the logic pulser, these circuits can be activated repeatedly without elaborate test equipment and without disconnecting any auxiliary logic.

The model DP-1 comes with a plug-in ground clip lead and the accessories that are available for the model LP-2. The output

impedance is greater than 300,000 ohms when the probe is in the open-output mode. And similar to the *model LP-2*, power is supplied to the *model LP-1* via color-coded clip leads, with less than 30 mA consumption.

The model LP-2 sells for \$24.95 and the model DP-1, \$74.95. They are both available from Continental Specialties Corporation, 70 Fulton Terrace, P.O. Box 1942, New Haven, CT 06509. R-E



With the new RCA 10J106A Color TV Test Jig you can troubleshoot a TV chassis without bringing the cabinet and picture tube into the shop. The 10J106A helps you isolate picture tube or chassis malfunctions quickly, and without disturbing your customer's picture-tube alignment.

The 10J106A features a 19-inch shielded picture tube; built-in high voltage meter calibrated to 35 kV; two unique front-panel switches for easy changing of yoke impedances; and a built-in speaker. Yoke, picture tube socket, and high-voltage extension cables are supplied, plus a Set-Up Index and instruction book. With the 10J106A you can service thousands of sets whether tube, hybrid or solid-state — including Precision-in-Line types.

The new RCA 10J106AX Color TV Test Jig is exactly the same as the 10J106A except that it comes without a picture tube for those who prefer the economy of installing their own tube.

The RCA 10J107 Color TV Test Jig Adapter modernizes most older test jigs to perform like the 10J106A. And, if you're a do-it-yourselfer, you can build your own jig from a salvaged TV receiver.

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able high- and low-pass triggering filters and a TTL compatible Z-axis input. Vertical response is typically down only – 6dB at 49MHz. The 1474 is excellent for microprocessor work, as signal delay and the 30MHz minimum bandwidth allow you to examine short pulse waveforms.



The new Model 1432 portable dual-trace scope is one of our best values. This compact portable offers optional rechargable battery pack and full lab-scope features. An automatic

battery charger is built-in as a standard feature. Sensitivity is 2mV/division over a DC to 15MHz range. Bandwidth response is typically down only –6dB at 25MHz. Special features include algebraic addition and subtraction of two input signals, 19 calibrated sweep ranges and front-panel X-Y operation.



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D) THIS

During the present energy crisis, the sun is in the running as the most viable alternate energy source. Here's how basic electronics can be adapted to solar heating in home and industry.

NTROLL

RODNEY A. KREUTER

SOLAR ENERGY, IT SEEMS, HAS BECOME almost a universal interest. Companies offering solar collectors and associated hardware are springing up like glitches on a TTL breadboard. However, most companies sell complete systems and most "do-it-yourself" magazines concentrate on collectors or storage system. Very little information seems to be available concerning the instrumentation or control portion of the systems.

This article attempts to proceed one

step further by providing an understanding of a simple instrumentation and control system. It is not meant to be a blowby-blow construction guide because no two solar systems are quite the same. It is hoped that it will enable you to design a system that will meet your special needs.

Hot-water preheater

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A good way to get started in solar energy is with a solar hot-water preheater. A substantial amount of the average utility bill goes to feed standard preheaters. Another advantage of a solar preheater is that the payback time is not too great and the cash outlay to get started is within

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so that the tank itself won't need as much energy to warm the water to the required temperature. (Note the phrase "as much.") A small solar collector in a lessthan-ideal climate will not supply all your needs; it will, however, help save a great

deal of energy. Figure 1 is a diagram of a hot-water preheater system. Basically, what happens is that the sun warms a water-antifreeze solution in loop 1. The pump sends the warmed solution around from the collector to a storage tank that is filled with colored water. (Colored water can be used to warn of leaks in the system.) The water in the storage tank heats up and, if the tank is well insulated, will stay

warm for quite some time. When cold water enters into loop 2, it gains heat from the storage tank and enters the hot-water heater. If the system has been well designed, the water will need just a little more energy to bring it

to the necessary temperature. The system sounds simple enough,

doesn't it? Well, it has a few flaws! The sun will warm the collectors only if there is sufficient radiant energy. The storage tank will only absorb heat from loop 1 if loop 1 is warmer than the water in the storage tank. Loop 2 will be warmed only if the tank is warmer than the cold-water

inlet and the hot water tank isn't full. If you don't know what the temperature of each component is, you shouldn't waste the energy used by the pump. This brings us to the LM3911.

Temperature transducers

National Semiconductor's LM3911 and the LX5600 are temperature transducers; they provide an answer to most of our temperature-measuring problems. The output of the sensors is 10 mV-perdegree Kelvin. Don't let the word Kelvin concern you; the output can be modified to read any temperature scale, but for a one-time system, the Kelvin scale is as good as any other scale. If you must convert the formula, it is: °C

The working temperature of the LM3911 is -25 °C to 85 °C (-13 °F to 185 °F); while the LX5600 has a range of -55°C to 125°C (-67 °F to 257 °F). Except for their range and cost, the two

devices are similar. The operation of the transducers is quite simple: Two diodes operated at two different current levels produce a voltage difference between them that is proportional to their absolute temperatures (hence, Kelvin). The output of the transducers will be about 3 volts or so, depend-

Jourales, Box 275.

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ing on how hot the IC is. (Very simple indoor-outdoor analog thermometer it

Zener diode from

called flakeboard and particle board) is recommended for strength and rigidity. You can use plywood, but voids in some plywood boards can be a nuisance when they appear at exposed edges. Oak or other hardwood bracing is necessary to further prevent cabinet vibration. To avoid counterboring holes for each of the drivers, two different boards with differentsize cutouts are glued together (see Fig. 4) and the base is constructed



from a common 2×4 (see Fig. 5). Before assembling the cabinet, cut all the required holes. Each piece should be checked for fit before it is glued and nailed in place. Check the cutouts for the speakers as well.

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Start assembling the cabinet by joining the woofer panel (shown as K in Fig. 1) to the bottom panel, F. Prior to gluing the panels together, three or four finishing nails should be hammered partway into the woofer panel. White glue should then be spread on both surfaces, the panels aligned properly and the nails driven into place.

Next, assemble one of the side panels (I or J). Nails can be partly driven into the side panel on two edges

before installing. Use white glue again, this time on the side of the woofer and bottom-panel assembly. Again, align the panels and drive the nails into place (see Fig. 6). These three panels form a rigid assembly that can be put aside for the glue to set.



FIG. 6

Next, add the other side panel, which can be installed similarly (see Fig. 7). The remaining panels can be added, working toward the top of the enclosure. Continue to check each



part for fit before adding it to the assembly. The last major part to assemble is the back, H. Before installing the back permanently, be sure the woofer subpanel, L, and braces G, O and P are in place (see Fig. 8). Run a bead of glue around the joints in the woofer section to form a good seal. Finally, make sure the two ³/₈-inch holes are drilled in panels C and E to allow passage of wires from the crossover network to the mid-range driver and tweeter circuits. After the back is installed, brace Q can be glued in place (see Fig. 9).

This completes the cabinet assembly. All the remaining components are installed from the outside. The cabinet can be completely finished at this time. I painted the entire enclosure with several coats of white vinyl Latex paint (see Fig. 10). Prior to painting,



FIG. 8







FIG. 10

you should fill all cracks with wood filler compound and sand the entire enclosure thoroughly.

Crossover

The heart of any good multi-way

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RADIO-ELECTRONICS

BUILD THIS

CONTROLL

During the present energy crisis, the sun is in the running as the most viable alternate energy source. Here's how basic electronics can be adapted to solar heating in home and industry.

RODNEY A. KREUTER



This article attempts to proceed one step further by providing an understanding of a simple instrumentation and control system. It is not meant to be a blowby-blow construction guide because no two solar systems are quite the same. It is hoped that it will enable you to design a system that will meet your special needs.

Hot-water preheater

A good way to get started in solar energy is with a solar hot-water preheater. A substantial amount of the average utility bill goes to feed standard preheaters. Another advantage of a solar preheater is that the payback time is not too great and the cash outlay to get started is within reason.

A preheater is a rather straightforward device. All it does is warm up the coldwater inlet to an existing hot-water tank so that the tank itself won't need as much energy to warm the water to the required temperature. (Note the phrase "as much.") A small solar collector in a lessthan-ideal climate will not supply all your needs; it will, however, help save a great deal of energy.

Figure 1 is a diagram of a hot-water preheater system. Basically, what happens is that the sun warms a water-antifreeze solution in loop 1. The pump sends the warmed solution around from the collector to a storage tank that is filled with colored water. (Colored water can be used to warn of leaks in the system.) The water in the storage tank heats up and, if the tank is well insulated, will stay warm for quite some time.

When cold water enters into loop 2, it gains heat from the storage tank and enters the hot-water heater. If the system has been well designed, the water will need just a little more energy to bring it to the necessary temperature.

The system sounds simple enough, doesn't it? Well, it has a few flaws! The sun will warm the collectors only if there is sufficient radiant energy. The storage tank will only absorb heat from loop 1 if loop 1 is warmer than the water in the storage tank. Loop 2 will be warmed only if the tank is warmer than the cold-water inlet and the hot water tank isn't full. If you don't know what the temperature of each component is, you shouldn't waste the energy used by the pump. This brings us to the LM3911.

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The working temperature of the LM3911 is -25 °C to 85 °C (-13 °F to 185 °F); while the LX5600 has a range of -55 °C to 125 °C (-67 °F to 257 °F). Except for their range and cost, the two devices are similar.

The operation of the transducers is quite simple: Two diodes operated at two different current levels produce a voltage difference between them that is proportional to their absolute temperatures (hence, Kelvin). The output of the transducers will be about 3 volts or so, depend-

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ing on how hot the IC is. (Very simple indoor-outdoor analog thermometer if you have a good VOM.)



FIG. 1—BASIC SOLAR ENERGY hot-water preheater showing important temperature measuring points.



FIG. 2—BASIC SENSOR CONNECTION and pin location.



FIG. 3—DIFFERENTIAL AMPLIFIER; the power connections are not shown.

Figure 2 shows the basic connection and pin location of the transducers. Note that the output voltage of the devices is *not* referenced to ground but to pin 3.

Differential thermometer

It's useful to know the temperature of each component of the solar energy system, but it's not essential. What *is* essential is to know that component A is somewhat warmer than component B. This is the principle of the differential thermometer. The output of the thermometer is proportional to the difference of the two input temperatures. This requires a differential amplifier, which is easy to obtain using an op-amp such as the one shown in Fig. 3.

Note that the differential amplifier is based on two input voltages that are referenced to ground. Since the output of the transducers is not referenced to ground, this would seem to complicate the circuit somewhat. Luckily, there is a simple solution to this problem.

Referring to Fig. 2, note the 6.8-volt

Zener diode from pin 3 to ground. This Zener diode is internal to the transducer and maintains the voltage from pin 3 to ground at 6.8.

Since V_o increases at a rate of 10-mVper °K, and the sum of V_o and V'_o must equal 6.8 volts, V'_o must decrease at 10mV-per °K.

Using this data, we can arrive at the differential thermometer shown in Fig. 4. The output will be proportional to the difference between temperatures T₁ and T_2 and will rise as T_1 rises, assuming that T_2 remains constant. When T_1 equals T_2 , the output may not be exactly zero, because op-amps are not perfect and the 6.8-volt Zener diodes may not be exactly matched. This will not affect the operation of the circuit, and, as a matter of fact, may be used to an advantage. You should interchange the sensors if you don't get a small positive voltage (about 30 mV to 100 mV) when the sensors are at the same temperature.

Hysteresis

All control systems need some type of hysteresis, which is a type of "deadband" or buffer zone. For example, thermostats have a built-in hysteresis of about 2 °F. Assume that the hysteresis is plus or minus 1 °F of the setting. If the thermostat is set at 68 °F, the furnace will come on when the temperature falls to 67 °F and stay on until the temperature rises to 69 °F. If no hysteresis was built into the system, the furnace would cycle on and off continuously.

The hysteresis in a solar system should be fairly large—5 °F to 10 °F is not unreasonable. Figure 5 shows a comparator that is used to provide an adjustable amount of hysteresis. The LED lights as a status indicator and alarm when the set amount of temperature difference has been attained.

Interfacing

At this point, the system monitors temperature, subtracts one temperature from another, compares this value to some preset value, and lights an LED if all the conditions are met. It still won't pump much water or close a valve.

Lighting an LED has a purpose other than just providing an output of the system. When devices must be operated at 117 VAC, such as a pump or a motor, it is necessary to isolate the control system from the AC lines. By using an LED and a phototransistor sealed in a light-tight tube, a very high degree of isolation can be achieved. You can even use two LED's—one as an output and the other as part of the photocoupler.

A circuit that handles the control of



FIG. 4—DIFFERENTIAL THERMOMETER measures temperature difference.



FIG. 5—COMPARATOR with hysteresis control.

the pump is shown in Fig. 6. The components might have to be scaled up or down depending on the amount of load current. And don't forget to heat-sink the triac.

Assembling the system

The complete control system is shown in Fig. 7. A regulated 12-volt power supply (see Fig. 8) is also necessary to power the system. The cost of such a supply is very low, so there is no reason to use an unregulated supply.

If you want to measure the actual temperature of one of the system components, you can use a good voltmeter.



FIG. 6—LIGHT-CONTROLLED TRIAC circuit. For diac triggering, $C = 0.22 \ \mu$ F, R = 10K. Substitute diac for 2N4992 (silicon bilateral switch) and phototransistor with V_{oso} of 80 volts or two 2N5779's connected in series.

First, measure voltage V_s of each sensor. This (V_s) is measured from pin 3 to ground and should read about 6.8 volts. Write it down for each sensor because it will not change but will be different for each one. Any time that you want to know the actual temperature, measure voltage V₀ from the output to ground. The temperature can be found from: °C = 100 (V_s - V₀ - 2.73).

A voltmeter, calibrated in degrees, can even be permanently installed in your system if you desire.

Next, you must consider the sensor. The LX5600 costs a little more than the LM3911, but it has an extended operating range and slightly better absolute accuracy. Naturally, the sensors must be thermally connected to the device to be monitored. A recommended technique would be to fabricate a heat sink that the sensor will slip into. (Use the T0-5 case.) The heat sink can then be mounted to the device. Grease the sensor with heat-sink compound (silicon grease) and slip it into the heat sink. This will prevent damage to the sensor. A solar collector should be monitored in the center if possible.

It will also be necessary to insulate and weatherproof the sensor leads. Some RTV insulation should work well. It may be possible to immerse the sensor in water if you are careful. The top of the case should have very little RTV on it to make sure it isn't thermally insulated. Another method would be to seal it in a test tube. Just make sure that the leads are well insulated.

Run shielded cable to your sensors to reduce noise pickup since open wire runs of longer than a few inches tend to produce too much noise.

Check the pump and valve specifications and choose the triac accordingly. Many different types of triacs are available, and most should work with this trigger system. Don't be afraid to experiment with different triac types.

Make sure that the phototransistor is



FIG. 7—SCHEMATIC DIAGRAM of complete control system. Note bypass capacitors for greater noise immunity and slight change of some component values. The LM324 contains four op-amps, so two complete loops could be handled by one IC.



FIG. 8-12-VOLT POWER SUPPLY consists of bridge rectifier and regulator.

rated at 80 volts V_{∞} or more if you plan to use a diac to trigger the triac. The silicon bilateral switch (shown in Fig. 6) might be hard to locate, although a GE semiconductor parts supplier should have it and the 2N5779 photo-Darlington transistor.

Calibrating the hysteresis control will be somewhat time-consuming. Allow one sensor to reach room temperature. This will represent the cooler component (storage tank). Feed this output into the noninverting input of the op-amp.

Prepare a warm water bath and place the other sensor in the bath (insulate the leads). This represents the warmer component (the solar collector). Feed the output of this sensor into the inverting input of the op-amp.

Now use a good thermometer to measure each temperature. Rotate the hysteresis control until the LED lights up. At this point, mark down on the dial the difference between the two temperatures. Repeat this at least five times. The total range, with the component values given, will be from about 1 °C to 20 °C. Therefore, do not raise the temperature of the bath to any warmer than room temperature plus 20 °C. **R-E**

Custom-built high-voltage Tesla coils now available

The Ultra High Voltage Division of Professional Sound Systems now manufactures a line of Tesla coils, kits and components that can be custom-built to fit individual needs. The coils are modular and symmetrically constructed, conservative in



design and can be used in high-voltage applications and for demonstrations.

There are 10 basic configurations from which to choose, with spark-discharge lengths ranging from 1.5 inches to over 15 feet. A full line of stock components is also available, from power-supply control consoles to oscillation transformer assemblies. All of these can also be tailored to a customer's special requirements. For information, write Professional Sound Systems, Ultra High Voltage Division, 4914 Baldwin Avenue, Temple City, CA 91780.

Report states service industry salares are rising

It appears that salaries in the service industry are on the rise, according to a report entitled Salaries and Related Matters in the Service Department—1978, published by Abbott, Langer & Associates, Park Forest, IL.

For example, the report lists that the

average national service manager's salary is presently \$25,658 and that of field service representative, \$13,291. The report categorizes these and other job listings by type of product or service (also by the size of the service company or manufacturer involved), as well as containing data on various types of employers. More than 25,000 positions in over 200 organizations are listed, including salaries for national regional and local service managers; field service supervisors, engineers and senior representatives; parts managers; service training instructors; technical writing supervisors; and more. Employers represented include firms manufacturing business, electrical and communications equipment; consumer electronics; computers and allied products; and medical and scientific equipment.

The report is available for \$60 from Abbott, Langer & Associates, Box 275, Park Forest, IL 60466.

BUILD THIS

Time Compensated Hi-Fi Speaker System

THIS ARTICLE GIVES YOU AN OPPORTUNITY to keep up with the latest advances in loudspeaker design by showing how to build a pair of high-quality, time-compensated speakers for less than \$200.

Practically any multi-way speaker system can be improved by adjusting the drivers for signal arrival time. Sound from the high-frequency speakers usually arrives first. By moving the high-frequency speakers back, the sound from the woofer, mid-range driver and tweeter can be made to arrive at your listening position simultaneously. Many of the good sounds from electrostatic speakers can now be heard with nonelectrostatic systems. Time-compensation can help produce a smoother frequency response (compared with flat baffle mounting) by reducing interference between drivers at the crossover frequency. This also produces improved transient response as well as incredible depth and stereo imaging.

The secret of proper system design is to align the *acoustic center* of each driver, as seen from the side, so that they are in the same vertical plane. Each type and size of driver has its own effective acoustic center that can be located at either the voice coil, behind the coil, or in front of it.

To locate the acoustic centers and position them correctly relative to each other, I recommend using an oscilloscope, a Bruel & Kjaer condenser microphone and a bucket-brigade audio-delay line. Each driver selected for this system has very little shift of its acoustic center through its frequency range. The crossover network is adjusted for smooth frequency response. It also maintains a uniform time response when combined with the driver time characteristics. Other kinds of drivers should not be substituted in this system since their time characteristics may not be the same.

Testing

The system should be tested for errors or a cold solder joint. Connect a $1^{1/2}$ -volt battery across the input terminals, with the positive end going to the red terminal. The woofer cone should move out. Connect the system to your amplifier and put on FM interstation hiss. A distinct band of frequencies should be heard from each speaker—lows from the woofer, midfrequencies from the mid-range and highs from the tweeter. These frequencies should be of approximately equal amplitude. The system can be fused with a $1^{1/2}$ amp normal-blow or fast-acting fuse.

The time-compensated system can be driven without stress by musical peaks of up to 100 watts. Of course, the continuous or RMS power rating of the drivers is less. Use an amplifier of 35 watts or more to avoid clipping on peaks. A severely clipping amplifier can damage the drivers and the crossover network.

Using the system

The best location for the speakers is on the floor against the long wall of the listening room. The distance between the speakers should be equal to or less than the distance from either speaker to your listening position. Avoid placing the speakers in the corners because this may provide too much bass and cause excessive room standing-wave problems. You may want to toe the speakers in toward the listening position to maintain the best time response for each speaker.

Now, you are in for a real stereo treat. In playing various types of stereo material, you will notice an unusually welldefined stereo image. You will also notice this image will vary considerably from one recording to another. Recordings made with microphones close to the instruments may sound almost monophonic in the left and right channels. A solo instrument or voice will sound almost monophonic in the center. With the microphone placed farther back and adding more of the reverberant field, recordings will have an incredible spaciousness and an evenly spread stereo image.

I have made many true stereo recordings using only two omnidirectional microphones. The recordings range from katydids on a summer night to church choirs. The realism of these recordings is increased dramatically with the timecompensated system. Another benefit is in transient performance. Try guitar, harp, or harpsichord recordings; then try cymbals, triangles, or snare drums.

The high frequencies in this system have been adjusted for the smoothest response. Depending on how recordings are made, however, the high-frequency balance will seem to vary. The easiest and best way to compensate for this is to use the treble control: By varying the setting from 10 o'clock to 2 o'clock, you can adjust the frequency balance for the best sound in each recording.

Once you get hooked on the superior sound from the time-compensated speaker system, you won't settle for anything less from any other system.
Here's your chance to own a state-of-the-art stereo speaker system designed so the sounds from the three drivers reach your ears simultaneously. Build it for less than \$200 using ordinary hand tools.

ROGER H. RUSSELL





Cabinet construction

FOR CORRECT SIGNAL-ARRIVAL TIME, THE MIDrange driver must be placed so that it is $7\frac{1}{16}$ inches back from the woofer. The tweeter must be placed so that it is $2\frac{1}{2}$ inches back from the mid-range. The result is a stepped-back arrangement whose construction is only slightly more complicated than that of a normal flat baffle design. The improvement in sound is well worth it. (Figs. 1, 2 and 3 are the construction



diagrams for the front, sides and back of the speaker cabinet.)

With careful layout, many of the parts for the pair of cabinets can be made from a single 4-foot by 8-foot sheet of wood. Some lumber yards have smaller sheets available at lower cost that can be used to construct the remainder of the cabinet. A portable circular saw, a saber saw, or even a handsaw can be used to cut the panels. Visible surfaces can be painted or covered with vinyl, or wood veneer can be used.

Three-quarter-inch chipboard (also



called flakeboard and particle board) is recommended for strength and rigidity. You can use plywood, but voids in some plywood boards can be a nuisance when they appear at exposed edges. Oak or other hardwood bracing is necessary to further prevent cabinet vibration. To avoid counterboring holes for each of the drivers, two different boards with differentsize cutouts are glued together (see Fig. 4) and the base is constructed



from a common 2×4 (see Fig. 5). Before assembling the cabinet, cut all the required holes. Each piece should be checked for fit before it is glued and nailed in place. Check the cutouts for the speakers as well.



Start assembling the cabinet by joining the woofer panel (shown as K in Fig. 1) to the bottom panel, F. Prior to gluing the panels together, three or four finishing nails should be hammered partway into the woofer panel. White glue should then be spread on both surfaces, the panels aligned properly and the nails driven into place.

Next, assemble one of the side panels (I or J). Nails can be partly driven into the side panel on two edges before installing. Use white glue again, this time on the side of the woofer and bottom-panel assembly. Again, align the panels and drive the nails into place (see Fig. 6). These three panels form a rigid assembly that can be put aside for the glue to set.



FIG. 6

Next, add the other side panel, which can be installed similarly (see Fig. 7). The remaining panels can be added, working toward the top of the enclosure. Continue to check each



part for fit before adding it to the assembly. The last major part to assemble is the back, H. Before installing the back permanently, be sure the woofer subpanel, L, and braces G, O and P are in place (see Fig. 8). Run a bead of glue around the joints in the woofer section to form a good seal. Finally, make sure the two $3/_{\theta}$ -inch holes are drilled in panels C and E to allow passage of wires from the crossover network to the mid-range driver and tweeter circuits. After the back is installed, brace Q can be glued in place (see Fig. 9).

This completes the cabinet assembly. All the remaining components are installed from the outside. The cabinet can be completely finished at this time. I painted the entire enclosure with several coats of white vinyl Latex paint (see Fig. 10). Prior to painting,



FIG. 8







FIG. 10

you should fill all cracks with wood filler compound and sand the entire enclosure thoroughly.

Crossover

The heart of any good multi-way



speaker system is the frequency-dividing network. Numerous listening tests as well as acoustic response measurements were made to insure that each speaker output complements the others closely in frequency range and amplitude. The frequencydividing (crossover) network, (see Fig. 11) was adjusted for best performance using Norelco (Philips) drivers (Fig. 12).

The network board is made of 1/2-



FIG. 12

inch particle board and is 7 inches square. A push-type connector is mounted on the opposite side of the components (see Fig. 13) so that it will be in the hole in the enclosure's back



FIG. 13

panel (H). The four coils are wound as detailed in Fig. 14. Coil forms are made from squares of γ_{a} -inch thick



FIG. 15

then be wound. After the coil is wound, wrap tape around the turns to hold them in place. The tape holding the wire on one coil form face can then be removed and both leads cut off at a length of about 3 inches from the form. Then scrape the insulation from the lead ends and tin the wires with solder.

The electrolytic-capacitor losses are sufficiently low not to affect the output of either the mid-range driver or tweeter. In the woofer and the midrange circuit, 10- and $4.7-\mu F$ capacitors are connected in parallel to produce a total capacitance close to 15 μF . The resistors in series with the mid-range and tweeter are 5-watt "cinder-block" types. All components, particularly the 6-mH coil, should be

COIL	а	DOWEL LENGTH	WIRE SIZE	NUMBER OF TURNS
L1-6.0mH	21/2"	1½"	#18	675
L21 <mark>m</mark> H	1″	1/2"	#24	77
L3-1.6mH	1½"	1"	#22	338
L43mH	1"	1/2"	#24	133

FIG. 14

Masonite glued to ³/₄-inch-diameter dowels. Three different coil forms are used for the four coils in the network, which are made of ordinary enameled magnet wire. The number of coil turns is based on "scramble" winding. This means neat orderly rows of turns are not necessary. However, windings should be kept reasonably tight. The finished 6-mH coil weighs about 1¹/₄ lb. Gluing the ends of this coil form will not be sufficient. Drive *brass* screws through the faces into the dowel to be sure the coil form stays together.

Coils can be started by winding a few turns of wire around your finger and then taping this wire to one face of the coil form (Fig. 15). The coil can

well glued to the board to hold them in place and prevent vibration. I recommend using RTV silicone adhesive for this purpose.

Wiring of the board is a perfectly straightforward procedure using terminal strips and mounting hardware. Component placement is shown in Fig. 16. Number 20 stranded wire with color-coded insulation is adequate as leads to the speakers. Use blackcolored wire for the negative lead on all the drivers. The following wire lengths should be used: the tweeter, 24 inches; the mid-range, 20 inches; and the woofer, 21 inches.

Final installation

Place caulking compound around the inside of the crossover opening in the back panel and then push the crossover panel into place. The compound holds the board while you center the terminal in the cutout. Start drilling 7/64-inch-diameter screw holes in the back of the cabinet using the holes in the crossover panel to locate them (see Fig. 13). Drill only partway into the cabinet back. Using No. 8 X 1-inch sheet-metal screws, fasten the crossover panel in place. Dress the purple and black tweeter wires from the crossover through the 3/8-inch holes in E and C and out of the tweeter cutout. The orange and black wires go through the 3/8-inch hole in E and out of the mid-range driver cutout. Seal this 3/8-inch hole with caulking compound to create an airtight woofer compartment. The red and black woofer wires can be brought out and taped to the front of the cabinet.



With a driver in place, use the mounting holes as guides to drill the screw holes into the mounting board. For the mid-range driver and tweeter, use a $\frac{5}{64}$ -inch drill. The mounting holes for the woofer are $\frac{9}{64}$ inch.

Next, fill the woofer compartment with glass-fiber insulation material (Fig. 17). Ordinary pink home insulation material 2 or 3 inches thick can be



FIG. 17

used. A roll of this insulation costs less than the small packages of acoustic glass-fiber material needed to fill two systems. Insulation performance is the same or better, and you can use the remainder in your attic or under the hood of the family car.

Remove the vapor-barrier backing and cut the glass-fiber into small pieces. This will produce the smoothest bass response. Three-inch cubes will do nicely, although size is not too critical. Wear rubber gloves to avoid possible irritation to your hands. Fill the enclosure completely with loosely packed insulation material. And make sure to fill under the brace, G. No material is needed in the upper compartments because both the midrange driver and the tweeters have sealed backs. The drivers can now be connected and installed.

Solder the color-coded wire to the positive terminal and the black wire to the negative lead. A red mark on or near the terminal indicates it is positive; if no mark appears, briefly connect a 11/2-volt battery to the terminals. When the speaker diaphragm moves away from the magnet, the positive end of the battery is connected to the positive lead of the speaker. The mid-range driver and tweeter can be installed using No. 6 \times 1/2-inch sheet-metal screws. The woofer can be installed with No. 10 \times ³/₄-inch sheet-metal screws. Place caulking compound only around the woofer; it should be placed between the woofer basket and the woofer subpanel, L (see Fig. 18). Again, this insures a seal in the woofer compartments.

The foam grille can be cut and installed using the self-adhering strip that comes with the grille along with cutting instructions. Three grille packages are all that are needed to cover two systems. This completes the assembly. **R-E**

SPEAKER SYSTEM PARTS LIST

The following items are available from McGee Radio & Electronic Corporation, 1901 McGee Street, Kansas City, MO 64108:

Part No. AD12250 W8: Two 12-inch Norelco woofers, two for \$77.

Part No. AD0211 SQ8: Two 2-inch softdome Norelco mid-ranges, \$19.95 each. (This part not in McGee catalog but available.)

Part No. AD0162 T8: Two 1-inch dome tweeters, \$9.95 each.

Two 3-ohm 5-watt resistors, \$.20 each.

Two 6-ohm 5-watt resistors, \$.49 each.

The following items are available from Radio Shack stores:

Part No. 272-999: Four $10-\mu$ F, 50-volt nonpolar capacitors, \$.99 each.

Part No. 272-998: Six 4.7- μ F, 50-volt nonpolar capacitors, \$.89 each.

Part No. 274-688: Two 5-lug terminalstrip packs of 4, \$.69 each.

Part No. 274-621: Two terminal boards, \$.99 each.

Part No. 40-1951: Three foam grilles, \$5.95 each.

Miscellaneous: One roll of glass-fiber insulation (approximately \$5.95); two 4-foot by 8-foot sheets of $\frac{3}{4}$ -inch particle board (\$17); five $\frac{1}{2}$ -lb (100-foot) rolls No. 18 magnet wire; two $\frac{1}{4}$ -pound (93-foot) rolls No. 22 magnet wire; two $\frac{1}{4}$ -pound (150-foot) rolls No. 24 magnet wire; white glue (\$1.79); eight No. 8 \times 1-inch sheet-metal screws; 16 No. 10 \times 1-inch sheet-metal screws; 14 No. 6 \times $\frac{1}{2}$ -inch sheet-metal screws; 2 flathead brass wood screws; hookup wire; solder; paint; caulking compound; hardwood bracing; 8-foot-long two-by-four; 1 $\frac{1}{4}$ -inch finishing nails.

CABINET LUMBER DIMENSIONS

To construct the cabinet for the timecompensated speaker system, the following lumber should be purchased:

3/4-inch-thick flakeboard:

Top panel (A): $3\frac{5}{4}$ inches X 16 inches Tweeter board (B): $4\frac{1}{4}$ inches X 16 inches

Tweeter bottom (C): $5\frac{3}{6}$ inches X 16 inches

Mid-board (D): $7\frac{1}{8}$ inches X 16 inches

Woofer top (E): $13\frac{1}{4}$ inches X 16 inches Woofer bottom (F): 14 inches X 16 inches

Mid-brace (G): $6\frac{3}{4}$ inches X 16 inches

Back panel (H): 31% inches × 16 inches Side panels (I & J): 32% inches × 14% inches (see Fig. 2)

Woofer board (K): $21\frac{1}{2}$ inches X 16 inches

Woofer board (L): $13\frac{1}{4}$ inches X 16 inches

3/8-inch-thick flakeboard:

Mid-board (M): $7\frac{1}{8}$ inches X 16 inches Tweeter board (N): $4\frac{1}{4}$ inches X 16 inches

3/4-inch hardwood:

Braces (O & P): 12 inches $\times 2\frac{1}{2}$ inches Brace (Q): 10 inches $\times 2\frac{1}{2}$ inches

2×4 inch fir:

Base (R & S): 12¹/₄ inches Base (T & U): 15 inches

RADIO-ELECTRONICS

www.americanradiohistory.com

COMPUTERS



The programmable read-only memory is becoming the workhouse of modern digital electronics and will play an ever-increasing role in your everyday activities. Here is what it's all about.

THE PROM (PROGRAMMABLE READ-ONLY Memory) is increasingly being accepted as a circuit element. The electronic hobbyist or home computer owner should become familiar with this very useful device. Because there have been numerous articles written about both the PROM and the EPROM (Erasable PROM), this article will just briefly mention their theory of operation, and concentrate on the ways these devices can be put to use.

What is a PROM?

Figure 1 shows the basic configuration of a 16×4 -bit PROM; that is, there are 4 address lines, and, therefore, $2^4 = 16$ states can be represented. Each of these 16 states is decoded into a single control line that leads to a set of junctions in the memory array. These junctions are either closed or fused open depending upon how the PROM is programmed. The logic state of the junctions selected by the address decoder passes through the buffer and appears at the output. Figure 1 shows 4 output lines; thus, there are $2^4 \times$ 4 or 16 \times 4 junctions. This PROM can also be described as containing 16 words with 4 bits-per-word. There are as many words as there are address states. Therefore, if the PROM had eight address lines

ROBERT H. PENOYER

and one output line, it would be a $2^8 \times 1$ -bit or a 256 \times 1-bit PROM, or containing 256 1-bit words.

Just as there are closed or fused-open junctions in a PROM array, the EPROM uses static charges on MOSFET transistors to achieve the effects of an open or closed junction. The charges on the MOSFET's can last for years or be erased in a few minutes by special ultraviolet lamps.

Using the PROM

The PROM serves two main purposes: First, a single PROM IC can replace an entire multiple-gate logic array. Say, for example, you needed a set of gates that would perform the function described in the truth table of Fig. 2. If standard gates were used, a complex network would result. Instead, let the four left-hand columns of Fig. 2 represent the address lines, and let the column on the right represent the output line of a 16×1 -bit PROM. Thus you would achieve the desired function using only a single IC. The result is a savings in wiring time, troubleshooting time and board space.

The second main use of a PROM is as a "look-up table." For example, suppose you wanted a counter to count in the sequence shown in the right-hand side of Fig. 3. This could be extremely difficult to accomplish using ordinary logic. Instead, you can apply the output lines of an ordinary binary counter to the address lines of a PROM. Upon reaching any of the 16 possible states, the counter causes the internal logic of the PROM to "look up" the desired output state and pass it through its buffer to the output, according to the truth table. Only two IC's, a 4-bit binary counter and a PROM are needed to arrive at a rather complicated sequential output.

Another example of using a PROM as a look-up table is a Baudot to ASCII code translator. The Baudot code can act as the address for a PROM, and the PROM output can yield equivalent ASCII characters.

Propagation delay and access time

As with any logic device, propagation delays in PROM's are important, particularly so if a PROM's output lines are used to drive counters or clocked logic of any type.

A specifically limited amount of time is required to receive an address, decode it, drive a set of junctions in the PROM array and transmit the result through the buffers to the PROM output. This is called the PROM's access time, and is



FIG. 1-PROM consists of an address decoder, output buffer and memory array.

A ₀	A ₁	A ₂	A ₃	F
0	0	0	0	1
0	0	.0	1	1
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
0	1	0	1	0
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	0
1	1	1	0	1
1	1	1	1	0

FIG. 2—COMPLEX LOGIC FUNCTIONS such as the one shown in the above truth table can be easily handled by a PROM.

	AO	A ₁	A ₂	Α3	00	01	02	03	
	0	0	0	0	0	0	1	0 🛥	-
	0	0	0	1	0	1	0	0	
	0	0	1	0	.0	1	0	0	
	0	0	1	1	1	.0	1	1	
1	0	1	0	0	1	1	1	0	
	0	1.	0	1	0	1	1	1	
	0	1	1	0	1	0	1	1	
	0	1	1	1	1	1	0	1	
	1	0	0	0	1	1	1	0	
	1	0	0	1	1	1	0	1	
	1	0	1	0	1	0	1	1	
	1	0	1	1	0	1	1	1	
	1	1	0	0	1	1	1	1	
	ì	1	0	1	0	0	0	0	
	1	-1	1	Ó	1	0	0	1	
-	1	1	1	1	4	0	1	0	

FIG. 3—COUNTERS with an unusual counting sequence can easily be designed using a PROM.

listed in the manufacturer's data sheet. During the access delay time, the state of the output lines on a PROM is unpredictable. A set of outputs can pass through several states during the transition from one address to the next. Therefore, if the outputs are driving clocked logic, the logic could receive undesired data. Obviously, this should not be allowed to happen. Luckily there are methods to get around this problem.

Buffer and latch isolation

As shown in Fig. 1, the output buffer of the PROM often has an enable control line. Typically, this enable line is used to select the device that is to be connected to a parallel bus system when many such tri-state devices are used. When enabled, the buffer outputs are at normal logic levels. When not enabled, the buffer outputs appear to be open circuits. If all the buffer output lines are pulled to +V through, say, 10K resistors (in the case of TTL logic) then when the output lines are disabled they will be at a known high logic level. Therefore, no output line can go low unless that particular bit was programmed low and the PROM output was enabled. Thus, it is only necessary to disable the output when changing addresses. Using such an arrangement, no glitches appear at the output and low-going pulses appear only when desired. Figure 4 shows a typical circuit using this technique.

table shown in Fig. 3. Therefore, as the counter passes through each binary state, the desired output appears on the PROM output lines. These lines are always enabled as shown in Fig. 5. Note that both the counter and the latch are triggered by positive-going clock edges, and there is an inverter in the latch clock line. This means that while the counter still triggers on the positive-going edge of the clock, the latch will trigger on the negativegoing edge. This provides a delay of onehalf clock period between the time the counter is updated and the resulting PROM output appears at the latch output. If the PROM access time is shorter than one-half clock period, its output will be settled by the time the latch uses it. The result is a clean accurate set of waveforms at the latch output.

PROM sources

PROM's and EPROM's are available in many configurations. Just check through manufacturers' catalogs for the type of PROM you need for your application. Sometimes the required number of







FIG. 5-CLOCK SIGNAL in synchronous circuits can be used to inhibit output during access time.

You can use a similar more desirable technique that requires no pull-up resistors on the buffered output lines. Let's say, for example, you want a circuit that counts as shown in the Fig. 3 truth table. Also assume that you could not arbitrarily allow the outputs to go high, as shown in Fig. 4.

Figure 5 shows an alternative technique: a synchronous binary counter drives the address lines of a PROM that is programmed according to the truth words and word length are not available and you have used a PROM with more words or bits than you need. In this case, you should consider the economics of wasting PROM capability.

Most large distributors can program a PROM for you if you purchase it from them. Find out all the necessary information before placing the order for your PROM; often the distributors will program the device for a small fee or at no additional cost. **R-E**

NOM Card For The 1802

Add-on math board for an 1802-based microcomputer. Based on a number-crunching IC, this board speeds execution time, reduces software overhead and saves memory

L. STEVEN CHEAIRS

NOW THAT YOU HAVE YOUR RCA 1802based microcomputer up and running, what do you do next? You might consider putting it to some serious work, but in doing so, you will probably run into the software wall. In other words, for most applications a good deal of programming will be required, and a good portion of it will be for mathematical operations. It's also a known fact that you can age very rapidly writing all the software needed to perform the required mathematical operations.

One alternative is to use hardware instead of software to perform these operations. The first idea I had was to use a scientific calculator IC. This would certainly reduce the software development time, leaving me only the interfacing to worry about. While this apparently solves the software problem, it creates a Pandora's box full of new ones.

First, most calculator IC's have onchip debounce circuitry designed to solve the problems generated by multiple character entry due to noisy keyboard switches. This is a very positive feature for a calculator, but, unfortunately, it tends to slow a microprocessor down.

Second, a calculator IC in its natural habitat is interfaced to a keyboard via a set of multiplexed input/output (I/O) pins. This requires complex interfacing to convert incoming data into the signals necessary to imitate a keyboard switch. While this is not an impossible task, it is a bit messy.

Third, a calculator is designed to stand alone, not act as a slave processor for a microcomputer system. The data is outputted in a multiplexed 7-segment, non-TTL format. Multiplexing data is not only acceptable but desirable. On the other hand, a 7-segment format is not exactly the easiest format for a computer to manipulate. It could of course be converted to a BCD (*Binary-Coded Deci*mal) format by several methods, such as a software look-up table, or a PROM could be programmed to convert a minimum of five input lines into the four BCD output lines. Another point to consider is that the calculator IC's do not have the control lines required to interface it to the processor.

You could try another approach, such as dedicating a CPU and a ROM as a mathematical processor. National Semiconductors has done just that with its new Number-Oriented Microprocessor (NOM). This special-purpose microprocessor, the MM57109, is available through distributors. This single IC will provide most, if not all, of the mathematical operations needed for any computer system. The software overhead is drastically reduced when this processor is used.

The MM57109

Figure 1 is the internal block diagram of the MM57109, showing both the signal lines and their point of origin. The internal register file is composed of five



FIG. 1—BLOCK DIAGRAM OF THE MM57109 number-oriented microprocessor.

registers (X, Y, Z, T and M); each has eight mantissa digits, two exponent digits, a decimal-point position indicator, and the mantissa and exponent sign bits. The program-storage ROM stores about 1500 eight-bit micro-instruction words. The 6-bit-long program instructions enter through the $I_{1.6}$ -lines and are converted into a sequence of these microinstructions. The BCD data words enter the control logic via the $I_{1.4}$ -lines.

Data is outputted, after receiving the OUT instruction, through the digit-dataout block. The digit-address-counter block sequences each digit during the I/O operations. The Read/Write control line is used during the OUT instruction to latch the data words into the interface register.

Figure 2 shows a table of the MM57109's important features. These features can be classified into four categories: scientific calculator-type instructions, I/O, branch control and interface.

Basically, the MM57109 looks like an RPN (Reverse Polish Notation) scientific calculator. The only major difference is in the I/O and control-interface circuitry. National Semiconductor engineers state that the MM57109 is a modified scientif-

Instructions	Input/Output	Branch Centrol	Interface
RPN	HOLD for asynchronous and single step operation	Conditional and unconditional program branching	Single phase clock
1 to 8-digit mantissa	Asynchronous digit input instruction (AIN) with AIN ready (ADR) input	Increment/decrement branch on non-zero for program loops	Low power operation
2-digit exponent	Multidigit I/O instructions (IN, OUT)		Generation of I/O control signals
Four-register stack, one-memory location	Floating point or scientific notation		Separate digit input, output, and address bus
Trigonometic functions logarithmic functions Υ e [*] , π, etc.	Programmable mantissa digit count for IN, OUT instructions		
Error Flag	Sense input and flag output		

FIG. 2—FEATURES OF THE MM57109 that are important to the NOM card constructor.

ic calculator. First, we'll take a look at the 1802-type interface; then, the instructions and programming techniques will be discussed. If you are not yet convinced that the MM57109 is the way to go, then look at Table 1. As stated earlier, the NOM is very easy to interface to almost any computer system. The MM57109 is



	MM57109	MICROPROCESSOR	CALCULATOR
Speed (math or I/O)	0.5–400 ms	0.5–500 ms	14-400 ms
Data length	Variable (1- to 8-digit mantissa)	Fixed	Fixed
Data format	Floating point, and scientific notation	Binary	Floating point, and scientific notation
Ι/Ο	Multidigit, asynchronous digit and single bit	Data bytes and single bit	Keyboard/display
Program	External ROM/PC, µP or FIFO	External ROM, internal PC	Key sequence



FIG. 3—1802 MICROPROCESSOR INTERFACE for the MM57109.

only one of two NOM interfaces that Questar Engineering is marketing; the other is for the S-100 bus.

Circuit operation

The basic circuit operation is shown in the block diagram of Fig. 3, while the complete schematic is shown in Fig. 4. <u>The I/O</u> signals (N_0 , N_1 , N_2 , TPB, MRD, MWR) from the 1802 are decoded, and are used to move data into and out of the NOM via two 8-bit data latches. One-half of the data output port, D_0 - D_3 , is the

All resistors 1/2 watt, 5% unless noted. R1-R5, R17, R18-10,000 ohms R6-300 ohms, 1/2 watt R7-1000 ohms R8-18,000 ohms R9, R10, R12-R15-2000 ohms R11, R16-9100 ohms C1, C5-1-µF, 35-volt, electrolytic C2, C3, C8, C11, C12-0.01-µF, disc C4, C9, C10-100-pF, disc C6, C7-10-µF, 20-volt, electrolytic D1-1N703 Zener diode (or equal), 3.9 volts IC1, IC2-4508, dual 4-bit latch IC3, IC13-4069, hex inverter IC4-4073, triple 3-input AND gate IC5, IC6, IC8-4013, dual D-type flip-flop IC7-4049, hex inverter buffer IC9-4528, BCD-to-decimal decoder/binary-to-octal decoder IC10-DS8800, dual TTL-to-MOS voltage PARTS LIST

converter

- IC11-MM57109, NOM
- IC12-4072, dual 4-input on gate
- S1-S4-DIP switch (8 SPST switches)
- Misc.—One 28-pin DIP socket for IC11, and a PC board.

The following are available from Questar Engineering Co., 50 S. MacDonald St., Mesa, AZ 85202: PC board, predrilled and etched, \$33 Complete kit of all parts, \$98 Elf II to SB-44 converter card, \$6.95 MM57109 NOM IC, \$18

DS8800 TTL-to-MOS converter IC, \$6.45. Questar also has a PROM containing a subroutine that will perform all the power-up housekeeping and FIFO interface between the 1802 and the NOM. Also included on the PROM is a monitor-type software package, \$28.50.

Note: The decision to use a dual 22-pin

card was based on the fact that this card has been a standard component of the electronic field for many years. The Vector-size card and hardware are readily available and less expensive than other components. There are a variety of other printed-circuit cards using this same bus, a few of which will be available in the future. These PC cards include a Vectortype graphic display that uses an oscilloscope as a display; a 2K-byte EROM/2Kbyte low-power RAM card; and a nonvolatile 4K-byte RAM board; the EROM's are programmed in place on the card. This permits the EROM to be treated like a RAM, plus a program can be developed in RAM and transferred to EROM without unplugging any components. The program can then be executed immediately from the EROM.



FIG. 4—COMPLETE SCHEMATIC FOR THE NOM interface. The top end of R17 goes to +5 volts which is equivalent to V_u. The -4-volt level is equivalent to V_{dd}.

BCD data from the NOM, while the other 4 bits are used to provide status information to the 1802 CPU. Note that the address counter digit from the NOM is not provided. This is because the address digit requires 4 bits of the I/O port (thus requiring a second output port). This may seem like a poor decision just to save one output port, but it is not. The output format of the NOM is defined by the internal architecture, and with a minor amount of software added to the 1802's program, the same information may be derived. Figure 5 shows the data formats for both the scientific-notation mode and Fig. 6 shows the floatingpoint mode.

The other 4 data bits inform the 1802 of the NOM's status; also any of these 4 bits can be configured to output an active low signal, which should be used to set one of the event flags or initiate an interrupt. Bits D_0-D_5 of the input port supply the instructions to pins I_1-I_6 pins; input data is placed on the D_0-D_3 lines and enters through I_0-I_3 . The 1802 uses the upper input data, bits D_6 and D_7 , to reset and/or halt the NOM. This data is shown in Fig. 7, along with port decoding information.

The control logic is actually quite simple; and in fact, the whole circuit is also very simple. It can be described in three parts—the *input decode*, the *NOM status register* and the *interrupt request circuitry*.

The input decode circuit is formed by IC3, IC4 and IC9-a. These IC's are used to decode \overline{N}_0 , N_1 , \overline{N}_2 , MRD, and TPB

 $(\overline{N}_0.N_1.\overline{N}_2.MRD.TPB)$, and to clock data and/or instruction into the input buffer. The product of \overline{N}_0 , N_1 , \overline{N}_2 , \overline{MRD} , and MWR ($\overline{N}_0.N_1.\overline{N}_2.MRD.MWR$) is used to enable the output buffers allowing status information and data from the DO_0-DO_3 output of the NOM to be transferred to the 1802. One-shot 1C9-a produces a pulse on the falling edge of control line N2, which is used to reset input-ready flip-flop 1C6-a.

The NOM status register is formed by one-half of the output buffer, IC_2 (bits D_4-D_7), and four D-type flip-flops along with two inverters. The least significant bit, D_4 , is the error flag; eight possible types of errors are shown in Table 2.

TABLE 2—Error Conditions of the MM57109 NOM

- 1. LN X and LOG X when X 0
- 2. When any result is $10^{-99} \mbox{ or } 10^{100}$
- 3. When TAN 90°, 270°, 450°, etc.
- 4. SIN X, COS X, TAN X for X 9000°
- 5. SIN⁻¹X, COS⁻¹X for X 1 or X 10⁻⁵⁰
- 6. For SQRT X when X O
- For /, INV, 1/X when X = 0
 In the floating-point mode for the our instruction if the number of digits to
- the left of the decimal point is equal to the Mantissa Digit Count.

Whenever an error occurs, an ECLR (*E*rror *F*lag Clear) instruction must be executed. The error flag can be tested at any time by the TERR instruction, a branch-type instruction (branches if ERROR = 1). The 1802 can also check this condition by reading the D_4 bit of the output port; this bit is reset after its access. Bit D_5 is the input-ready signal; it indicates the NOM is ready to execute the next instruction or to get the second word of a two-word instruction.

In order to permit asynchronous operation between the 1802 and the NOM on the rising edge of bit D_5 , the NOM is placed into a hold state, hold = 1. When flip-flop IC6-a is reset by control line N2 (as stated earlier, this occurs on the falling edge of control line N0, which is used to load new instructions into the input port) the NOM will execute the next instruction. When the user's program is informed by bit D_5 that the input is ready, then the program provides the next instruction to the NOM.

Bit D_6 is the output-ready signal. Upon receiving this information, the user program stores the data into a software FIFO—a table in memory that acts like a first-in/first-out memory. The reason that a software FIFO is needed is due to the method in which the NOM outputs data. An OUT instruction is sent to the NOM, which, in turn, causes the NOM to output the first digit. Ten microseconds later, the output-ready flip-flop is set, and the DO_1 - DO_4 output bits are clocked into the lower bits (D_0-D_3) of the output port. This data must be read and

DA4-DA1	IN: OUT:	D ₄ DO ₄	D ₃ DO ₃	D ₂ DO ₂	D ₁ DO ₁
0 1	ι.	Most Significant Least Significant		Exponent Digit	~~~~~~
2 3	2	Sign (Mantissa) Unused	0	0	Sign (Exponent)
4		Most Significant	Mantissa (Fol	lowed by Decima	al Point) 👘
		• 4	*		
Mantissa Digit Count + 3		Least Significant	Mantissa Digi	t	

FIG. 5-IN/OUT INSTRUCTIONS (Scientific Notation Mode).

DA ₄ -DA ₁	Decimal point position	IN: OUT:	D4 DO4	D ₃ DO ₃	D ₂ DO ₂	D ₁ DO ₁
2	S. 19 10		Sign (Mantissa)	0	0	0
4	11		Most Significan	Position t Mantissa	Digit=0-9	
· +						
Manting	12 Manufine					
Digit Count + 3	Digit Count		Least Significar	it Mantissa	Digit=0-9	
Also; The r Fort The s The c	nantissa digit count be sign of the manti ign of the exponent fecimal point positio digit count), which column in the table	is set by t issa zero re is equal t on indicat indicates : The dec	the SMDC instruct epresents positive to zero in the float or is a value in the a digit, as given b imal point is loca	tion, initia and one re ting point of a range from y the decimination ted to the second	Ily it is equal to presents negative node. If down to (1 ral point position ight of this dist	eight, /e. 12-mantis n indicato
i. 6IN/OUT II	NSTRUCTIONS (FI	oating-Po	oint Mode).		ight of this dign	ι.
	6 16 14 13 12		D ₇ B	R OR IR	ER D ₄ D ₃	D ₂ D ₁
	a				Ь	
				1		

 $N_0=0$, $N_1=1$, $N_2=0$, MRD=0 Wh R is reset, H is hold, I_1-I_6 is the instruction inputs,



the flip-flop cleared (by reading the port) within 140 μ s because the second digit will be outputted at that time. Every 140 μ s, a new digit will be made available, along with a data-ready signal, until the full number is outputted. The last bit, D₇, is the branch signal. This signal indicates a program branch has been encountered; input ready is set during this signal.

 D_1D_3 is the digit data out,

BR is the branch output,

OR is the output ready,

IR is the input ready,

FIG. 7-PORT FORMAT. a) Input; b) Output.

ER is the error output.

When:

Where-

The interrupt-request circuitry is formed by the 4-input OR gate, IC12-a, and the D-type flip-flop, IC8-b, along with a DIP switch and four pull-down resistors. The four status signals (error, input ready, output ready and branch) are connected to a 4-input OR gate via a set of SPST switches, along with the pull-down resistors. This permits any of the status

signals to clock a logic 1 into the D-type flip-flop, providing a total of 16 possible interrupt (or event-flag) conditions. For example, if you're only interested in knowing when the output is ready (this implies that no branch instructions are to be used, that the data/instructions inputted are free of errors, and sufficient time is allowed between instructions so that a new instruction can never be inputted in the middle of an instruction already being executed), then all but the output-ready switches are opened. Thus, a logic 1 is clocked into IC8-b only when the outputready signal is active. The Q output relays this information to the 1802 via either the IRQ, or one of the four event flags (EF_1 , continued on page 79

Radio-Electronics Tests Sansui G-9000 AM/FM Receiver



CIRCLE 104 ON FREE INFORMATION CARD

LEN FELDMAN CONTRIBUTING HI-FI EDITOR

SANSUI'S TOP RECEIVERS THIS YEAR ALL feature a DC-configured power-amplifier section. This means that there are no input coupling capacitors to the power section, and that all capacitors in the feedback network have been eliminated. The advantages claimed for this circuitry are in improved transient response (lower transient intermodulation distortion) and a frequency response that goes right down to DC. The audible difference between an AC-coupled amplifier and a DCcoupled one may be subtle to inexperienced listeners, but serious audiophiles report somewhat cleaner and more accurate sound reproduction from such DC-configured circuits.

From our point of view, the Sansui model G-9000 offers a good deal more than just a DC amplifier. The front panel, shown in Fig. 1, is loaded with features that will delight the audio buff seeking maximum control and flexibility. The light-colored, sloped frequency scales (the FM scale is linearly calibrated with markings at every 200 kHz) are surmounted by four well-illuminated meters, two of which are power-output meters, logarithmically calibrated from 0.1 watt to 300 watts (referred to 8-ohm loads). The other two meters are signal-strength and center-of-channel indicators for the tuner. To the left of the meters are four

indicator lights, two showing which speaker pair is activated; the other two serve as a power-on indicator and a "protector" indicator. The protector indicator flashes intermittently for a few seconds when the power is first turned on until voltages have been fully stabilized, after which sound is heard from the speakers.

Five indicator lights to the right of the meters denote the program source selected. A series of positive-feel toggle switches just below the dial area to the left handle power, speaker selection, bass and treble control turnover frequencies (200 Hz or 400 Hz for the bass control, 3 kHz or 5 kHz for the treble), tone control defeat and -20 dB audio muting. Similar toggle switches to the right handle FM muting, stereo or mono listening modes, 25- μ s or 75- μ s de-emphasis, FM noise filter and wide or narrow bandwidth for the FM IF circuits. A microphone mixing level control and microphone input jack are located at the extreme lower right-hand side of the panel.

Major controls along the bottom of the front panel include BASS, TREBLE and MID-RANGE tone controls (each with fixed, detented steps for easy resetting), balance control, program SELECTOR switch and TAPE MONITOR switch (with positions for monitoring either of two connected tape decks or dubbing from one to another). Two massive knobs in the center of the panel take care of frequency tuning (the

MANUFACTURER'S PUBLISHED SPECIFICATIONS: FM TUNER:

Usable Sensitivity: mono, 8.7 dBf (1.5μ V); stereo, 15 dBf. **50-dB Quieting:** mono, 12.5 dBf; stereo, 34.0 dBf. **S/N Ratio:** mono, 80 dB; stereo, 76 dB. **Harmonic Distortion** (wide): mono, 0.06% at 1 kHz and 100 Hz; 0.08% at 6 kHz; stereo, 0.1% at 100 Hz and 6 kHz, 0.08% at 1 kHz. **Selectivity:** 90 dB (narrow); 55 dB (wide). **Capture Ratio:** 0.9 dB. **Image, IF and Spurious Rejection:** 110 dB. **Frequency Response:** 30 Hz to 15 kHz, +0.2, -1.0 dB. **Stereo Separation:** 50 dB at 1 kHz; 40 dB at 100 Hz and 10 kHz.

AM TUNER:

Usable Sensitivity: 300 µV/M internal antenna. Selectivity: 30 dB. S/N Ratio: 50 dB. Distortion: 0.45%. Image and IF Rejection: 70 dB.

AMPLIFIER:

Power Output: 160 watts-per-channel into 4 or 8 ohms, 20 Hz to 20 kHz at no more than 0.03% total harmonic distortion. **IM Distortion:** 0.03%. **Damping Factor:** 60. **Frequency Response:** power amplifier section, DC to 200 kHz, +0, -3 dB; overall, auxiliary inputs, 5 Hz to 50 kHz, +0.2, -1.5 dB; phono, RIAA ± 0.2 dB. **Input Sensitivity:** phono 1 & 2, 2.5 mV; high level, 150 mV; mike, 6.0 mV. **S/N Ratio:** phono, 78 dB ("A" weighted); high level, 95 dB. **Bass Control Range** (400-Hz turnover): ± 10 dB at 50 Hz. **Treble Control Range** (1.5 kHz turnover): ± 10 dB at 10 kHz. **Mid-Range:** ± 5 dB at 1.5 kHz. **Subsonic Filter:** -3 dB at 16 Hz (6 dB-per-octave). **High-cut filter:** -3 dB at 3 kHz (6 dB-per-octave).

GENERAL SPECIFICATIONS:

Rated Power Consumption: 680 watts. Dimensions: 22^{1} W \times 8 H \times 19¹/₂ inches D. Net Weight: 59.3 lb. Suggested Retail Price: \$1050.

smoothest-acting flywheel-dial arrangement we've ever experienced) and master volumecontrol settings. The volume control contains an index tab that can be set at preferred maximum listening levels. Its clutch-like action prevents the volume control from being accidentally turned to overload or excessive listening levels-a nice feature if there are young children in the house who might inadvertently turn the volume all the way up and destroy speaker voice coils in the process! Three square pushbuttons between these two large controls activate subsonic and high-cut filters as well as the loudness compensation circuits. A similar pushbutton near the program selector switch provides a third circuitinterruption point for the insertion of a fourchannel adapter, graphic equalizer, audio timedelay unit, or a Dolby noise-reduction adapter. A headphone jack just below the POWER switch on the lower left completes the panel layout.

The rear panel of the model G-9000 contains three AC convenience outlets (one switched, two unswitched). One of the most pleasing physical features of this receiver is how the input/output jacks and terminals are positioned. These connections are located in recessed areas in the side wood panels of the unit, rather than at the rear. All input and (tape-out) terminals, as well as AM and FM antenna terminals, can be reached from the right side of the unit, while two sets of springloaded speaker terminals, preamplifier-output/main amplifier-input terminals and a switch that separates the two major receiver sections electrically are located on the opposite side panel (see Fig. 2). A cleverly designed channel along each side of the unit keeps cables



neatly tucked out of sight. This innovative arrangement makes installation extremely simple, especially for a large, heavy unit such as this one, which might be difficult to hook up if all connections had to be made at the rear panel.

No schematic diagram was supplied with our sample test unit, but it is clear from the receiver's internal layout (shown in Fig. 3) that the huge toroidally wound power transformer has two separate secondary windings, each of which supplies power to a single channel; voltages are filtered separately by two pairs of large filter capacitors.



3

The power amplifier, as mentioned earlier, is a DC-coupled circuit from input to output, with no capacitors either in the signal path or in the overall negative-feedback loop. If components are connected directly to the main amplifier (by separating the preamplifier and main amplifier sections electrically), and if the outputs of those components contain any DC signal, the protection circuit immediately disconnects the speakers from the output stages. In that event, the switch separating the preamplifier output from the main-amplifier input has a third setting that introduces an input capacitor so that operation can be resumed.

FM measurements

Table 1 summarizes FM measurements made for the *model G-9000*. Where measurements differ between the wideband and narrowband positions, both sets of measurements are shown separated by a slash (/). For example, while 1-kHz mono distortion measured an incredibly low 0.03% in mono using the preferred wideband setting, when using the narrowband setting (used only if adjacent-channel interference is encountered in crowded FM listening areas), distortion increased to 0.15%.

Sansui evidently decided to make the narrowband IF response very narrow indeed because distortion rises markedly when this setting is used, particularly in the stereo mode. Wideband distortion readings, on the other hand, are among the lowest we've ever encountered for an FM tuner section or, for that matter, for a separate component tuner.

Separation capability of the *model G-9000* also varies depending upon whether the narrow or wideband settings are used. Figure 4 shows the frequency response of the left channel as well as crosstalk in the opposite channel (lower trace). The vertical scale is 10 dB-per-division, and we measured static separation of nearly 50



TABLE 1

RADIO-ELECTRONICS PRODUCT TEST REPORT

Manufacturer: Sansui

Model: G-9000

FM PERFORMANCE MEASUREMENTS

SENSITIVITY, NOISE AND	R-É	R-E
FREEDOM FROM INTERFERENCE	Measurement	Evaluation
IHF sensitivity, mono (µV) (dBf)	1.5 (8.7)	Superb
Sensitivity, stereo (μV)	5.5 (20.0)	Very good
50-dB quieting signal, mono (µV)	1.5 (8.7)	Superb
50-dB quieting signal, stereo (μV)	18 (30.3)	Superb
Maximum S/N radio, mono (dB)	80	Excellent
Maximum S/N ratio, stereo (dB)	76	Excellent
Capture ratio (dB)	1.0	Excellent
AM suppression (dB)	65	Excellent
Image rejection (dB)	100+	Superb
IF rejection (dB)	100+	Superb
Spurious rejection (dB)	100+	Superb
Alternate channel selectivity (dB)	92/53	Excellent
FIDELITY AND DISTORTION MEASUREMENTS		
Frequency response, 50 Hz to 15 kHz (±dB)	+0, -1.5	Good
Harmonic distortion, 1 kHz, mono (%)	0.03/0.15	Superb
Harmonic distortion, 1 kHz, stereo (%)	0.06/0.5	Superb/see
		text
Harmonic distortion, 100 Hz, mono (%)	0.06/0.07	Superb
Harmonic distortion, 100 Hz, stereo (%)	0.09/0.7	Superb/see
		text
Harmonic distortion, 6 kHz, mono (%)	0.06/0.13	Superb
Harmonic distortion, 6 kHz, stereo (%)	0.10/0.45	Excellent
Distortion at 50-dB quieting, mono (%)	3.0/5.0	See text
Distortion at 50-dB quieting, stereo (%)	0.8/0.6	Good
STEREO PERFORMANCE MEASUREMENTS		
Stereo threshold (uV) (dBf)	3.0 (14.8)	Verv good
Separation, 1 kHz (dB)	48/36	Excellent
Separation, 100 Hz (dB)	43/34	Verv good
Separation, 10 kHz (dB)	42/30	Excellent
MISCELLANEOUS MEASUREMENTS		
Muting threshold (μV) (dBf)	4.5 (18.3)	Very good
Dial calibration accuracy (±kHz at MHz)	Perfect	Superb
EVALUATION OF CONTROLS. DESIGN.		
AND CONSTRUCTION		
Control layout		Excellent
Ease of tuning		Very good
Accuracy of meters or other tuning aids		Superb
Usefulness of other controls		Good
Construction and internal layout		Excellent
Ease of servicing		Good
Evaluation of extra features, if any		Very good
		Evently 1
UVERALL FM PERFURMANCE RATING		Excellent

dB at mid-frequencies.

Note that our test equipment is now equipped to provide flat frequency-response readings (compared with the response readings shown in previous test reports that include the de-emphasis characteristics of the tuner) so the



scope traces represent actual response.

When the narrowband setting is used, separation is somewhat diminished, as shown in Fig. 5, although overall frequency response remains unaltered and quite good.

In recent months, we have received requests to report on the AM sections of the tuners and receivers we test. While the harmonic distortion and signal-to-noise ratio measured for the AM section of the *model G-9000* were both within specifications, the AM frequency response (not specified by Sansui) was as poor as it is on most "hi-fi" receivers. To demonstrate this, we swept modulating frequencies much as we do for FM response measurements; the results (although hard to believe) are shown in the scope photo of Fig. 6. Perhaps Sansui and other manufacturers will pay more attention to



their AM bandwidth if and when stereo AM becomes a reality in the near future. For the moment, the less said about AM response in typical high-fidelity receivers and tuners the better.

Amplifier measurements

Table 2 lists amplifier measurements made on the *model G-9000*. The amplifier delivered more than its rated output at all frequencies before significant distortion was observed. The reason for the two power readings at 20 kHz (into 8-ohm loads) in Table 2 is because we were uncertain whether Sansui wishes to rate distortion at 0.02% or 0.03% (rather an academic point, since neither level of harmonic distortion would be audible). In any event, if the figure is 0.03%, the amplifier delivers greater than its 160-watt rating even at the 20-kHz extreme; if 0.02% is really the rated specification, it falls a bit short of the 160-watt mark at the high-frequency extreme.

We were pleased to find variable turnover tone controls on this high-powered, high-quality stereo receiver because the user then has much greater tone control capability. The range of tone controls using inner 400-Hz and 2.5-kHz turnovers is shown in Fig. 7, along



with the high-cut filter action. This filter, with its very moderate slope, clearly offers no advantages over treble-cut filters since the two curves almost coincide. Sansui could have provided a steeper slope (12 dB-per-octave) or at least raised the cutoff frequency of their high-cut filter.



Figure 8 shows the bass and treble control range when the 200-Hz and 5-kHz turnover points are selected. Superimposed on these curves is the mid-range tone control range whose well-centered frequency provides the type of presence control action expected of it.

The loudness compensation circuit at various listening levels has a fairly typical response.

Summary

Table 3 contains our overall product evaluation along with our summary comments. We believe the Sansui *model G-9000* represents the most advanced circuit design yet seen in all-in-one receivers, and, considering what separate components offering the same quality, power and flexibility can cost, turns out to be very fairly priced. **R-E**

TABLE 2

RADIO-ELECTRONICS PRODUCT TEST REPORT

Manufacturer: Sansui

Model: G-9000

AMPLIFIER PERFORMANCE MEASUREMENTS

	R-E	R-E
POWER OUTPUT CAPABILITY	Measurement	Evaluation
RMS power/channel, 8-ohms, 1 kHz (watts)	174.8	Excellent
RMS power/channel, 8-ohms, 20 Hz (watts)	169.3	Excellent
BMS power/channel, 8-ohms, 20 kHz (watts)	158.0/162.0	See text
BMS power/channel 4-ohms 1 kHz (watts)	256.0	Excellent
BMS power/channel 4-ohms 20 Hz (watts)	232.0	Excellent
RMS power/channel 4-ohms 20 kHz (watts)	203.0	Very good
Frequency limits for rated output (Hz-kHz)	10-30	Good
Trequency limits for fated output (fiz-kfiz)	10-00	4004
DISTORTION MEASUREMENTS		
Harmonic distortion at rated output, 1 kHz (%)	0.006	Superb
Intermodulation distortion, rated output (%)	0.009	Superb
Harmonic distortion at 1-watt output, 1 kHz (%)	0.018	Very good
Intermodulation distortion at 1-watt output (%)	0.025	Excellent
DAMPING FACTOR, AT 8 OHMS	100	Excellent
PHONO PREAMPLIFIER MEASUREMENTS		
Frequency response (RIAA ± dB)	+0, -0.5	Very good
Maximum input before overload (mV)	340	Superb
Hum/noise referred to full output (dB)		
(at rated input sensitivity)	78 ("A" weighted)	Excellent
	2 80 20	Superb
Frequency response (Hz-KHz, ± dB)	3-80, 3.0	Superb
Hum/noise referred to full output (dB)	93	Supero
Residual hum/hoise (minimum volume) (dB)	102	Excellent
TONAL COMPENSATION MEASUREMENTS		
Action of bass and treble controls	See Fig. 7	Excellent
Action of secondary tone controls	See Fig. 8	Excellent
Action of low-frequency filter(s)	000 · · g. ·	Excellent
Action of high-frequency filter(s)	See Fig. 8	Fair
Action of high-nequency inter(3)	oberig. e	
COMPONENT MATCHING MEASUREMENTS		
Input sensitivity, phono 1/phono 2 (mV)	2.5/2.5	
Input sensitivity, auxiliary input(s) (mV)	150	
Input sensitivity, tape input(s) (mV)	150	
Output level, tape output(s) (mV)	150	
Output level, headphone jack(s) (V or mW)	100 mW	
EVALUATION OF CONTROLS,		
CONSTRUCTION AND DESIGN		
Adequacy of program source and monitor switching		Excellent
Adequacy of input facilities		Excellent
Arrangement of controls (panel layout)		Excellent
Action of controls and switches		Excellent
Design and construction		Very good
Ease of servicing		Good
OVERALL AMPLIFIER PERFORMANCE RATING		Excellent

TABLE 3

OVERALL PRODUCT ANALYSIS

Retail price	\$1050
Price category	High
Price/performance ratio	Excellent
Styling and appearance	Very good
Sound quality	Superb
Aechanical performance	Very good

Comments: If ever a manufacturer could boast that a receiver offers all the features and performance of high-priced separate components, Sansui has certainly earned that right with their model G-9000. The DC-amplifier configuration of the power section provides sound quality as good as any we have heard from the most sophisticated separate power amplifiers. To judge this quality, in our listening tests we used several mintcondition direct-to-disc recordings.

As for control flexibility, the model G-9000 has it all; so much, in fact, that the front panel may seem somewhat intimidating on first use. Soon, however, the logical front-panel arrangement becomes familiar, and easy to use and enjoy.

Sansui was among the first to offer variable IF bandwidth for FM in a tuner, and they have extended that feature into this receiver. We found, however, that while the wide setting offers about the lowest distortion FM we've ever measured, the narrow position (useful when stations are very close together on the dial) is almost *too* narrow and should be avoided unless there is just no alternative for a desired incoming signal.

The well-calibrated power-output meters should insure against inadvertent overload and clipping, although that is hardly likely to happen because of the receiver's high power-output capability. Since many otherwise excellent speakers cannot handle the full power output of this receiver, prospective users are urged to check out the maximum power-handling capacity of the speakers they intend to use with it. The model *G-9000* is truly one of the best "component" systems we have ever checked.

Lectrotech Model PPI-400 Peak Power Indicator

LEN FELDMAN CONTRIBUTING HI-FI EDITOR



CIRCLE 105 ON FREE INFORMATION CARD

THE LECTROTECH MODEL PPI-400 PEAK-POWER indicator is an accessory device intended for connection either to the amplifier output terminals or to the speaker terminals of a highfidelity component system. It indicates instantaneous peak-power output from a stereo amplifier by means of flashing LED's, each calibrated to light at a different input voltage to the unit.

The front panel of the *model PPI-400* is shown in Fig. 1. The left side of the panel contains a pushbutton POWER on/off switch. The dark-colored center of the panel contains two vertical rows of LED's, calibrated from 0 dB at the top to -30 dB for the lowest indicator in each row. The bottom four LED's in each row are green, the next pair are yellow and the top two LED's in each row are red.

A selector switch on the right-hand side of the panel selects the sensitivity range of the indicators, as related to the nominal impedance of the speakers being used. For 4-ohm speakers, the six calibrated switch settings provide a power range (for 0-dB indications) from 25 watts to 600 watts-per-channel. For 8-ohm speakers, the ranges are from 12.5 watts to 400 watts; whereas for 16-ohm speakers, the power ranges from 6.25 watts to 200 watts. This arrangement demonstrates that the unit actually responds to input voltage rather than to true power input and, therefore, will not take into account any variations in speaker impedance with frequency. The device, therefore, displays errors depending upon a speaker's departure from nominal impedance at different audio frequencies.

A seventh selector switch setting permits you to calibrate the instrument to any desired 0-dB reference level other than those already provided. This switch position, labeled AUX on the panel, is calibrated by installing two appropriate resistors across two sets of terminals at the left of the rear panel that come supplied with jumpers. The jumpers are removed and resistors are substituted. You select the resistors in accordance with the formula, R = TABLE 1

RADIO-ELECTRONICS PRODUCT TEST REPORT

Manufacturer: Lectrotech

Model: PPI-400

OVERALL PRODUCT ANALYSIS

Retail price	\$129.95
Price category	Mediur
Price/performance ratio	Fair
Styling and appearance	Good
Sound quality	N/A
Mechanical performance	Good

Comments: Certainly this accessory device does what it is supposed to do with a fair degree of accuracy. It provides an approximate indication of instantaneous peak-power output from a stereo audio amplifier. The LED indicators do not suddenly light up or go out. Rather, there is a transitional region for each LED that spans between 2 dB and 3 dB. You must decide subjectively when a given LED is really lit. A vague area of indication that is 3 dB in amplitude represents a power difference of 2:1—which can be trying when you are judging whether or not your amplifier is going into clipping or not!

In addition, by just adding a bit more circuitry and switching, the model PPI-400 could have been made to read voltages that are typical of those applied to tape-deck inputs. Thus, the unit could have served as a peak indicator to augment most cassette or open-reel tape-deck VU meters. We are told by the people at Lectrotech that such a dual-purpose unit is evidently planned, but it will undoubtedly cost more than the single-purpose *model PPI-400* we tested. Presently, the minimum sensitivity of 3.13 mW across 8 ohms is equivalent to 0.16 volt, somewhat more than is usually available at the record-out jacks of most amplifiers or receivers, and greater than the amount of drive usually required by most tape-deck line inputs.

If you want to know approximately how much power you are feeding to your loudspeakers at all times, the model PPI-400 will serve that purpose, and if your hi-fi component system is fairly expensive and lacks peak-power indication, the added cost of a device such as the model PPI-400 may not seem unreasonable—especially if it prevents speaker burnout even once in the life of your stereo system.

 $2\sqrt{PZ} = 10$, where P is the desired power for 0-dB reference, Z is the loudspeaker impedance and R is the required resistor, in 1000 ohms.

This additional switch position makes it possible to calibrate and use the instrument with two monophonic amplifiers (for example, musical instrument amplifiers) even if the power-output rating of each amplifier is different (two different resistance values can be used).

Measurement and use tests

To test the *model PPI-400* we hooked up a suitable amplifier with which to evaluate the unit.

Since the device is most useful in measuring short-term or instantaneous power, we used a variety of tone bursts to determine whether the LED's respond quickly enough to music-like transients. Specifically, we used the new test signal required in the dynamic headroom test of the recent IHF Amplifier Measurement

MANUFACTURER'S PUBLISHED SPECIFICATIONS:

Input Impedance: 20,000 ohms, minimum. Accuracy: ± 0.25 dB. Frequency Response: 20 Hz to greater than 20 kHz. Maximum Input Power: 1250 watts continuous at 8 ohms. Minimum Input Sensitivity: 3.13 mW at 8 ohms. Loudspeaker Impedance Range: 2 to 35 ohms. Power Range and Impedance Combinations: 18 plus auxiliary. Dimensions: 14 W \times 3³/₄ H \times 8 inches D. Weight: 3¹/₂ lb. Power Requirements: 105 to 125 V, 50 to 60 Hz, 7 watts. Suggested Retail Price: \$129.95 (optional walnut cabinet, \$24.95 extra). Standards (IHF-A-202). This signal consists of 20 ms of a 1-kHz test tone at full amplitude, followed by 480 ms of the same test frequency reduced by 20 dB. The signal's repetition rate is twice per second. Under these test conditions, a standard output meter reads approximately 12 dB below the full-amplitude value of the signal, while the LED's continued to read correct peak instantaneous power.

It was somewhat difficult to judge the accuracy of the device since individual LED's do not light up completely when triggered, but start dimly and then with increased signal amplitude, begin lighting up fully. The change in amplitude of applied voltage between the barely visible illumination of an LED and the full brightness of the same LED ranged from about 2 dB to 3 dB. It is possible, of course, to calibrate the *model PP1-400* so that when the LED's are either barely lit or fully bright, this corresponds to the desired power level, but some eyeball judgment is necessary.

Summary

Our overall product evaluation, together with a summary of its usefulness, is shown in Table 1. The *model PPI-400* is not a precision instrument, but then it is not very expensive for a device of this type. It could prove useful if you own an amplifier with higher poweroutput capabilities than your speakers can handle. Setting the 0-dB calibration point for the speakers' maximum power-handling value could protect the speakers against possible overdrive damage. **R-E**

Jerstanding Namic Jadroom



Dynamic headroom, a new addition to the Institute of High Fidelity amplifier measurement standards. Tells why amplifiers with the same rated power may perform differently under varying signal levels.

LEN FELDMAN CONTRIBUTING HI-FI EDITOR

EVER SINCE THE FEDERAL TRADE COMMISsion issued its rule regarding disclosure of the power output ratings of audio amplifiers, manufacturers of high-fidelity amplifiers and receivers have been faced with a mixed blessing. On the one hand, the requirement that amplifier makers list the continuous power rating of their amplifiers has forced less-than-honest manufacturers to abandon such meaningless power output terms as "peak power," "instantaneous peak power," "music power," "dynamic music power" and more. The continuous power rating, coupled with a statement of load impedance, power bandwidth (the frequency extremes over which the product will actually deliver its rated power) and harmonic distortion has enabled prospective buyers of audio amplifiers to compare brands



FIG. 1—POWER SUPPLY for hi-fi amplifier has large filter capacitors. There is little variation in output voltage between no-load and full-load conditions.



FIG. 2—POWER SUPPLY for hi-fi amplifier has small filter capacitors and a high internal resistance in either the transformer or bridge rectifiers. This supply delivers an output voltage with a wide variation between no-load and full-load conditions. and models on a reasonably equal basis. This uniformity of specifications is all to the good.

On the other hand, it didn't take the experts long to conclude that two amplifiers that have exactly the same continuous power output rating (including the same power bandwidth and even the same rated harmonic distortion) may not necessarily deliver the same program *loudness* to identical speaker systems when fed with actual program signals. Obviously, to properly define the useful power output capability of an amplifier, more information is needed than the simple statement of "continuous power output" capability.

Power supply reserve

The reason for this can be found by examining the two power supply diagrams of Figs. 1 and 2. Note that both supplies have nominal output voltages of plus and minus 50 volts under no-load (no-signal) conditions. The alternate voltages (designated with an asterisk) are those that are present when the amplifier is delivering a large amount of current to the speaker loads. Under a full-load condition, the power supply shown in Fig. 1 delivers plus and minus 48 volts, while the power supply shown in Fig. 2 delivers a much lower output of only plus and minus 40 volts. What does this mean in terms of continuous power output capability?

Assuming that the output signal can swing over the entire peak-to-peak value of the power supply voltage, we see that in the case of the power supply of Fig. 1, a peak-to-peak swing of 96 volts is possible under full-load conditions. This corresponds to an RMS AC value of 33.94 volts (peak-to-peak value divided by 2.828, or multiplied by 0.5×0.707). The continuous power output of this first amplifier, before clipping, would be approximately 144 watts. (P = E²/Z, where E = 33.94 volts and Z, the impedance, is assumed to be 8 ohms.)

Now let's calculate the continuous power output for the amplifier being powered by the supply shown in Fig. 2, where the available supply voltage has dropped to plus and minus 40 volts. The permissible peak-to-peak swing of the output signal voltage is 80 volts. This corresponds to an RMS value of only 28.29 volts, or an equivalent power across 8 ohm loads of 100.03 watts.

Thus we see that even though both power supplies (under no-load conditions) provide the same operating voltages to their associated amplifiers under no-signal (or low-signal) conditions, their maximum continuous power output ratings will differ substantially, with the amplifier powered by the supply shown in Fig. 2 able to deliver only 69% as much continuous power as the amplifier powered by the supply shown in Fig. 1.

Power supply regulation

There are several reasons why the voltage output of the supply shown in Fig. 2 dropped more quickly than the voltage delivered by the supply shown in Fig. 1. For one thing, its filter capacitors are of considerably lower value. The primary filter capacitors in such a supply act as a power or energy reservoir. The greater their value, the greater the amount of energy that can be stored in them. Note, too, that the primary and secondary windings of each of the power transformers used in the two supplies may have different internal resistances and, therefore, different AC voltage drops may appear across these windings before the AC voltages are ever rectified. The bridge rectifiers, too, may differ in internal resistance and may therefore develop greater voltage drops across their terminals as the current demand increases.

Short-term signals

If we were to apply a very short signal burst to each of the amplifiers associated with the power supplies, the situation would be quite different. If the signal burst were short enough, the filter capacitors would maintain their full (or nearly their full) charge for the duration of the short pulse and the available voltage at the power output stages of each amplifier would be very close to the no-load value of plus and minus 50 volts. Under these circumstances, each amplifier would be able to provide a peak-to-peak signal swing of 100 volts, which would correspond to a short-term power output of 156.3 watts!

These short-term musical signals are exactly what an amplifier is called upon to reproduce when it is hooked up to speakers and fed with program sources in a "real-world" high-fidelity system. No one (at least no one we know of) spends much time listening to continuous sinewave test signals. Yet, if we were to be guided by the continuous power ratings of the two amplifiers used in our example, one would have a rating of just over 100 watts-per-channel while the other would be rated at 144 watts-per-channel. In auditioning these two amplifiers you might well conclude that the 100-watt unit (which would undoubtedly sell for less money than the higher powered model) "sounds" just as loud as the higher powered unit before audible clipping takes place.

At the present time, the Institute of High Fidelity is completing its work on a new amplifier measurement standard. One of the most important new measurements that is being incorporated in this new standard has been given the tentative name, Dynamic Headroom. This measurement seeks to take into account the wide discrepancies that may occur between the continuous power ratings of amplifiers and their ability to deliver power over the short terms typically required during music signal reproduction. In order to simulate these shortterm conditions, studies were conducted by the IHF committee regarding the actual duty-cycle and power distribution of musical signals. A test signal was arrived at which, it is felt, approximates what an amplifier must be able to handle when reproducing typical music signals. This new test signal is shown in Fig. 3. It consists of a 1-kHz signal which is at an



FIG. 3—NEW TEST SIGNAL for determining dynamic headroom of an amplifier. Signal consists of 1-kHz sinewaves with a 10 dB amplitude change.

arbitrary level for 20 milliseconds (twenty cycles of this signal will therefore appear) and the same frequency at an amplitude 10 dB lower for another 1980 milliseconds. This complex signal is repeated, therefore, every two seconds. A portion of the test signal is shown in the scope photo of Fig. 4.



FIG. 4—NEW TEST SIGNAL as it would appear on an oscilloscope.

We den to check ow an amplifier ous lab at the time, this ten. The amplification of er output rating of the test To confirm this, we de to de to on test signal into the and be on of our scope display so this in our sion equalled 10 volts is write reached clipping when the the sinewaves reached 40 sions), as shown in Fig. 5.



FIG. 5—CONTINUOUS POWER OUTPUT can be determined by determining the input level required to drive an amplifier into clipping with a 1-kHz input signal.

sponded to an RMS value of 14.14 volts, or just over 25 watts across an 8 ohm load.

Next, we applied a signal similar to that shown in Figs. 3 and 4. The oscilloscope's sweep rate was increased so that we might be able to examine the crests of the sinewaves during the 20-millisecond duration of the higher amplitude pulses.



FIG. 6—OUTPUT OF AMPLIFIER that is being driven just to the point of clipping by new test signal.

Again, the gain was increased until evidence of clipping appeared, without changing the vertical sensitivity of the scope input. The results are shown in Fig. 6. Using our new test signal, the peakto-peak amplitude of the sinewaves reached 60 volts. This corresponds to an RMS value of 21.22 volts, or a short-term power output capability of 56.27 watts!

Specifying dynamic headroom

The IHF Amplifier Standards Committee was faced with the problem of how best to specify the new measurement results. They could, of course, simply list a second power rating and call it "dynamic power." If that were to be done, however, the Federal Trade Commission requires that such a "secondary" power rating must be printed in the spec sheets (or in any advertising material) using

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letters that are no greater than two-thirds the size of the letters used to specify the continuous power rating. In addition, the appearance of an alternate power rating in watts might, in time, lead to the same sort of confusion that arose years ago and that prompted the Federal Trade Commission to promulgate the power rule for audio amplifiers in the first place.

The committee therefore decided that Dynamic Headroom should be specified in terms of decibels *above* the rated continuous power. Not only does this get around the problem of having multiple (and confusing) wattage listings for the same amplifier, but it serves to give the potential purchaser some idea as to how much louder the amplifier will sound when reproducing music signals as compared with its ability to reproduce a continuous sinewave test tone.

In the example just taken from our actual measurements in the lab, since the short-term power was 56.27 watts and the continuous power rating was 25 watts, the ratio of those two numbers is 2.25, which translates to a Dynamic Headroom of 3.52 dB. Based upon our experience with a variety of amplifiers, that degree of Dynamic Headroom is very great indeed. Generally, you can expect the Dynamic Headroom of typical audio amplifiers to range from 0 dB to around 3.0 dB. A Dynamic Headroom of 0 dB would mean that the amplifier has a very stiff power supply-one whose voltage does not vary at all from "no-signal" to full-signal conditions. An amplifier with a Dynamic Headroom of 3 dB would be one with a very "soft" or poorly regulated power supply whose voltage drops by a factor of approximately 30% when full current is delivered to the load.

Checking dynamic headroom

You can check the dynamic headroom of your own amplifier or receiver even if you lack the test equipment needed to produce the special signal we have described. All you need is an accurately calibrated oscilloscope and an audio oscillator which can apply a single-tone 1-kHz signal to the high level inputs of your equipment. First, determine the amplitude of a 1-kHz output signal that causes the amplifier to barely clip. Record the amplitude in peak-to-peak volts, as observed on the scope. Then, apply a music signal to the set (either from a recording or from an FM tuner) and observe the new clipping level, which should be somewhat greater than that obtained during the first sinewave test. Even if your scope is not calibrated, you can still determine the Dynamic Headroom of your equipment quite accurately, in dB. For example, we used still another amplifier and, without regard to its continuous power rating, we applied a 1-kHz test tone until clipping occurred. We set the sensitivity of the vertical amplifier of the scope so that this amplitude occupied four divisions, vertically (see Fig. 7). Then, applying a music signal to the amplifier, we noted that at clipping, the vertical amplitude was 4.6 vertical divisions on the scope (positive peaks were about 3/10ths of a division higher than before, and so were negative clipped peaks). These results are shown in the scope photo of Fig. 8. To calculate the Dynamic Headroom of this amplifier, divide 4.6 by 4.0 to obtain a voltage ratio of 1.15. This corresponds to a dB difference of 1.21. So, the Dynamic Headroom of this amplifier is 1.21 dB.

The concept of Dynamic Headroom is

ic "town meeting of the air." This was made possible by Warner Cable's participatory two-way cable QUBE system, which allows viewers to express their opinions on the air on matters of public interest, such as municipal services, public utilities, etc., in their areas.

Here's how QUBE works: Town officials, broadcasting live from QUBE's TV studios, posed questions to which viewers responded by pressing a button on their home terminals, registering their opinions or criticisms. The results were then tabulated by a computer for display on home TV screens. A special hookup to the QUBE studios also made it possible for participants to phone in their own questions to the town officials. Random numbers were assigned so that no individual home was known or identified with the opinions expressed.

National satellite network transmits public service programs

This past September, the Public Service Satellite Consortium (PSSC) used Denver, CO, as the "launch site" of a new continuing education program transmitted via a communications satellite. This transmis-



FIG. 7—AMPLIFIER'S DYNAMIC HEADROOM can be obtained by first determining the input level required to drive the amplifier into clipping with a 1-kHz input signal.



FIG. 8—CLIPPING LEVEL of program material is used to determine amplifier's dynamic headroom.

just one of the new measurement ideas which will be incorporated in the new IHF Amplifier Measurement Standards, but it is one which should help to clarify one of the seeming discrepancies which still exists when comparing the performance of similarly power-rated audio products that, under actual use, "sound different." As soon as the members of the IHF approve of the entire new standard, we will detail the other important measurement techniques incorporated in that standard in a future article. **R-E**

sion was a pilot demonstration of the nonbroadcast use of the public television satellite system.

The program was a special education presentation created for members of the American Dietetic Association who viewed it at more than 100 selected locations in eight major cities across the U.S. Pretaped portions of the program were linked up with a panel of experts in Denver, and the dieticians viewing it on screens in auditoriums, schools and hospitals could question the experts via telephone.

The signal was beamed to Western Union's communications satellite Westar I, which then retransmitted it to public television stations in Anchorage, AK, San Diego, Indianapolis, Las Vegas, Columbus, OH, Cleveland, OH, Columbia SC and Spokane, WA.

According to the PSSC, this new satellite system will connect all PBS stations by the end of 1978. Because the system allows each station to receive up to four transmission simultaneously, this will allow PBS to transmit nonbroadcast programs for its members, more than 100 nonprofit organizations and other nonprofit groups. The system is expected to be operative by early 1979. R-E

Pioneer Electronics receives award for contribution to arts

U.S. Pioneer Electronics, a leading audio component manufacturer, was a first-time winner this year in the 12th annual "Business in the Arts" awards program, cosponsored by *Forbes* magazine and the Business Committee for the Arts, a national organization that promotes greater business and industry involvement in the arts. The award this year was an original print by American artist Romare Bearden.

Pioneer was awarded its prize for a national campaign to raise funds for the Metropolitan Opera. Not only did the company match every dollar contributed by the general public and authorized Pioneer dealers, but obtained matching funds from the National Endowment for the Arts; this helped quadruple the original public donations. The company footed the bill for all advertising and promotional campaigns. A quarter of a million dollars was raised for the Met.

Electronic town meeting via 2-way cable TV

Upper Arlington, OH, a suburb of Columbus, recently experienced its first electronDECEMBER 1978

ALL ABOUT

Hobby Computer



STATIC RAM MEMORY from Vector Graphic is designed for the S-100 bus structure. Four Motorola MC7805CP 3-terminal voltatge-regulator iC's are along the right-hand edge.



TYPICAL POWER SUPPLY for hobby computers has heavy power transformer and computer-grade electolytic filter capacitors. The voltage regulators are mounted on the motherboard.



VECTOR model 1+ microcomputer has power supply along right side of cabinet. Cooling fan in right rear corner exhausts hot air.

The power supply is perhaps one of the most critical of all often used in microcomputer circuitry, operate from a DC supply be allowed to go above 5.6. This is the story of high-current power

MICROCOMPUTERS BUILT WITH THE S-100 BUS USE DISTRIBUTED regulation to supply +5 volts DC to the various circuits. Distributed regulation uses one or more three-terminal IC regulators (i.e., LM309, 7805, LM340-5) mounted on each printed circuit board. The main high-current power bus on the S-100 motherboard is unregulated +8 volts DC.



FIG. 1—SIMPLE FULL-WAVE RECTIFIER uses two diodes and a centertapped transformer.

But certain other mainframes use +5 volts DC regulated on the main power distribution bus, and obtaining that type of supply at a reasonable cost is quite a chore! At current levels of around 5 amperes, you cannot use a simple three-terminal IC regulator. Finding a series-pass transistor able to handle the load current and possessing a *beta* high enough to allow use of the simple Zener-controlled base circuit type of regulator is almost



FIG. 2—BRIDGE RECTIFIER provides full-wave rectification.

impossible. In this article we will discuss several approaches to solving the problem of high-current supplies.

Pseudo-distributed system

The microcomputer kit I purchased recently uses a motherboard with the +5 volt DC and ground foil traces connected to the individual card-edge connectors. The total current demand of a fully populated motherboard is approximately 16 amperes. In trying to develop a power supply, one solution I tried with moderate success was to cut the +5-volt foil trace on the motherboard at strategic points, and then mount external threeterminal IC regulators nearby. One regulator served each section of the board. The three types mentioned earlier are suitable for current drains of 1 ampere in the TO-3 case, and 750 mA in the plastic case. The LM323 will handle 3 amperes, and the Lambda LAS-1905 will handle 5 amperes.

The pseudo-distributed system works well, but is sloppy. In some cases, this approach is made more difficult by the fact that not all motherboards are laid out in the nice straight lines of the S-100 bus! The +5-volt DC line may wander, breaking off at points, and then rejoining later on. These difficulties often force us to look at other alternatives.

Rectifiers and filters

In the regulator circuits to follow we will show only the regulator and associated circuitry, since this is where the main problem in design is. All these circuits will be preceded by a rectifier and filter circuit such as those shown in Figs. 1 and 2.

Both of the circuits shown in Figs. 1 and 2 are full-wave rectifiers, meaning that they make use of both alternations of the AC sinewave from the power mains. This type of rectifier is not only easier to filter (the traditional justification) but also results in a higher average DC output voltage, and requires less power (i.e., $V \times A$) from the transformer primary for any given load current.

The rectifier circuit in Fig. 1 uses two solid-state diodes and a center-tapped transformer. The center tap is taken as the

Power Supplies



VECTOR'S VP2 ENCLOSURE permits you to design and house your own microcomputer. Card-guides are provided. Power supply can be installed in smaller compartment.



RAM MEMORY BOARD from Electronic Control Technology mates with S-100 bus. Type LM340T regulator IC's are on massive heat-sink in upper left corner.



COMBINATION RAM AND ROM memory board is designed for S-100 bus configuration. Distributed voltage regulation uses six 3-terminal voltage-regulator IC's.

hobby computer components. TTL (Transistor-Transistor Logic) IC's, with a nominal level of 5 volts and a maximum level that must not supply regulation and how you can design and build your own supply.

JOSEPH J. CARR

common (or ground) terminal, while the positive output is taken from the junction of the two diode cathodes. The alternate circuit, shown in Fig. 2, is a bridge rectifier that can be made from four discrete diodes, or be purchased as a prepackaged bridge.

Almost all voltage regulator circuits require a DC input that is at least 2.5 volts higher than the rated output voltage. In our case, with +5-volts DC output, the DC input voltage should be not less than +7.5. In case you have wondered, this is probably the reason why Altair, the original S-100 bus designers, specified +8 volts for the main power bus. An ordinary 6.3-volt AC (RMS) filament transformer will deliver this potential when full-wave rectified and filtered. The filter will charge to $1.4 \times E_{\rm RMS}$, which in this case is $1.4 \times 6.3 = 8.8$ volts. In most cases, a 6.3-volt high-current filament transformer is sufficient.

The standard 6.3-volt filament transformer is a good choice for use in a circuit such as Fig. 2 because the bridge rectifier uses the entire secondary. Keep in mind, however, that the *transformer can deliver only half its rated current in the bridge configuration.* In the case of Fig. 1, a 12.6-volt transformer will provide 6.3 volts AC either side of the center tap, so it is a good choice. It will deliver the same output potential as the bridge rectifier used with a 6.3-volt transformer, and will supply its rated current.

Fortunately, 6.3 and 12.6 volts AC are the filament ratings of many high-power transmitting tubes used in commercial and military transmitters. Many such transformers are still available on the surplus market, although the supply is down from its heights of only a few years ago. You should be able to save a considerable amount of money by checking the local electronic surplus outlets, ham friends (who tend to save such items), or by attending hamfests and auctions. If a push comes to a shove, or you have money to spend, then go to your local parts distributor and buy a new transformer outright. I have used several Triad types that are particularly useful because their tapped primary offers secondary AC voltages of either 6.3 or 7.5. Table 1 gives the type numbers and ratings. The rectifier diodes or bridge-rectifier stacks should be rated to handle more current than is expected at full load, but keep in mind that they will tend to run very hot if operated at a point near their maximum ratings. It is preferable to select diodes with a 25 or 50 percent margin. For example, for a 20-ampere power supply select a 25- to 30-ampere diode. Also keep in mind that the minimum peak inverse voltage (PIV) rating must be not less than 2.82 times the applied RMS voltage. This is not too much of a problem in \pm 5-volt circuits operated from 6.3-volt transformers, but is very definitely a factor at higher voltages. You cannot, for example, use a 25-volt PIV rectifier in a 12-volt DC supply!

TABLE 1-TYPICAL FILAMENT TRANSFORMERS

Triad No.	Secondary voltage	Amperes
F-22U	6.3VAC	20
F-24U	6.3/7.5 VAC	8
F-28U	6.3/7.5VAC	25
F-56X*	25.2 VAC	2.8

*Ideally suited to making the \pm 12 volt regulated supplies needed for most microcomputers. Use a fullwave bridge *and* the transformer center tap to form two halfwave bridges. This supply will deliver up to 1 ampere at each voltage.

Capacitor C1 in Figs. 1 and 2 should have a capacitance of *not* less than 2000 μ F-per-ampere of load current. In a 20-ampere supply, then, the filter capacitor should be at least 40,000 μ F. It should have a DC working voltage rating of at least 15 volts (WVDC). I used an 80,000 μ F/15 WVDC capacitor in testing these circuits with a 15-ampere load. The 2000 μ F-per-ampere spec is a *minimum*, not an *optimum*, rating.

The 5-volt, 5-amp supply

The Lambda Electronics (515 Broad Hollow Rd., Melville, L.I., N.Y., 11746) model LAS-1905 is one of the most powerful

5-volt three-terminal IC regulators on the market; it has an output-current specification of 5 amperes!

Figure 3 shows a typical circuit using the LAS-1905. Note that this circuit is not too much different from other three-terminal IC regulator circuits. This regulator, however, has better protection than some less powerful and less costly versions. Some of the features more than justify its \$14 price tag. A block diagram of the internal circuitry is shown in Fig. 4. Note that it contains safe-area protection, thermal-overload



FIG. 3—THREE-TERMINAL IC REGULATOR provides 5 volts at 3 amperes.

protection and the all-important current-limiting protection.

The use of current-limiting is almost mandatory once the current level goes much over an ampere or two because of the damage short circuits can cause to printed-circuit traces and other components, including the power supply itself! As the current level increases, so does the possibility of vaporizing the main power bus in your mainframe! In my own Z-80 system, current-limiting saved the motherboard once when some of the protective foil that comes wrapped around 2102 memory IC's accidentally stuck to a printed-circuit card right where the +5-volt DC and ground terminals enter the board. Without the 10-ampere current-limiting circuitry then used in my supply (the circuit in Fig. 5), the tracks may well have vaporized!

The power supply regulator in Fig. 3 is especially useful for powering single-board computers that normally tax 3-ampere regulators, and wipe out 1-ampere types! Note that most regulators tend to offer deteriorated transient response near their full rated output current, so the extra margin of the LAS-1905 would prevent certain types of power-supply-induced "glitch."



FIG. 4—INTERNAL CIRCUITRY of Lambda Electronics LAS-1905 threeterminal regulator.

Use the LAS-1905 for applications where an LM-323 might have to work close to its 3-ampere rated output current.

A 5-volt DC, 10-amp supply

Figure 5 shows a regulator circuit taken from the Motorola applications literature on their MC1469R voltage regulator IC. This circuit can deliver up to 10 amperes with the transistor shown and the 60-milliohm series resistor. The same circuit can deliver up to 15 volts DC for use with CB and ham transceivers if resistors R4 and R5 are changed appropriately. The output voltage is given (approximately) by the formula

$$E_{out} \simeq 3.5 \times 1 + \frac{R4}{R5} - 0.6$$

Transistor Q2 and resistor R1 serve as the current-limiting circuit, and will shut down series-pass regulator Q1 if the voltage drop across R1 exceeds 0.6; the forward bias potential of Q2. The maximum output current, then, is set by resistor R1 and is equal to 0.6/R1. In the case of our 10-ampere supply, R1 is 60 milliohms, i.e., 0.060 ohm. This is not a standard off-the-shelf value so it must be made. I used five 0.33-ohm auto radio fuse resistors, while a friend made one from magnet wire using the ohms/foot data given in the ITT *Radio Engineers Data Book* (Howard W. Sams & Co.).



FIG. 5—10-AMPERE REGULATOR based on Motorola IC regulator and series-pass transistor.

The power supply in Fig. 5 uses a sense line. The MC1469R regulator is a feedback type that uses an error amplifier to compare the actual output voltage with an internal reference voltage. The sense terminal is the input to the internal error amplifier. In supplies that deliver more than 5 or 10 amperes, it is considered good engineering practice to run the sense line to the load separately from the main power bus. This arrangement allows the regulator to sense the voltage actually delivered to the load, rather than the regulator output voltage. If the main power bus is more than a few inches long, or if it is made of a marginally sized conductor, then the IR-drop due to current flow can be considerable. In one large-scale TTL project I built, this drop was almost 1 volt in an 18-inch line, and that can cause all kinds of trouble in ticklish TTL circuits! There are enough real problems in digital electronics without making more by poor power supply technique!

5-volt, 20- or 30-amp supply

The design and construction of a thermally stable, regulated, power supply able to deliver over 10 amperes or so is not an easy chore. While it is easy to theorize on paper, you find that it is a long way from the design desk to the final product! Techniques that should have worked sometimes succumb to some unsuspected glitch or hitch that inevitably fouls up the works. In that case, it might be wise to use a 20- or 30-ampere hybrid voltage regulator such as the Lambda LAS-5205 (20 A) or LAS-7205 (30 A). Figure 6 shows a circuit based on these devices. Both of these modules contain all required circuitry to make a regulator circuit with complete current-limiting and thermal-overload protection.

These regulators have two heat-dissipating surfaces. The lower and larger of the two is thermally connected to the highcurrent series-pass transistors, while the upper is thermally



FIG. 6—HYBRID REGULATOR circuit and external components can be used to provide output currents in the 20- to 30-amp range.

connected to the regulator reference and control circuitry, affording a degree of thermal isolation for these circuits. The regulators also have two separate E_{in} terminals, labeled E_{in1} and E_{in2} in Fig. 6. Terminal E_{in1} is to the main series-pass transistor, and must be at a potential of 2.5 volts higher than E_{o} , or in this 5-volt example, 7.5 volts DC. The other terminal, E_{in2} , supplies the control circuitry and must be at a potential, or in this case, 12.5 volts. If the main, high-current, supply delivers a potential of 12.5 volts or more from the rectifier-filter, then E_{in1} and E_{in2} may be strapped together. But if an 8-volt DC high-current supply is used, then E_{in2} must be tied to some other source. In most microcomputers there is a +12-volt regulated source. Use the +5-volt terminal to supply E_{in2} in that case.



FIG. 7—SCR CROWBAR using discrete components provides shortcircuit protection.

The high cost of these hybrid modules may seem a little steep for amateur and hobbyist use, until one begins to price out commercial +5-volt, 20-ampere supplies. There is at least one on the market that will deliver the current and costs only \$115, but most run closer to \$200. The \$80 price of the LAS-5205 then seems less frightening, especially if you are able to scrounge or buy the transformer and filter capacitor from a surplus source. Note that 6.3- and 12.6-volt center-tapped transformers have a high "scroungability" quotient!

Overvoltage protection

All series-pass transistors work hard and tend to get very hot inside of the case. This heat tends to cause marginal transistors to give up and short out prematurely, which would place the full rectifier-filter voltage on the main power bus of your expensive computer. TTL IC's may well blow out with as little as 7 or 8 volts, so the short-prone series-pass transistor is a scenario for disaster! Your cabinetful of expensive microcomputer may well become a *silicone*-to-carbon converter in a quick hurry!!!

The way to protect your equipment from this type of disaster is to use an overvoltage-protection circuit such as the SCR crowbars of Figs. 7 and 8. The circuit in Fig. 7 uses discrete components, while the circuit in Fig. 8 is an IC version produced by Lambda.

The circuit in Fig. 7 uses a high-current SCR to blow the main bus fuse if an overvoltage condition is sensed. This brute force approach is probably the origin of the inelegant nickname "crowbar." Diode D1 is a 5.6-volt Zener, so it will not pass current as long as the output voltage is normal (i.e., +5 volts). But if the series-pass transistor goes bananas and places the +8



FIG. 8—CROWBAR PROTECTION circuit from Lambda Electronics is available in self-contained modules.

volts on the output, then D1 will conduct and pass current to the gate of SCR1. This current will turn on SCR1, creating a short circuit across the +5-volt line, thereby blowing the fuse. Not very elegant as circuits go, but it will save hundreds of dollars worth of circuitry in the event of a catastrophe. Select an SCR with a current rating considerably higher than the line current; a 30- to 50-ampere rating will do nicely.

I have used the 2-, 6- and 35-ampere Lambda two-terminal overvoltage protectors in various power supplies. In my main computer mainframe, an L-35-OV-5 is used as a reasonably priced alternative to the discrete circuit. The internal circuitry for these devices electronic crowbar is shown in Fig. 8 while the ratings of various 5-volt models (i.e. 6.6V trip point) are given in Table 2.

Transient protection

Lightning and other disturbances can create voltage transients close to 1000 volts on the primary side of your computer

TABLE 2-CROWBAR PR	ROTECTION MODULES
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Lambda Model No.	Current Rating*
L2-OV-5	2 amperes
L6-0V-5	6 amperes
L12-OV-5	12 amperes
L20-OV-5	20 amperes
L35-OV-5	35 amperes

*All models have a trip voltage of 6.6 volts

supply. One amateur computernik friend of mine lost over 20 TTL devices in a single thunderstorm! The General Electric MOV (metal-oxide varistors) offer some degree of protection against this type of damage. Check the GE literature for a suitable model for the current level expected.

DECEMBER

HELSTEREO



SPEAKER how various types

Recently audio engineers have found that ordinary that a loudspeaker sounds. Newly developed

THERE IS A GROWING BODY OF OPINION IN some audio circles that speaker cables have been neglected when considering the performance of a high-fidelity system. However, when one prominent speaker engineer was asked what part he felt cables contributed to speaker performance, his answer was: "Ever since I discovered that no one-not you, not Ican hear the difference between 10-kHz sinewaves and squarewaves, I've been cynical of the claims made by the ultrawide band and no-phase-shift advocates." He was referring to the fact that research has shown that even the best-quality conventional speaker cable cannot pass a squarewave, and causes phase rotation at high frequencies.

Today's thinking has it that the inductance and capacitance of lamp cord (zip cord) should not present a problem in today's audio systems. However, it is generally conceded that for very long runs of speaker wire that is connected to low-impedance speakers, it might be necessary to artificially lower the cables' typical impedance. Therefore, if we assume there is no significant magnetic coupling to other cables capable of absorbing power in low-impedance circuits, there is no reason to believe that speaker cable should significantly affect frequency response.

If you visit some hi-fi stores today, you'll find an increasing number of socalled "super cables," such as Disc Washers' Smog Lifters, Polk's Sound Cables, Audio Source's High Definition Cables, M & K's Mogami cables, or the Fulton line of cables. These represent several different types. For instance, 140strand braided cable (Smog Lifters); 10 pairs of braided insulated wires (Audio Source's High Definition); a stacked coaxial cable (M & K's Mogami); the Fulton cable, which is a very large parallel multishroud type, or the Polk cable, which is also braided with a very fine intertwine. The price of these cables runs from 50 cents-per-foot to \$1.50-per-foot and more, making them considerably more expensive than conventional zip cord that costs 15 cents or less per foot. Zip cord is usually recommended by most audio stores and industry authorities.

It is stated in a typical amplifier instruction manual that No. 18 lamp cord is sufficient for normal lengths (to about 30 feet) between speaker and amplifier. However, No. 16 wire is generally recommended depending on distance. A leading speaker manufacturer has prepared a recommended connection-wire chart that is shown in Table 1.

The wire lengths listed in Table 1 were calculated on a maximum audible coloration of ± 0.5 dB. Following the guide lines provided, the most discerning listener will be unable to detect any coloration introduced by the speaker wire. Most listeners will not notice any effect even if wire lengths are increased as much as 50%.

Audio critics like Leonard Feldman, Radio-Electronics' Contributing High-Fidelity editor, have stated that the

TABLE 1—Recommended Connection Wire		
Maximum wire length (ft)	Wire gauge	
30	18 gauge, zip cord (or two-conductor wire)	
45	16 gauge, two-conductor wire	
70	14 gauge, two-conductor wire	

damping factors of a good amplifier can be practically eliminated by a poor hookup to the speakers, and you should play it safe by using heavy wire and making good connections. Even the finest cables can be rendered ineffective with poor connections.

Speaker designer Roy Cizek has emphasized in a series of articles and speeches to audio groups that "even the 'heavy' gauges No. 14 and No. 16 lamp cord are often insufficient." Mr. Cizek discovered that even small amounts of resistance can affect frequency response by destroying the effective damping of the amplifier-speaker system. He also pointed out the effects of a speaker-line fuse, and recommended using No. 12 or No. 10 wire.

Both Mr. Feldman and Mr. Cizek have pointed out that using heavier speaker wire should not be just the concern of the audiophile trying to extract the last bit of performance out of a hi-fi system. Most audio systems sold have 25 watts of amplifier power or less, and combined with a low-impedance speaker, certain wire lengths may throw away up to 30% or 40% of the amplifier power. As Mr. Cizek states, "contrary to common practice, it can be especially important to use heavier wire with smaller amplifiers or receivers, since they have low power output and low damping factors to begin with."

What the advocates of better cables point out is that many speakers are designed to provide good efficiency and transient response when effective damping is high. The low internal impedance of today's modern transistorized amplifiers reduces the amplifier's damping capability. With a low damping factor, the speaker continues to vibrate after the signal is cut off, which results in muddiness and overhang. Good speaker damp-

CABLES affect sound

speaker cables can adversely affect the way cables of unique construction solve the problem.

HARRY MAYNARD

ing plays the same role as a shock absorber on a car, preventing the suspension system from overoscillating on a bumpy road. The damping factor of a good amplifier can be critical to reproducing sharp, clean transients and to the integrity of the bass.

Those who champion the new super cables claim that the damping factor can be rendered ineffective because the speaker impedance can be increased by up to two or three measurable ohms by using zip cord. The goal is to keep the impedance of the speaker cable low.

The damping factor is defined as the ratio between speaker impedance and amplifier output (or internal) impedance. Mr. Cizek's recommendation of No. 12 or No. 10 wire is based on the assumption that "if you decrease the effective damping factor by using a small wire and fuse in the line you tend to produce peaks in the frequency response corresponding to those in the impedance curve as well as poor response and increased ringing." (See Fig. 1.)



FIG. 1—SMALL WIRE DECREASES the effective damping factor and produces peaks in the frequency response.

Now the plot thickens. On several trips to Japan I discovered that leading Japanese companies presently offer for sale super cables of varying configurations; for example, J.V.C.'s *Super Cord* (sold only in Japan, not in the U. S.). You will also find Pioneer cable and several other brands. The merits of various cables is a topic of much discussion among Japanese audiophiles, who are a match in their enthusiasm for high-quality audio with any audiophiles in the world. Much of the advanced research on the effect of speaker cable on hi-fi systems has been done in Japan.

Using a pair of J.V.C. Super Cords, my not-too-golden ears detected a significant improvement in sound quality coming through my speakers as compared with the No. 16 wire I had been using. I have since experienced the same improvement in sound quality with a wide variety of super cables compared with conventional cables.

Research conducted by J.V.C. showed that conventional cable could not pass a 100-kHz squarewave and that there was phase rotation. In addition, the magnetic fields of wires running parallel to each other set up what is known as self-inductance. Parallel wires create phantom channels to each other and round off high frequencies. In tests, several researchers have discovered this results in a loss of audio articulation.

In discussing the merits of super cable and various experiments conducted by J.V.C. and other electronics manufacturers, several leading Japanese executives admitted that there was much that the audio industry has yet to learn about the complex interfaces of different speaker cables and amplifier-speaker combinations. To condemn certain amplifiers because they do not perform properly with certain speakers is similar to condemning certain high-performance cars when given improper fuel—it is an unfair judgment.

Perhaps the most extensive research was performed by Kenwood in Japan in developing their *model L-07M* amplifier and *model L-07C* preamplifier. Discovering the effects of "the neglected cable," as described by Kenwood engineers, oc-



curred quite by chance. Kenwood feels that the super cables "have yielded improvements in sound quality on account of their superior transmission qualities," but this is only part of the picture. A speaker cable has to be considered an extension of the amplifier.

The problem is carefully defined and fully explained in the 47-page owners' manual (No. 7454859-0084-00) for the model L-07M amplifier and model L-07C preamplifier system. For those who wish to delve deeply into the complex interface problem of speaker/wire/amplifier, I strongly recommend getting this manual. We can only summarize its main points here.

Kenwood engineers, using very elaborate testing techniques, found that the speaker-cable impedance plays a significant role in high-frequency response and that the DC resistance contributes significantly to distortion (see Fig. 2). Furthermore, they claim that there is "surprisingly large distortion at the speaker inputs that is caused by speaker cables of even high quality and emphasizes the importance of solving this problem." (See Fig. 3.)

The importance of the neglected cable was discovered by measuring various energy losses for different lengths and kinds of cables. The greatest losses, even when using the best cables, could be held to from 0.5 dB to 0.6 dB below 10 kHz but there were still problems above 10 kHz. As described by Kenwood engineers, "when we made actual listening tests, we sensed there was something missing. It was like a bucket with a hole near the top and performance never went above the level of that hole.

"So our engineers devised a new test using a 30-kHz tone burst and measuring cable performance at the speaker terminals. They found that although the pulse signal was perfect, there was a deformation of the waveform. The difference in wave height and overshoot is actually caused by the counter-electromotive force from the speaker, the result of greater resistance and weaker damping of the long cable. Fundamentally, even very low distortion, which cannot be measured, can be detected by our sense of hearing."



FIG. 2—SPEAKER-CABLE IMPEDANCE has a significant effect on high-frequency response.



FIG. 3—SPEAKER CABLE CAN CAUSE surprisingly large distortion at the speaker inputs.

The most disturbing finding was that the amplifier should ideally be located much closer to the speakers—this is hardly reassuring for the millions who can't or find it inconvenient to do so. The farther the speaker is from the amplifier the slower the slew rate.

Obviously, to do away with the speaker

TABLE 2—Comparison of Audio Cable and Speaker Cord			
	Audio cable	Speaker cord	
Role	Voltage transmission	Power transmission	
Transmitting impedance	10 ohms ~ 1000 ohms	Almost zero	
Load conditions	20 ohms \sim 100,000 ohms. Changes somewhat with frequency and is constant without regard to signal level; slight input capacitance only; no reactance.	Indication 4~14 ohms. Changes considerably with frequency; changes with signal level; reactance component is large and complex; counterelectromotive force produced by speaker.	
Effect on performance	Small	Large	
Effect on tonal quality	Small	Large	
Change with length	Small	Large	
Extraneous induction	Easy	Difficult	
Effect of cord characteristics	Since the load conditions other than component C are large, effect is small.	Components L, C and R have a large effect.	

Note: In the past the quality of the system was not improved while pursuing the characteristics on the amplifier side because the dynamic characteristics with the speaker connected such as these were not considered. As shown in Fig. 7, the deterioration in distortion when the speaker is connected exceeded our imagination.

cable is impractical. Kenwood found however that their specially developed cable (not sold in the U.S.) could be used up to one meter (3.25 feet) from the speaker system, with virtually no effect on tonal quality. On the other hand, the audio cable between power amplifier and control amplifier can be long since it is merely a signal transmission line (see Table 2). I know some people who have suffered from RF problems, which they claimed was only eliminated by using specially constructed (expensive) audio cables like Verion, who might suggest that audio cables are important too. But that's another subject.

The Kenwood solution is obviously directed at the perfectionist audiophile (assuming you agree with their analysis of the problem). There are alternative solutions if you agree that the problem of transmission losses does exist and can be heard. If you don't like the price of the super cables, for less cost you can use the heaviest zip cord you can find, if your amplifier is not bothered by capacitance problems. Don't be afraid of reducing the size of the wire at the speaker terminals (by no more than half) so it will fit into spring-loaded speaker or amplifier terminals. But be careful, since the total resistance of the length of the wire must be considered.

If you believe that there is a problem of self-inductance, you'll usually find that the super cables are sold on a money-back basis. So if you don't hear a difference you can return them. Most of the special cables are sold by specific lengths and several have special tip ends that allow you to make excellent uniform connections.

For example, Disc Washers' *Smog Lifters*, shown in Fig. 4, have special plastic Y-finished tip ends that resist potential shorting, these cables sell for \$1.40-per-foot.

The M & K *Mogami* cable (sold for \$1.50-per-foot) must be debraided, and should not be tinned if splicing is neces-



FIG. 4—DISC WASHERS' Smog Lifter cables have special plastic tips.



FIG. 5—M & K MOGAMI cable must be debraided; no tinning before splicing.

sary. (See Fig. 5.) A firm connection between the wire ends to be joined should be made before soldering. You are also warned to avoid speaker switches, *continued on page 93*

How To Design Digital Circuits

Part 1—With digital circuitry becoming an increasingly important factor in our everyday lives, it's time that we learn how to design logic circuits. Get in on the start of this series as the author discusses digital logic design—beginning with Boolean algebra and Karnaugh maps.

JERRY WOOLSEY

TODAY'S ELECTRONICS HOBBYIST HAS available to him a previously undreamedof assortment of hardware for his projects. Whereas 15 or 20 years ago electronics magazines ran construction articles on simple two- or three-tube circuits, using point-to-point wiring, the projects of today consist of computer CPU boards and computer terminals on complicated double-sided PC boards. Digital circuits are now appearing in almost everything electronic, including "linear" applications such as tuners, TV sets and synthesizers.

To enjoy fully the electronic technology of today, a hobbyist needs to know not only how to bias transistors and match impedances, but also how to analyze and design digital circuits. Although most experimenters can do this using bruteforce methods, there are some fairly simple methods for reducing the number of gates in, and hence the complexity of, a digital circuit.

Digital electronics is the realization of Boolean algebra, and some knowledge of it is required to design a digital circuit. Since the subject of Boolean algebra has been covered in magazine articles as well as in many textbooks, it is assumed the reader has a fair knowledge of it, and is able to write his desired function in both equation and truth-table form. In this article, we will see how to apply the fundamentals of Boolean algebra to construct both parallel and series circuits from a truth table, and then *reduce the gates to the minimum needed*. Throughout this article, the AND function will be implied between two variables if no operator is given between them, i.e., $x \cdot y$ will be written simply as xy.

Combinational switching circuits

A combinational switching circuit is a digital circuit whose output at any time is dependent only on its input at that time, regardless of any previous input or output. Thus, no "memory" circuits are included. (A flip-flop is considered a memory circuit.) The first part of this article is concerned only with these circuits.

A Boolean equation, no matter what form, can always be reduced or expanded to give an equation in either a sum-ofproducts (S-P) or product-of-sums (P-S) form. In the S-P form, the equation is an OR (sum) of several AND (product) groups. In the P-S form, the equation is an AND of several OR groups. As an example, take the following equation:

a = x (y + z) + y 1) This can be expanded by multiplying through the x to get

a = xy + xz + y (S-P) (2)which is in S-P form, or may also be written in P-S form as

$$a = (x + y + z) (x + y + \overline{z}) (\overline{x} + y + z)$$
(P-S) 3)

A primitive implementation of equation 1 is shown in Fig. 1, using the S-P form of the equation. So we pick up the IC data book and notice one peculiar thing: almost all the gates available are NAND, with several AND, but few OR and NOR types. Why should this be so when most functions are written as strictly AND and OR? To understand why, we can apply DeMorgan's theorem to equation 2. This theorem states that if we invert the individual members of one side of an equation, then change the signs between members from AND to OR and vice-versa, then invert the entire side of the equation, the equation is still true. To illustrate, let's apply this theorem to equation 2. First, we invert the individual members to obtain

 $a = \overline{xy} + \overline{xz} + \overline{y}$

Now we change the signs between members and get

 $a = \overline{xy} \cdot \overline{xz} \cdot \overline{y}$ Finally inverting the entire string, we get

$$\mathbf{a} = \overline{\mathbf{x}\mathbf{y}} \cdot \overline{\mathbf{x}\mathbf{z}} \cdot \overline{\mathbf{y}}$$

This says that to obtain the result a, we NAND x and y, NAND x and z, INVERT y, and NAND the three results. Figure 2 shows the logic circuit. Thus, the NAND gates can perform the AND and OR functions. When a group of NAND gates feed another NAND gate, the first gates perform an AND function, and the gate they feed performs the OR function on each AND'ed group. We thus only need to keep a supply of NAND gates to realize any equation in S-P form. In this case, an extra inverter is needed, but inverted variables are often already available from another output, and even if not, this could be performed by a NAND, keeping the three input gates on one package.

It should be noted that the P-S form can be implemented in circuit form by using NOR gates, the first input gates



perform the OR function, and the second set of gates perform the AND function. However, P-S forms can always be expanded to S-P forms, so the remainder of the article will deal only with S-P forms.



FIG. 1—LOGIC CIRCUIT that performs the Boolean algebra expression shown.



FIG. 2—NAND GATES can be used to perform the same function as shown in Fig. 1.



FIG. 3—SIMPLIFIED LOGIC CIRCUIT is equivalent to circuit shown in Fig. 2.

Using the theorems of Boolean algebra, it can be seen that equation 2 can be reduced to:

$$a = xz + y$$

This means that the circuit shown in Fig. 3 is equivalent to the one shown in Fig. 2. Obviously, this is much simpler, and can be done on only one IC.

Reduction of mathematical expressions by Boolean algebra theorems is tedious and often hit-and-miss. To alleviate the problem, we turn to a tool that eliminates much of the work.

The Karnaugh map

The Karnaugh map is simply a rearranged truth table that can readily give valuable information for circuit design and reduction. There are 2ⁿ boxes in the map, where n is the number of inputs to the circuit. Each row and column is

numbered in binary, and the value in each box is the output of the circuit when the coordinates of the box are the input. The numbering of the columns starts at the left at zero, and is arranged such that the number of the next column to the right differs in only one bit position. Thus, 00 is followed by 01, which is followed by 11, which is followed by 10. The rows are numbered similarly. Figure 4 illustrates the numbering and the corresponding decimal coordinates of the boxes for functions with two, three and four inputs. Beyond four inputs, the Karnaugh map becomes too cumbersome, so other methods have been designed for these situations.

As an example, suppose a three-input circuit with inputs x, y and z were to produce a logic-1 output when x = 0 and y = z = 1. Then we would enter a 1 into the box numbered 3 in Fig. 4-b (xyz = $011 = 3_{10}$). If a zero were to be produced when x = y = 1 and z = 0, the box numbered 6 would contain a zero. In certain cases, such as BCD circuits, some input combinations are meaningless (1010 is not a BCD number). In these instances, we enter a "d" in the appropriate box to indicate a "don't-care" condition. This tells us the output may be either 0 or 1 with the given input, since that input would never occur. This may be used to further aid in circuit reduction.

Figure 5-a shows the truth table for equation 1, and Fig. 5-b shows the Karnaugh map derived from it.

Now we come to the interesting property of the map. By definition of the structure of the map, any two adjacent boxes (horizontally or vertically, but not diagonally) differ in coordinates, i.e., in input conditions, by only one bit. For example, the boxes where (x = 0, y = z)= 1) and (x = z = 0, y = 1) are adjacent, and differ in coordinates in only the z-input. This property also holds when "wrapped-around," i.e., the top right-hand box (x = z = 0, y = 1) is adjacent to the top left-hand box (x = y = z = 0), since they differ only in the y-bit of the coordinate. The same holds true for vertical wrap-around. Two of these adjacent boxes are said to form a 1-cube, since there are 2^1 boxes in the cube.

Now refer to Fig. 5-b. If we take two adjacent boxes that contain a 1, for example (x = z = 1, y = 0) and (x = y = z)



FIG. 4—KARNAUGH MAPS are used to simplify logic circuits. A Karnaugh map for a 2-input circuit is shown in *a*, 3-input circuit is shown in *b* and a 4-input circuit is shown in *c*.

= 1), we find that the output of the circuit must be a 1 whenever x = z = 1, independent of the value of y. That is, whenever xz is true, the equation is true. Similarly, box (x = 0, y = z = 1) and box (x = z = 0, y = 1) are adjacent and contain ones, so we see that the output is true whenever x = 0 and y = 1, independent of z. Thus, $\overline{x}y$ being true will cause the equation to be true, or a 1 to be output. Taking all combinations of two adjacent boxes, both of which contain a 1, the following equation is derived

 $a = xz + \overline{x}y + xy + yz + y\overline{z}$ which is equivalent to equation 1, but is obviously not reduced. The reason for this is that several conditions for an output have been duplicated by more than one term of the equation. For example, if x = y = z = 1 is entered, the three terms xy, xz and yz will all be true, causing the output to be true, but it is only necessary to have one term true to cause the output to be true. Thus, we have redundant members in the equation. If we take only three adjacent sets of boxes to cover all the 1-outputs, we can obtain the equation

$a = xz + \bar{x}y + xy$

Now all the boxes containing a 1 have been covered by at least one of the terms of the equation, which means that the equation is a true representation of the truth table. But the equation is still not completely simplified. If we look at the 1-cube (two adjacent boxes) consisting of (x = 0, y = z = 1) and (x = z = 0, y = z = 1)



FIG. 5—TRUTH TABLE for equation a = xy + xz+ y is shown in *a* and resulting Karnaugh map derived from the truth table is shown in *b*.

1), and the 1-cube consisting of (x = y = z = 1) and (x = y = 1, z = 0), we see that the output of the function is always 1 whenever y = 1, regardless of the value of x or z. Thus, we have formed a 2-cube (2^2 boxes) , and have found that y = 1 satisfies the conditions for generating a logic-1 output for each of the four boxes. Note that, in looking at the coordinates of each of these boxes, y is the only coordinate that does not change, and is always 1. We need now to cover only one more box where a logic-1 output is to be generated, box (x = z = 1, y = 0). To do this, we

could simply say we need xyz to be true for the output to be a logic 1, but we have another adjacent 1-labeled box (x = y =z = 1) and if we use this to form a 1cube, that term of the equation reduces to xz, since a 1-output is independent of y if x and z are 1. Thus, we obtain

$$= v + xz$$

as our final equation, and implement it as shown in Fig. 3.

When "d" (don't-care) outputs are specified, these are included as 1-outputs if it enables us to make larger cubes with other 1-outputs, hence simplifying the equation, or as 0-outputs if they are not used in making larger cubes.

Even larger cubes may be found in four-input functions. A 1-cube is two adjacent boxes containing either a 1 or "d"; a 2-cube is two adjacent 1-cubes (i.e., a 2×2 box or 4×1 horizontal or vertical row); and a 3-cube is two adjacent 2-cubes (i.e., a 4×2 horizontal or vertical box). If a map consists only of 1and d-labeled boxes, the function is always true, or a constant 1.

The step-by-step procedure for circuit reduction, then, is as follows:

- Draw the truth table, and fill in the boxes of the Karnaugh map with a 1, 0 or d, using the inputs as coordinates and the outputs as box entries. For example, see Figs. 6-a and 6-b.
- Examine the map for any 3-cubes, i.e., a 4 × 2 box containing no zeroes. Don't forget to check for possible wrap-around. In Fig. 6, a 3-cube is formed by decimal coordinate boxes 0, 1, 2, 3, 4, 5, 6 and 7 (see Fig. 4-c). The coordinates abcd of these boxes are examined, and it is found that b, c and d take on all

а	b	C	d	DUTPUT	DECIMAL
0	0	0	0	1	0
Ō	0	0	1	1	1
Ō	0	1	0	d	2
Ō	0	1	1	1	3
Ō	1	0	0	d	4
Õ	1	0	1	1	5
Ō	1	1	0	1	6
ā	1	1	1	d	7
1	Ó	Ó	0	1	8
1	Ō	0	1	0	9
1	Ō	1	0	1	10
1	0	1	1	0	11
1	1	0	0	0	12
1	1	Ō	1	d	13
1	1	1	0	Ó	14
1	1	1	1	1	15



FIG. 6—TO SIMPLIFY a logic circuit, first draw a truth table that represents the circuit function as shown in *a*. Next, a Karnaugh map is derived from the truth table as shown in *b*.

values (0 and 1) while a is always 0. Thus, when a = 0, the output is always 1 independent of b, c and d, so one term of the final equation is simply \bar{a} . Place a check in each of the boxes of this 3-cube to indicate that they have been covered. When the coordinates of any of these boxes are input, the output will be 1 simply because \bar{a} is 1. The map now appears as in Fig. 7-a.



FIG. 7—KARNAUGH MAPS are reduced by first looking for a 3-cube (a 4×2 box containing no zeroes). When a 3-cube is found, check the individual boxes as shown in a. Next, look for 2-cubes and check these boxes as shown in b.

3) Examine the map for any 2-cubes, that is, any 2×2 or 4×1 box containing no zeroes, and at least one 1-labeled box not yet checked. In our example, decimal boxes 5, 7, 13 and 15 form such a cube. Examining the binary coordinates of these boxes, we find that b and d are always 1, while a and c may take on any value. Thus, our second term of the equation is bd, giving $f = \bar{a} + \bar{a}$



FIG. 8—LOGIC CIRCUIT is derived from reduced Karnaugh map and is shown in a lowerter can be eliminated as shown in b.

bd so far. Check off the boxes covered by the second term. Check for more 2-cubes. In the example, we have another 2-cube that is not so obvious, due to wrap-around. This is the cube containing decimal boxes 0, 2, 8 and 10. From the binary coordinates, we find b and d are always 0, while a and c can take on any value. Thus our next term for an output of 1 is $\overline{bd} = 1$, and our function is now $f = \overline{a} + bd + \overline{bd}$. Check off the boxes covered. The map now looks like Fig. 7-b.

- 4) Examine the map for any 1-cubes, i.e., two adjacent boxes each containing a 1 or a d that also contains one 1-labeled box not checked. Write down the nonvarying coordinates as a term of the function, and check for more 1-cubes. No 1cubes remain in the example.
- 5) If any 1-labeled boxes remain unchecked, write down their coordinates as a term of the function. For example, if the box with coordinates (a = 0, b = c = d = 1) contained a 1 but was not yet checked off, we would write the coordinates as abcd and insert it as a term in the equation. At the completion of this step, all boxes with a 1 in them should be checked off.
- 6) By inspection, make sure no cube is completely covered by other cubes. Each cube, no matter what size, must contain at least one 1-labeled box not contained in any other cube. If it does not, discard its corresponding term from the equation.
- OR all the terms derived above to get the final reduced function. In our example we get: ___

 $f = \bar{a} + bd + \bar{b}d$

8) Feed each term into a NAND gate, and feed the outputs of the NAND gates to another NAND gate. The circuit is complete. See Fig. 8-a.

In searching for cubes to cover an unchecked 1-labeled box, the largest possible cube should be chosen, even if it covers other boxes already checked, so that the number of inputs to each gate is minimized.

Note that Fig. 8-a has \overline{a} as an input that simply gets inverted before going to the output gate. Instead of this, it would be simpler to feed a directly to the output gate, as in Fig. 8-b. Also note that the output of the gate fed by bd cannot be used as input $\overline{b} d$ to the gate below it since $\overline{b} \overline{d} \neq \overline{b} d$.

As was noted earlier, the Karnaugh map method is too difficult beyond four inputs. The designer has to start considering mirror-images, and mistakes are easily made. It also does not reduce the circuit fully if multiple outputs are desired, as in a BCD to seven-segment decoder. Fortunately, there is another fairly simple method to use in these cases.

Quine-McCluskey method

The Quine-McCluskey method works on the same principle as the Karnaugh map, but is performed in tabular form. As an example of this method, we will construct a circuit to produce a 1-output, called f, whenever a 2-bit number A, whose bits are designated as a_1 and a_2 , is larger than a 2-bit number B, whose bits are b_1 and b_2 . The truth table for this function is given in Fig. 9.

a1	a ₂	b ₁	b ₂	f
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	1
0	1	0	1	0
0	1	1	0	0
0	1	1	1	0
1	0	0	0	1
1	0	0	1	1
1	0	1	0	0
1	0	1	1	0
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	0

FIG. 9—QUINE-McCLUSKEY METHOO is used when a circuit with four or more inputs must be designed. First a truth table describing the circuit function is generated as shown.

NO. OF 1-BITS	INPUT (DECIMAL EQUIVALENT OF a1 a2 b1 b2)	
1	4 8	
2	9 12	
3	13 14	

FIG. 10—ADJACENT BOXES and cubes are determined in a table generated from the truth table shown in Fig. 9.

The Karnaugh map obviated adjacent boxes and cubes. In the Q-M method, we write down a table to help show adjacency (see Fig. 10). The decimal value of each set of inputs that will generate either a 1- or don't-care output is listed in ascending order in groups according to the number of 1-bits in the input. For example, $A = a_1a_2 = 10$ and $B = b_1b_2 = 01$ produces a 1-output, and the number of 1-bits in A and B is two, so a 9 $(a_1a_2b_1b_2 = 1001)$ is placed in the 2-bits group. Within each group, the decimal inputs are listed in ascending order. It can now be seen that two inputs producing a 1-output are adjacent, in the same sense as in the Karnaugh map, if three conditions are met:

- 1) The number of 1-bits of each input differs by exactly one.
- The decimal input with the smaller number of 1-bits must be smaller than the input with the larger number of 1-bits.
- 3) The difference of the two decimal inputs must be a power of two.

According to condition 1, inputs listed in the 3-bit group can only be adjacent to inputs in the 2-bit or 4-bit groups; inputs in the 1-bit group can only be adjacent to inputs in the 0-bit or 2-bit groups, etc. This is consistent with the definition of adjacency being a difference in only one bit of the input.

According to condition 2, the decimal input 4 may be adjacent to decimal input 12, since 4, having fewer 1-bits than 12, is smaller than 12. If, for example, the decimal input 3 produced a 1-output, it would be placed in the 2-bits group, but could not be adjacent to 4, since it has more 1-bits but is less than 4. This would cause more than one bit in the input to be different.

Condition 3 is obvious, since only one input bit may differ for adjacency. If the difference of the two numbers is not a power of two, more than one bit differs.

NO. OF 1-BITS	INPUT	1-CUBES
1	41 81	4, 12 (8) 8,9 (1)
2	91	8,12 (4) 9,13 (4)
3	13 U 14 U	12, 13 (1) 12, 14 (2)

FIG. 11—SIMPLIFICATION starts by listing the 1-cubes.

We now use these rules to make a third column, consisting of a list of adjacent boxes, or 1-cubes. We take the first input number in the table, 4, and check it for adjacency with the entries in the next bit group. Box 4 is not adjacent to 9, since the difference, 5, is not a power of two. Box 4 is, however, adjacent to 12, since the difference is 8, and 4 is less than 12. Thus, we enter into the third column the numbers 4 and 12 together, with their difference in parentheses (see Fig. 11). Since the inputs 4 and 12 have been covered by a higher cube, we place a check next to them in the column labeled input.

The input 4 cannot be adjacent to any other bit group, so we look at input 8. Box 8 is adjacent to 9, since the difference is a power of two, so we enter the numbers and difference as a 1-cube and place a check next to the 8 and 9 inputs to indicate they have been covered by a higher cube. Box 8 is also adjacent to box 12, so we repeat the process for them. Since we are through checking the 1-bit group, we place a line under 8,12(4) in the 1-cube column and start checking the 2-bit group. Box 9 is adjacent to box 13 but not to box 14. Box 12 is also adjacent to box 13, as well as 14, and we are finished creating 1-cubes. The 1-cube column now contains a list of all the possible 1-cubes that could be extracted from the Karnaugh map. Since all the inputs in the input column have been checked off, they are all contained in higher cubes.

We now have two groups of 1-cubes, and use these to form 2-cubes. The same conditions hold for forming adjacent cubes, except now the numbers in parentheses must also match. Looking at the first 1-cube entry, 4,12(8), we see that it is not adjacent to any 1-cube in the second group, since none have an 8 in parentheses. Going to 8,9(1), we find an entry, 12,13(1) in the second group that has the same number in parentheses. Since the difference of 8 and 12 (or 9 and 13) is also a power of two, and 8 is less than 9 and in a lower group, we enter this in the next column as a 2-cube, and indicate both the first and second differences in parentheses. The two entries that formed this cube are checked, since they are covered by the higher cube (see Fig. 12).

Another entry, 8,12(4), is adjacent to the entry 9,13(4), so it is entered as a 2-cube and the separate 1-cubes are checked. However, this is identical to the previous 2-cube and is thus stricken. No further adjacency is found, and there are no more groups to check for adjacency, so the checking of the 1-cube column is complete.

We now go to the next column and continue until no adjacencies are found. The same rules are followed in each column, checking each entry against each *continued on page 92*

NO. OF 1-BITS	INPUT	1-CUBES	2-CUBES
1	41 81	4,12(8) 8,9(1)ビ	8,9, 12, 13 (1,4)
2	91	8,12 (4)	
	12 12	9,13 (4)	
3	13 -	12,14 (2)	

FIG. 12-ALL 2-CUBES are listed. Second 2-cube is crossed off since it is covered by first entry.

BUILD THIS

REMOTE TELEPHONEEAR-Listen via Long Distance

This device—the fourth in a series of phone gadgets lets you monitor sounds in your home or office when you call your telephone from a remote location.

JULES H. GILDER

IN THE APRIL AND MAY 1977 AND MAY 1978 issues, we showed how to construct addon telephone accessories that let you turn on and turn off various household appliances by remote control, build a handsoff telephone amplifier and assemble an autodialer and cassette interface that dialed authorities or neighbors in case of a fire or intruders in your home.

If you were interested in these items, you'll flip over the Remote Ear that lets you dial your home phone and then listen for the sound of running water or a radio that was inadvertently left on. Or maybe you just want to check your house and see that everything is quiet and no one has broken into it.

The Remote Ear is an adaptation of the Teleswitch circuit (April 1977). It automatically connects a microphone and amplifier to the telephone so that you can monitor a remote location. As you will quickly see from the schematic, the Remote Ear uses the same type of signal detectors as the Teleswitch. However, instead of having controlled outlets to turn devices on and off, the Remote Ear has a small three-transistor amplifier connected to it.

This amplifier is identical to the one described in the Speakerphone circuit (May 1977). Its signal is very clear and audible. The output of the amplifier, which is located in the area that you want to monitor, is fed to a small speaker that is acoustically coupled to the telephone mouthpiece.

Since it is unlikely that remote listening will be done for long periods of time, the Remote Ear has a built-in timer that allows you to listen for about three minutes. Longer or shorter listening times can be set by adjusting the timing resistor in the emitter circuit of unijunction transistor Q2.

About the circuit

Sound switch 1 and the unijunction timer that is associated with it (Q1) are the same as were used in the Teleswitch. Sound switch 2, however, is slightly modified. Instead of having one relay connected to the 2N3904 collector, there are two relays, RY2 and RY4.

The operation of the Remote Ear involves several steps. When the telephone rings the first time, sound switch 1 triggers and causes RY1 to close. This applies power to sound switch 2 and to the two timing circuits consisting of unijunction transistors Q1 and Q2.

If the phone rings more than once, within 20 seconds sound switch 2 triggers and RY 2 closes. Contact RY2-1 disconnects the power from the first unijunction transistor timing circuit and from sound switch 1. This prevents the Remote Ear from being activated and makes it necessary to wait three minutes before the next attempt.

If, however, the phone rings only once, there is enough time for a charge to build up on C1 and for Q1 to trigger, activating RY3. When RY3 is activated it switches the RY2 coil out of the control circuit of sound switch 2 and replaces it with the RY4 coil.

The first time the telephone rings only once it arms the circuit. The next time the telephone rings it turns on the listening circuitry. This is done by sound switch 2 activating RY4, which, in turn, controls the amplifier and the answering solenoid. Relay RY4 latches closed and is held in that position until a reset pulse from unijunction timer Q2 turns off the 2N3904 controlling RY2 and RY4 and unlatches SCR2.

The telephone is actually answered by a solenoid that pulls up when RY4 closes. This releases the cradle switch and answers the phone. The handset of the telephone is placed on the table alongside the telephone. The loudspeaker connected to the output of the amplifier is held next to the mouthpiece (rubber bands can be used). Thus, the sound picked up by the crystal microphone is amplified and acoustically coupled to the telephone.

After three minutes, or whatever time period you selected has elapsed, a reset pulse is generated and the bases of the control 2N3904's are brought to ground potential, turning these transistors off and unlatching the SCR's. The unit is now ready for its next monitoring period.

Construction

This project is constructed from four modular circuits. The first two circuits are sound switches identical to those built in the Teleswitch (April 1977). After the sound switches are built, they should be mounted in a metal chassis that is large enough to be placed under the telephone. A $5 \times 9 \times 2$ -inch aluminum chassis was used for the prototype. A $\frac{1}{8}$ -inch hole should be drilled where each of the crystal microphones is mounted so that sound will reach them more easily.

After the sound switch modules are mounted, assemble the control module using the circuit shown in the schematic. The circuit can be fabricated by wiring the components on perforated board or you can design a printed circuit.

After the control module is completed, mount the board, using spacers, at any convenient spot under the chassis. The only component left to be mounted is the amplifier. The amplifier used here is identical to the one used for the Speakerphone. Mount this module also on the chassis, using spacers.

Once all four modules are mounted, do the relays. Now connect all the wires

solenoid until the fully extended plunger holds the cradle switch down. Attach the solenoid to the wooden support with two screws.

Now attach two conductors of conventional lamp cord to the two terminals on the solenoid. Then, insulate these terminals with electrical tape. Bring the lamp cord down the support, attaching it to the wood in several places with staples. Be careful that staples do not pierce the insulation of the wire, causing a short circuit. radio. Place the speaker next to the telephone mouthpiece.

Ask a friend to call and let the telephone ring several times. On the first ring, RY1 should close. On the second ring, RY2 should close, opening RY1. After three minutes, a reset pulse from Q2 should reset the lower SCR opening, RY2.

After another three minutes have passed and RY2 has reset, have your friend call again, tell him to ring only



RADIO-ELECTRONICS

from the modules and the relays that go together directly to the positive side of the supply. If an external battery is going to be used to supply power, connect these leads to a screw terminal that is insulated from the chassis. If the power supply described for the Teleswitch is used, connect the leads to the positive terminal of the supply. Do the same for all ground leads. Connect all remaining wires to their proper locations.

Cut a piece of 1×2 -inch wood to a length of 10 inches. This will be used as a vertical support for the solenoid that will hold the phone in the unanswered position until the proper command signal is given. To position the arm, place the telephone on top of the phone cradle where the handset is normally placed. Mark the spot, because that is where you want to mount the arm, and mount the arm using at least two screws. Next, place the solenoid on the inside of the arm and take the handset off the telephone. Position the Bring the wire into the bottom portion of the chassis through a grommet-lined hole and attach one of the two strands to one set of normally open contacts on RY4. Attach another piece of single-conductor lamp cord to the other contact of the set. This wire, along with the unused wire from the solenoid, will be connected to the AC line.

Mount two miniature jacks to the chassis for the microphone and the speaker. The speaker can be acoustically coupled to the telephone by simply holding it next to the telephone mouthpiece with a few rubber bands. The microphone should be located in the spot you want to monitor.

Installation and operation

Installing the Remote Ear simply requires placing the telephone on the chassis, removing the handset and allowing the solenoid to hold the cradle switch down. Now, place the microphone in the room you want to monitor and turn on the once, and then call back 20 seconds later. On his second call, the phone should be answered automatically after the first ring. This is done by RY4, which becomes activated by sound switch 2 when the phone rings the second time. Relay RY4 closes the circuit to the solenoid and causes it to lift up, releasing the cradle switch of the telephone.

Relay RY4 was activated because after 20 seconds had elapsed, Q1 produced a pulse that activated RY3 and switched the power line from RY2 to RY4.

When the call is answered your friend should hear the radio playing. If he does not, check to make sure that the speaker is properly placed next to the mouthpiece of the telephone. Three minutes after the first ring, Q2 generates a reset pulse and releases RY4. This causes the solenoid to drop and hang the phone up. Simultaneously, it opens the circuit to the amplifier. The Remote Ear is now ready to use again. **R-E**

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BUILD THIS



-TANK---

arcade quality TV game

Part 2. The object is to use your cannon to destroy your opponent first, but watch out for the land mines and anti-tank barriers. The circuit provides a composite video signal to your TV set and produces realistic sound

L. STEVEN CHEAIRS

LAST MONTH, WE PROVIDED THE COMplete schematic of the tank game and discussed in detail the circuit operation.

This month, the article concludes with the foil pattern, component placement diagram and construction details.

Building the game

Before beginning construction, you will need an etched and drilled PC board. You can use the foil pattern in Fig. 4 or purchase the board from the source listed in the parts list. Begin by installing the five jumpers on the drilled board (Fig. 5) and then solder all resistors, capacitors and IC sockets to the board. Next, solder in the diodes, transistors and the regulator IC.

Before proceeding, connect a 12-VAC transformer to the AC input; connect a DC voltmeter across the power-supply pins of the game IC; pin 1 is ground and pin 16 is $+V_{cc}$. Now, apply line power to the transformer—7 volts should be indicated on the meter. If 7 volts is not shown but some value close to it, then a new value for R1 or R2 can be chosen by trial-and-error. If the voltage is drastically different, then a circuit problem exists; use normal troubleshooting techniques to locate and repair the problem.

Next, install the CMOS IC's. Again apply power; using an oscilloscope, adjust

the amplitude of the clock at pin 19 of the LSI IC. Remove the power source and discharge the capacitor. Install the AY-3-8700-1 or AY-3-8710-1 IC; the circuit board is now complete. Wire the external components to the PC board (see Fig. 6) and install the unit into a case. If an RF modulator is used, it can be mounted in the case with the PC board or inside the TV set. One last note, the best results were obtained from the prototype with the TV set's contrast control turned up and the brightness control turned to medium-low.

Special considerations

There are several considerations that should be noted for the AY-3-8700-1. First, as the tanks rotate, the shape of their images will vary. Next, the border width will vary from integrated circuit to integrated circuit. Also, the mines could disappear upon interaction with the tanks. When a score is recorded the black tank rotates and the white does not. The 4-second delay makes this effect immaterial. If the tanks exit the screen area, sometimes they will disappear and never return.

For the AY-3-8710-1, the following considerations are important. Upon resetting of the game a random explosion may occur (it may be visible below the bottom border). Also, during the game the gun of either tank may misfire; that is, shells may explode in a spot where the player is not aiming or the shell may not fire from the tank. These do not affect the normal events of the game.

It is possible for a tank to get trapped in a border. When this happens, the game is ended and the other tank is declared the winner. If the barrier interaction switch is



FIG. 4—FOIL PATTERN for the battle game PC board.

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TANK CONTROL: A AND B – FORWARD MOTION B AND C – LEFT TURN D AND C – REVERSE MOTION A AND D – RIGHT TURN

FIG. 6—HOW SWITCHES ARE CONNECTED to the game IC on the board. Lower section of the component side of board is shown for reference.

PARTS LIST

All resistors are 1/4 watt. 5%. R1-180 ohms R2-510 ohms R3-470 ohms R4-1800 ohms R5—1000 ohms R6—270 ohms R7, R16, R19, R20, R23, R28-10,000 ohms R8—1600 ohms R9—2400 ohms R10-12 megohms R11-220 ohms R12-5000-ohm, PC-type potentiometer R13-2.2 megohms R14-2200 ohms R15, R21, R26-20 megohms R17, R22-3.9 megohms R18-22,000 ohms R24, R25-10 megohms R27-30,000 ohms R29-15 ohms C1, C2-100µF, 50-volt electrolytic C3-2.7-µF tantalum C4-C6, C13, C14-0.1-µF disc C7, C8-30-pF disc C9, C10-0.01-µF disc C11, C12-0.22 µF C15, C16, C20-5 µF C17-0.47 µF C18-200 pF disc C19-100-pF disc C21-220-µF, 15-volt electrolytic D1-D10-1N4148 or similar D11-D14-1N4005 or similar Q1-Q3-2N3904 or similar IC1-AY-3-8700-1 or AY-3-8710-1 LSI game IC2, IC3-4001, CMOS quad NOR gates IC4-78M05, 5-volt regulator J1-miniature open-circuit jack S1-S4-SPDT center-off, momentary-contact toggle switches S5-S7-SPST normally open pushbutton switch S8—SPST switch S9-SPST toggle switch T1-12VAC, 1A secondary transformer XTAL—4.090900-MHz crystal SPKR—8 ohms MISC.—12 X 7 X 3-inch aluminum chassis, line cord, hook-up wire, four 11/2-inch stand-off busings. The following parts are available from Questar Engineering Company, McDonald Street, Mesa, AZ 85202: PC board, \$12.95; AY-3-8700-1 or AY-3-8710-1 (please specify), \$29.00; crystal, \$5.50; set of all switches, \$12.25. Kit of all parts, \$63.95.

selected, the tank cannot drive through barriers. If a tank gets trapped in a barrier, momentarily flip the barrier interaction switch to allow the tank to free itself. Also, sometimes the tanks may get locked together; the only way to separate them is to reset the game. In playing both the AY-3-8700-1 and AY-8710-1 games I very seldom have problems of the type outlined above. These special considerations are presented so that you know what to do in case a problem is encountered. **R-E**



A look at some clever reader solutions to reader problems. EARL "DOC" SAVAGE, K4SDS, HOBBY EDITOR

THE TIME HAS COME TO TRY TO CATCH UP on some of the more interesting mail that readers have been sending. I answer letters directly as much as possible, especially if an SASE is enclosed, but there just isn't time to answer all of them. I'd like now just to share some reader ideas with you.

A reader in Puerto Rico makes the point in his letter that he can find published circuits to do everything he has needed so far. These circuits can all be found in manufacturers' data sheets, application notes and various magazines and books. He then says that he would feel pretty silly sending in a circuit obtained from another source, and I agree with him.

Well, it seems to me that although this reader must have a tremendous collection of well-filed and indexed publications there is plenty yet to be discovered. There are many new ways to do old things and new applications for existing components (IC's, etc.).

For example, the lowly 555 has been around for several years now and 1 would like to have just 1/1000th of a penny for every word that has been written about it. I doubt (although 1'm not absolutely sure) that there remains any undiscovered way to make the 555 function as a timer. Quite probably, they all have been discovered and discussed.

Yet, in spite of all that has been written, I am *sure* that many other applications for the 555 remain. Perhaps some are being discovered right now. There are ways to use the 555 that have not yet been dreamed of.

I agree that we should keep up with what is going on by buying all the magazines and books we can find or afford! After all, there is no point in "re-inventing the wheel" every time we begin a construction project. *Someone* is always finding new and better ways of doing things and we should be aware of them.

Recently, I received a letter describing a fantastic reader-built project. There was not a single new device in the project, but the way some of them were used— WOW! I immediately sent the letter to the editor of **Radio-Electronics**, and if things go as planned, that article may

appear in Radio-Electronics.

So, keep on reading, learning, experimenting and building. There is plenty to be discovered. Even if we don't find anything new, though, trying is more fun than anything else I can think of!

Rocket-launching circuit

A while back (July 1978) we discussed several problems on which readers had asked for help. Well, at least one of those problems hit a nerve. Apparently there are a number of model rocketry buffs or rocketeers among us.

A reader asked for a rocket-launching circuit, and it has been interesting to observe the different approaches that have been developed. Once again this proves that there are many approaches to solving a problem.

One of the best launching circuits was sent in by Tim Coffman (Route 2, Box 448, Liberty, MO 64068). I have given his full address because he wrote that he would be glad to correspond with other readers who are interested in model rocketry.

Unfortunately, there isn't enough space to go into Tim's system in detail. In brief, then, he uses eight IC's and two 7-segment digits to count down to 0, fire the igniter and, finally, count up until rocket touch-down to give the flight time. Three of those IC's are used to detect the 0 count, reverse the counters and start the firing. The timer is the ever-useful 555 operating at 1 Hz.

This rocket circuit is complete with appropriate safety switches and LED status indicators. The count can be placed on HOLD at any time, and the firing is done through an SCR rather than a relay. Altogether, it is a very straightforward circuit.

I must admit I am not a rocketeer. I have had a healthy respect for rockets ever since I saw someone's hand badly damaged by carelessly handled fuel. If you are just starting this hobby, be very careful to observe strict safety rules and precautions.

More rockets

A further note about model rockets: I received a letter and catalog from CNA,

Box 1252, Lewiston, ME 04240. This company specializes in rocketry electronics with small and large launching systems and other devices. Company president Alfred Celetti recommends that readers interested in model rockets and electronics write the National Association of Rocketry, Box 275, New Providence, NJ 07974.

Super-simple oscillator

A Canadian reader, Guy Isabel, sent in two useful circuits. One is an interesting timer with no moving parts—not even a pushbutton switch. The other is a neat oscillator.

Guy's super-simple oscillator uses four of the six gates in a 7404 hexadecimal inverter plus *one* additional part (see Fig. 1). The output frequency is determined by the value of the capacitor (which should *not* be an electrolytic). As capacitor C is changed from 300 μ F to 300 pF, the frequency changes from 1 Hz to 1 MHz.



This oscillator can be used to drive LED's, counters, transistor switches, relays and so on. It could be used as a signal source to test audio amplifiers and certain receivers. In many circuits, it could replace a 555. Used with a switch and several capacitors, it could provide selectable frequencies. And why not use a variable capacitor from an old broadcast radio to cover a range of frequencies?

In addition, Guy Isabel has offered to help those who need circuits for special applications. You can write him at 1725 East, Henri-Bourassa, Apt. 25, Montreal, P. Q. H2C 1J5, Canada.

Low-voltage detector

Hobby Corner received an interesting letter and circuit from Dave Corner of Chicago. Figure 2 is a diagram of Dave's low-voltage alarm circuit.

The values of R1, R2 and D1 are selected for the voltage applied. Using a 12-volt battery, R1 = 10K, R2 = 5.6K and D1 is a 5-volt Zener diode, or a string
of forward-biased silicon rectifiers equaling about 5 volts. Transistor Q1 is a general-purpose UJT (Unijunction Transistor), and Q2 is any small-signal or switching NPN transistor.



When this detector is connected across the battery terminals, it draws little current and does not interfere with other devices powered by the battery. If the voltage drops below the trip voltage you have selected with the R1 setting, the speaker beeps a warning. The frequency of the beeps is determined by the amount of undervoltage.

If other voltages are being monitored, select R1 so that it draws only 1 mA or 2 mA (remember E = IR). Zener diode D1 is about one-half of the desired trip voltage, and R2 is selected to bias it at about 1 mA.

Thanks, Dave, for sharing this useful circuit with us.

Solder cream

Multicore Solders has a new line of solder *cream* that comes in tubes like toothpaste. This product doesn't even *look* like solder, but it is and does a beautiful job.

The type of solder useful for your projects is labeled "Ersin... for electrical soldering." All you do is squeeze a dab onto a joint and apply heat. For small joints that can't conduct the heat away so fast, the heat source can be a candle, match or cigarette lighter. On larger joints, you should use an iron or torch.

The rosin flux is incorporated in the cream along with the invisible solder. As the cream is heated, it changes to solder and then solidifies to make a good electrical and mechanical joint.

This solder cream is especially handy for use in places that are hard to reach with wire solder. For example, you can coat the end of a wire with cream, insert it into a pin like a phono plug or a PL-259, and then just heat the outside of the pin. It really simplifies the process.

I don't think solder cream will ever replace regular solderwire, but it can perform some jobs more conveniently.

Multicore also manufactures two other

types of solder cream, one an all-purpose variety that can be used for many kinds of metals, including stainless steel and silver. It can be used in the place of the acid-flux solderwire that cannot be applied on electrical joints.

The third solder cream is a lead-free cream composed of tin and silver. This cream can be used on stainless steel, silver and other metals; it is also nontoxic.

Solder cream is handy to have around. If you can't obtain it from a local dealer, write to Multicore Solders, Westbury, NY 11590. R-E

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CIRCLE 35 ON FREE INFORMATION CARD



8085 A look at the software required to control an eight channel analog signal monitor. C. TITUS, P. RONY, D. LARSEN, and J. TITUS*

IN A PREVIOUS COLUMN, WE DESCRIBED the needs of the 8085 control system and the use of the I/O ports and programmable timer to form an eight channel analog monitor. The necessary initialization of the I/O ports was also discussed. Now we will discuss the software that is necessary for proper operation of the system. It is assumed that the control process is very simple, perhaps just sensing only upper and lower limits of the analog signals.

The programmable timer within the 8155 generates an interrupt every 10 ms.

RST75,	PUSHPSW MVIA 020	/SAVE REG A & FLAGS /CLEAR INTERRUPT FLAG
	SIM MVIA 013 SIM	/RE-ENABLE INTERRUPTS
	LDA SEC	/GET # OF LOOPS REMAINING
	DCRA STA SEC 0	/DECREMENT IT BY ONE /SAVE IT
	jnz notyet 0	/IF NOT ZERO, DO ANOTHER LOOP /THROUGH THE INTERRUPT
	MVIA 144 STA SEC 0	/YES, IT'S ZERO, SO RESET THE SECOND /COUNTER TO 100 (10 MSEC LOOPS) /STORE IT
	LDA BCDTIM 0	/get the time
	STC CMC ADI 143	/SET THE CARRY FLAG /COMPLEMENT IT TO CLEAR IT /ADD 239 = DECIMAL 99
	DAA STA BCDTIM 0	/DECIMAL ADJUST IT FOR A SUBTRACTION OF ONE /OF ONE, AND THEN STORE IT
	INZ NOTYET 0	/IF THE RESULT IS NOT ZERO, LOOP THROUGH /AGAIN
	/ADC SERVICE	ROUTINE
	IN 201	/INPUT THE BCD DATA FROM THE SWITCHES
	STA BCDTIM 0	/UPDATE THE BCD TIME
ADC,	NOP NOP NOP	/THE ADC SERVICE STEPS GO HERE
NOTYET,	NOP POPPSW RET	/ETC. /RESTORE REG A & FLAGS /RETURN TO MAIN PROGRAM
		FIG 1

*This article is reprinted courtesy American Laboratories. Dr. Rony, Department of Chemical Engineering, and Mr. Larsen, Department of Chemistry, are with the Virginia Polytechnic Institute & State University. Both Mr. J. Titus and Dr. C. Titus are with Tychon, Inc. Since the basic unit of time in this system is a 1-second interval, 100 1-second interrupts must be counted before any action can occur. When the 1-second point has been reached, the program must check to see if it must perform some other action, or continue timing for another 1-second interval. A read/write memory location, SEC, is set aside that will be used to count the 100, 10-ms interrupts. Another location will be required to contain the number of seconds that must be delayed between sampling. Since the thumbwheel-switch data will be entered in binary-coded decimal (BCD) format, you have to decide whether it will be processed in binary or BCD format. We have chosen to process it in BCD format to eliminate a BCD-to-binary code conversion process.

A typical timer control subroutine is shown in Fig. 1. Note that there are steps in this subroutine that clear the RST 7.5 flag and then re-evaluate the RST 7.5 interrupt mask. The information stored in location SEC and BCDTIM has also been used.



In our example, it can take up to about 200 μ s to proceed through the steps shown, leaving 9800 μ s for the remaining program steps. If a slow A/D converter is used to acquire the eight samples, much of the 9800- μ s period would be gone, leaving little time in the interrupt-service subroutine before the next 10-ms interrupt occurs. If this happens, the interrupt-service subroutine is interrupted and the computer becomes interrupt-bound. Most A/D converters can perform conversions quickly so this will not be considered further.

We suggest that you acquire the analog

data in the subroutine and then proceed to a data or control-processing section of the program that is outside of the interrupt-service subroutine. The control or processing of the program will be interrupted briefly every 10 ms, but it will have up to 1 second to process the old data. It has been assumed that the processing takes less than 1 second. The software example in Fig. 2 shows how the control processing software has been removed from the interrupt-service subroutine. There are other equally valid solutions to this problem. Remember, however, that when you do not intend to use a return address on the stack, you must increment the stack pointer twice to avoid loading the stack with useless information.

This application does not use the serialin (SID) or serial-out (SOD) connections on the 8085. These connections could be used as a single line-control input and a single line-control output, respectively. They can also be used to serialize ASCII characters for output or to parallel the serial bit stream to reconstruct parallel data bytes. Thus, a software UART could be constructed very easily. **R-E**

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ANNUAL INDEX JANUARY—DECEMBER 1978

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Any requests postmarked on or before February 28 are free. After that date there is a 25¢ fee. Questions and comments about anything other than the Index that are included with your request cannot be handled. Send them separately to our Editorial Offices. **NOM CARD FOR 1802** continued from page 48

 \overline{EF}_2 , \overline{EF}_3 , \overline{EF}_4). The flip-flop is reset when the status/output register is read.

The only portion of the interface we have not discussed is the clock—a simple 400-kHz RC oscillator operating between +5 volts and -4 volts, and the hold/reset circuits. The reset circuit is a TTL-to-MOS voltage converter since the reset, hold and oscillator pins are non-TTL inputs. The reset must be held low for at least eight oscillator periods as part of its power-on sequence (8 oscillator periods

equals 20 μ s). Thus, when the system is powered-up as part of the initialization routine, the reset is set for a minimum of 20 μ s, during which time the input-ready flip-flop is clocked three times. The first two times it is set, write an 80_{HEX} to the input port to clear the flip-flop; this is necessary because the hold is set each time the output ready occurs. When the third signal occurs, the NOM interface is ready for its first instruction. If it is not needed at this time, store a 40_{HEX} in the input port. The hold circuit is formed by a TTL-to-MOS voltage converter driven by a 2-input OR gate.

continued next month



service clinic

Trouble with the color and how to localize the problem. JACK DARR, SERVICE EDITOR

THERE ARE REALLY ONLY THREE COLOR problems and they should be easy to analyze. These are: no color at all, the wrong color and the loss of color sync. These problems originate in three separate circuits—the bandpass amplifier, the 3.58-MHz reference oscillator and the color AFPC (Automatic Frequency and Phase Control) circuit. If you check a set and a perfect black-and-white picture appears, this clears everything else in the set!

A complete loss of color can be due to several causes. First, a loss of the color signal itself can be due to a dead 3.58-MHz oscillator, or to faults in the colorkiller circuit. To find out which circuit is at fault, feed a color-bar signal into the set and scope the bandpass-amplifier output signal. This signal is normally present at the color control, but it will always appear at the input to the demodulators. If you see a normal "comb" pattern at the right amplitude, the bandpass-amplifier stage is working all right, as well as the killer circuit.

Check for the oscillator signal with a scope. This signal must be at the right amplitude. If the oscillator isn't working, there will be no color.

If you do not see the comb pattern at the bandpass-amplifier output, check all DC voltages, tubes/transistors and pay special attention to the bias on the second bandpass amplifer. This is where the killer bias is used. The DC voltage shown on the service schematic are for no-signal conditions, meaning no color.

The grid will be at a high negative voltage (for a tube circuit; cutoff polarity for a transistor). This voltage should drop to a much lower value when a color signal is fed in. Remember, this is just a plain IF amplifier circuit! If necessary, you can override the killer bias on the grid to check whether the color signal goes through. If so, check out the killer-bias circuitry. This may mean bad diodes, tubes, transistors, or just control misadjustment.

With reference to color alignment, the key word is *don't!* If you must perform alignment, do so only as a last resort and only after finding definite clues that it is needed. Use a sweep curve on your scope to check for misalignment. This will give you a definite clue. However, you should remember that, unless it is very bad, misalignment does *not* cause a complete loss of color, nor will it ever cause a sudden dropout of color. In most cases, alignment is only needed because someone has tampered with the set. Check the scope pattern. If the comb is flat on top and the bars are clean and sharp, it is probably not necessary to align. (Hint with older scopes, for a clearer pattern use a crystal-detector probe to trace the signals through the bandpass-amplifier stages.) With wideband scopes, use a lowcapacitance probe.

If the color oscillator doesn't function at all, this causes a complete loss of color. Some Service Clinic readers write: "My color bars are bluish and greenish, but I can't get any reds!" In quite a few sets, this is because of a dead oscillator. The weak tints observed are because enough of the signal burst leaks through to make the demodulators try to work. The color burst phase is between blue and green. (By the way, this usually means that the demodulators are working!)

The 3.58-MHz signal is critical. A phase shift of only one-sixteenth of *one-cycle* causes blue to change to green, etc.! A working oscillator/AFPC circuit holds the frequency so steadily that this shift seldom occurs. For a network program, a frequency counter on this oscillator should read 3,579,545 Hz. (This frequency is generated by networks using atomic clocks, rubidium and caesium.)

A defective crystal changes the frequency. So far, I have not yet discovered a set that has a dead oscillator due to a bad crystal. If the crystal is just a little off-frequency, the oscillator tends to pull against the AFPC circuit control signal. This makes the color-sync extremely sensitive and prone to fall out with interruptions.

There are two types of oscillator circuits. One is a Pierce oscillator circuit. The crystal controls the frequency, which, in turn, is controlled by the signal burst through the AFPC circuit. Don't be afraid of the AFPC circuit—it basically resembles any horizontal AFC circuit! The oscillator frequency is controlled by a DC voltage developed across a diode pair. To adjust this, kill the AFPC circuit so that the oscillator works independently. Now, adjust the reactance coil, etc., until the colors lock in momentarily. Taking the shunt off the AFPC circuit should lock the color in firmly. If the color falls out of sync, the AFPC circuit isn't operating.

There is one obscure *external* cause for color-sync problems. This is the horizontal oscillator automatic frequency control. In many older sets, this control held the picture in sync over a considerable range. However, at the ends of the control range, the *color* changed hue and fell out. This was due to the change in *phase* of the pulse from the flyback used to gateout the burst signal. Changing the phase of this pulse far enough results in no signal burst or a very weak burst. Make sure that the horizontal-hold control is centered in its range.

The other type of color oscillator circuit is actually a burst amplifier. This circuit picks off the burst signal and feeds it to a sharp filter, usually a crystal. This causes the crystal to ring; this ring lasts long enough for the next burst signal to arrive. It therefore develops a continuous output that is actually the network signal burst itself. If the gating pulse is out of phase, the burst will not be strong enough to make the circuit work.

In circuits with a reactance-tube AFPC control, this control is actually a voltage-controlled oscillator (VCO). The AFPC diodes develop a small DC-control voltage by comparing the signal burst to the oscillator frequency, similar to horizontal AFC. In many sets, there should be zero voltage on the grid of the reactance tube with the oscillator locked on. (In some, this voltage is offset. Make sure to check the schematic voltage values.) Typical voltage values might range from +5 to -5.

If you run into one with tricky color sync, check to make sure that this voltage "crosses zero" to about the same voltages. This is actually just like any FM discriminator, which it is. Its output signal is an S-curve. If the crystal is just slightly off, you'll probably discover that the grid voltage is either a positive or a negative voltage, but it will not cross zero. It will come down to zero perhaps, and then return in the same polarity. Try a new crystal to see if you can obtain a zero crossing and lock in at 0 volt.

All TV set service data includes com-

RADIO-ELECTRONICS

plete instructions for making color-setup tests, alignment tests and so on. Follow these instructions to the letter. Watch out for deviations; some instructions are different. The tests are usually quite simple: in most, the only instrument you need is a DC voltmeter. Watch the screen to observe what's happening. Color troubles can be easy to troubleshoot if they are approached methodically and logically, one step at a time.

service questions

VERTICAL PROBLEM

David Day, of Flori-Day Electronics, Apalachicola, FL, sends these tips along. (He had two Zenith model 14B38Z chassis, both with automatic-gain control (AGC) problems. I suggested some possible cures, including replacing the VDR in the AGC circuit.)

"Here's some feedback for you! I changed the VDR's on both chassis. This didn't help, so I started on the first chassis. I checked all DC voltages around the AGC tube, and found that the voltage on pin 5 of the 8BA11 was only -4 instead of -11. You also suggested tracing the circuit and following it back to the continued on page 82



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SERVICE QUESTIONS

continued from page 81

21HB5 horizontal-output grid. When I did this, the bias on the 21HB5 was only -20 volts or so! (I found that I could clamp the pin 5 voltage to -11 and the picture and sound were good.) Why was the voltage so low? Was there leakage in the capacitor? When I checked, capacitor C65 was leaky. I replaced it and bingo! Button this one up.

The second chassis went white in a few minutes and then lost the horizontal drive completely. I followed same circuit; same problem, only heat-sensitive. I replaced C65; no luck. I replaced C66; no luck. Although the 21HB5 tube had been checked and showed good, I didn't change it because I didn't have a new one at the time. So I replaced the 21HB5. Hallelujah! Now the grid bias holds steady at -39 volts, which it should. It wouldn't do this before. So much for AGC problems (which aren't)!

(Congratulations on the persistence and perspicacity, Dave! We have run into quite a few oddball problems like this in horizontal-output tubes in the past few years. In general, it's a good idea to try a new tube and see if this clears the problem. The cause seems to be excessive grid emission (although this is only my opinion). In this case, the analysis became complex until we traced down the source of the negative voltage used to bias the AGC tube. This is a mildly unusual circuit, although quite workable if everything is in good shape.)

FUSE BLOWS VERY FAST

The main power-supply fuse blows very quickly in this Truetone model GEC-4316B. I've checked everything without result! I replaced a few parts, including the horizontal output transistor; no luck. It looks simple but isn't!-D. M., Brunswick. GA.

Here's my favorite remedy: Hook the set up to a variable voltage transformer (variac). Connect an AC ammeter across the empty fuse holder. (If you don't own an AC ammeter, hook up a 0.5-amp pilot light across the fuse holder.) Turn the line voltage up very slowly until you notice just a small current flow, or until the bulb lights up a little.

Check the DC power-supply voltages at a point where there should be voltage but it's missing. This solid-state power supply starts to conduct at a very low voltage, which allows you to get data without excessive smoke!

HEATER CIRCUIT OUT

Have I got problems in this Philco model 4C490! The high voltage is 28 kV, there's good sound, the dial lights work, but there's no raster because the picture tube heater is dead! The heater is not continued on page 84

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SERVICE QUESTIONS continued from page 82

open. Also, D101 was bad. I replaced it, but the new one lasts about 5 or 6 seconds and then shorts. Help!—D. W., Feeding Hills, MA.

The picture tube heater circuit in this set uses an instant-on transformer that places 5.0 volts on the tube heater. When you turn the switch on, the *primary* of this transformer is shorted out. In operation, the picture tube heater is fed from a winding on the flyback circuit. (This produces a 15,750-Hz pulse, at about 26 volts P-P, which is equal in *heating effect* to the normal 6 VAC 60-Hz pulse.)

If you used the substitute for D101 recommended in the parts list—RCA SK-3016—this is probably causing your trouble! The RCA SK-3016 is a "sinewave diode," and the one you need *must* be a fast-recovery type. Try using SK-3175, SK-3515, etc. This applies to *all* sets using DC power supplies derived from flyback pulses. **R-E**

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entry in the group below it, seeing if the differences in parentheses are the same (though they may be in any order), and seeing if the difference of the first numbers is a power of two. Indicate the end of the check of each group by a horizontal line in the next column, to show a new group there.

We now have a list of cubes that cover the 1-output boxes, similar to the cover created in the Karnaugh map. However, using the Karnaugh map, one can visually inspect to obtain the largest cubes to cover each 1-labeled box. This is not the case using the Q-M method, so a "cover map" must be drawn. On this map, we draw vertically the decimal equivalents of the cubes that are left unchecked in Fig. 12, and draw horizontally the decimal equivalent of each input that is to produce a 1 (but not a don't-care) output (see Fig. 13). A check is now placed in each box of

	4	8	9	12	13	14
4,12	~			~		
12,14				~		~
8, 9, 12, 13		~	~	~	~	

FIG. 13—COVER MAP is obtained by listing the decimal equivalents of the unchecked cubes from Fig. 12.

the table under the column that is covered by a given row, i.e., the row labeled 4,12 has a check in columns 4 and 12.

Now we look at each column to see which rows are needed. We see that the column labeled 4 has only one check, in row 4,12. Thus row 4,12 (i.e., the term corresponding to 1-cube 4,12, as in the Karnaugh map) is essential to obtain an output of 1 when 4 is input, so we place a check by row 4,12. We then place a check above the columns numbered 4 and 12 to indicate that these have been covered. Column 8 has only one check, indicating row 8,9,12,13 is essential. A check is placed by this row and above columns 8, 9, 12 and 13. The only uncovered column is 14, which can only be covered by row 12,14. That row and column 14 are checked. The map now appears as shown in Fig. 14.



FIG. 14—ESSENTIAL ROWS are determined and then confirmed by placing a check next to them.

All columns with only one check in them should be handled first in this manner. For the remaining unchecked columns with more than one table entry check, choose the largest possible cubes to cover the most unchecked columns. In this manner, the function will be completely reduced. In any case, all columns must have a check above them when done, so that all output situations are covered. Any rows that do not have a check beside them are eliminated.

The remaining rows (those with a check to the left of them) then correspond to the inputs to each of the gates. To determine the inputs, the decimal numbers in the labels of each row are written in binary, and the input bits that do not change between the numbers are the inputs to the gate. For example, Fig. 15 illustrates that for row 8,9,12,13, bit a

	a ₁	\mathbf{a}_2	bı	b ₂
8	1	0	0	0
9	1	0	0	1
12	1	1	0	0
13	1	1	0	1

FIG. 15—TRUTH TABLE of final function is shown.

must always be on, and bit b_1 must always be off, while a_2 and b_2 may be either 0 or 1, to generate a 1-output for this term of the expression. Thus, the output is 1 if $a_1\overline{b_1} = 1$. Similarly, row 4,12 indicates a 1-output if $a_2\overline{b_1}\overline{b_2} = 1$, and row 12,14 indicates a 1 output if $a_1a_2\overline{b_2} = 1$. Our final function is thus:

 $f = a_1\overline{b}_1 + a_2\overline{b}_1\overline{b}_2 + a_1a_2\overline{b}_2$ This function is implemented as shown in Fig. 16.



FIG. 16—LOGIC CIRCUIT implementation of truth table shown in Fig. 15.

Next month we will discuss multipleoutput functions and sequential circuits along with cover maps and truth and state tables used in digital circuit design. **R-E**



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patches, etc., because they deteriorate the quality of the audio signal. M & K claims its cable has an inductance that is 12 times lower than No. 12 zip cord and comparable resistance.

The Fulton cable uses silver-plated wire; the tips are finished off in spade lugs; 30-foot sections of No. 16 wire cost \$1.34-per-foot; and No. 12 cable costs \$2.60-per-foot.



FIG. 6—POLK AUDIO SPEAKER CABLE consists of 144 strands of low-resistance wire braided into two sets of conductors at right angles to each other.



FIG. 7—AUDIO SOURCE's *High Definition* cable, with 10-ohm impedance, has 10 pairs of braided wire, connected in parallel and with special tips.

The Polk cable comes in a round version (see Fig. 6) at \$1.34-per-foot; in four different lengths, up to 50 feet; and is claimed to have a characteristic 9-ohm impedance. The Polk cable consists of 144 strands of separately insulated, low-resistance wire braided into two sets of conductors that constantly lie at right angles to each other to avoid inductance.

All the braided cables are arranged this way because it is claimed that this eliminates interference between the adjoining magnetic fields, thus minimizing selfinductance. In addition, the two polarities are brought as close as possible to each other to insure minimal and correct characteristic impedance.

The Mogami cable's stacked arrangement has an inner core of nonconductive material for spacing and extra strength; a final conductor layer has 60 strands of wire about equal to No. 11; a layer of insulation made of tough synthetic material; and another conductor layer wound *continued on page 102*

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DIP SOCKET low-profile unassembled Molex sockets available in 14-pin and 16-pin versions. IC DIP sockets come in display card package containing four terminal carrier strips and four IC nests. When assembled, components form two complete DIP sockets whose terminals are made of 70/30 spring-tempered tin-plated brass. Assembly instructions are given on the back of the



card. Socket assemblies are available from Waldom distributors. Prices: parts for two 14-pin sockets, 95¢; two 16-pin sockets, \$1.—Waldom Electronics, Inc., 4301 W. 69th St., Chicago, IL 60629.

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MAGNETIC SCREWDRIVER, model 70035, has magnet built into the shank to hold interchangeable bits plus the screw. Confordome handle has a removable dome cap that keeps three extra bits



stored inside handle while fourth bit is being used. Comes with ³/₁₆-in. and ⁹/₃₂-in. slotted, No. 1 and No. 2 Phillips bits.—Vaco Products, 1510 Skokie Blvd., Northbrook, IL 60062.

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WIRELESS REMOTE CONTROL, model GP-500, attaches to the antenna terminals of any blackand-white or color TV set. The unit's 10-channel capacity is tunable to either VHF or UHF, and each channel can be preset. A built-in RF preamplifier has a power gain of 30 dB (typical). Other specifications include: Maximum noise—90 dB (VHF) and 12 dB (UHF); IF rejection—40-dB minimum (VHF) and 60-dB minimum (UHF); minimum image rejection—50 dB (VHF) and 40 dB (UHF); input impedance—75 ohms; tuner frequency bands—55.25 to 83.25 MHz, 175.25 to 211.25



MHz (VHF); 471.25 to 888.25 MHz (UHF). The model GP-500 components are encased in twotone high-impact plastic; the receiver measures 6 \times 10¹/₄ \times 3¹/₄ inches; the transmitter measures 2¹/₂ \times 5³/₄ \times 1 inch. Suggested list price: \$99.95.—GP Electronics, Ltd., Subsidiary of Gold Peak Industries, Ltd., Box 261, Middletown, NJ 07748.

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SOLDER STRIP PACKAGE, Emergency Solder, is designed for on-the-spot repairs and requires only a match to melt the solder. Multiple cores of noncorrosive, nonconductive flux are contained in the strips. Emergency Solder can be used on any solderable metal (not suitable for aluminum). Package includes 36 inches of solder strip, and complete directions.—Multicore Solders, Westbury, NY 11590.

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DUAL-TRACE 30-MHz OSCILLOSCOPE, model *LBO-520*, offers built-in 120-ns delay line. Among the unit's other features are a 5 mV-per-division vertical sensitivity; display modes include Channel 1, Channel 2, alternate, subtract, add and X-Y modes; continuously variable sweep speeds from 0.2 µs-per-centimeter to 0.5 second-per-centi



meter; \times 10 magnification. In addition, the *model LBO-520* provides trace rotations; + and - polarity; an uncalibrated warning indicator lamp; and lever-type input switches. Priced at under \$1000, the instrument comes with contoured

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handle that doubles as locking bale; probes and accessories are included.—Leader Instruments Corp., 151 Dupont St., Plainview, NY 11803. CIRCLE 128 ON FREE INFORMATION CARD

LOGIC MONITOR, model LM-2, is a self-powered unit that clips right onto IC under test; a series of 16 LED's at top of the clip follow IC pin pattern. A rotary switch selects the proper logic threshold



for monitoring RTL/DTL, TTL/HTL and CMOS circuits. A separate cable for CMOS circuits uses the circuit voltage to determine the logic level and operates up to a 30-kHz input frequency at 50% of duty cycle. The *model LM-2*, with self-contained 117 VAC 50- to 60-Hz power supply, is priced at \$129.95. A 220 VAC 50- to 60-Hz model is also available.—Continental Specialties Corp., 70 Fulton Terrace, New Haven, CT 06509. CIRCLE 129 ON FREE INFORMATION CARD

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(1 \times 3³4-inch handle); and ¹4 \times 4-inch blade with No. 2 tip (1¹/₄ \times 4¹/₄-inch handle). Screwdrivers are made of chrome vanadlum steel, with crossground tips and easy-grip handles. Suggested resale price, \$7.63.—Hunter Tools, 9674 Telstar Ave., El Monte, CA 91731.

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computer products

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Z80 COMPUTER BOARD, *90F/MPS*, is a Z80based, single-board device with resident floppydisc controller that supports up to four 5¹/₄-inch or 8-inch single-density floppy-disc drives. The basic board contains 4K bytes dynamic RAM (expandable to 64K bytes); six 2708/2716 EPROM sockets; 1K byte resident PROM monitor



with static RAM scratch pad; PROM programmer; counter/timer; hardware UART with RS232C/TTY serial I/O; plus a programmable 8-bit parallel I/O port with two expansion sockets for additional PIO IC's. Prices: single unit, \$995; OEM discounts available.—**Quay Corp.**, Box 386, Freehold, NJ 07728.

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COMPUTER PRINTOUT PICTURES are now available in a 23-picture set printed on 58 14% \times 11-inch sheets. The set includes four pictures of Snoopy, five Christmas scenes, Abe Lincoln and others. Price: \$7.75.—Data Analysis Systems, Dept. F, Box 162, Franktown, CO 80116.

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capability (no edge connectors are provided). The board accepts from 14- to 40-lead DIP packages, and additional space is provided for up to 26 or more resistors or transistors. The board also has provision for three-way keyed mounting, 2 card ejectors, filter capacitors for the voltage regulators and transient suppressor capacitors at approximately 6-inch spacing. Prices: kit, \$29.95; SS-50 bus extender board, \$49.95; edge connectors available separately.—**AUM-Ideas**, Box 2582, Richardson, TX 75080.

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COMPUTER SYSTEM, *PeCos I*, is a complete personal computing system that does not require hookup to any RF adapters, TV sets, audio cassettes, etc. It provides 24K ROM and 16K RAM; a 60-key keyboard with upper and lower case; a 9-inch CRT displaying 16 lines of 40 characters each with automatic scrolling and speed control; built-in dual cassette decks, each with 80K byte storage capability; 6502 microprocessor; power supply; and RS 232 port for connecting a printer. Software is implemented in PeCos, an English-like language derived from Rand JOSS; and includes full 9-digit floating point



arithmetic computation. The unit measures $18\frac{1}{2} \times 19\frac{1}{2} \times 8\frac{1}{2}$ inches. Suggested retail price: \$1695.—APF Electronics, Inc., 444 Madison Ave., New York, NY 10022.

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graphic display is 160H by 100V with expansion to 320H by 200V using super-dense graphic option.

The keyboard contains the following modes: edit function, scrolling, monitor, 25 cursor/edit functions, graphic subroutines and graphic drawing. The display address and display mode (text, graphic or "split screen") are software-programmable. Prices: assembled (with 4K ROM control and super-dense graphic option) \$499.95; kit (without ROM software) \$299.95.—MiniTerm Associates, Inc., Dundee Park, Andover, MA 01810.

CIRCLE 125 ON FREE INFORMATION CARD

PROGRAMMABLE TERMINAL SYSTEM, EXOR 68, is multifunctional—consists of basic display unit with 12-inch CRT, six keyboard options and a group of micromodule subassembly boards. The CRT display unit consists of video monitor that can display 128 ASCII characters in 24 lines (up to 1920 characters); switches select word length, baud rate, communications mode and modem



control. Other features include inverted cursor, scrolling and page/edit/protect display modes; remote or keyboard data entry capability; and optional motherboards. Basic display unit is available separately for end applications without keyboards. Keyboard options include Standard TTY/control key format plus 5 other variations. End-use micromodule options range from partial computers (memory, interface, etc.) to complete single-board assemblies; all are compatible with *EXOR 68* system. Basic display unit with extended communications and display features plus keyboard and cable assembly sells for \$2600 (approx.) in single quantities.— Motorola Microsystems, 3102 N. 56th St., Phoenix, AZ 85018.

CIRCLE 126 ON FREE INFORMATION CARD

COMPUTER TERMINAL, model OE1000, comes as a kit or assembled, and interfaces to any computer having a 300-baud serial data output port. It offers the full duplex mode with either a 20-mA current loop or an RS 232 voltage swing. Displays 96 ASCII characters and 32 special characters in a 16-line by 64-character format,



with either upper and lower case or TTY modes. Other features are full cursor control, automatic scroll, erase to end of line, erase to end of screen and clear screen. Prices: kit, \$275; assembled, \$350.—Otto Electronics, Box 3066, Princeton, NJ 08540. **R-E**

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More information on new products is available from manufacturers of items identified by a Free Information number. Free Information Card is inside back cover.

DIRECT-DRIVE TURNTABLE, model PS-TI, is a semiautomatic unit that features a linear-torque, brushless, slotless DC servomotor with ring-shaped magnet and fixed coil. The speed-monitoring system consists of a magnetic coating on outer rim of the platter, which is tracked by a magnetic head to detect speed variations. Other



features include a J-shaped tonearm (statically balanced to provide 7-9-Hz resonance), counterweight with stylus pressure gauge, antiskating device, safety clutch, reject button and illuminated strobe. Suggested list price: \$130.—Sony Corp. of America, 9 W. 57th St., New York, NY 10019.

CIRCLE 113 ON FREE INFORMATION CARD

AM/FM STEREO RECEIVER, model CR-3020, incorporates preamp containing peak-reading meters, coil-head amplifier, signal meter, LED display and two headphone jacks (with separate

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volume control). General specifications: continuous RMS power, 200 watts-per-channel (at 4 ohms, 0.05% THD); 20-Hz-20 kHz THD (phono 1 & 2 to record output), 0.003% at 5 volt output; input sensitivity (phono 1), 2 mV; frequency response (aux, tape 1 & 2 to speaker output) 5 Hz-100 kHz, -3 ± 2 dB; S/N ratio (phono 1 & 2) 96 dB (10-mV input).

FM section: usable sensitivity at 300 ohms, 11.2 dBf; 50-dB quieting (mono) 15.3 dBf, (stereo) 37.2 dBf; 1HF distortion (stereo), 0.1% (local), 0.6% (DX). AM section: 1000-kHz selectivity, 45 dB (\pm 10 kHz), 35 dB (\pm 9 kHz).

The model CR-3020 comes in an ebony cabinet, measures $24\% \times 7\% \times 19\%$ inches and weighs 81 lb, 8 oz. Price: \$1400.—Yamaha International Corp., 6000 Orangethorpe Ave., Buena Park, CA 90622.

CIRCLE 112 ON FREE INFORMATION CARD

and RSQ8-F are made of acrylic plastic and feature cloth-roll suspension, 1½-Ib magnet construction and an aluminum voice coil. The speakers are easily mounted and come with complete mounting instructions. The model DK5-F (shown) is a 5-inch two-way speaker with a 360° swivel tube for directional sound and a decorative grille; its specifications include a 48-Hz-20-kHz frequency response, 8-ohm impedance, 65-Hz resonant frequency and 25-watt power handling capability. The model RS69-F is a 6 \times 9-inch two-

CEILING SPEAKERS, models DK5-F, RS69-F



way recessed round speaker with a 35-Hz-19,500-Hz frequency response, 8-ohm impedance, 50-Hz resistant frequency and 40-watt power handling capability. The *model RSQ8-F* is an 8-inch two-way recessed square speaker with a white exterior and chrome grille. Its specifications are: a 28-Hz-21-kHz frequency response, 8-ohm impedance, 50-Hz resonant frequency and 45-watt power handling capability. List prices: *model DK5-F*, \$54.95; *model RSG9-F*, \$64.95; *model RSQ8-F*, \$69.95.--**Rohn Electronics, Ltd.**, 5 Pearsall Ave., Glen Cove, NY 11542.

CIRCLE 111 ON FREE INFORMATION CARD

AM/FM TAPE CAR STEREO, 8-track model 873 and cassette model 633, both deliver 20 wattsper-channel RMS; the tuner sections feature FET frontends and PLL circuitry in the multiplex



decoder, plus local/distance switch and FM muting. The model 873 player had indicator lights and a dial-in-door cartridge slot. The model 633 cassette player (shown) provides automatic reverse, pushbutton eject, locking rewind/fast forward switches, plus tape direction indicators. Both units contain bass/treble, balance/fader, and volume/loudness controls. Suggested retail prices: *model 873*, \$209.95; the *model 633*, \$244.95.—J.I.L., Dept. P, 737 W. Artesia Blvd., Compton, CA 90220.

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HI-FI CABLES/CONNECTORS, full line of stereo cables and connectors for all systems. Product line includes 2-pin DIN speaker, line plugs and sockets; 3- and 5-pin DIN plugs and sockets; leads with either RCA or DIN plugs and sockets in



different lengths and configurations; leads with combined RCA/DIN plugs or sockets; a headphone extension cable; and an FM dipole antenna.—AudioSource, 1185 Chess Drive, Foster City, CA 94404.

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MICROCOMPUTER HANDBOOK, by Charles J. Sippl. Petrocelli/Charter, Div. Mason/Charter Publishers, Inc., 641 Lexington Ave., New York, NY 10022. 454 pp. 61/2 × 91/2 in. Hardcover \$19.95.

This book is designed to serve the needs of designers, students, hobbyists and all those who require a thorough understanding of low-cost microminiaturized computer systems. Systems engineers and developers who must integrate these computers into existing systems will also find this book a useful guide.

Early chapters deal with and contrast standard computers and minicomputers. Other chapters describe the various types of microcomputers and their capabilities, software, programs and many applications. All terminology is carefully defined, and photos and diagrams clarify the text.

THE DESIGN OF OPERATIONAL AMPLIFIER CIRCUITS, WITH EXPERIMENTS, by Howard M. Berlin. E&L Instruments, Inc., 61 First St., Derby, CT 06418. 266 pp. 6 × 9 in. Softcover \$8.50.

The beginning experimenter and hobbyist will find this latest in the Bugbook series useful in a home study program dealing with the design and operation of different types of op-amp circuits. Each chapter contains its own set of experiments on a wide spectrum of circuits, from linear amplifiers to single-supply units. Chapter 1 introduces the reader to the basics; other chapters deal with the fundamental circuits using bipolar and Norton-type op-amps; and Chapter 10 discusses the instrumentation amplifier used in augmenting low-level signals.

WORKSHOP IN SOLID STATE, Second Edition, by Harold E. Ennes. Howard W. Sams & Co., Inc., 4300 W. 62nd St., Indianapolis, IN 46268. 384 pp. 51/2 × 81/2 in. Softcover \$7.95.

The student and technician with previous training in electronics will find this book helpful in making the transition from vacuum-tube circuitry to solid-state devices. The material was originally developed to aid in training broadcast technicians, but the basic principles apply to other branches of electronics as well.

The text covers the fundamentals of solid-state devices, circuits for both linear and pulse applications, logic-circuit principles and testing and servicing information. Test questions follow most chapters, with answers in an appendix in the back of the book.

HOME-BREW HF/VHF ANTENNA HANDBOOK, by William Hood. TAB Books, Blue Ridge Summit, PA 17214. 210 pp. 5 × 81/4 in. Softcover, \$5.95; hardcover, \$8.95.

This how-to guide on building antennas contains complete down-to-earth instructions on designing, constructing, installing, selecting, buying and customizing any HF/VHF antenna, from a basic dipole to stacked-beam arrays. The first two chapters deal with essential antenna principles, formulas, systems and configurations. Also included are in-depth examinations of wave propagation, radiation characteristics, transmission lines, baluns, etc., plus instructions and schematics for constructing dummy antennas, SWR meters, impedance bridges, L- and Pi-networks, and many more. Contains four appendixes and an index. R-E





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Continued from page 59

Keep it cool

While giant strides have been made in the miniaturization of electronic components, nobody has yet miniaturized the *joule* (i.e., watt-second). Heat causes many electrical component failures, and it is likely that series-pass transistors and regulators will fail if allowed to get too hot. Additionally, most such devices have a temperature coefficient that defines an output voltage change in percent-per-degree centigrade (%/°C).

At current levels up to about 5 amperes, ordinary heatsinking and convection cooling will usually suffice, but at higher current levels it becomes increasingly necessary to use a blower or fan in addition to the heat sink.

In one configuration of my Z-80 system, I used a heat-sinked HEP S7000 in the circuit of Fig. 3 to provide 5 volts at 10 amperes. The heat sink was one of the large finned International Rectifier models sold in hobbyist outlets. This transistor got so hot after 20 minutes of operation at near full load that a first degree burn would reward anyone foolish enough to touch it! But a 50-cfm "whisper" fan reduced the temperature to the "barely hot" level in only a few minutes!

In short, it is good practice to always use forced-air cooling on power regulators and series-pass transistors operated at more than a few amperes of constant load current. Keeping the case temperature low will not only improve longevity, but will also prevent output voltage drift due to thermal changes. The rules for keeping a regulator and rectifier cool are:

- 1. Mount the IC regulator, series-pass element and rectifiers on heat sinks, not just on the chassis.
- 2. Use silicone/heat-transfer grease between all devices and their respective heat sinks.
- Blow 40 to 105 cubic-feet-per-minute (cfm) of air across the heat-sink fins. Such fans or blowers can usually be obtained at low-cost surplus or somewhat higher cost at retail. The investment is well worth it—remember that bit about the silicon-to-carbon converter!



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DECEMBER 1978

SPEAKER CABLES continued from page 93

in the opposite direction. Both conductor layers are wound at a 45-degree angle, constantly crisscrossing each other.

The High Definition cable manufactured by Audio-Source (see Fig. 7) is the least expensive, costing 80 cents-per-foot, with a characteristic 10-ohm impedance; having 10 pairs of braided wire; and connected in parallel to reduce DC resistance and with specially finished tips.

It is impossible to predict accurately how certain speaker/wire/amplifier com-

binations will perform. Some amplifiers cannot handle capacitances greater than 6000 pF and shut down. Some amplifiers cannot handle too little capacitance, and shut down or produce a metallic or ringing sound. Dr. Maier, president of the Disc Washer Group, distributors of Smog Lifters, says: "Very fine amplifiers with highly balanced output stages, with complementary power supplies will sustain up to one microfarad just like certain amps will play 'into' and sound good on electrostatic speakers. Amps, which do not like electrostatic speakers, and which sound terrible on them, tend to be candidates for super cable problems."



Some high-powered amplifiers have had problems with too small a capacitance—1500 and 15,000 pF. Engineers say this can be avoided by inserting an inductor in the output circuit.

The Smog Lifter cables are claimed to have a lower capacitance (480 pF) than other super cables. Dr. Maier believes "the capacitance of the Smog Lifters allows most amps to be used, but we are including a disclaimer of consequential damage with our words of caution for the protection of everybody."

The problem is somewhat similar to using penicillin or any other drug-not all people respond the same way. There is a dynamic equation between speaker wire and amplifier. So here's a word of caution for any potential user: Not all people will hear the difference between a No. 16 wire and a No. 22 wire; nor may they hear the difference between the super cables and the best of conventional cord. You may discover that upgrading your cable doesn't actually make a difference. In my tests, which were not highly controlled blind listening tests, I discovered there was a difference-a cleaner percussive sounds and a tighter bass.

If you're looking for better sound from your audio system, try the super cables (or even just heavier wire) on a moneyback basis; you may be pleasantly surprised with the results. **R-E**





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DATABOOKS & MANUALS NSC TIL Data \$ 35 St. AMI MOSIL'SI Data 355 NSC Linear APProte 355 Obsome Intro to Micro Vol 8.50 NSC Linear APProte 355 Obsome Intro to Micro Vol 8.50 NSC Linear APProte 355 Obsome Intro to Micro Vol 8.50 NSC Linear APProte 355 Obsome Intro to Micro Vol 8.50 NSC Audo Data 356 Obsome Intro to Micro Vol 8.50 NSC Audo Data 356 Obsome Intro to Micro Vol 8.50 NSC Audo Data 356 Obsome Intro to Micro Vol 8.50 NSC Audo Data 356 Obsome Intro to Micro Vol 8.50 NSC Audo Data 356 Obsome 6000 Programming 8.50 NSC Micro Pota NSC Micro Data 356 Thow Semis Data 750 NSC Micro Manual 750 Til Memory Data 355 Intel Databoot 355 Til Tomosto S Dootes 850 Intel MCSt0 Manual 750 Til Memory Data 355 Intel MCSt0 Manual 750 Til Ottometoniconics 355 Intel MCSt0 Manual 455 Til Linear Data 355 Intel MCSt0 Manual 455 Til Interacobating	Mot Vol 4 Macil Data 955 Mot Vol 5 CAQS Data 295 Mot Vol 5 CAQS Data 295 Mot Vol 5 Cando Lana 295 Mot Vol 9 Schothky, TL 295 Mot Mol 9 Schothky, TL 295 Mot Mot 9 Schothky, TL 295 Mot Mol Pactifice Tota 295 Mot Recrifter Data 295 Mass Schothky, STI Vol 1011 3995 Bass Schothky, STI Vol 1011 3955 Bass Schothky, STI Vol 1011	ATS 2376 ATS 2376 ATS 2376 ATS 2376 ATS 2376 ATS 2376 ATS 2376 ATS 2376 THE RODUCT STATUS ATS 2376 ATS 2376 ATS 2376 THE RODUCT STATUS ATS 2376 ATS 2376 ATS 2376 THE RODUCT STATUS ATS 2376 ATS 2376 ATS 2376 ATS 2376 ATS 2376 ATS 2376 ATS 23766 ATS 2376 A	9 U2917 Free to Vortage. 2 25 9 U2917 Free to Vortage. 2 26 9 U2917 Free to Vortage. 2 26 9 M42017 Erection to 10/2 30 3 49 9 M10025 or MH0025 Mos Driver. 2 50 95 1978 CATALOGUE 3 50 95 SEND 25 ¢ POSTAGE 3 50 95 DISCOUNT COMPUTER Discount Computer 95 Discount Computer 0 00
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Part no 2

Baud rate is continuously adjustable from 0 to 30.000 • Plugs into any peripheral connector • Low current drain. RS-322 input and output • On board switch selectable 5 to 8 data bits, 1 or 2 stop bits, and parity or no parity either odd or even • Jumper selectable address • SOFTWARE • Input and Output routine

from monitor or BASIC to teletype or other serial printer • Program for using an Appie II for a video or an intelligent terminal. Also can output in correspondence code to interface with some selectrics. Board only -- \$15.00 with parts -- \$42.00; assembled and tested -- \$62.00

MODEM *

Part no. 109

• Type 103 • Full or half duplex • Works up to 300 baud • Originate or Answer • No coils, only low cost components • TTL input and output-serial Connect 8 ohm speaker



and crystal mic. directly to board ● Uses XR FSK demodulator ● Requires +5 volts ● Board \$7.60; with parts \$27.50

DC POWER SUPPLY *

Part no. 6085

 Board supplies a regulated +5 volts at 3 amps. +12, -12, and -5 volts at 1 amp.
 Power required is8 volts AC at 3 amps., and 24 volts AC C.T. at 1.5 amps.
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TAPE INTERFACE *

Part no. 111

 Play and record Kansas City Standard tapes • Converts a low cost tape recorder to a digital recorder • Works up to 1200 baud • Digital in and out are TTL-serial • Output of board connects to mic. in of recorder • Earphone of

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Part no. 106 • Stand alone TVT

32 char/line, 16 lines, modifications for 64 char/line included • Parallel ASCII (TTL) input Video output • 1K on board memory • Output for computer controlled curser • Auto scroll •

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add \$3.00



Non-destructive curser • Curser inputs: up, down, left, right, home, EOL, EOS • Scroll up, down • Requires +5 volts at 1.5 amps, and -12 volts at 30 mA • Alt 7400, TTL chips • Char. gen. 2513 • Upper case only • Board only \$39.00; with parts \$145.00

TIDMA *

TATE OF THE PARTY OF THE PARTY

 Tape Interface Direct Memory Access
 Record and play programs without bootstrap loader (no

prom) has FSK encoder/decoder for direct con-

nections to low cost recorder at 1200 baud rate

and direct connections for inputs and outputs to a

digital recorder at any baud rate.
 S-100 bus com-

TTL compatible • All characters contain a start bit, 5 to

8 data bits, 1 or 2 stop bits, and either odd or even parity

All connections go to a 44 pin gold plated edge connections

tor . Board only \$12.00: with parts \$35.00 with connector

patible • Board only \$35.00; with parts \$110.00

UART & BAUD RATE

GENERATOR*

Converts serial to parallel

and parallel to serial . Low

cost on hoard haud rate

generator · Baud rates: 110,

150, 300, 600, 1200, and

2400 • Low power drain +5

volts and -12 volts required

8K STATIC RAM

Part no. 300

 BK Altair bus memory
 Uses 2102.Static memory chips
 Memory protect
 Gold contacts
 Wait states
 On board regulator
 S-100 bus compatible
 Vector input option
 TRI state buffered
 Board only
 \$22.50;
 with parts
 \$160.00

RF MODULATOR *

Part no. 107

 Converts video to AM modulated RF, Channels 2 or 3. So powerful almost no tuning is required. On board regulated power supply makes this extremely stable. Rated very highly in Dector Debted. Journal



RS 232/TTY * INTERFACE

Part no. 600



RS 232/TTL*

Part no. 232

• Converts TTL to RS-232, and converts RS-232 to TTL • Two separate circuits • Requires -12 and +12 volts • All consections go to a 10

 All connections go to a 10 pin gold plated edge connector
 Board only \$4.50; with parts \$7.00 with connector add \$2.00

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Mention part number and description. For parts kits add "A" to part number. In USA, shipping paid for orders accompanied by check, money order, or Master Charge, BankAmericard, or VISA number, expiration date and signature. Shipping charges added to C O.D. orders. California residents add 6.5% for tax. Outside USA add 10% for air mail postage, no C.O.D.'s. Checks and money orders must be payable in US dollars. Parts kits include sockets for all JCs, components, and circuit board. Documentation is included with all products. All items are in stock, and will be shipped the day order is received via first class mail. Prices are in US dollars. No open accounts. To eliminate tariff in Canada boxes are marked "Computer Parts." Dealer inquiries invited. 24 Hour Order Line. (408) 226-4064 ***** Circuits designed by John Bell



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RADIO-ELECTRONICS
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 | 7400 | TTL COM
 | Sligilita
 | 1000
 | | ELEPHONE/KE
 | YBOARD CHIPS | C14 05
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| - AMMAN
 | SN7470N .2
SN7472N .2 | 9 T MMY
 | Electronic Home NIGILLE
 | ACL
 | AY-5-9100
AY-5-9200
AY-5-9500 | Repertory Dialler
CMOS Clock Gene
 | erator | 14.95
4.95
 |
| SN7401N 18
SN7402N 18
 | SN7473N 3
SN7474N 3
SN7475N 4 | 5 SN7416UN .89
5 SN74161N .89
9 SN74162N 1.95
 | Security limer
A microprocessor-based pre-programmed light control that lifs into a
 | in the the
 | AY-5-2376
HD0165
740022 | Keyboard Encoder
Keyboard Encoder
 | r (88 keys)
r (16 keys) | 14.95
7.95
 |
| SN7403N 18
SN7404N 18
SN7404N 18
 | SN7476N .3
SN7479N 5.0 | 5 SN74163N 89
0 SN74164N 89
 | Intrucers and burghts by turning kghts on and off in a "real-life" pattern
while you're away
 | 1 1 5 -
 | | ICM I
 | CHIPS | 9.90
 |
| SN7405N .29
SN7407N .29
 | SN7482N .9
SN7483N .5 | 9 SN74165N .69
9 SN74166N 1.25
9 SN74167N 1.95
 | Unlike other electromechanical immers Vigilita can simulate the lighting
patterns of five different rooms as selected by the user. Vigilite can also
 | to Un
 | ICM7045
ICM7205 | CMOS Precision 1
CMOS LED Stopw
 | Timer
vatch/Timer | 24 95
19.95
 |
| SN7408N 20
SN7409N 20
 | SN7485N .7
SN7486N .3 | 9 SN74170N 1.59
5 SN74172N 6.00
 | control over-head lights, which other timers cannot Three Vigitie units,
simulating krichen, bathroom, and bedroom lighting can give a home
ultimate protection, because the user chooses a lighting patient that
 | Land W L
 | ICM7208
ICM7209 | Seven Decade Co
Clock Generator
 | unter | 19.95
 |
| SN7410N 18
SN7411N .25
SN7412N .25
 | SN7490N .4
SN7490N .4 | 5 SN74173N 1.25
5 SN74174N .89
9 SN74175N 79
 | depicts his real life pattern. He then sets the Vigikie clock and room
pattern accordingly (See bar chart below.) The house actually looks
occupied, although nd one is home
 | Tot .
 | N
MOMEEZI | MOS READ D
 | NLY MEMORIES | 12 50
 |
| SN7413N 40
SN7414N 70
 | SN7492N 4
SN7493N 4 | 3 SN74176N .79
3 SN74177N .79
 | Easy to install, the Vigikte unit contains an accurate digital LED clock,
plus pre-programmed independent lighting patterns for bedroom, bath-
 | Part Number
 | MCM6574
MCM6575 | 128 X 9 X 7 Math
128 X 9 X 7 Math
128 X 9 X 7 Alpha
 | anumeric Control | 13.50
13.50
 |
| SN7410N 25
SN7417N 25
SN7420N 20
 | SN7494N .65
SN7495N .65
SN7496N 65 | 5 SN74179N 1.95
5 SN74180N .79
5 SN74181N 1.95
 | room, Mtchen, living room, and outside porch tights. All-solid-stale com-
ponents assure the user long product life and reliability.
 | VGL-1
 | | Character Generat
MISCELI
 | |
 |
| SN7421N 29
SN7422N 39
 | SN7497N 3 00
SN74100N .89 | 0 SN74182N .79
9 SN74184N 1.95
 | Technical Specifications
 | \$39.95 ea.
 | TL074CN
TL494CN | Quad Low Noise I
Switching Regulat
 | bi-fet Op Amp
for | 2.49
 |
| SN7425N 29
SN7426N 29
 | SN74109N 55
SN74109N 55
SN74115N 1 95 | 9 SN74186N 9.95
SN74188N 3.95
 | 60Hz, 21/2 Amps Vision, applances, or other motor
driven equipment
in software equipment
in software equipment
in software equipment
4) Om visitia un reassert for
 | and the second se
 | TL496CP
11C90
95H90 | Single Switching
Divide 10/11 Pres
 | Regulator
scaler
10/11 Prescaler | 1.75
19.95
 |
| SN7427N .25
SN7429N 39
SN7420N 39
 | SN74121N 35
SN74122N 35 | 5 SN74190N 1.25
SN74191N 1.25
SN74191N 1.25
 |
 |
 | 4N33
MK50240 | Photo-Darlington
Top Octave Freq.
 | Opto-Isolator
Generator | 3.95
17.50
 |
| SN7432N .25
SN7432N .25
SN7437N .25
 | SN74123N 49
SN74125N 49
SN74126N 49 | SN74192N 79
SN74193N 79
SN74194N 89
 |
 |
 | DS0026CH
TIL308
MM5320 | 5Mhz 2-phase MC
.27" red num. dis
TV Camera Svoc
 | DS clock driver
splay w/integ. logic chip
Generator | 3.75
 |
| SN7438N 25
SN7439N .25
 | SN74132N .75
SN74136N 75 | SN74195N 69
SN74196N .89
 | XC556H red 5/\$1
 | S1 FIELD EFFECT
 | MM5330
LD110/111 | 4½ Digit DPM Lo
3½ Digit A/D Con
 | gic Block
overter Set | 5.95
25.00/set
 |
| SN7441N .89
SN7442N .49
 | SN74142N 2.95
SN74143N 2.95 | SN74197N .09
SN74198N 1.49
SN74199N 1.49
 | XC556C clear 4/\$1 XC209Y yellow 4,
.200" dia.
 |
 | | SO-LIT 1
 | SN 76477
SOUND GENERAT | ÖB
 |
| SN7443N 75
SN7444N 75
 | SN74144N 2 95
SN74145N 79 | SN74S200 4.95
SN74251N 1.79
 | XC22R red 5/\$1 XC526R red 5/
XC22G green 4/\$1 XC526G green 4
 |
 | (Same as MCT | 2 or 4N25)
 | Generates Complex S
Low Power - Program | nmable
 |
| SN7445N 75
SN7446N .69
SN7447N 59
 | SN74147N 1 95
SN74148N 1 29
SN74150N 89 | SN74279N 79
SN74283N 2.25
SN74284N 3.95
 | MV108 red 4/51 XC526Y yellow 4.
MV108 red 4/51 XC526C clear 4.
 | 151 4 DIGIT5" CHARACTERS
THREE ENUNCIATORS
 | 2/99 | ¢
 | 3.95 eac | ;h
 |
| SN7448N 79
SN7450N 20
 | SN74151N 59
SN74152N 59 | SN74285N 3.95
SN74365N .69
 | .085" dla190" dla
 | \$1 INCLUDES CONNECTOR
 | AY-3-8500-1 and 2 | 2.01 MHZ Crystal
 | (Chip & Crystal 7 | 5/00
 |
| SN7451N .20
SN7453N .20
SN7454N .20
 | SN74153N 59
SN74154N 99
SN74155N 79 | SN74367N .69
SN74368N .69
 | INFRA-RED LED XC111Y yellow 4
1/4"x1/4"x1/16" flat XC111C clear 4
 | /\$1 T10017-Transmissive \$7.95 /\$1 T1001A-Reflective 8.25
 | XR205 \$3.40 | nay, o games anu
 | XR22420 | CP 1.50
 |
| SN7459A 25
SN7450N 20
 | SN74156N 79
SN74157N 65 | SN74390N 1.95
SN74393N 1.95
 | DISPLAY
 | LEDS
 | XR210 4.40
XR215 4.40
XR320 1.55 | EX/
 | AR XH2264
XH2556
XH2556
XH2567 | 4.25
 |
| 20% Discount 100 pcs
CD4000 23
 | C/MOS | CD4070 55
 | TYPE POLARITY HT PRICE T
MAN 1 Common Anode-red 270 2.95 M
 | YPE POLARITY HT PRICE MAN 6730 Common Anode-red ± 1 560 .99
 | XR-L555 1.50
XR555 .39 | XR1800
 | 3.20 XR3403
XR4136 | 1.25
 |
| CD4001 .23
CD4002 23
CD4006 1.19
 | CD4028 .8
CD4029 14 | C04071 23
39 CD4072 49
19 CD4076 1 30
 | MAN 2 5 x 7 Dot Matrix-red .300 4.95 h MAN 3 Common Cathode-red .125 .25 h MAN 4 Common Cathode-red .187 1.95 h
 | AAN 6740 Common Cathode-red-D D 560 99 AAN 6750 Common Cathode-red ± 1 560 99 AAN 6750 Common Acthode-red ± 1 560 99
 | XR567CP .99
XR567CT 1.25 | XR2206
XR2207
XR2208
 | 3.85 XR4194
5.20 XR4202 | 2.65
1.45
3.60
 |
| CD4007 25
CD4009 49
 | CD4030 4
CD4035 | 19 CD4081 23
19 CD4082 23
 | MAN 7G Common Anode-green 360 1.25 M
MAN 7Y Common Anode-green 300 1.99 C
 | MAN 6780 Common Audde-red
 | XR1310P 1.30
XR1468CN 3.85 | XR2209
XR2211
 | 1.75 XR4212
5.25 XR4558 | 2.05
 |
| CD4010 .49
CD4011 .23
CD4012 .25
 | CD4040 1 1
CD4041 1.2
CD4042 0 | 9 CD4093 99
25 CD4098 2 49
30 MC14409 14 95
 | MAN 72 Common Anode-red .300 .99 C
MAN 74 Common Cathode-red .300 1.25 C
MAN 82 Common Apode-red .300 0.99
 | DL704 Common Cathode-red 300 99 DL707 Common Anode-red .300 .99 DL707 Common Anode-red .300 .99 DL708 Common Cathode-red .300 .99
 | XR1466 1.39
XR1489 1.39 | XR2240
 | 4.35 XR4739
3.45 XR4741 | 1.47
 |
| CD4013 39
CD4014 1.39
 | CD4043 .8
CD4044 8 | 9 MC14410 14 95
9 MC14411 14.95
 | MAN 84 Common Cathole-yellow 300 99 D
MAN 3620 Common Anode-orange 300 99 D
 | DL720 Common Anode-red .500 1.45 0L741 Common Anode-red .600 1.25 0L746 Common Anode-red ± 1 .630 1.49
 | TYPE VOLTS W | PRICE
 | 1N4002 100 PIV 1 AMP
1N4003 200 PIV 1 AMP | 12/1 00
 |
| CD4015 1 19
CD4016 49
CD4017 1 19
 | CD4046 1 7
CD4047 2 5
CD4048 1 2 | 9 MC14419 4.95
0 MC14433 19.95
16 MC14506 75
 | MAN 3630 Common Anode-prange = 1 300 .99 C
MAN 3640 Common Cathode-prange 300 .99 C
MAN 4510 Common cathode-prange 300 .99 C
 | 0L747 Common Anode-red .600 1.49 0L749 Common Cathode-red ± 1 .630 1.49 0L750 Common Cathode-red ± 1 .630 1.49
 | 1N746 3.3 400
1N751 5.1 400 | 0m 4/1.00
0m 4/1.00
 | 1N4004 400 PIV 1 AMP
1N4005 600 PIV 1 AMP | 12/1 00
 |
| CD4018 99
CD4019 49
 | CD4049 4
CD4050 4 | 9 MC14507 99
9 MC14562 14 50
 | MAN 4640 Common Cathole-orange 400 99 C
MAN 4710 Common Anode-red 400 99 F
 | DL33B Common Cathode-red .110 .35
NO70 Common Cathode .250 .69
 | 1N753 6.2 400
1N754 6.8 400 | 0m 4/1.00
0m 4/1.00
0m 4/1.00
 | 1N4000 500 PIV 1 AMP
1N4007 1000 PIV 1 AMP
1N3600 50 200m | 10/1.00
 |
| CD4020 1 19
CD4021 1 39
CD4022 1 19
 | CD4051 1 1
CD4053 1 1
CD4056 2 9 | 9 MC14583 3 50
9 CD4508 3 95
5 CD4510 1 39
 | MAN 4730 Common Anode-red ± 1 400 99 F MAN 4740 Common Cathode-red 400 99 F MAN 4740 Common Anode-red 400 99 F
 | ND358 Common Cathode ± 1 .357 .99 ND359 Common Cathode .357 .75 ND503 Common Cathode .357 .75
 | 1N757 9.0 400
1N759 12.0 400 | 0m 4/1.00
0m 4/1.00
 | 1N4148 75 10m
1N4154 35 10m | 15/1.00
 |
| CD4023 23
CD4024 79
 | CD4059 9.9
CD4060 1.4 | 5 CD4511 1 29
9 CD4515 2 95
 | MAN 4840 Common Cathole-yellow 400 .99 F
MAN 6610 Common Anode-orange-D.D .560 .99 5
 | ND507 Common Anode (FND510) .500 .99
082-7730 Common Anode-red .300 1.30
 | 1N965 15 400
1N5232 5.6 500 | 0m 4/1,00
0m 28
 | 1N4734 5.6 1w
1N4735 6.2 1w | 28
 |
| CD4025 23
CD4026 2.25
CD4027 69
 | CD4066 7
CD4068 3
CD4069 4 | 9 CD4518 1 29
9 CD4520 1 29
5 CD4566 2 25
 | MAN 6630 Common Anode-orange = 1 ,560 99 H
MAN 6640 Common Cathode-orange-D.D. 560 99 H
MAN 6650 Common Cathode-orange - 1 550 99 5
 | IOSP-3400 Common Anode red .800 2.10 IOSP-3403 Common Cathode red .800 2.10
 | 1N5234 6.2 500
1N5235 6.8 500
1N5235 7.6 500 | 0m 28
0m 28
 | 1N4736 6.8 1w
1N4738 8.2 1w
1N4742 12 1w | 28
 |
| 74000 39
 | 74000 |
 |
 | 002 7000 4 x 7 0gl 0 git 1 HDP
 | 1102.30 7.3 300 | 20
 | 1144142 12 14 | 20
 |
| 74002 39
 | 74000 | 740163 2.49
 | MAN 6660 Common Anode-orange .560 .99 5
MAN 6680 Common Cathode-orange .560 .99 5
 | 082-7302 4 x 7 Sgl OrginEnDP 600 15.55
082-7304 Overrange character (±1) .600 15.00
 | 1N5245 15 500 | 0m 28
0m 28
 | 1N1/44 15 1w
1N1183 50 PIV 35 AMP | 28
 |
| 74C02 .39
74C04 .39
74C08 .49
 | 74C85 2.4
74C90 1.9 | 74C163 2,49
74C164 2,49
9 74C173 2.60
15 74C192 2,49
 | MAN 656U Common Andoe-orange 550 99 5
MAN 656U Common Cathode-orange 550 99 5
MAN 6710 Common Anode-red-D.D. 560 99 5
 | 082-7340 4 x 7 Sgi Digit-LinuP 600 19.33
082-7340 0verrange character (=1) .600 15.00
082-7340 4 x 7 Sgi Digit-Hexadeormal 600 22.50
CLOCK CHIPS
 | 1N5242 12 500
1N5245 15 500
1N456 25 40r
1N458 150 7m
1N485A 180 10r | 0m 28
0m 28
m 6/1.00
6/1.00
m 5/1.00
 | 1N4744 15 1w
1N1183 50 PIV 35 AMP
1N1184 100 PIV 35 AMP
1N1185 150 PIV 35 AMP
1N1185 200 PIV 35 AMP | 28
1.60
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| 74C02
 | 74C85 2.4
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74C95 1.9
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74C107 1.2 | 74C163 2,49
74C164 2,49
9 74C173 2,60
5 74C192 2,49
5 74C193 2,49
5 74C195 2,49
5 74C252 5,95
 | MAN 6680 Common Anode-orange 560 99 5
MAN 6680 Common Anode-orange 560 99 5
MAN 6710 Common Anode-red-D 560 99 5
RCA LINEAR
CAJ0371 2.15 CAJ082N 2.00
CALCULATOR
CHIPS/DRIVERS
 | 0.62:7304 0.42:7340 0.001-FhDP 600 15:90 0.82:7340 0.42:7340 0.41:01-FhDP 600 22:50 0.82:7340 0.42:7340 6.01:01-FhDP 600 22:50 CLOCK CHIPS MOTOROLA MOTOROLA 5:39 MM5508 5:4:95 MOTOROLA 5:75 MM5511 4:5:57 5:75 5:4:55
 | 1N5292 12 500
1N5245 15 500
1N456 25 400
1N458 150 7m
1N485A 180 100
1N4001 50 PiV 1 A | Jm 28 Jm 28 Jm 6/1.00 m 6/1.00 m 5/1.00 m 5/1.00 MP 12/1.00
 | 1N4744 15 1W
1N1183 50 PIV 35 AMP
1N1184 100 PIV 35 AMP
1N1185 150 PIV 35 AMP
1N1186 200 PIV 35 AMP
1N1188 400 PIV 35 AMP
BGE DECTIFIEDC | 28
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| 74002 39
74002 39
74008 49
74010 39
74010 39
74014 1.95
74020 39
74030 39
74030 39
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74030 39
 | 74C85 2.4
74C80 1.9
74C93 1.9
74C95 1.9
74C107 12
74C151 2.9
74C151 3.0
74C157 2.1 | 74C163 2.49
74C173 2.60
5 74C173 2.60
5 74C192 2.49
5 74C193 2.49
5 74C193 2.49
5 74C193 2.49
5 74C195 2.49
5 74C925 6.55
0 74C923 6.25
0 74C923 8.95
5 74C925 8.95
 | MAR Bold Common Andrez-erange Sold 99 5 MAR Bold Common Cathode-range Sold 99 5 MAR Bold Common Cathode-range Sold 99 5 MAR Bold Common Cathode-range Sold 99 5 RCA LINERAR Sold 99 5 CA30131 2.15 CA3082N 2.00 CALCULATOR CA20237 2.56 CA3082N 2.00 MM572S 22 CA30351 2.49 CA3068N 8 MM572S 22 CA30357 2.54 CA3046N 8 MM5738 2 CA30371 7.5 CA3084 2.35 MM5738 2
 | 082-7304 04-71-784 041-041-784 041 050 15 00 25 050 15 00 25 050
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110545 15 500
110456 25 407
110458 150 7m
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0m 28
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6/1.00
m 5/1.00
MP 12/1.00
MD FW BRII
15A @ 400V
15A @ 600V
 | 1N4/44 15 1w 35 AMP
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1N1184 100 PIV 35 AMP
1N1185 150 PIV 35 AMP
1N1186 200 PIV 35 AMP
1N1188 400 PIV 35 AMP
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74C14 1.95
74C20 39
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74C23 39
74C42 1.95
74C48 2.49
74C73 89
74C73 89
 | 74000
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74000 1.9
74003 1.9
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740154 3.0
740154 3.0
740154 2.4
740160 2.4 | 74C163 2.49 74C163 2.49 9 74C173 2.60 55 74C193 2.49 5 74C193 2.49 5 74C193 2.49 5 74C192 2.90 5 74C225 59 00 74C225 8.95 5 74C326 8.95 9 80C37 1.50 9 80C37 1.50
 | MAY Bobbil Common Androde-sname Solid 99 5 MAY Bobbil Common Cathode-sname Solid 98 5 MAY Bobbil Common Cathode-sname Solid 99 5 MAY Bobbil Common Cathode-sname Common Cathode-sname Cathode 99 5 RCA LINEMAR Cathode Cathode Cathode Cathode Cathode Cathode CA02037 2.56 CA30381N 1.60 MM5/25 S2 CA30337 1.35 CA10989N 375 MM8/25 S2 CA304981 2.00 MM5/25 S2 CA30498 375 MM8/86 2 CA30498 2.00 MM5/25 S2 CA30498 375 MM8/86 2 CA30498 32 S1 MM8/86 2 CM30498 32 S1 MM8/86 2 CM30498 32 S2 S
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 | 1N3242 12 500 1N3245 15 500 1N4365 25 407 1N458 150 7m 1N458 150 7m 1N4001 50 Pivit 1A SCR A C36D 1 C36M 32 2N2328 1 | Jm 28 Dm 28 m 6/1.00 m 5/1.00 MMP 12/1.00 ND FW BAR 400V ISA @ 400V ISA @ 300V ISA @ 50V ISA @ 50V
 | 1N4/24 15 1W 1N1183 50 PIV 35 AMP 1N1184 100 PIV 35 AMP 1N1185 50 PIV 35 AMP 1N1186 200 PIV 35 AMP 1N1186 200 PIV 35 AMP DGE RECTIFIERS SCRI2N1849) SCR SCR PW BRIDGE REC SCR | 28
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| 74C02 39
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7
 | MAR Bobb Common Androd-pringe Soli 99 5 MAR Bobb Common Cathod-pringe Soli 99 5 MAR Bobb Common Cathod-pringe Soli 99 5 RCA LINEAR CallCULATOR Soli 99 5 RCA1 LINEAR CallCULATOR CallCULATOR CallCULATOR CA03037 2.15 CA0303N 1.60 MM5/75 92 CA30351 1.56 CA0305N 1.60 MM5/75 92 CA30351 1.56 CA0305N 3.75 DM8684 2 CA30400 D.331301 1.25 DM8685 1 CA30400 1.25 CA3059N 3.25 CA31401 1.25 DM8887 1 CA3060N 3.50 CA.16000 3.50 CA.16000 1.25 DM8887 1 2.30600 3.50 CA.16000 3.50 CA.16000 3.50 CA.16000 3.50 CA.16000 3.50 CA.16000 3.50 CA.16000 <
 | CLOCK CHIPS MOTOROL State
 | 1h5/24/2 12 50/0 1h5/24/5 15 50/0 1h4/35 15 50/0 1h4/35 15 7/m 1h4/35 150 7/m 1h4/35 150 7/m 1h4/36 150 7/m 1h4/37 150 7/m 1h4/36 150 7/m 1h4/37 100 150 20/01 50 1/m MDA 980-1 1 MDA 980-3 MDA 980-3 1 9/09/07 | Jm 28 Jm 28 Jm 6/1.00 Jm 5/1.00 MMP 1/1.00 IMMP 1/1.00 IMMP 1/1.00 IMMP 1/1.00 IMMP 1/1.00 ISA @ 400V ISA @ 600V ISA @ 50V IZA @ 200V On TRANSI
 | 144244 15 10
141183 30 PV 35 AMP
141184 100 PV 35 AMP
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DGE RECTIFIERS
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 | 74C05 2.4
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 | MAX Bobbil Common Androd-pringe Solid 99 5 MAX Bobbil Common Cathod-pringe Solid 99 5 MAX Bobbil Common Cathod-pringe Solid 99 5 RCAL LINEAR Call Call Call Call Call CA03037 2.15 CA3093N 2.00 ChitSynDRIVERS Call Call<
 | CLOCK CHIPS MOTIONUL State
 | IN5242 12 SUC IN5245 15 SUC IN4565 25 40 IN4585 150 7m IN4585 180 7m IN4001 SO PIVIA SO PIVIA C360 1 1 C360 1 1 MDA 980-1 1 MDA 980-3 PN2907 Pastic 7/1.1 C16681 MPSA05 2 2 | Jm 28 m 6/1.00 m 5/1.00 m 5/1.00 m 5/1.00 m 5/1.00 MD FW BRII 154.@ 600V 154.@ 000V 124.@ 300V 124.@ 200V 0 MALE3055 0 MALE3055 0 2/13/32
 | 144243 15 DE 36 AVF 35 AMF
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141186 200 DFV 35 AMF
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 | MAR Bobble Common Andrez-erange Solid 99 5 MAR Bobble Common Cathode-erange Solid 99 5 MAR Bobble Common Cathode-erange Solid 99 5 RCA LINERAR Solid 99 5 CA30313T 2.15 CA3082N 2.00 CALCULATOR CA3032T 2.46 CA3068N 5 MM5735 22 CA3035T 2.48 CA3068N 5 MM5735 22 CA3046N 1.30 CA3130T 1.37 DM8664 2 CA3060N 3.50 CA.LED.MM87738 CA306N 2.00 CA306N 3.50 CA.LED.Griver 1 CA306N 3.50 CA.LED.Griver 1 CA306N 3.50 CA.LED.Griver 1 1 2.00 CA306N
 | CLOCK CHIPS MOTIONE State
 | 1h2-24 12 502 1h3545 15 500 1h4554 150 77 1h4501 50 71 1h4001 50 71 2N2328 1 MDA 980-1 1 MDA 980-3 1 PN2907 Plastic 71.1 C106816 MPA006 5.77 T1597 61.1 11588 | Jm 28 m 6/1.00 m 5/1.00 m 5/1.00 m 5/1.00 m 5/1.00 ND FW BRII 154 @ 400V 154 @ 500V 124 @ 500V 0 TRANSIS 0 NL395 00 NL395 00 PN3567
 | 144244 15 15
141184 100 PV 35 AMP
141184 100 PV 35 AMP
141186 200 PV 35 AMP
141186 200 PV 35 AMP
DGE RECTIFIERS
SCR
PW BRIDGE REC
FW BRIDGE REC
FW BRIDGE REC
STORS
5100 243905
51100 243905
51100 244905
51100 000000000000000000 | 28
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6/1.00 |
| 74C02 39 74C02 39 74C08 49 74C10 39 74C11 39 74C12 39 74C13 39 74C14 195 74C23 39 74C42 195 74C42 195 74C43 2.49 74C74 89 74C74 89 74C74 89 74C74 89 74G74 89 7430204 75 1430204 75 1430204 100 1430204 100 1430204 <td< td=""><td>74650 24
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74695 19
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74633 19
746137 12
746151 29
746151 24
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746151</td><td>74C163 2.49 74C163 2.49 74C173 2.60 5 74C192 2.49 5 74C193 2.49 5 74C193 2.49 5 74C193 2.49 5 74C323 625 0 74C325 895 5 74C325 895 9 80C35 1.50 9 80C37 1.50 9 80C37 1.50 15 LM733N 1.00 15 LM733N 1.00 15 LM733N 1.90 15 LM733N 1.90 15 LM741-14N 39 15 LM744N/H 39 15 LM744X/H 39 15 LM748X/H 39</td><td>MAR Bobbil Common Androde-sname Sold 99 5 MAR Bobbil Common Cathode-sname Sold 99 5 MAR Bobbil Common Cathode-sname Sold 99 5 RCA LINEARAR Sold 99 5 CA10317 2.56 CA3033N 2.00 CHPO/RIVER CA30337 2.56 CA3038N 1.60 MM5725 52 CA30337 1.35 CA308N 1.60 MM5725 52 CA30351 2.00 CMPS/PORIVERS MM5725 52 CA30490 30 DM6895 2 CA3045N 3.50 CA10011 1.25 DM6895 2 CA30490 49 37.47 30 CA30601 2.50 CA30601 35 CA3041 49 37.47 30 1.20 MM897 40 1.24 2.54 50-100 1.24 2.49 50-100 1.24 2.49 50-100 1.20 1.20 1.20 1.20 1.20</td><td>Sign 2012 CLOCK CHIPS MOTOROLA Sign 2014 4 r 7 Sgi (bq)-Hexadecimal 600 22.50 Sign 2014 4 r 7 Sgi (bq)-Hexadecimal 600 22.50 Sign 2014 4 r 7 Sgi (bq)-Hexadecimal 600 22.50 Sign 2014 4 r 7 Sgi (bq)-Hexadecimal 600 22.50 Sign 2014 4 sgi MC100R1 54 sgi MC100R1 54 sgi MC100R1 Sign 2014 4 sgi MC100R1 54 sgi MC100R1 54 sgi MC100R1 Sign 2014 4 sgi MC100R1 54 sgi MC100R1 54 sgi MC100R1 Sign 2014 4 sgi MC100R1 55 sgi MC302P 25 sgi MC305P 2 sgi MC305P Sign 2014 4 sgi MC100R1 55 sgi MC305P 2 sgi MC305P 2 sgi MC305P 2 sgi MC305P Sign MS318 9 sgi MC4016[r/1416] 7 sgi MM337P 3 sgi MC404P 6 sgi MC404P 6 sgi MC404P Sign MS32P(198A 4 sgi MC4040P 3 sgi MC4040P 6 sgi MC404P 3 sgi MC4040P 3 sgi MC4040P</td></td<> <td>Ih5242 12 SUC Ih5245 15 SOC Ih436 15 SOC Ih4364 180 SO Ih4001 SO Pivi 1A C36D 1 C36M 3 2N2328 3 2N2328 1 MDA 980-1 MDA 980-1 1 MDA 980-1 MDA 980-3 1 TIS87 6/1.1 TIS87 6/1.1 TIS87 6/1.1 40409 1 1 40409 1</td> <td>m 28 m 6/1.00 m 6/1.01 m 6/1.02 m 5/1.02 m 5/1.02 m 5/1.02 m 5/1.02 m 5/1.02 m 6/1.02 m 7/1.02 m 7/1.02</td> <td>144244 15 the
141183 30 PV 35 AMP
141184 100 PV 35 AMP
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DGE RECTIFIERS
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STORS
51100 2439305
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74695 24
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 | MAX Bobb Common Adhed-pringe Sold 99 3 MAM Bobb Common Cathod-pringe Sold 99 5 MAM Bobb Common Cathod-pringe Sold 99 5 RCAL LINEAR Common Annote-red 0.0 Sold 99 5 CA03037 2.15 CA3083N 2.00 CALCULATOR CA0337 2.56 CA3038N 1.60 MM5725 22 CA30357 2.46 CA3058 1.60 MM5725 22 CA30807 3.50 CA30807 3.50 CA1407 1.25 DM8887 CA305807 2.00 CA35000N 3.50 CA1-LED dmwe 1 CA30807 2.50 CA3500N 3.50 CA1-LED dmwe 1 CA30807 2.50 CA3500N 3.50 CA1-LED dmwe 1 CA30807 2.50 CA3500N 3.50 CA1-LED dmwe 1 CA30807 3.50 CA SOLDERTAIL LOW PRC 6 pm LP
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 | MAR Bobb Common Cathode-range Sold 99 5 MAR Bobb Common Cathode-range Sold 99 5 RCA LineAme Catculator AMA Bobb Common Annoer et 0.0 Sold 99 5 RCA LineAme 2.00 Sold 99 5 CA12013T 2.15 CA1033N 1.60 MM5/75 92 5 CA0303T 1.50 CA1089N 1.60 MM5/75 92 5 CA1073S 2 CA3035T 1.50 CA1089N 3.75 DM8864 2 CA3049N 1.39 DM8865 1 CA304SN 1.30 CA3137 1.25 DM8867 1 5 MM5/75 92 CA3050N 3.50 CA.1ED offer 1 CA3050N 3.5 CA3161 1.25 DM8867 1 2 1 1 1 1 1 1 1 1 1 1 1 1 1
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RADIO-ELECTRONICS

Rockwell AIM 65 The Head-Start in Computers

AIM 65 Technical Overview

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6

AXYP

COMMANDS

Major Function Entry

ESC-Re-enter Monitor

Enter Assembler

ADVANCED INTERFACE MONITOR

-Enter and initialize Text Editor -Re-enter Text Editor

Display/Alter Registers and Memory

(RESET Button)-Enter and initialize Monitor

-Enter and initialize BASIC Interpreter -Re-enter BASIC Interpreter

Instruction Entry and Disassembly —Enter mnemonic instruction entry mode K —Disassemble memory

lay/Alter Registers and Memory Alter Program Counter to (address) Alter Accumulator to (byte) Alter X Register to (byte) Alter Y Register to (byte) Alter Processor Status to (byte) Alter Stack Pointer to (byte)

at (address) (SPACE)—Display next four memory locations / —Alter current memory location

 Manipulate Breakpoints

 —Clear all breakpoints

 4 —Toggle breakpoint enable on/off

 8 —Set one to four breakpoint addresses

 ? —Display breakpoint addresses

Control Peripheral Devices L —Load object code into memory from peripheral I/O device D —Dump object code to peripheral I/O

-Toggle Tape 1 control on/off -Toggle Tape 2 control on/off

Text Editor Commands R — Read lines into text buffer from peripheral

R —Read lines into text buffer from peripheral I/O device
 Insert line into text buffer from Keyboard
 Kapert line into text buffer from Keyboard
 CBPACE)—Display current line of text
 CList lines of text to peripheral I/O device
 Move down one line
 Go to top line of text
 B —Go to bottom line of text
 F —Find character string
 C —Quit Text Editor, return to Monitor

3 -Verify tape checksum CTRL PRINT-Toggle Printer on/olf

LF -Line Feed PRINT-Print Display contents

Call User-Defined Functions -Call User Function 1 -Call User Function 2 -Call User Function 3

Execute user's program Toggle instruction trace mode on/off Toggle register trace mode on/off Trace Program Counter history

Control Instruction/Trace

device

F3

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microcomputer systems

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- Complete 64-character ASCII alphanumeric
- format Fast 120 lines per minute
- Quiet thermal operation
- · Proven reliability

FULL-SIZE ALPHANUMERIC KEYBOARD

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- 26 alphabetic characters
- 10 numeric characters
- 22 special characters
- 9 control functions
- 3 user-defined functions

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Provides legible and lengthy display • 20 characters wide

16-segment characters

High contrast monolithic characters Complete 64-character ASCII alphanumeric

format

PROVEN R6500 MICROCOMPUTER SYSTEM DEVICES Reliable, high performance NMOS

technology
 R6502 Central Processing Unit (CPU).

operating at 1 MHz. Has 65K address capability, 13 addressing modes and true index capability. Simple, but powerful 56 instructions.

 Read/Write Memory, using R2114 Static RAM devices. Available in 1K byte and 4K byte versions

 8K Monitor Program Memory, using R2332 Static ROM devices. Has sockets to accept additional 2332 ROM or 2532 PROM devices, to expand on-board Program Memory up to 20K

R6532 RAM-Input/Output-Timer (RIOT) ombination device. Multipurpose circuit for AIM 65 Monitor functions.
Two R6522 Versatile Interface Adapter (VIA)

devices, which support AIM 65 and user functions. Each VIA has two parallel and one serial 8-bit, bidirectional I/O ports, two 2-bit peripheral handshake control lines and two ully-programmable 16-bit interval timer/event counters

BUILT-IN EXPANSION CAPABILITY

-Pin Application Connector for peripheral add-ons

44-Pin Expansion Connector has full system

bus
 Both connectors are KIM-1 compatible

TTY AND AUDIO CASSETTE INTERFACES Standard interface to low-cost peripherals • 20 ma. current loop TTY interface

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- Two audio cassette formats: ASCII KIM-1 compatible and binary. blocked file assembler compatible

ROM-RESIDENT ADVANCED INTERACTIVE

MONITOR Advanced features found only on larger

- systems
- Monitor-generated prompts
- Single keystroke commands
- Address independent data entry
- Debug aids
- Error messages
- · Option and user interface linkage

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2 Display all registers Displays four memory locations, starting

Rockwell's AIM 65 Advanced Interactive Microcomputer can get you into the exciting world of microcomputers a lot easier and at a lower cost than you may have thought possible. And you'll be working with the 6500 family, the advanced state-of-the-art NMOS system that's an everincreasing favorite for new commercial and hobbyist applications.

As a learning aid. AIM 65 gives you an assembled, versatile microcomputer system with a fullsize keyboard. 20-character display and, uniquely, a thermal printer. An on-board Advanced Interactive Monitor program provides extensive control and program development functions. And our AIM 65 User's Manual will help you along each step of the way

You'll master fundamentals rapidly. Then you'll appreciate the fact that unlike the computer "toys" on the market. AIM 55 offers flexibility and expandability you would expect to find in a sophisticated microcomputer development system.

THERMAL PRINTER GIVES YOU HARD COPY — FAST AND QUIET. AIM 65's 20-column Thermal Printer prints on low-cost, thermal roll paper at a fast 120 lines per minute. It produces all of the standard 64 ASCII characters with a crisp-printing five-by-seven dot matrix. AIM 65's on-board printer is a unique feature for a low-cost computer.

EXTENDED ALPHANUMERIC DISPLAY IS BUILT FOR UNDERSTANDING, NOT DECIPHERING. AIM 65 comes with a 20-character true Alphanumeric Display. Information is displayed with bright, magnified 16-segment font monolithic characters. It's both unambiguous and easily

FULL-SIZE KEYBOARD IS DESIGNED FOR HUMANS, NOT ELVES. AIM 65's terminal-style keyboard frees you from the hassles of fumbling around with a tiny calculator-type keypad. And its 54 keys provide 70 different alphabetic, numeric, control and special functions.

ON-BOARD ADVANCED INTERACTIVE MONITOR GETS YOUR PROGRAMS UP AND RUNNING. The ROM-resident AIM 65 Advanced Interactive Monitor Program provides a comprehensive set of easy-to-use, single-keystroke commands for debugging your programs, and offers features normally available only in larger, expensive microcomputer development systems. And with the AIM 65 Monitor, there's no guesswork involved; the Monitor gives a self-explanatory prompt when it needs information and it will generate a meaningful error message if an error has occurred. The AIM 65 Monitor includes commands to

- Enter and edit programs directly no "opcode" memorization
- POWER SUPPLY SPECIFICATIONS ++ 5 VDC + 5% regulated @ 2.0 amps (max) +24 VDC + 15% unregulated @ 2.5 amps (peak) 0.5 amps (average) · List programs on Printer or TTY
 - · Display/alter registers and memory
 - Set breakpoints, trace and debug program execution
 - · Control the Thermal Printer
 - Transfer information to/from attached Cassette Recorders or TTY

 - Execute programs in on-board or external RAM. ROM or PROM memory · Interface the optional AIM 65 Assembler and BASIC Interpreter

AIM 65'S ADVANCED R6500 NMOS ARCHITECTURE. The R6502 Central Processing Unit is the heart of the AIM 65. It provides demonstrated speed and simplicity, plus 65K addressability and the power of a 56-command, minicomputer-like instruction set.

The R6532 RAM-Input/Output-Timer (RIOT) combination device is used by the AIM 65 Monitor for scratchpad memory and Keyboard operations.

Two R6522 Versatile Interface Adapter (VIA) devices are provided. One device supports AIM 65's Thermal Printer and the TTY and Cassette Interfaces, the other supports two user-dedicated 8-ine I/O ports, plus an 8-bit serial I/O port and access to two 16-bit interval timer/event counters, on the module's Application Connector.

AIM 65 comes with two R2332 4K Read Only Memory (ROM) devices installed. These hold the Advanced Interface Monitor program. Spare sockets allow the user to expand on-board ROM up to 20K bytes. These sockets will accept user programs on R2332 ROMs or compatible PROMs, or can be used to install the optional AIM 65 Assembler and BASIC Interpreter ROM devices.

On-Board Read/Write RAM memory is available in 1K-byte and 4K-byte configurations

AIM 65 HAS EXPANSION BUILT IN. Am bo that SetAmarion Bull, I'm. And to allow AIM65 to grow the way you want it to, we've provided an Application Connector and an Expansion Connector. The Application Connector permits you to plug on a TTY (20 ma. current loop) and one or two standard audio casette recorders. If also has the pinouts for the VIA's General-Purpose i/O poils. The Expansion Connector extends AIM 65's system bus — address, data and control — out to additional merry. or anything else you might attach. And. BASIC high-level language programming is a built-in option.

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The Super Elf includes a ROM monitor for proam loading, editing and execution with SINGLE gram loading, evining and execution. STEP for program debugging which is not in-cluded in others at the same price. With SINGLE STEP you can see the microprocessor chip operating with the unique Quest address and data bus displays before, during and after executing instructions. Also, CPU mode and instruction cycle are shown on several LED indicator lamps

An RCA 1861 video graphics chip allows you to connect to your own TV with an inexpensive video modulator to do graphics and games. There is a speaker system included for writing your own music or using many music programs already written. The speaker amplifier may also be used to drive relays for control purposes

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