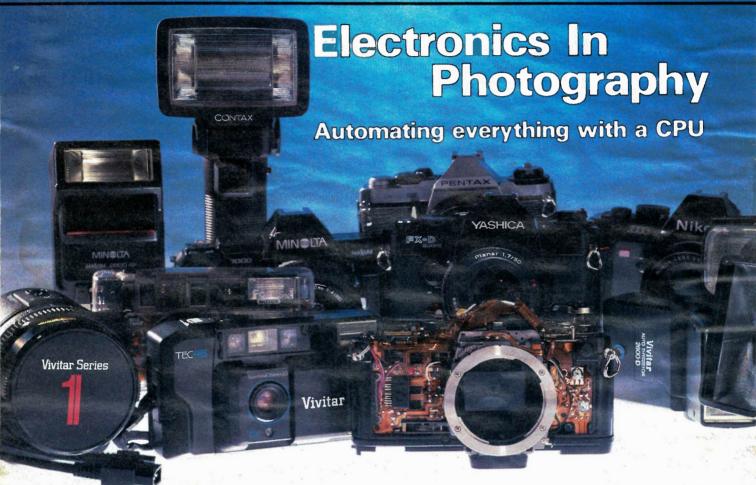
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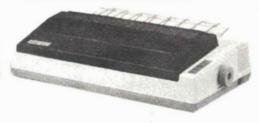
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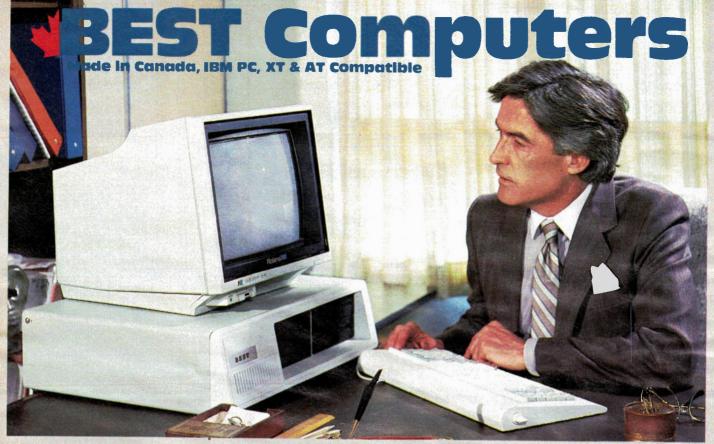
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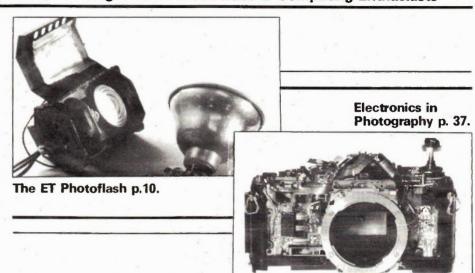
Advertisers' Index

Active Components Sales Corp.	
Aptron Design Ltd.	
Atlas Electronics Ltd.	
BCS Electronics Ltd.	
Canada Remote Systems	30
Canadian Distribution Inc.	41
Comptech Systems	40
Computer Mail Order	40
Computer Parts Galore	8, 9
Dacor Ltd.	68
Dactron	63
Duncan Instruments	64
EDG	31, 33, 35
Electronic Control Systems	30
Electronic Supplies Inc.	
Exceltronix	.2, 3, 36
Hitachi Denshi Ltd. (Canada)	72
Information Unlimited	32,41
Interfax Systems Inc.	
Kaientai Electronics Merchants Ltd.	30
KB Electronics	6, 41
Leasamatic Canada	40
Len Finkler & Co.	
Mark Gee Enterprises Ltd.	62
McGraw Hill	29
Mesurina Ltd.	41
Metermaster	34
Micohmega Electronics	62
Orion Electronics Supplies Inc.	66
Patron Components Inc.	35
RETS Electronic Training	41
Solen Electronics Inc.	
Soltech Industries Inc.	
Sunix Inc.	62
Surplustronics	
The Software Link Inc	
Tranpac Trading Corp.	42
Varah's Direct	7
Westown Electronics	61

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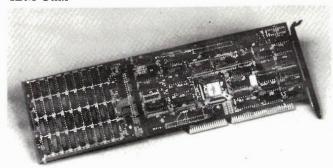
December 1985 Vol. 9 No. 12



Features Impedance 18 **Surface Acoustic Waves** 27 37 **Electronics In Photography Projects** The ET Photoflash 10 **Heat Pen** 43 47 FET Amplifier **Series For Your Information** 6 **Designer's Notebook: Diodes** 54 **Electronics From The Start, Part 6** 58 **Computing Today: Data Encryption** 25 **Columns and Information** Computing Now! This Month 51

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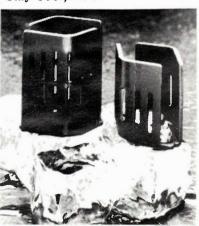
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continued on page 36

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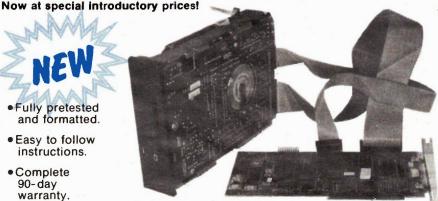
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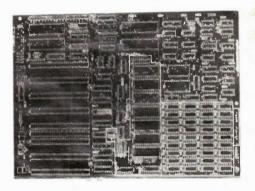
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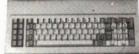


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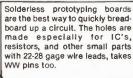
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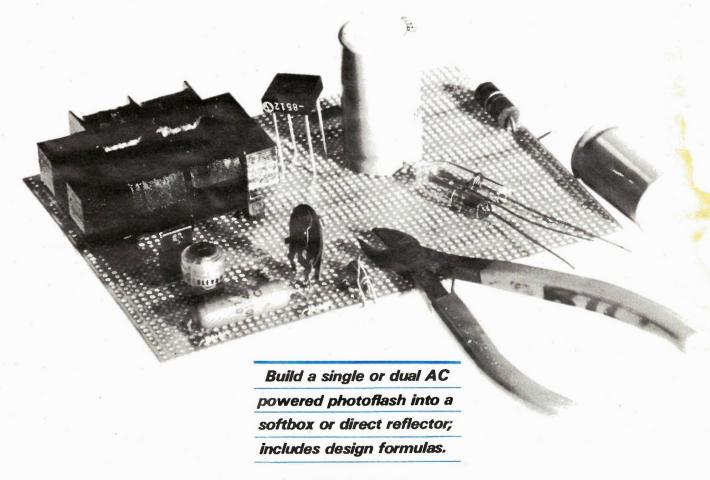
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All cards come with a detailed parts list and placement drawing, we also have all parts needed for them.

Photoflash Project



By Bill Markwick

IF YOU do any studio photography, or you'd like to have a larger flash unit that will fill a room with soft light, or you'd just like to tinker with xenon tubes, here's the Electronics Today Photoflash. Most of the parts are available from Radio Shack, and you can substitute without too much trouble.

However, because of the variables involved, I'd like to point out that this project is for the determined tinkerer. The construction details and mounting methods are left largely to you.

You can use one or more flashtubes, each with a maximum power of 50 watt/seconds (joules). The tube (or tubes) can be mounted in a homemade softbox, or fit into a reflector or other lamp, or just used bare-bulb for a floodlight effect. The recycle time at full power is about seven seconds, limited to avoid overheating the low-cost flashtubes.

Feel free to tinker with the various components. About all that can go wrong is that you overheat and blow the \$4.95

flashtube, or take a poke from the 300V supply (painful but not likely to be lethal).

Oh, speaking of the things going wrong - ('here it comes... he's gonna tell us we can't get some of the parts.') you may not be able to get some of the parts. At least not at the corner Radio Shack. At the time of planning this project, RS stocked trigger coils, photoflash capacitors, and flash tubes. As we went to press, the coil and capacitor had been discontinued, but there may still be stock available on the shelves. If not, there are substitutions listed for you, but they may mean top dollar. The RS versions were light-duty, very inexpensive, and suitable for tinkering. The subs are a bit more robust and rather more expensive. However - onward.

Theory

The flashtube consists of a glass tube with electrodes at each end and a filling of xenon gas. A third small electrode is attached to the outside of the tube. The in-

ternal electrodes are connected across a large capacitor charged to a high voltage (the tube is normally non-conductive), and the external electrode is attached to the output of a very high voltage trigger coil.

When the main reservoir capacitor is fully charged and you're ready to go, a smaller charged capacitor is switched across the primary of the trigger transformer, producing a decaying pulse with a maximum amplitude of about 4kV. This high voltage pulse on the external electrode ionizes the xenon gas inside the flashtube and makes it conductive; the effect is rather like triggering an SCR. The sudden low resistance of the flashtube discharges the main capacitor in about a millisecond, and the sudden rush of high current through the gas produces a burst of light. The light has an odd, discontinuous spectrum, rather spiky, but the eye and the film see it as pretty much the same thing as daylight. Its colour temperature may not be tightly specified

Electronics Today December 1985

200 VOLTS		225 VOLTS 250 VC	LTS
CAP. = 100	W/S = 2	CAP. = 100 W/S = 2 CAP. =	100 W/S = 3
CAP. = 200	W/S = 4	CAP. = 200 W/S = 5 CAP. =	200 W/S = 6
CAP. = 300	W/S = 6	CAP. = 300 W/S = 7 $CAP. =$	300 W/S = 9
CAP. = 400	W/S = 8	CAP. = 400 W/S = 10 $CAP. =$	400 W/S = 12
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CAP. = 600	W/S = 12	CAP. = 600 W/S = 15 CAP. =	600 W/S = 18
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CAP. = 800	W/S = 16	CAP. = 800 W/S = 20 CAP. =	800 W/S = 25
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CAP. = 1000	W/S = 20	CAP. = 1000 W/S = 25 CAP. =	1000 W/S = 31
275 VOLTS		300 VOLTS 325 VC	LTS
CAP. = 100	W/S = 3	CAP. = 100 W/S = 4 CAP. =	100 W/S = 5
CAP. = 200	W/S = 7	CAP. = 200 W/S = 9 CAP. =	200 W/S = 10
CAP. = 300	W/S = 11	CAP. = 300 W/S = 13 $CAP. =$	300 W/S = 15
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CAP. = 900	W/S = 34	CAP. = 900 W/S = 40 CAP. =	900 W/S = 47
CAP. = 1000	W/S = 37	CAP. = 1000 W/S = 45 CAP. =	1000 W/S = 52

Watt/second values for various capacitors and operating voltages.

on the hobbyist tubes, but it will be in the neighbourhood of 5500 degrees K, matching nicely to daylight colour films.

Once the voltage of the main capacitor drops to 50V or so, the tube becomes non-conductive once again and the capacitor begins charging for the next blast.

The Flashtube

The flashtube we used, Radio Shack number 272-1145, is ideal for this application because it's inexpensive, runs at 300V, and puts out 50 watt/seconds of power if you don't overdo it (the specs say maximum 4 flashes per minute at 50W/S). It was still in stock the last time we checked.

If you want to try others, the FT152A from Canadian General Electric is a good one, with its 125W/S rating. If you keep to the 50W/S figure, you can fire it off continuously every five seconds or so. Most camera stores that deal with professional photographers can order it for you, but it takes a long time to get one if it's not in stock, and it's over \$50. The CGE FT118 and FT151 are more common, but I hesitate to recommend them because they prefer to run from 400V.

Flashtubes will only operate over a narrow range of voltages. The RS, for instance, must be run between 200 and 300V. If you go below the minimum, you'll get erratic triggering or none at all, and if you exceed the maximum, the tube

may fire on its own as the gas breaks over and becomes conductive.

There's yet another flashtube that used to be available from Radio Shack: the Jumbo Xenon Strobe, number 272-1147. If you can still get this one (it's unlikely), it's a good sub. The specs on its packaging are wrong, though, and your RS dealer may not want to talk about it since its deletion.

If you've managed to locate a source of other tubes, make sure the tube can: put out 50W/S per flash at a rate of four flashes per minute, run at 300V, trigger from 4kV, and dissipate 12 watts or more.

The dissipation, incidentally, is found by multiplying the energy per flash (explained later) times the number of flashes per second:

Power = $W/S \times flashes/sec$

Our flash is held to a recycle time of about seven seconds to give the tube a chance to cool. You can fire it off as fast as the capacitor recharges, as long as you don't exceed four flashes per minute total. The above formula works out to a dissipation of 3.3 watts at 50W/S and a F/S rate of .067 (four flashes/min). You can fire four flashes in rapid sequence, but then let it cool for a while.

A word of caution: a General Electric application note points out that tubes can be damaged in one flash by an overload, and we found out the hard way that

they're right. The tube life (normally 8,000 flashes at full power for the RS) will be shortened drastically if it flashes at all. Don't exceed the maximum watt-second rating if you can avoid it.

The Capacitor

The capacitor determines the stored energy for the flash for a given voltage. The formula is:

 $W/S = CV^2/2$ where C is in farads and V is in volts. The energy varies directly with the capacitance and directly with the square of the voltage; small changes in voltages make a big difference.

However, should you like a flash with different settings, the best way to do it is usually to change capacitors, because the tube may not take kindly to having its voltage changed; too low and you get erratic firing, too high and you shorten the life or make it fire from breakover. If you're interested in using the variable-voltage method, the 272-1145 is supposed to run as low as 200V; this gives a one-stop variation over the range from 200 to 300V.

The capacitors we used were the Radio Shack 272-1148, 500u at 330V, two in parallel for 1000u. If you can't find these, you can use any large electrolytic, or parallel several together, as long as the voltage ratings are 330V or more (more is better). Surplus stores are a good place to look, or you can go for computer-grade

from any major electronics supplier.

In another more powerful flash that I built for use around here, I used surplus-store capacitors, 1000u at 450V. They only cost \$5 each, but they're the size of a 28 oz. can of tomatoes. Because they were a good deal, I built a separate power supply so I could run the tubes in various softboxes, reflectors, spotlights,

You could also invest in some real heavy-duty proper photoflash capacitors. The Mallory EAF102 X 36 OU4C3PL is a 1000u, 360V unit available on special order from Mallory dealers. These are high quality professional units with very low internal resistance, but a bit expensive. Since photoflash capacitors are a bit rare, you might have to contact the makers: Emhart Canada Ltd., Mallory Components Division, 222 Dixon Road, Weston, Ontario, M9P 2M2, (416) 244-4239.

Resist the temptation to put capacitors in series; there's the problem of dividing up the operating voltage properly between them.

If you'd like to have a variable-power flash, you can switch in different capacitors. Each doubling or halving of capacitance represents one stop, photographically speaking. The listing shows the output in watt-seconds for various combinations of voltages and capacitances.

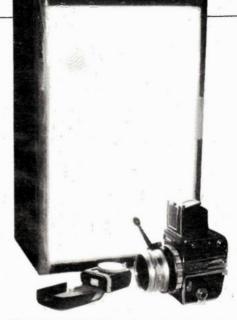
If you're building a multiple unit, remember that each tube requires its own capacitor. If you try to run several tubes from a single capacitor bank, the tube with the lowest resistance gets most of the power. It could be high enough to damage it. I've only used one trigger coil for simplicity, but for more than two tubes, you should add another for each pair of tubes.

You can add lots more than two tubes if you want, though the recycle time will get longer.

The Resistors

The purpose of the series resistor (the 2k2) is twofold: first, and most important, it isolates the tube from the transformer to make sure that the tube cuts off and doesn't keep glowing when the capacitor is discharged; second, it increases the recycle time to give the tube a chance to cool. The value of 2k2 was arbitrarily chosen for a reasonable recycle time; you can substitute if you like. By the way, these 1W resistors will get very hot during charging; because it's for such a short time you can get away with it. If you prefer, you can use 2W to 5W units.

There should be an isolating series resistor for *each* flashtube used. The minimum value for the resistor is: R = T/7C, where T is the time between flashes in seconds and C is the storage capacitor in farads. For longer charge times, the formula for the time in seconds is: T = T/7C



The well-worn ET mini softbox, about 1 x 1 x 2 feet. A sheet of glass protects the double layer of frosted film.

5RC. It may turn out to be slightly longer or shorter than calculated because the R should include the effective resistance of the transformer secondary, and also, electrolytic capacitors have very wide tolerance ranges.

The 47k shunt resistors are important to prevent the output voltage from rising; we used a 229-series transformer because it was small and inexpensive, but its 230V output means that the rectified and filtered output can rise to well over 330V, exceeding the capacitor's voltage rating. The 47k keeps the power supply under load at all times. If you're using more than two tubes, you don't need extra 47k resistors as long as there's at least one in the circuit.

The Transformers

Anything will do for a power transformer as long as the DC output doesn't exceed the capacitor voltage ratings. The 229B230 I used has a 50mA, 230V secondary, adequate for fast recharge even with multiple tubes. You can also use the 229A230, a 25mA unit, or any other suitable transformer. The AC voltage from the secondary is: VAC = VDC x .707. Just work backwards from the desired DC, or if you already have a junkbox full of transformers, the DC will be: VDC = VAC x 1.414.

Measure the AC secondary voltage with no load connected; if we ignore the 47ks for the moment, the capacitor voltage will rise to the peak of the unloaded AC voltage. Remember that commercial specs for power transformers are usually full-load values. You may have to adjust the shunt resistor if the voltage exceeds your capacitor's rating. The current rating isn't too important; the lower it is, the longer the recycle time, and vice versa.

The trigger coil is designed for Class I

triggering, which is what most photof tubes use. This requires an output puls 4kV, an energy of at least 3.2 millijo (a millijoule is 1/1000 of a watt/secol and a duration of at least 50uS. The 272-1146 coil specs want a primary in of 300V from a .022u capacitor; this is ly 1mJ. I've increased the trigger capacito .1u, giving 4.5mJ, enough to fire tubes. If you get occasional misfiring two tubes, solder another .1u 400V act the first.

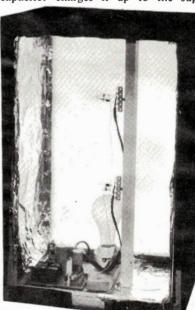
If you can't find this coil, Hammakes a photoflash trigger, the 616B Class I type. It's a heavy-duty unit the cost you about \$20, though it will forever. It prefers 120V on the capacinstead of 300V. Not to worry; put a resistor across the .1u trigger capacito

Occasionally you'll come act tubes that require a high-energy triging such as the CGE FT151. These use Could triggering: 10kV and 20mJ. Hammer makes the 616E Class II trigger; it will a work with the Radio Shack tubes, thou I've found that the 10kV pulse will be through normal wire insulation. You need sleeving or HV wire for the trigileads should you use Class II.

The Firing Circuit

You could use the shutter contacts your camera to directly connect the trig capacitor across the coil, but this me slight sparking of the contacts and 30 on your leads. I've used an SCR, the 276-1020 (6A, 400V), to reduce the trig current to a few milliamps and operating voltage to about 25V. The Sc can be almost any unit with a curr rating of 1A or more and a voltage rat of 400V or more (2N3529, C6D, S401 etc.). It doesn't need heatsinking becait doesn't dissipate any significant pow

The 1M resistor in series with capacitor charges it up to the sup



The interior of the softbox, showing the perboard, 150W lamp and two 50W/S tubes.

voltage within 1/2 second. When the SCR is triggered, it effectively connects the coil primary across the capacitor, discharging it rapidly to produce the required HV pulse.

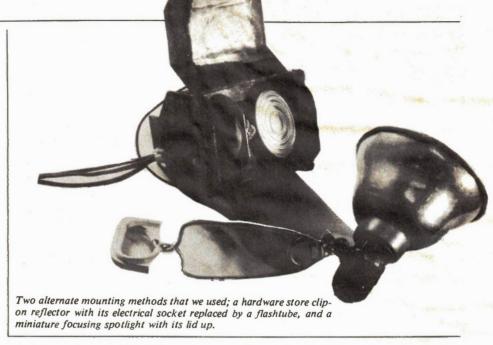
The 1uF capacitor is used to make sure that a good strong initial current is applied to the gate for reliable triggering. This can be any value you have on hand from 1u to 10u, with a minimum voltage rating of 35V.

Occasionally SCRs can be triggered by static or noise on the shutter leads. If this happens, try connecting a small capacitor from the gate to the cathode; a .001u is a good starting point and should short-circuit noise voltages.

Other Components

A neon light (with or without an internal resistor) is included to let you know when the flash is charged. Neons won't come on until they reach a specified firing voltage; because of variations in this voltage I've included a trim pot. You'll find that neon firing is "soft"; as the voltage rises, the tube lights long before it reaches maximum brightness, especially if the neon has an internal current-limiting resistor. For this reason, the capacitor continues charging after the lamp has lit, even if you're finicky about setting the trim pot. Refer to the watt-second listing and use a DC voltmeter on the power supply to see how much the power will increase after the lamp lights. You might find another 10 or 15 percent is added after the neon triggers. I've never found it makes much difference photographically.

If you're using a softbox, you'll probably want to add a modelling light;



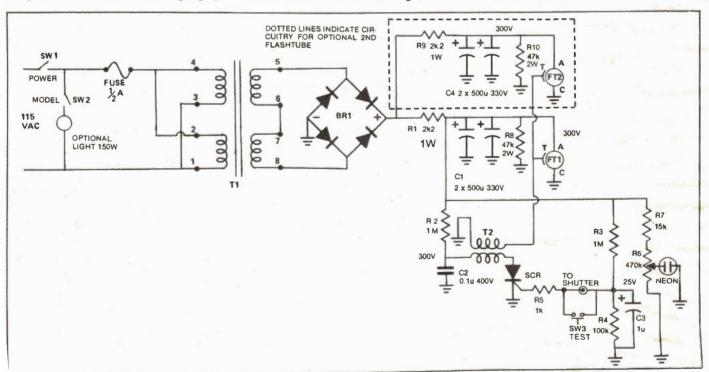
otherwise you're using it blindly as far as shadowing goes. I used a 150W kitchen bulb; it isn't much light, but it gives you an idea of the effect.

The rectifier bridge is a readily available VS447; you can use any other 400V bridge, or use four 400V diodes such as the 1N4004.

Construction

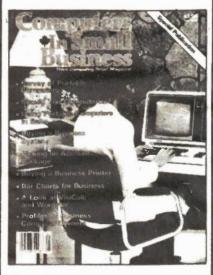
I used perfboard for the components because it's a small circuit. You could use Veroboard or make your own PCB. There isn't anything critical about the circuit; use common sense when dealing with the 300V wiring. A 1000u at 300V discharging through your pinkies is *very* startling. The 4kV out of the trigger coil is harmless but unnerving to touch.

The softbox can be made from almost anything. I used 1 x 2 spruce covered in fibreboard and fastened together with an electric glue gun and a few nails. The inside is lined with aluminum foil to minimize light absorption, and the front is covered with two layers of drafting film ("mylar"). Any translucent white film will do, even paper. The circuit is fastened to the wood frame, and the tubes are mounted on terminal



The schematic of the photoflash, single or dual tube. See the text for parts substitutions.

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T2 Class I trigger coil, Radio Shack
BR1
5CK Kaulo Snack 276-1020
Misc
Flash cord with sync connector, 1/2 amp
pigtail fuse, socket for domestic lightbulb,

pigtail fuse, socket for domestic lightbulb, 150W bulb, perfboard, etc.

strips (RS 274-688) fastened to a crossbar with wood screws.

We couldn't locate a chassis-mount sync connector in time, so I bought a short flash extender cord from the local camera store and cut one end off. It hangs out of the box, a bit inelegant, but it works.

If you go for the clip-on reflector, you might want to locate an old or new tube socket to hold the flashtube. Strip out the regular bulb socket from the insides of the unit and you'll find that a tube socket fits in nicely. Pull out the tube pins, solder the wire leads to the tube, and epoxy everything in place. To avoid trying to run 4kV up the cable, glue the trigger coil in behind the tube socket; the leads from the SCR/capacitor can be light duty paired wire. The leads to the flashtube should be fairly heavy (at least 18 ga.), particularly if the extension cable is lengthy.

Also shown in the photo is a focussing spotlight we found at a store-window decoration supplier. It originally had a 150W miniature bulb on a platform that slid for focussing. I removed the original wiring and epoxied a tube socket and coil in its place, giving us a photoflash that focuses from a tiny spot to a wide flood. Next I have to add a small modelling lamp, probably a 115VAC halogen projector lamp.

Use

If you make the softbox version, you'll have the equipment to make first-rate still photos. The worst way to do a product shot or portrait is on-camera direct flash. Despite all the hoopla about intelligent flashes and so on, there's no getting away from the fact that on-camera flash wipes out surface detail and depth by flooding light along the camera axis (detail and

depth are created by shadows). Put the flash above and to the side, and you'll create a much better illusion of 3-D; soften the shadows with the softbox and you're on your way to super quality.

The exposure is something of a guess because of so many variables: power, tubes, method of mounting, etc. However, a starting point for the softbox is a guide number of 60 with ISO 100 and two 50W/S tubes; this will get you started. Divide the distance in feet into 60 to get the aperture. Make a series of test shots at various apertures to find the optimum. know it isn't much of a guide number, even when you compare it to tiny portables, but a lot of light is lost in the softbox itself; you're also covering a much larger area than the little portables.

The bare-bulb effect is the easiest: just mount the tube or tubes in a tube socket or utility box or what have you. The light obtained is a combination of direct and bounce, and it's ideal for photos of entire rooms. Whatever mounting method you use, put sleeving on any exposed tube leads to prevent shocks.

If the tube (or tubes) are in a clip-on reflector, you'll get the equivalent of a powerful floodlight. The guide number for a single tube at 50W/S and ISO 100 is

The flash duration can only be guessed at because I don't have proper specs for the tubes, something typical of a large number of Radio Shack components. The duration depends on the capacitance, the voltage at which the tube stops conducting, and the effective resistance of the tube during conduction. A typical value for small tubes is 1 millisecond, and ever if it strays considerably from this, the flash is short enough for sharp photos even if the subject is moving. Perhaps one of our readers might accept the challenge of designing a flash-duration measure-ment method. Photograph a fan blade rotating at a known speed? Use a photocell in place of the film? If you come up with a working method, let us know and we'll mention it in a future issue.

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Modem7. Allows you to communicate with any CP/M based system and download files. Complete details were in Computing Now! November 1983.

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LU. Every CP/M file takes up unnecessary overhead. If you want to store lots of data in a small space, you'll want LU, the library utility. It permits any number of Individual files to be stored in one big file and cracked apart again.

RACQUEL. Everyone should have one printer picture in their disk collection.

MORTGAGE. This is a very fancy mortgage amortization program which will produce a variety of amortization tables.

NSBASIC. Large disk BASIC packages, such as MBASIC, are great... and very expensive. This one, however, is free... and every bit as powerful as many commercial programs. It's compatible with North Star BASIC, so you'll have no problem finding a manual for it.

Z80ASM. This is a complete assembler package which uses true Zilog Z80 mnemonics. It has a rich vocabulary of pseudo-ops and will allow you to use the full power of your Z80 based machine... much of which can't be handled by ASM or MAC

VFILE Easily the ultimate disk utility. VFILE shows you a full screen presentation of what's on your disk and allows you to mass move and delete files using a two-dimensional cursor. It has heaps of features, a built-in help file and works extremely fast.

ROMAN. This is a silly little program which figures out Roman numerals for you. However, silly programs are so much

CATCHUM. If you like the fast pace and incredible realism of Pacman, you'll go quietly insane over Catchum... which plays basically the same game using ASCII characters. Watch little "C's" gobble periods while you try to avoid the deadly "A's"... It's a scream.

> Order as AFS #2 and specify system

OIL. This is an interesting simulation of the workings of the oil industry. It can be approached as either a game or a fairly sophisticated model.

CHESS. This program really does play a mean game of chess, it has an on-screen display of the board, a choice of colours and selectable levels of look ahead.

DEBUG. The DDT debugger is good but this offers heaps of facilities that DDT can't and does symbolic debugging... it's almost like being able to step, trace and disassemble through your source listing.

DU87, The older DUU program does have some limitations. This version overcomes them all and adds some valuable capacities. It will adapt itself to any system. You can search, map and dump disk sectors or files. It's invaluable in recovering damaged files, too.

ELIZA. This classic program is a micro computer head shrinker... It runs under MBASIC, and, with very little imagination, vou will be able to believe that you are conversing with a real psychiatrist.

LADDER. This is... this program is weird. It's Donkey Kong in ASCII. It's fast, bizarre and good for hours of eye strain.

QUIKKEY. Programmable function keys allow you to hit one key to issue a multicharacter command. This tiny utility allows you to define as many functions as you want using infrequently used control codes and to change them at any time... even from within another program.

RESOURCE. While a debugger will allow you to disassemble small bits of code easily enough, only a true text based disassembler can take a COM file and make source out of it again. This is one of the best ones available.

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FORTH. This is a small FORTH in Microsoft BASIC. You can build on the primitives intregral with the language.

LIFE. An implementation of the classic ecology game written in 8088 assembler.

MAGDALEN This is another BASIC music program,

CASHACC. This is a fairly sophisticated cash acquisition and limited accounting package written in BASIC. It isn't exactly BPI, but it's a lot less expensive

DATAFILE. This is a simple data base manager written in... yes, trusty Microsoft BASIC

UNWS. Wordstar has this unusual propensity for setting the high order bits on some of the characters in the files it creates. Here's a utility to strip the bits and "unWordstar" the test. The assembler source for this one is provided.

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Almost Free PC Software

Volume Five

We've ventured once more into the phone lines, scouring the public domain for the cream of its software. Distilled from several megabytes of code, this disk represents the best of what's floating around on the bulletin boards of the continent. It wasn't easy, and a lot of disks bit the dust in the process of creating this collection.

Whether you are interested in business programs, games, hardcore hacking or just making your computer a more productive tool, you'll find something of interest on this disk.

AREACODE is a useful tool if you use the telephone a lot. Give it an area code and it will tell you what city it corresponds to.

 \mathbf{D} is another sorted directory program. However, this one emulates the CP/M style \mathbf{D} , which is arguably a lot more useful for most applications.

FRACTALS This is an amazing implementation of the Mandelbrot microscope, generating unearthly fractal images on the tube of your system. Mere words fail to describe them.

FROGGER is an implementation of the classic arcade game. Just try not to get the highway littered with frog guts.

HIDE is a package of utilities which allow you to create, enter and remove invisible DOS directories. This allows you to set up a hard drive system with areas that are only available to users that know about them.

LAR This library utility allows you to concatenate several small files into a library to save on disk overhead and then extract the individual files when you need them. It saves a lot of space when it's used with files you don't use often.

MAIL 1 is a mailing label utility in BASIC.

MORERAM This is an assembler program . . . you need MASM and LINK to make it work. It lets you do a number of things to the memory settings on your motherboard, including using more than 640 K and allowing for four flopples to facilitate RAM disks. It will also allow you to set the switch settings of your motherboard for 64 K so things will boot up quicker and then change the RAM setting after booting.

MORTGAGE generates amortization charts. Read 'em and weep.

MXSET lets you control the parameters of Epson compatible printers from the command line. It's a lot easier than LPRINTing characters from BASIC every time you want to change modes.

 ${\bf NUSQ}$ unsqueezes files that have been previously compressed to save space. It's primarily of use to BBS types . . . but it's extremely small.

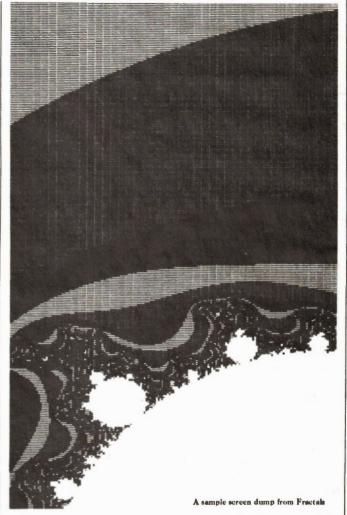
PARCHK This is an assembler program . . . you need MASM and LINK to get it going. It installs a trap for parity errors in your computer so that they don't hang your system and helps you locate where the funky RAM is.

PCBOSS This is a more user friendly working environment than is MS-DOS. It makes your whole system menu driven, with absolutely no command names to remember. If IBM were dead it would be rolling in its grave over this

VDEL This is a delete with verify program. You could type VDEL *.ASM and it would show you the name of every .ASM file in the current directory and ask you if you want it deleted.

WHEREIS finds files in a complex hard disk system.

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Impedance

Impedance is one of the first electronic terms the non-specialist encounters, yet it is one of the hardest to comprehend. An explanation.

By Nigel Cawthorne

THE WORD 'impedance' turns up all over the place. Microphones, loudspeakers, antennas, 'Inputs' and 'Outputs' are all described in terms of impedance, which is misspelled 'impedence' about half the time.

The symbol of impedance is the letter Z. Impedance is a very important concept in electronics and can cause much confusion. Why is one piece of coax described as 75 ohm and another as 50 ohm? Why is one microphone high impedance and another low?

To find the answers to these questions, we can start by revisiting that old friend of electronics, Mr. Ohm, and have another look at his law.

Ohm's Law

Before solving the mysteries of impedance it is worth just making sure that we know what happens when different DC voltages are applied across a resistor. In summary, Ohm's Law says that the current (I) flowing through a resistor (R) is proportional to the voltage (V) applied across it. Fig. 1 illustrates this with a 12V battery and two different values of resistor.

Ohm's Law applies to DC voltages and currents. 'Direct' currents are drawn from sources such as batteries where the '+' and '-' sides do not change. The voltage across the terminals of a 12V car battery is always in the same direction or 'polarity'.

Alternating Current

If there were only DC in the world, calculations in electronics would be much easier, but on the other hand, without AC most of electronics would not work anyway.

Alternating Current (AC) is the common currency in the electrical and electronics world (excuse the pun). The power line is AC, radio signals that leave transmitting aerials and travel through

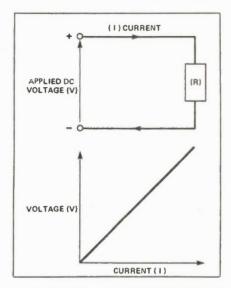


Fig. 1. Ohm's Law. a) With a 12V battery and 75R resistor. The current (I) is V/R = 12/75 amps = 12x1000/75 milliamps = 160 mA. (b) With a 240VDC supply and a 6k8 resistor. The current (I) is V/R = 240/6800 = 240 x 1000/6800 = 35.3 mA.

space are in the form of AC, the human voice when transformed into electrical voltages by a microphone is also in the form of AC. The output of a signal generator, whether it is a low frequency audio signal or a high frequency radio signal is in the form of AC.

AC is where the '+' and '-' sides are alternating (see Fig. 2). At one moment, one of the two wires will be positive with respect to the other, and just a very short moment later, the reverse will be true. The two wires will have changed polarity with respect to each other. The speed at which they change polarity is the 'frequency'.

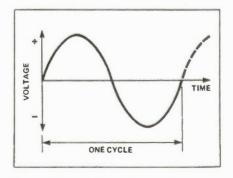


Fig. 2. The waveform of alternating current, which continually reverses its polarisation.

Frequency

Frequency is a measure of the number of 'cycles per second', or the number of times that the signal changes from one polarity to the other during a second. A frequency of one cycle per second is called a Hertz. The frequency of the power line in the UK and Europe is 50Hz (in some countries of the world, including the USA and Japan, the power line is at 60Hz). That is why many pieces of electrical equipment which are designed to work in all countries of the world will be described as being suitable for a 50-60Hz power line. The voltage in one country may not be the same as in another, but that is another matter.

Although voltages may differ from one country to another, there are several types of equipment that depend on the power line frequency. Among these are turntables and electric clocks, both of which depend on the power line for their accuracy.

Radio signals which are also in the form of AC change polarity not at tens of cycles per second as does the power line supply, but at millions (MHz) or billions (GHz) of times per second.

Ohm's Law Again.

Now back to Mr. Ohm and his law. Fig. 3 shows an alternating voltage being applied across the same resistance as in Fig. 1. The only difference this time is that voltage is alternating and changing polarity. As you would expect when the voltage varies, the current also varies in a similar way. The voltage and current keep in step with each other. As the voltage increases in one direction, so the current also increases. Ohm's Law is still holding true even though the voltage is not staying still.

In fact Ohm's Law would not be much use as a 'law' if it didn't hold true even when things were moving.

With a resistance and an AC voltage source nothing has changed from the DC voltage example in Fig. 1, other than the voltage source itself which is now swinging from one way to the other and back again rather than staying still.

Impedance can be defined as the Alternating Current (AC) equivalent of DC 'resistance'. The example in Fig. 3 has illustrated that for a pure resistance, the impedance is the same as the resistance. It is only when components other than

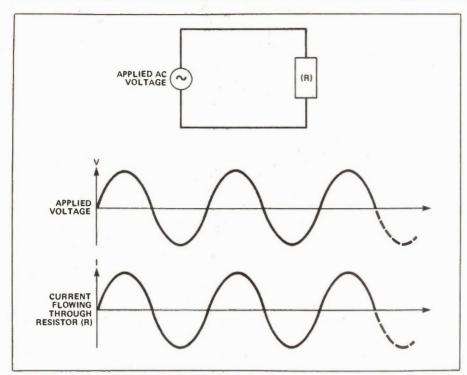


Fig. 3. When alternating voltage is applied through a reistor, the current varies with the voltage just as it does with direct current.

resistors are introduced that the definition of impedance begins to get more complicated.

Coils and Capacitors

Fig. 4(a) shows a simple capacitor circuit. When a DC voltage is applied across a capacitor a current will flow initially until the capacitor is fully charged up, i.e.,, until the voltage across the plates of the capacitor equals the voltage that is being applied to it from the battery. Once that state is achieved, which for a normal sized capacitor would be in a very short space of time (say a few milliseconds) nothing else happens after the voltage is applied.

Once the capacitor is charged up, there will be no further flow of current and the circuit will be static (this assumes for the moment that the capacitor is pure, i.e., that it has no leakage resistance). In practice all capacitors have some leakage resistance, which may be very high (thousands of millions of ohms perhaps), but this is ignored for the purposes of our discussion, which is aimed at getting a clear idea of what 'impedance' is all about.

Coils

In a similar way to a capacitor, a coil also reaches a steady state soon after a DC voltage has been applied. With a theoretical coil, however, the steady state is a current flowing through the coil, which will depend only on the 'source resistance' of the voltage supply. Where the source resistance is zero (impossible in practice) the current through the coil

case of the coil) reach a steady state. Other than disconnecting the supply nothing further will change.

This is where Alternating Current comes in again, because with AC, the voltages and currents are changing all the time. That is why both coils and capacitors act very differently under the influence of AC and DC. With DC they both reach a steady state where nothing further happens, but with AC where the voltages and currents are changing all the time, both these components start to behave in more complicated ways. Without going into the mathematics involved, we shall look at the way these two components, coils and capacitors, behave when AC is applied. It is an important part of coming to grips with the real meaning of impedance.

Reactance

Before getting to fully understand impedance, we have to grapple with another of those 'nasties' of the electronics world: reactance.

Whenever 'Reactance' appears in electronics textbooks it is usually tied in with a lot of mathematical hieroglyphics and symbols. To understand reactance

Fig. 4 Shows four simple circuits, (a) with DC voltage across a capacitor, (b) with DC current across a coll (c) with AC voltage across a capacitor and (d) with AC voltage across a coil. See the text for a full explanation.

would be infinite. For coils it is more usual to consider a 'constant current' source being applied across them rather than a constant voltage as in the case of the capacitor.

The idea of a 'constant current' source is always hard to imagine. We can easily understand the idea of a constant voltage source such as a well charged battery, but a 'constant current' source is not so easy to swallow. Fig. 4 (b) shows a simple coil circuit being fed from a constant current source.

Both the coil and the capacitor will under influence of DC supplies (voltage in the case of the capacitor and current in the let's go back to our simple circuits in Fig. 4. This time, instead of applying DC to our simple coil and capacitor, we are now going to apply AC.

Capacitors with AC

Fig. 4c shows a capacitor connected to an AC source such as a signal generator. There are two things that can be varied on an AC source: the voltage and the frequency. What happens if the frequency of the applied voltage is varied?

As we saw with the DC applied to the capacitor, once the capacitor was charged up, nothing else happened. Because AC implies that the voltage applied across the

capacitor is continually being reversed in polarity, the capacitor will be charged in one direction and then discharged as the voltage is reversed. The way that the capacitor reacts to this will depend on the frequency of the AC. If the frequency is relatively low, the capacitor will have time to fully charge in one direction, before being discharged as the voltage changes polarity.

However if the frequency of the applied AC source is high, the capacitor will never be able to fully charge nor discharge, because the applied voltage will keep changing direction before it gets a chance to do so.

A capacitor will behave in different ways depending on the frequency of the AC that is applied to it. Electrically the way that a capacitor reacts to different applied frequencies of AC is called 'reactance'. For a capacitor, the higher the frequency of the applied AC, the lower the reactance.

This makes sense when you consider that the lowest possible frequency of AC is 0Hz, which of course is DC. At a frequency of 0Hz the reactance of a capacitor is infinite, which ties in with our previous experiment with the capacitor and the DC voltage source, i.e., that no current passes through the capacitor once it has charged up. At higher frequencies the reactance of the capacitor is less and correspondingly more current passes through it.

Coils With AC

A similar but opposite argument applies to a coil. For a coil, the reactance is proportional to the frequency. At the lower frequencies the reactance of the coils is low, as shown by our experiment with DC which showed that a high current would pass through a coil at DC (which is 0Hz AC).

As the frequency increases the reactance of the coil increases. The exact relationship between reactance (X), frequency (F) and coil (L) and capacitor (C) size is shown in Fig. 4. These relationships include the expression 'omega' which is 2 x frequency. The purpose of this article is to describe the meaning of impedance without resorting to the use of any complex mathematics. We shall leave aside the fuller mathematical implications of reactance because these are not necessary for a basic understanding of what impedance is all about.

Impedance

In a nutshell, the impedance of a circuit or component is the combined effect of its resistance and its reactance. Unfortunately we cannot just add these two figures together to get the right answer. They have to be added together using another of those nasties: vectors.

The impedance of a resistance is the same as its resistance, but as soon as inductance or capacitance are added to the circuit, these will add components of reactance which have to be combined with the resistance to give the impedance.

Impedance thus has two parts to it. The non-reactive part which does not change with frequency and the reactive part (coils and capacitors) which does change with frequency.

Incidentally if you are into vector addition, the impedance is the vector sum (components at right angles) of reactance and resistance. See Fig. 5.

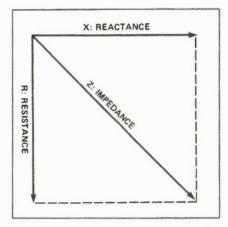


Fig. 5. A vector graph of the relationships between impedance, resistance and reactance.

Coaxial Cable

Let's take a practical example and see if we can use it to explain impedance more fully. Coaxial cable is a very good example. Coax is described as having an impedance (typically 75 ohms or sometimes 50 ohms). What does this mean?

Fig. 6a shows a straight piece of coax cable with its inner conductor connected to one side of a battery and its outer shield connected to the other side. The far end of the coax is connected to a voltmeter. An ammeter is connected in series with the input. What happens when the battery is connected? Well, absolutely nothing.

The voltmeter at the far end will show the same voltage as across the terminals of the battery and the ammeter in series will read zero.

Because of the insulation between the inner and outer conductors of the coax, no current at all will flow across the coax. So what is happening and why on earth is the coax called '75 ohm', or whatever? Where is the 75 ohms? It looks like an open circuit, because no current is flowing at all. To get the answer we have to apply AC.

Coax with AC

Ohm's Law showed us that if we had a 12 volt battery and a 75 ohm resistor, we would get a current of 160mA. So what has happened to our piece of coax? Where are the 75 ohms gone?

The answer is that for a DC voltage

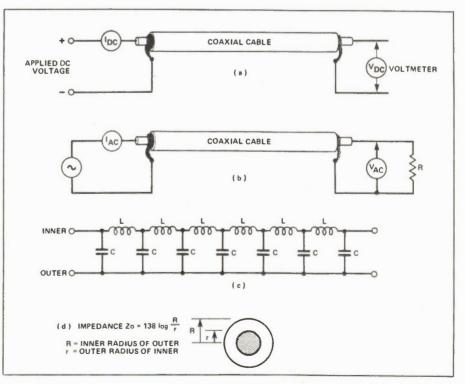


Fig. 6. The behaviour of coaxial cable (a) with DC (b) with AC and (c) how the length of the cable 'looks' to an electrical current. 6d gives a graphic illustration of the impedance of the cable related to its dimensions (see overleaf).

coaxial cable is not 75 ohms. The actual resistance measured across the end of a non-terminated piece of coaxial cable will depend on the quality of the insulator. Normally no current at all will flow through the insulator, which is confirmed by our simple experiment at DC.

The mystery of the missing '75 ohms' is solved by doing two things. Firstly an AC voltage is applied at one end of the coax cable and secondly, the other end is terminated with a resistor of the same value as the nominal impedance i.e., 75 ohms. See Fig. 6b.

Coax Analysed

Inside the coax itself, there is capacitance between the inner and the outer conductors. This 'capacitor' within the coax is cylindrical in form, and runs the whole length of the cable. Although not strictly speaking in the shape of a coil, the coax does have a measure of inductance. All wires, even those running in straight lines, have some degree of inductance. This also applies to coax cable.

The mechanical structure of the coax is constant all along its length. As shown in Fig. 6c, coaxial cable can be considered electrically as being the equivalent of a whole series of tiny capacitors and coils in series one behind the other. The capacitance and inductance values of these distributed components run along

the whole length of the coax.

So what AC 'sees' when it is applied to coax is not an open-circuit as DC does, but a component made up of countless small capacitors and inductors. That is why AC behaves very differently when applied to a piece of coax than does DC.

It can be shown that the impedance of coaxial cable is related only to the 'inner diameter of the outer conductor' and the 'outer diameter of the inner conductor'. Fig. 6d gives the exact definition.

The impedance of a particular design of coax cable is independent of the frequency being fed to it. At all frequencies the impedance of the coax will be the same. For most other circuitry in electronics this is not true. The impedance of a circuit or component (unless it is purely resistive, in which case it has no reactance anyway) will normally vary with frequency. This is why for products such as microphones, headphones or antennas, the impedance should be defined at a specific frequency or across a band of frequencies.

Resonance

No discussion of impedance is complete without introducing the idea of resonance. If we take the simple coil and capacitor circuit in Fig. 7, we can use it to demonstrate resonance. Resonance is said to occur when the reactance of the capacitor and the inductance are equal and opposite and cancel out. As the frequency applied to the circuit is increased the reactances in the two components will change. The graph in Fig. 7 shows that at

VARIABLE **FREQUENCY AC SOURCE** (f) INDUCTIVE REACTANCE **INCREASE IN** FREQUENCY (f) CAPACITIVE

Fig. 7. A simple coil and capacitor circuit demonstrating resonance. Resonance occurs when the induction and capacitance combine to cancel each other out at a certain frequency. The principle is used in tuning radio receivers.

one frequency the reactances of the two components will add up to zero. This is where the reactance line crosses the horizontal axis.

This resonance effect is used in many electronic circuits. It is used for 'tuning in' receivers. By using a variable capacitor rather than a fixed one, the resonant frequency of the tuned circuit can be varied and different stations tuned in.

Impedance Varies

In summary we can say that impedance is a way of describing the 'resistance' offered to an alternating current applied to a circuit or component. It is important to note that for individual components such as circuits containing coils and capacitors, aerials, microphones or even loudspeakers that the impedance will vary with frequen-

Impedance comes about because circuit components which are inductive or capacitive or which 'look like' coils or capacitors behave differently at different frequencies: their reactance changes. The impedance of a circuit is the combination of the inductive or capacitive reactance with the resistance in the circuit. Because the reactance of coils and capacitors is different at different frequencies, the impedance of the circuit changes with frequency too.

Coax cable is an exception. Because of the 'distributed' nature of the inductance and capacitance along the length of the cable, the impedance will be independent of frequency. It has been shown that the impedance of coaxial cable is made up of purely reactive elements (i.e., the effects of the imaginary coils and capacitor that run all along the length of the cable).

Mystery Solved

For many years in his early days in electronics, the writer could never understand why coax cable was described as 75 ohms, when nowhere with a multimeter could

this Fig. be measured.

Without having gone too deeply into the morass of complex numbers and calculus that can so easily surround any discussion that gets near to what in the text books is termed 'AC theory', it is hoped that the above explanation will go some way to explaining the apparent mystery of the 'ohms' in coax cable as well as outlining the meaning of 'impedance' in electronics.

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Digital IC

ELECTRONIC THEORY

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covered by the earlier books is assumed. BOOK 1. This book contains all the fundamental theory necessary to lead to a full understanding of the simple electronic circuit and its main components

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PROJECTS

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F.G. RAYER, T.Eng. (CE). Associates

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Furthermore, a number of projects have been arranged

Furthermore, a number of projects have been arranged so that they can be constructed without any need for soldering and, thus, avoid the need for a soldering iron.

Also, many of the later projects can be built along the lines as those in the "No Soldering" section so this may considerably increase the scope of projects which the newcomer can build and use

BP37: 50 PROJECTS USING RELAYS.

scene.

F.G.RAYER, T.Eng.(CEI), Assoc.IERE

F.G.RAYER, T.Eng.(CEI).Assoc.IERE
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Although one of the more recent branches of amateur electronics, electronic music has now become extremely popular and there are many projects which fall into this category. The purpose of this book is to provide the constructor with a number of practical circuits for the less complex items of electronic music equipment, including such things as a Fuzz Box, Waa-Waa Pedal, Sustain Unit, Reverberation and Phaser-Units, Tremelo Generator etc.

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R.A. PENFOLD
We have all built circuits from magazines and books only to find that they did not work correctly, or at all, when first switched on. The aim of this book is to help the reader overcome just these problems by indicating how and where to start looking for many of the common faults that can occur when building up projects.

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R.A.PENFOLD
There is now a vast range of ICs available to the amateur market, the majority of which are not necessarily designed for use in a single application and can offer unlimited possibilities. All the projects contained in this book are simple to construct and are based on a single IC. A few projects employ one or two transistors in addition to an IC but in most cases the IC is the only active device used.

223: 50 PROJECTS USING IC CA3130

RA.PENFOLD

In this book, the author has designed and developed a number of interesting and useful projects which are divided into five general categories: I — Audio Projects II — R.F. Projects III — Test Equipment IV — Household Projects V Miscellaneous Projects.

BP117: PRACTICAL ELECTRONIC BUILDING BLOCKS

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This book is designed to aid electronics enthusiasts who

ins book is designed to aid electronics entrusiasts who like to experiment with circuits and produce their own projects rather than simply follow published project designs. The circuits for a number of useful building blocks are included in this book. Where relevant, details of how to change the parameters of each circuit are given so that they can easily be modified to suit individual requirements.

BP102: THE 6809 COMPANION

Written for machine language programmers who want to expand their knowledge of microprocessors. Outlines history, architecture, addressing modes, and the instruction set of the 6809 microprocessor. The book also covers such topics as converting programs from the 6800, program style, and specifics of 6809 hardware and software availability.

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BP24: 50 PROIECTS USING IC741

RUDI & UWE REDMER
This book, originally published in Germany by TOPP, has achieved phenomenal sales on the Continent and Babani decided, in view of the fact that the integrated circuit used in this book is inexpensive to buy, to make this unique book available to the English speaking reader. Translated from the original German with copious notes, data and circuitry, a "must" for everyone whatever their interest in electronics.

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In this book, R.A. Penfold has designed and developed several modern solid state short wave receiver circuits that will give a fairly high level of performance, despite the fact that they use only relatively few and inexpensive com-

BP91: AN INTRODUCTION TO RADIO DXing This book is divided into two main sections one to amateur band reception, the other to broadcast bands. Advice is given to suitable equipment and techniques. A number of related

BP105: AERIAL PROJECTS

constructional projects are described

R.A. PENFOLD
The subject of aerials is vast but in this book the author has considered practical designs including active, loop and fer-rite aerials, which give good performances and are reasonably simple and inexpensive to build. The complex theory and math of aerial design are avoided.

OTHER PUBLISHERS

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T. HARTNELL & M. RAMSHAW (1983)
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D/A TAB No. 1271 A useful handbook for computerists interested in using their machine in linear applications. Topics discussed include voltage references, op-amps for data conversion, analogue switching and multiplexing and more.

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TAB No. 1200 An excellent reference or how-to manual on building your own microcomputer. All aspects of hardware and software are developed as well as many practical circuits.

PH180: 1984 CANADIAN BUSINESS GUIDE TO MICRO-COMPUTERS K. DORRICOTT

Written by the managing director of Deloitte, Haskins & Sells, a Canadian partnership of public accountants and other professional advisors to management, this book is one of the most complete comprehensive guides to microcom-puters available. Starting with a general overview of microcomputers and their business applications, the author helps you assess your computer needs, compares and evaluates computer systems and application packages, and gives you tips on "doing it right". A must for anyone thinking of purchasing a microcomputer for business.

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Flankandan Zadan Maria

Computing Today: Data Encryption

A look at the techniques used in protecting data from unauthorized interference.

By Paul Chappell

CODES and ciphers have been used throughout history to protect information. Julius Caesar is reputed to have used a simple shift cipher, well known to all schoolboys, in which the original message (plaintext in the jargon) is enciphered by replacing each letter with the next letter of the alphabet. This is a shift of one; the next-but-one letter would be a shift of two, and so on.

There is a difference between a code and a cipher, by the way. Suppose I agree with you that the word 'fusebox' means 'meet me outside the newsstand' and 'fish' means 'I've lost my copy of Electronics Today'. To make use of this form of communication I might write you a letter and contrive, in the most natural way possible, to include a sentence like 'There is a fish in my fusebox'. This is a code.

A cipher, on the other hand, is a general purpose method of substituting new symbols for those used to express the original message. The shift cipher is one example, morse code (morse cipher?) is

Codes have the advantage of being impossible to break given only the coded message. As any word can quite legitimately be used to represent any other word or phrase, there is no way for anyone without extra information to get any leverage. The drawback of codes is that they can only be used to convey messages for which code words have previously been arranged. If I wanted to say 'meet me outside the store' there is no way to modify 'fusebox' to convey this new message.

Ciphers, with a few exceptions, can be cracked given sufficient time and computing power but have the advantage of being able to convey any message at all.

For electronic data communication we are concerned almost exclusively with information that can be represented in binary form. The preceding discussion applies equally well to this; a code may involve agreeing that 010 will mean 1101011100, a cipher may say that every third '1' in the plaintext message is changed to '0' and every second '0' is changed to '1'. The general term for scrambling a message is encryption, and unscrambling is called decryption.



Requirements

Before leaping into a description of current data encryption techniques, it would be as well to take a look at what we'd like them to do.

First of all, it goes without saying that we'd like the system to be secure; nobody should be able to make sense of any message they intercept, nor should they be able to inject their own message into the system and have it accepted as genuine. Oddly enough, it is not difficult to make a completely secure system if this is the only requirement.

An example of such a system is the one-time pad. Both the sender and the receiver have identical lists (or pads) of random numbers. The encryption process proceeds in the same way as the shift cipher, except that the number of letters you shift by is taken from the number on the pad.

The drawback of this system is that it is extremely cumbersome to use. A pad of numbers at least as long as the total number of characters in all the messages must be prepared and conveyed in some safe way to the intended recipient, and must be closely guarded against prying eyes. The encryption process can be automated but will not be easy to use because somebody must feed in the numbers as well as the text to be sent. The system is, in fact, in daily use for communication between governments and military commanders, but is not suitable for general use.

If we can't have an unconditionally secure system we must content ourselves with a computationally secure one. The idea is that, although the system can be broken with unlimited time and computing power, the effort and time involved would be so immense that it is for all practical purposes perfectly secure. Here we hit another little problem: how can we tell how much computation a cryptanalyst will be faced with?

I'll come back to this later on with reference to specific systems, but I should mention that it's not enough just to calculate the astronomic number of permutations that would have to be investigated on a trial and error basis. Time after time, systems that were judged to be secure in this way have been broken by the discovery of a mathematical pattern in the cipher that the designer was not aware of.

It should be possible to enter the message and have it conveyed safely to the intended recipient without having to take any further part in the encryption process or observe any special precautions. It may also be necessary to use the system to communicate with a number of different people, so standardization of the hard-

ware is essential.

Now, if the hardware is to be standard and readily available, any kind of encryption that involves performing some fixed process on the plaintext will not be secure. Transposing characters, substituting one symbol for another and similar procedures, however complicated, just won't do. Anyone intercepting the

ting one symbol for another and similar procedures, however complicated, just won't do. Anyone intercepting the ciphertext can also buy the hardware and use it to obtain the original message.

The way around this is to standardize the hardware but allow it to be personalized by means of a key, in the form of a string of digits chosen by the user which controls the way the message is encrypted. The point is that anyone intercepting the message will also need to know the key to make any sense of it.

Finally, the possibility of adding a 'signature' to the message would be useful. It would allow the system to be used for transmitting contracts, for instance, and in general would allow verification that the sender is who he claims to be.

Private Key Encryption

In this system the encryption algorithm is standardized and security depends on the use of a private key. The following example demonstrates how it works.

My algorithm is simple: modulo 2 addition. This is built into my encryption machine and I make no attempt to keep it secret. What I do keep secret is my key: 10010110. Let's suppose I want to send a message to the Editor explaining why this feature is a month late. The first thing I must do is to send him my key by courier. Then I type in my message 'I overslept', whereupon my encryption machine generates the corresponding ASCII codes (ciphers?):

1001001 0100000 1001111 1010110 1000101....

I E My private key is added modulo 2 to each group of 8 bits: plaintext

10010010000010111100101101011010-001..... kev

10010110100101101001011010010110-100..... ciphertext

000001001001110101011110111001100-101.....

The editor's decrypting machine uses the same key and also applies modulo 2 addition to give the plaintext version of my message.

A more realistic system would involve using a much longer key. The key itself would not be added to the message but would be used to generate pseudo-random sequences of bits to be added.

This type of cipher is known as a stream cipher because it operates on each bit of the data stream individually. Another type that is widely used is the block cipher where chunks of data, or even the entire message, are altered as a whole by a combinatorial process controlled by the key.

By far the most common private key system is DES, the Data Encryption Standard adopted in 1977 by the American National Bureau of Standards. The key used in this system is 56 bits long. ICs incorporating the DES algorithm are available from a number of manufac-

The Data Encryption Standard

A DES IC will operate on a 64-bit input block to produce a ciphertext block of the same length. The easiest way to use one is to split the plaintext message into 64-bit blocks and encrypt each block individually. This is known as an Electronic Code Book implementation. It is suitable for use with data to be stored on disk which would be dealt with in blocks anyway. For general purposes, however, it violates a golden rule of security; never encrypt data in the same way twice, as this makes the system easier to break.

With a little extra hardware a Chain Block system can be made. The first data block of 64 bits is added modulo 2 to an Initial Vector, which is just an impressive name for a 64-bit long string of 1's and 0's. The vector for the first block to be encrypted will probably be supplied by the hardware; the person who is to receive the message must also have the vector. The result of the modulo 2 addition is then encrypted by the DES algorithm.

When the next block of 64 bits is read in, it is added modulo 2 to the encrypted version of the previous block and the result is then processed by the DES algorithm. The encrypted version of each block thus becomes an initial vector for the subsequent block.

An example will make the process a little clearer. For this we will use the ET miniDES algorithm which, in the form of a look-up table, goes like this:

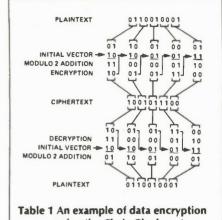
	Key=0	Key=1
Input	Output	Output
00	01	10
01	11	00
10	00	11
11	10	01

You will see that miniDES operates on two bit data blocks and has a single bit key. For the purposes of the example we'll assume that a key of 0 has been chosen, so it's the first two columns of the table we're concerned with.

Let's suppose our plaintext data is 0110010001. The Electronic Code Book encryption of this will be 1100110111. Notice that whenever the plaintext block 01 is enciphered, it always results in 11.

Now we'll chose an initial vector for the Chain Block system, let's say 10. The encryption process will now proceed as shown in Table 1.

Notice that the plaintext block 01 is encrypted a different way on each occurrence: first as 10, then 01 and finally 00. Another feature of this arrangement is



using the Chain Block system.

that errors in any block will carry through into all subsequent blocks (perhaps not in this simplified system, but they certainly do with the real DES algorithm). This prevents any tampering with the encrypted message. For instance if someone attempts to alter 'delete file 14' to 'delete file 15', the entire message will be corrupted.

Other possible implementations are Cipher Feedback and Output Feedback. Output Feedback in effect uses DES as a random number generator for a stream cipher. Cipher Feedback is similar, but the random numbers generated are 8-bits long and are added modulo 2 to 8-bit blocks from computer peripherals, etc.

Public Key Encryption

The main drawback of the private key system is the inconvenience of having to distribute the key to everyone you wish to communicate with. As the key should be changed from time to time, ideally after each message, and as multiple encryption with different keys is often used for additional security, the question of key distribution can become quite a headache.

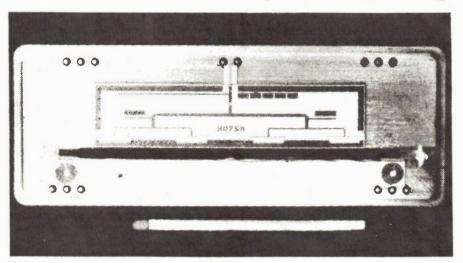
Just imagine that a system could be developed which would allow any user to send this key quite openly to another, or even publish it in a public directory. It seems impossible at first sight; armed with your key and the method you've used to encrypt your data, even an amateur should be able to decipher any messages vou send. Surprisingly enough, it is possible. The theoretical method was published in the mid-70s and the basis of a practical implementation shortly afterwards. The process described here is the Rivest-Shamir-Aldeman system, named after its inventors.

The basis of the system is a mathematical trick known as a one-way function. Many arithmetic processes are more difficult to perform in one direction than the other. For instance, it is generally more difficult to find square roots than it is to find the square of a number. For some mathematical functions the difficulty of reversing them is so immense as to be virtually impossible, or at least unfeasible

Surface Acoustic Waves

Acoustic waves sent and received over a piezoelectric crystal make nearly ideal filters.

By Andrew Armstrong



FILTERS, RESONATORS and delay lines are essential elements in a wide range of electronic equipment and a great deal of time and effort has been devoted to improving them, either by perfecting existing techniques or developing new ones. Filters and resonators have traditionally relied upon frequency-selective properties of reactive networks and much has been written about the best ways of combining reactive components (capacitors, inductors and crystals) to obtain the desired results. Delays can be produced by passing the signal from a transmitting transducer to a receiving transducer and placing between them some substance which slows down the passage of the wave. Examples range from the glass block and piezoelectric transducers used in television receivers to the coil-spring and magnetic transducers used in some audio effects units.

These effects remained largely unchanged until the development of the field effect transistor (FET) with its excellent switching characteristics, and the introduction of integrated circuitry which made it possible to build circuits of previously unimaginable complexity. The devices which resulted were the switched capacitor filter and the bucket brigade delay line, or charge coupled device (CCD), both of which use linear circuit elements controlled by electronic switching. The advantages in each case include greater flexibility; the break frequency of the filter and the delay period of the bucket brigade device can both be

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altered simply by changing the frequency of the external clock.

The development of the complex digital integrated circuits has taken this flexibility several stages further. It is now possible to convert an analogue signal into digital form and then carry out a wide range of filtering operations, if necessary under software control, so that the parameters can be readily changed to meet specific requirements. Delay can be obtained simply by using readily available memory chips, allowing longer periods to be achieved without attendant degradation of the signal. Unlike linear systems, in which phase and frequency are heavily interdependant and it is not easy to design for one without affecting the other, digital filtering allows many different parameters to be specified to a high level of accuracy.

The last few years have seen the emergence of another new signal processing technology which combines many of the advantages of linear and digital techniques. Surface Acoustic Wave (SAW) devices use a tiny transducer to launch a wave across the surface of a piezoelectric crystal and another transducer to pick the wave up again. In this respect they are not unlike conventional glass delay lines, but the crucial difference is that the behaviour of the wave can be modified by use of complex transducers which work, in effect, as multiple taps on the delay line. The result is a filter whose characteristics can be controlled almost as well as can those of a digital system but which can be produced at a fraction of the cost.

The Nature Of The Wave

Before going any further, we'll look at what a surface acoustic wave actually is and how they are generated.

The easiest way to understand what is involved is to consider a very simple crystal structure; a cubic lattice with identical atoms at each vertex. An ordinary wave set up in the structure simply involves compression and rarefaction of the structure of the atoms. To put it crudely, one could imagine the little model atoms bouncing back and forth on their springs. The atoms are stiffly supported, being anchored by six bonds. This type of wave is called a bulk wave, and is of the type generated in conventional glass delay lines.

A surface wave involves atoms on the surface swinging about in an arc rather than a straight line. Atoms on a face of the crystal are less firmly anchored, with only five bonds attached, so the propagation speed of a surface wave is lower than that of a bulk wave. This means that a surface wave is confined to the surface, and does not tend to excite bulk waves. It is possible to launch bulk waves when trying to launch surface waves, and vice-versa, but they are different entities and there is limited interchange between the two. The rate of propagation of these acoustic waves is very much less than that of electromagnetic waves, so a relatively long delay line may be made in a small space.

Surface acoustic wave devices are made on crystals of particular types, the main ones being quartz, lithium niobate, and lithium tantalate, all of which are piezoelectric crystals. This is essential, because the transducers which launch the wave consist of interleaved strips or fingers of metalization which are energized with an electrical signal to excite a piezoelectric response and therefore launch a wave.

Figure 1 shows the layout of a typical SAW filter. The transducers are sym-

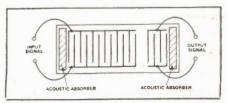


Fig. 1 The layout of a typical SAW filter. The two transducers consists of a number of fingers of metalization on the surface of the crystal.

metrical, so acoustic waves are radiated in both directions. The acoustic absorber on the far side is there to prevent the reverse wave from either propagating around the crystal or being reflected from the edge. Either event would interfere mightily with the intended function of the device. Similar reasoning applies to the presence of the absorber behind the receiving transducer.

In this simple example, the waves launched by each pair of fingers in the transducer may either reinforce or cancel the waves from other pairs, depending on the acoustic wavelength. The even spacing of the fingers shown would give rise to a flat-topped filter response.

LC filters can provide a good approximation to any reasonable amplitude response characteristic, but the resulting phase response is likely to be less than ideal. An enormous complexity is required to achieve a particular phase response as well, and it is normally very impractical. A SAW filter, however, can meet this kind of requirement because phase and amplitude response may be determined separately.

The example in Figure 1 shows a transducer which has evenly spaced, fully overlapping fingers. The spacing may be varied to adjust the phase response, and the degree of overlap may be varied to control the intensity of the wave. Figure 2 shows a Sin(x)/x transducer which will give a bandpass response showing some resemblance to the square response often found in textbooks. Real transducer patterns have many more fingers, of course.



Fig. 2 Simplified view of a transducer arranged to give a sin(x)/X characteristic.

The method used to design SAW filters is clearly unlike that used for LC filters. SAW filters are normally of a type described as transversal filters, which is a fancy way to describe the process of producing a desired output by adding many signals tapped from a delay line. CCD filters also work on this principle, and another technology which lends itself to this method is digital filtering.

It is interesting to compare digital and SAW devices when used for signal processing rather than filtering. In digital terms, the accuracy of a SAW device would be perhaps five bits, but the processing speed is two orders of magnitude faster than that of any digital IC currently in use or under development. There are applications which use digital and SAW technologies together to give the best of both worlds, but such applications are a specialized topic on their own.

Real Devices

So far all I've said about SAW devices sounds almost too good to be true, but there are some drawbacks. A look at some device configurations will help us understand the problems and their effects on the functioning of SAW devices.

One annoying phenomenon is signal coupling via bulk waves. While the method of excitation is designed to produce surface waves, bulk waves can be launched at frequencies outside the passband. If these waves reach the receiving transducer, a response out of the normal passband will be generated. The angle between the direction of the propagation of these waves and the plane of the transducers affects things in much the same way as coloured light is split by a diffraction grating. This effect is illustrated in Figure 3.



Fig. 3 Although the method of excitation is desired to produce surface waves, bulk waves are also generated and if launched at certain angles to the surface of the crystal they may reinforce and give rise to a response out of the normal passband of the filter.

To cause problems, the bulk wave launched by the transmitting transducer must be reflected back from the bottom face of the crystal to the receiving transducer. This effect can be minimized by roughening the bottom face of the crystal, making it as thin as possible, and coating the bottom with a sound-absorbing glue.

A far more effective measure is to offset the transducers and couple energy between them by means of a multistrip coupler. This idea is now widely used. In simple terms, the coupler acts as both a receiving transducer at one end and as a transmitting transducer at the other.

Another important advantage of this coupling technique is that both transmitting and receiving transducers may be weighted to give a particular response, a process known as apodising. If two apodised transducers are not offset but used directly in line, to work out the response one has to consider the effect of the wave pattern launched by each pair of fingers in the transmitting transducer as a whole, and the effect of the wave pattern on each receiving finger. This is equivalent to the mathematical process of convolution, which is rather complicated. Offsetting the transducers simplifies things considerably.

Driving Techniques

To use SAW filters it is necessary to couple signals to and from them, which is not made easier by the complex impedances of the transducers. To take a practical example, the Signal Technologies BP1102 70MHz bandpass filter (2MHz bandwidth) is quoted as having input and output impedances of 6R in series with 10p. At 70MHz, 10p has a reactance of 227R, which makes a series inductor to tune out the capacitance very desirable.

This is only the start of the problems. The BP1102 is quoted as having a 24dB insertion loss with a matching inductor. Without one, it has a midband attenuation of 44dB. Unfortunately, even at 2 MHz bandwidth the use of a matching inductor can cause response ripple due to an effect called triple transit, in which the acoustic wave bounces from the receiving transducer back to the transmitter and then back again.

Acoustic absorbers are used to minimize reflections from the end of the crystal, but the reflections in question here are from the actual transducers. They occur whenever there is a significant electrical load coupled to the transducers because power is drawn from any incident wave and dissipated. This inevitably disturbs the wave and causes reflections.

The transducer is in fact a three-port network with a matched load on one of the ports. If an extra load is added on the electrical port, the mechanical impedance of the acoustic wave no longer matches.

One solution to this dilemma is to match the input source to the transmitting transducer, and use a high impedance input amplifier on the on the receiving transducer (the output of the SAW device). This means that there is little reflection from the receiving end and the triple transit effect is small. Another solution is to drive the input of the device from a high impedance and with a high voltage signal, either by means of a step-up transformer or a cascode amplifier with a high-voltage supply. This delivers plenty of power to the input while presenting an electrical mismatch.

Material Characteristics

The BP1102 is made on a quartz substrate, ST cut (similar to the more familiar AT cut used for oscillator crystals) and has the advantage of almost zero temperature coefficient. In general, a given material can only offer a bandwidth which is a certain percentage of the operating frequency before the insertion loss becomes too high: for quartz this bandwidth is only a few percent.

One of the wider bandwidth filters from the same family has a 9MHz bandwidth at 72MHz and is made of lithium niobate. It boasts an insertion loss of 24dB without any electrical matching at all, but it has a temperature coefficient of -90 PPM/degree C. The percentage bandwidth available with lithium niobate and lithium tantalate is about 40 percent, which might suggest that they are suitable for use as an input filter in, say, TV receivers. Such a filter could provide a flat response over the entire TV broadcast band and about 60dB rejection outside it.

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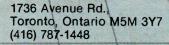
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The drawback is that the loss is much too great, and the resultant noise figure would render reception impossible in any but the strongest signal areas. Development work aimed at low loss devices is being carried out and there are several angles from which the problem can be attacked. One of the obvious ones is to develop a transmitting transducer which launches all energy in one direction, and a receiving one which picks up almost all the energy flowing past it.

The normal types of transducer launches the same power in each direction, so one half of the signal actually transferred to the substrate is wasted in the acoustic absorber. By the same token (because of reciprocity) the receiving transducer can pick up only one-half of the incident signal power. This immediately gives a 12dB loss, even if every other aspect of the device is free of losses.

A unidirectional characteristic can be achieved by clever phasing which cancels the wave in one direction. It is difficult to prevent unwanted reflections or other response ripples when designing this type of transducer, which is why it is only used where low loss is important.

Other Applications

So far we have concentrated mainly on transversal filters, but this is not the only application of SAW devices. It is also possible to produce resonators which are similar in principle to microwave resonator cavities, and if two of these are coupled, a narrow bandpass filter with low insertion loss may be made.

The diagram in Figure 4 shows a transducer between two reflectors. The reflectors consist of many strips rather than one thick one, suitably phased for frequency of operation. If too heavy a strip is placed in the path of the wave, the mechanical impedance mismatch causes bulk waves to be launched. As it stands, this type of resonator may be used as the frequency determining element in an oscillator.

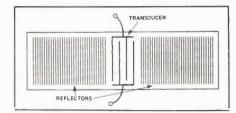


Fig. 4 A SAW device designed to act as a resonator. The reflectors on either side of the transducer consist of metallisation which are phased to suit the frequency of operation.

It may also be coupled electrically to another similar resonator, in which case a bandpass response can be generated similar to the response given by coupling two ordinary tuned circuits. The response is similar to that obtained with electrical coupling but only one substrate is used, making it preferable for large volume applications. The device layout and energy

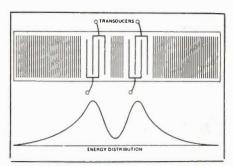


Fig. 5 Two SAW resonators fabricated on one substrate to produce a bandpass response.

distribution is illustrated in Figure 5. Little of the energy coupled into the input resonator is wasted, so a narrow bandwidth low insertion loss device may be made.

Another interesting SAW application not so far mentioned is the convolver, a device which carries out the mathematical process of convolution in real time on two input waveforms. Effectively it multiplies the two waveforms and integrates the result. In order for the multiplication to take place, the signal level must be high enough for non-linearity to occur. To achieve this, wide transducers are used to launch the waves, and they are then focused to a narrow beam by a metalization structure which looks like a lens.

To maintain a focused beam, a metalized track is deposited. This works in a way closely analogous to that of an optical fibre in guiding the wave. The piezoelectric effect generates a voltage in the strip which is integrated by its capacitance.

The Future

It is likely that the developments in low insertion loss devices will continue and will result in their extensive use in receiver inputs stages. We may eventually achieve a low enough loss to permit the use of SAW devices in input filters for TV reception covering the range from 40 to 800MHz. The technology is already at the stage where an input filter having a 2MHz bandwidth at a centre frequency of 900MHz and an insertion loss of 2dB is becoming practical. This would be ideal, for example, in cordless telephone applications.

If satellite TV ever turns out to be as major a development as it's been cracked up to be, there will be a strong incentive for developments such as the use of a SAW resonator to make a quadrature detector working at IGHz. Along with a specially developed IC for the job, it is easy to imagine that a TV receiver head about the size of a matchbox could be developed.

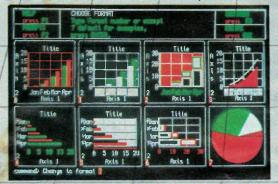
developed.

It is quite likely that some of the signal processing application of SAW devices, for example in chirp radar, will gradually be taken over by higher and higher speed digital processors, but there is likely to be more than enough expansion in other applications to offset this for the foreseeable future.

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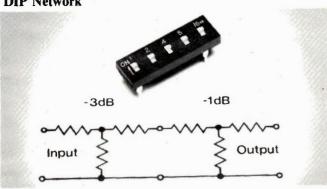
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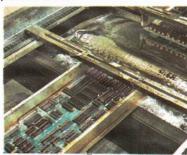
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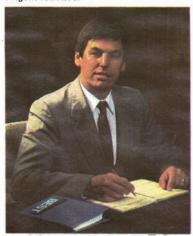
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New FAST BESTMark III

To meet the demanding needs of industry for faster Information processing, Exceltronix is pleased and proud to introduce the Mark III personal computer. Based on the success of the BEST Mark II, the Mark III goes a step beyond. The system is based on the 8088·2 microprocessor, which is capable of running almost twice as fast as any 8088 based system.

Superb IBM PC/XT software and hardware compatibility has been retained.

• A hardware switch allows you to go from 4.77MHz to 8MHz operation. Any software that implements a lot of memory manipulation will run visibly faster. Compilers work much faster, spreadsheets will speed through calculations, and even video intensive games run quicker.

• All the same cost effective package that made the Mark II a success, has been continued in the Mark III. We consider features such as a Real Time Clock, Parallel Printer Port, Serial Communications Port and a minimum of 256K RAM memory, necessities in today's personal computer. These have been included in the base price along with a colour video board, so even if you don't need the options today, tomorrow you will not have to pay to have them installed.

Made in Canada with pride.



Monitor and printer not included. IBM is a registered trade mark of IBM Canada Limited.



The FAST BEST MK III

As BEST MK II plus speed selectable: 4.77 and 8MHz (most software will run on the higher speed), uses 8088-2 processor.

\$189500

Other Configurations:

With 640K RAM.....\$1995
With 10 Meg Hard Drive/1 Floppy/256K...\$2795
With 10 Meg Hard Drive/1 Floppy/640K...\$2895
With 20 Meg Hard Drive/1 Floppy/640K...\$3095
With 20 Meg Hard Drive/1 Floppy/640K...\$3195

Standard Features common the BEST Mk II and MK III

- Superb IBM Compatibility.
- Phoenix BIOS, as used in many major brand IBM compatible systems.
- Minimum of 256K Memory using 41256K RAM chips, expandable to 640K or higher on the main board.
- Seven Expansion slots
- Real Time Clock/Calendar, with software with battery backup.
- Parallel Printer port
- Serial Communications Port with option for a second for only \$39 extra).
- Two double sided, double density 360k 5.25 inch floppy disk drives
- Colour graphics video board, with both RGB and composite video outputs.
- Pre-socketed for the optional Intel 8087 math co-processor
- A reset switch
 A 150 watt power supply, which will handle additional boards and a hard disk drive.
- IBM compatible, high quality keyboard.

Warranty

We have such confidence in the time tested reliability of the BEST that we offer a 300 day warranty which is way above the industry standard. On-site service plan available at extra cost.

Options:
Tape Drive-from\$1295
Second Floppy on H.D. systems ...\$180
Second Serial Port\$39

The BEST Mark II

Made in Canada

Superb IBM PC/XT Compatibility

In the past two years, thousands of BEST personal computers have been working their way into the Canadian business, educational and home environment. Based on the 8088 microprocessor, the BEST Mark II is an inexpensive entry into the personal computer field.

As with all the BEST product range, it is made in Canada to the highest standard of quality.

It is the success of the original BEST and the Mk II that prompted us to develop the newer and faster Mk III and Mk IV.



Monitor not included. IBM is a registered trade mark of IBM Canada Limited.

BEST MARK II

Standard Mark II with 256K RAM and two 360K DD/DS diskette drives, Serial and Parallel Ports, Real Time Clock, Phoenix BIOS. Uses 8088 processor.

\$169500

Other Configurations: \$1795 With 640K RAM. \$1795 With 10 Meg Hard Drive/1 Floppy/256K \$2595 With 10 Meg Hard Drive/1 Floppy/640K \$2695 With 20 Meg Hard Drive/1 Floppy/256K \$2895 With 20 Meg Hard Drive/1 Floppy/640K \$2995

If you want the Best, buy the BEST

SUPER-FAST BEST Mark IV

In today's industry, more than ever, time is money. It is for that reason we developed the Mark IV personal computer.

Still retaining IBM PC and XT software and hardware compatibility, the Mark IV uses the true 16-bit 8086-2 microprocessor. Although the system runs at the same clock speed as the Mark III, the 8086-2 has a 16-bit external data bus compared to the 8088 and 8088-2's 8-bit data bus. This allows memory access to occur much faster than with most other IBM PC/XT and compatible computers.

The bus structure of the Mk IV remains compatible with the standard IBM 8-bit I/O channel but still has the benefits of the 16-bit architecture.

Couple the 8086-2 microprocessor with the 8087 math co-processor and you have one of the fastest, most reliable business or engineering alds available in this price range.

Since the BEST Mark IV is geared towards the serious microcomputer user, a standard complement of 640K RAM is installed on the main board, along with a long list of standard features.

Made in Canada with pride. Superb IBM PC/XT Compatibility



Monitor and printer not included. IBM is a registered trade mark of IBM Canada Limited

Standard Features of the BEST Mk IV

- Superb IBM Compatibility.
- Canadian made
- Phoenix BIOS, as used in many major brand IBM compatible systems.
- 640K of RAM (Random Access Memory).
- Seven Expansion slots, so that you have lots of room to tailor the system to your needs, with the wide range of peripheral cards available.
- Real Time Clock/Calendar, with software and battery backup.
- Parallel Printer port, to interface dot matrix, letter quality printers and

- digital plotters
- Serial Communications Port that supports all RS232-C signals (a second serial port can be added for only \$39.00 extra).
- Two double sided, double density 360k
 5.25 inch floppy dlsk drives, with the option of adding an additional two drives.
- Colour graphics video board, with both RGB and composite video outputs.
 Four modes of operation are available with this board.
- Pre-socketed for the optional Intel 8087 math co-processor.

- A reset switch, which can be a life saver, in the event of a program hanging your system.
- A 150 watt power supply, with more than enough power to supply expansion boards, disk drives, and hard disk drives.
- IBM compatible, high quality keyboard.

Warranty

We have such confidence in the time tested reliability of the BEST that we offer a 300 day warranty which is way above the industry standard. On-site service plan available at extra cost.

SUPER-FAST BEST MK IV

As BEST MK III plus TRUE 16-Bit machine, 8086-2 processor, IBM compatible 8-Bit I/O channel bus, even faster than MK III due to 16-Bit architecture. With 640K RAM.



Other Configurations:
With 10 Meg Hard Drive/1 Floppy/640K \$3395
With 20 Meg Hard Drive/1 Floppy/640K \$3695
Options:
Tape Drive-from
Second Floppy on H.D. systems
300/1200 Baud Modern Board\$379
BEST Monochrome Card

The History of the BEST line

As the personal computer industry expands at an unbelievable rate, it is comforting to know that a retail outlet has had many years experience of serving the computer buying public.

Equipped with the popular PHOENIX BIOS, the BEST satisfies the needs of students, educators, engineers and business people all across Canada. As more software and hardware appears on the market, peoples needs diversify and the BEST computer is keeping pace with these changes with astounding success.

Now we are pleased to offer you a complete line of personal computers to match all of data processing needs. All of the BEST personal computers are manufactured to the highest quality at our facility in Toronto.

You now have the option of four different models of the BEST series of personal computer ranging from the Mark II (an IBM PC/XT compatible) with a minimum of 256K of on-board memory through to the AVT-286 (an IBM AT Compatible), a personal computer based on the Intel 80286 microprocessor.

All of the BEST personal computers come with standard features that other manufacturers consider expensive options. It is our belief that by including options such as printer ports, serial communications

ports, real time clock and video board, your use of a personal computer will be more profitable, and reliable. The excellent reliability enables us to offer warranties far, far better than the industry standard.

A complete description of the personal computers manufactured is included in this catalogue. Before you buy your system our computer fluent sales staff can provide you with advice on the hardware and software required to solve your problem. And remember that after you purchase the BEST computer that suits your needs, Exceltronix and its sister companies will be around long after some other retailers have come and gone, to offer you unparalleled service and selection, as we have been for the last six years.

Special Requirements?

We have the in-house experts in both hardware and software to interface a lot of complex equipment to IBM, BEST or compatible computers. We can recommend or customize existing software packages for your business or industrial needs. We have excellent in-store and on-the-road sales staff with computer expertise and a superb team of in-house hardware and software engineers.

We can probably help you with every aspect of your computer needs at reasonable, honest prices. Call us.

If you want the Best, buy the BEST

B5: the IBM AT Compatible

Designed and made in Canada with pride.

Proving once again that Multiflex is an industry leader, by producing more than just affordable alternatives in personal computer design, we are pleased to announce the BEST AVT-286 Microcomputer, our new, fast IBM AT compatible. The AVT-286 is based on the 80286 microprocessor. The AVT-286 is supported by the Phoenix AT compatible Bios, the same software house that supplies the Bios for all other BEST computers as well as many of the well known IBM compatibles currently manufactured.

Check the long list of standard features that are included on the new BEST AVT-286:

- The AVT-286 runs faster than the (12MHz) IBM AT but retains superb software compatibility
- The main board features a standard 640K RAM using state-of-the-art memory chips for fast, reliable data processing.
- Seven expansion slots of which five support IBM AT signals. The two remaining slots are compatible with IBM PC or XT peripheral boards.
- Presocketed for the optional 80287 math co-processor
- Two floppy diskette drives, one formatted for a capacity of 1.2 Megabytes, the other for 360K to read and write normal diskettes.
- The BEST Colour Video board which offers the user four modes of operation, composite and RGB output.
- An AT compatible keyboard.
- On board Real Time Clock/Calendar with battery backup.
- Parallel Port (for printers etc.) and Serial Port (for communications).



Monitor not included. IBM PC, IBM XT and IBM AT are registered trade marks of IBM Canada Limited.

- Heavy duty Power Supply as standard allows for adding extra cards as well as a Hard Disk without requiring an upgrade.
- Attractive flip-top case.
- A keyboard lock with unique security key which prevents any unauthorised use of the system.
- A front control status panel provides information on the keyboard lock status, reset, power-on, disk activity and parity errors.
- A hardware reset button so the system can restart without having to power

The BEST AVT-286

Canadian designed and made, Super IBM AT compatibility, 640K RAM, Two 5.25in. Disk Drives (one high density 1.2Mb, one 360K), Serial and Parallel Ports, High quality keyboard, Keyboard lock and Status Panel. See detailed description above.

Simpler or more complex configurations available at most competitive prices.

As standard configuration with a 20 Megabyte, fast stepping Hard Drive and Controller.

Warranty

We have such confidence in the time tested reliability of the BEST that we offer a 300 day warranty which is way above the Industry standard. On-site service plan available at extra cost.

Phoenix Pfaster286

Why throw away your PC or XT Investment to achieve the performance of a 80286 based microcomputer? You can run software up to five times faster than an XT, and 60% faster than an AT. The Pfaster 286 card is installed in your PC/XT mother board, with no hardware changes and the software utilities included with the card allow you to boot your system in either the normal 8088 mode or go directly into the 286/coprocessor mode.



Pfaster286 features

- AT ROM compatibility
- 80286 CPU
- Optional 80287 floating point
- co-processor

 8 MHz processing speeds, no wait states 1Mb RAM, expandable to 2Mb or board

- Expandable to 4Mb RAM with the
- optional plggyback board

 16Kb of EPROM, expandable to 256Kb
- Supports eight levels of priority interrupts, and four DMA channels
- Compatible with networking software
- Utilities to perform disk caching, print spooling and diagnostics

The Pfaster286 can be used with any programs written for the 8088, but a few timing programs sensitive will only run on the 8088. Compilers, assemblers, development tools and most applications (such as 1-2-3 form Lotus) run on the Pfaster286 without modification at greatly increased speed.

Pfaster286 with 1M RAM Please Call Each additional 512K RAM ... Please Call 80287 option Please Call

The BEST IBM AT Compatible 4 Meg Memory Card

In order to take advantage of the memory addressing capabilities of the IBM AT or the BEST AVT-286

business computer, we developed a memory card to give the user up to 4 Megabytes of dynamic RAM. The memory card will run at speeds up to 16MHz, which is fast enough to meet the needs of any 80286 microprocessor on the market today. The design uses state-of-the-art (256K x 9) memory arrays, to allow for maximum memory in the smallest physical space. The ninth bit is used as a parity bit to insure the validity of

switch selectable above 1 Meg in blocks of 512K. Please call for most competitive prices.

the data. The card is available in memory sizes from 512K to 4 Megabytes in steps of 512K. Boundaries are

BEST AT Compatible Enhancement Card

This card includes the following features: Two Serial Communications Ports, One Parallel Printer Port and Memory starting from 128K to 512K in selectable boundaries. This card will allow IBM AT users with 256K or 512K of on-board memory to expand their AT machine to full 640K capability as well as provide them with two serial ports and one parallel port.

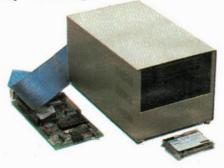
\$299.00

If you want the Best, buy the BEST

Upgrade your IBM

BEST External Hard Disk Drive and Tape Back-up Unit

Easily Convert your IBM PC or Compatible into a Hard Drive System with Tape Backup.



As more people become educated in the use of personal computers, the office computer becomes victim to late night hackers and curiosity seekers. The data upon which your business decisions are made every day is in danger of being wiped away by a wrong sequence of key presses or the data may be seen by those without proper authorization. Protect your data and yourself by removing the hard disk drive from the system.

Multiflex has designed a unit which allows you to physically remove the mass data storage device and lock it in another room without disassembling your entire system. The external hard disk drive and tape backup unit is self contained with its own power supply and connects to back of your system through a ribbon cable. Simply follow normal shut down procedures then unplug the unit and carry it to a safe location.

With Seagate 20 Megabyte Hard drive and Scorpion Tape Backup unit \$2795.00

With 10 Megabyte Hard Drive and Irwin Tape Backup unit \$2395.00

Convert your IBM PC or XT into a Hard Drive System within Minutes

Do it yourself with no mechanical alterations.



This unit rapidly converts your system into a Hard Drive unit, and it is compact enough to fit in a brief case. The unit contains it own power supply, fan, a reliable Seagate 20 Megabyte hard drive and a controller card which plugs into your existing system.

With Seagate 20 Megabyte Hard Drive \$1495.00 With 10 Megabyte Hard Drive \$1295.00

BEST EXPANSION System for your IBM PC

If you are one of the many who invested thousands of dollars in an IBM PC, you may be realizing the limitations of the hardware you own. The power supply may not be able to handle the addition of a hard disk drive or the five expansion slots may already be filled with necessities, leaving no room for the luxuries of more memory or communications hardware.

The BEST Expansion System was designed to function in a transparent fashion to the IBM operating system. The host system (IBM PC or XT) will look at a peripheral card in any of the eight slots of the expansion system as if it were installed in the slots on its own main board.



The Expansion System is packaged in an attractive fliptop case with its own power supply and ventilation fan. Two 3 foot long ribbon cables connect the host computer to the expansion system, which allows the expansion system to be placed on top, beside or underneath of your existing IBM PC or XT system.

The expansion chassis is powered by a 150 watt power supply with power connectors for two disk drives included as a standard feature. This power supply has enough power to run four diskette drives, expansion cards, or hard disk drives.

Although the system provides you with more slots and the capacity for a hard drive, your existing system will run with less load and therefore cooler.

Complete BEST Expansion System including the peripheral adapter to plug into your existing IBM PC or XT system, 8-slot expansion bus, with flip-top case and 150W power supply and cooling fan.

Price \$499.00

Add a Tape Drive to your Existing System

Those who have used computers for any length of time can explain the absolute necessity for an external backup of the data held in a Hard Disk. Our self-contained unit, including power supply, (which looks similar to the Hard Drive Unit illustrated left) has a cable and peripheral card which simply plugs into your IBM or compatible system.

Tapes for these systems are readily available.

With Irwin Tape Drive and BEST Controller \$1495.00 With Scorpion Tape Drive and Controller \$1695.00

Add Extra 360K Disk Drives to your System

If you need more than the two existing drives for your system, our self-contained external unit with two 5.25in. 360K disk drives with its own power supply, fan and controller will be of interest.

\$695.00

The BEST Colour Graphics Video Board



The BEST Colour Graphics Video Board was designed for those personal computer users who require an inexpensive, but versatile video display. The user has a choice of three types of monitors that can be connected to the card, a composite monochrome monitor, composite colour monitor or direct drive RGB colour monitor. Software utilizing a light pen can be run since the BEST Colour Video board supports the necessary hardware.

The video board is capable of operating in four modes, two text and two modes of graphics display

Text display

The BEST Video Card can display either 80 or 40 characters on one line. The character generator contains all the standard ASCII characters plus block graphic characters and a set of international characters such as the English pound and Japanese Yen. Depending on your choice of monitor, the application text can be displayed with a variety of foreground and background colours. In addition, black and white mode allows for the following attributes: reverse video, blinking and highlighting.

The operator can switch between 40 and 80 column display by using the DOS MODE command. The 80 column mode gives crisp characters on either colour or monochrome monitors. The 40 column text mode is suitable to use if you were to use a common television set in conjunction with a RF modulator that can be directly attached to the video board.

Graphics Display

The BEST video board is capable of displaying three styles of graphics.

Low resolution — Monochrome or Colour

In the low resolution mode either colour or monochrome graphics can be displayed. The screen is capable of displaying 200 rows of 320 pixels (a pixel is one dot on the screen). In the colour graphics mode each pixel can be one of four colours, and the background can be one of 6 colours. Many software packages are written with colour graphics capabilities. LOTUS 123, and Symphony for example become more powerful and easier to use with the addition of crisp and vivid colour graphics.

High resolution — monochrome

For applications such as computerized drafting or intricate business charts, colour may not be desired, but high resolution is required. The BEST card is able to double the resolution of the display to 200 rows of 640 pixels, by selecting the monochrome instead of colour graphics. In the high resolution mode an inexpensive composite monitor can still be used.

\$179.00 With Warranty

The BEST Monochrome



In the office environment where a great deal of word processing, or data entry takes place, eye fatigue may be a problem. A solution to this problem is an upgraded text display card for your BEST, IBM PC or compatible. The monochrome card displays a character that Is made up of 7 x 9 matrix of dots in comparison to the 5 x 7 matrix used on the standard colour graphics video board. The finer dot pattern makes text appear much cleaner and easier to read. The monochrome card will display characters in four modes; normal, intensified, reverse video and blinking. The display is 80 x 25 characters. Note: This is a half size board which will fit even those computers with restricted space.

\$179.00 with Warranty

Phoenix Video Board

The choice of a video display card can be a difficult one. There are many different capabilities for each style of video card available. If your display needs are as diverse as the number of cards available, you may have to install two or three video cards in your system. Not only is this expensive, but it also steals precious expansion slots from your system. Phoenix Computer corp. has a designed an expansion card to satisfy all of your display needs. The Phoenix video card can emulate the following styles of display cards.

IBM colour graphics card — 40 or 80 column

IBM colour graphics card — 40 or 80 column character display

320 x 200 colour graphics

640 x 200 monochrome graphics

IBM monochrome text display — 80 column high resolution text display

Techmar

Hercules high resolution monochrome graphics display 120 x132 column colour text display mode

Depending on which display mode you choose any monitor up to 25kHz colour monitors can be interfaced to the Phoenix video card. The setup software is menu driven, and allows the user to program its own character set, as well as selecting from a 64 colour pallet.

\$395.00 With Warranty

Hercules

Colour Graphics Board\$312.00
4 colour graphics and printer board that fits into standard PC/XT or compatible slot. Also included is a parallel interface.

Monochrome Board\$600.00

A high resolution monochrome display of graphics. Supports word processor, and business graphics software.

Tecmar

Graphics Master Board/Paint\$799.00

Displays alphanumeric text and graphics on any monochrome display or other standard composite or RGB monitors. Comes complete with a light pen and PC paint brush.

EPROM PROGRAMMER



This card can program any one of the following EPROMs: 2716, 2732, 2732A, 2764 and 27128s. Two sockets are available on the adaptor board, one for the 28-pin EPROMs, the other for 24-pin types. These sockets are standard sockets, however as an option ZIF sockets for a be used (we recommend ZIF sockets flarge numbers of EPROMs will be programmed). Also as an option an extension board is available. This board at taches to the adaptor via a ribbon cable and extends out the back panel. This is to allow EPROMs to be programmed without removing the cabinet cover every time programming is to be performed. Also as a standard feature, the source software is supplied to allow users to modify the program to suit their needs.

As an option a serial port can be included on the

As an option a serial port can be included on the card; this serial port has the same features as the port described with the floppy disk controller (see the floppy disk description for documentation of the serial port). MAIN EPROM PROGRAMMER CARD (WITH SOFTWARE)

With 2 standard EPROM sockets \$99.00

EPROM PROGRAMMER WITH ZIF SOCKETS (WITH SOFTWARE)
With one 24-pin, ZIF socket and one 28-pin ZIF socket

XTERNAL CARD

SERIAL OPTION

BEST MODEM

The BEST modem is a smart 1200/300 direct connect modem. It can either be a stand-alone unit in which case it requires a small wall adaptor, or it plugs in one of the IBM slots. When used as a stand-alone unit, the modem looks like a Hayes 1200 Smart Modem, that Is, it emulates the same instruction set. When it is used in the IBM, it looks like an intelligent serial communications port which also supports a super-set of the Hayes instruction set.

The modem supports auto-dial, auto-answer, and auto-speed select directly from software control. The modem also has a speaker so that aural monitoring of the call is possible. There are also LED monitors so that the state of the modem can always be known. These LEDs are: Modem Ready, Auto-Answer enabled, Carrier Detected, Transmitting,

Receiving, Data Set ready.

Software packages such as Crosstalk, PC-talk, and Hayes' Smart-com II also will run with this modem.

A version with 300 Baud only is available



300 Baud \$179.00 300/1200 Baud \$379.00

The BEST Quanta Board



Do you find that your PC is not able to communicate with the outside world and you are constantly having to tell it the correct time and date? A simple solution for a system's short coming would be the BEST Quanta Board. Another of the multifunction boards designed with the personal computer user in mind, its features include the following:

Serial Ports

• Two serial communication ports that are configured under PC or MS-DOS as COM1 or COM2. Both communications ports support RS232-C signals (TxD, RxD, DTR, DSR, RTS, CTS, CD, and RI) at communication rates of up to 9600 Baud. One or both the serial ports can be disabled, to alleviate contention between any other serial port your system may already contain. The serial ports can also be configured to support the IBM PC mA current loop. The current loop allows the system to communicate with some types of teletype printers.

Parallel Printer Port

 A parallel printer port which supports many of the popular dot matrix and letter quality printers, as well as digital plotters that are commercially available. The parallel port can be selected as the primary or secondary printer port (PL2 or PL3 using DIP switches).

Games Port

A game port which allows up to four game paddles, or two Joy sticks to be connected to the system. The port is not limited to entertainment software. The port actually gives a value proportional to the resistance on the input, which allows your system to control industrial applications and CAD (computer aided design) software.

Real Time Clock/Calendar

 A real time clock/calendar with software to interface the clock hardware with the TIME and DATE functions of MS-DOS and PC-DOS. The clock continues to keep the correct time when the system is powered down by utilizing a replaceable lithium battery. The clock is based on the MM58274 CMOS chip and it is accurate to within seconds every year.

\$159.00 Cables Extra.

BEST Multi-Function Disk Drive Controller



The BEST Multi-Function disk drive controller is much more than its name implies. This card makes the most use of an expansion slot in your system by including many needed options on one card. This board may be the last you will have to install in your IBM PC or compatible system because of the extra features we have included.

Fioppy Controller

Floppy disk drive controller, which can handle up to four double sided, double density 5.25in. 360K disk drives.

The controller circuitry will also control some Tape Back Up units such as those manufactured by Irwin Magnetics in place of one of the four disk drives.

Serial Ports

 Two serial communications ports that support RS232-C standard signals (TxD, RxD, CTS, RTS, DSR, DTR, and RI).

Parallel Printer Port

 Parallel Printer Port that can be configured as either your primary or secondary printer port.

Real Time Clock/Calendar

Real Time Clock with software, to Integrate the clock with your version of PC-DOS
or MS-DOS. The clock is designed around an ICL clock chip and is accurate to within
seconds a year. A battery back up continues to keep the time during power down.

seconds a year. A battery back up continues to keep the time during power down.

This is the same floppy controller used in all versions of the BEST personal computer, with excellent reliability and compatibility. Using PC-DOS 2.0 or later, each diskette has a formatted capacity of 360 Kilobytes.

\$199.00

The BEST Economy Floppy Controller

If all you need is an IBM Floppy Controller for your IBM or compatible 360K DS/DD disk drives, we have an economy BEST Floppy Controller which does not have provision for the extra features described in the Multi-Function Disk Drive Controller.

\$99.00

The BEST 256K/512K PentaRAM Board



Almost every option your system is lacking can be supplied by the BEST PentaRAM board. This combination of options is one of the most economical ways (from the point of view of both your pocketbook and the expansion slots on your system) to expand your system. All communication ports follow the industry standards. The additional memory continues on from where your main board's memory stopped, in a completely transparent fashion. In detail the features of the BEST PentaRAM board are as follows:

Memory

• Up to 256K RAM using 4164's or 512K RAM using state-of-the-art 41256's can be added to your system. The starting memory boundary can be set to 256K, 384K, 512K, or 640K by configuring a bank of dip switches. The added memory is necessary for anyone wishing to operate a RAMdisk, and much of today's software requires a minimum of 384K RAM (Symphony, Framework, etc.) which many of the older PC's are not capable of holding on the main board. Each bank of memory contains an extra chip to support a parity bit for reliable data handling. (If the above boundaries do not match your system's configuration, they can be modified at the factory).

Serial Port

• A serial communication port that is configured under PC-DOS or MS-DOS as COM1 or COM2. The communications port supports RS232-C signals (TxD, RxD, DTR, DSR, RTS, CTS, CD, and Rl) at communication rates of up to 9600 Baud. The serial port can be disabled, to alleviate contention between any other serial port your system may already contain. The serial port can also be configured to support the IBM PC mA current loop. The current loop allows the system to communicate with some types of teletype printers.

Parallel Printer Port

 A parallel printer port which supports many of the popular dot matrix and letter quality printers, as well as digital plotters that are commercially available. The parallel port can be selected as the primary or secondary printer port (PL2 or PL3 using DIP switches).

Games Port

A game port which allows up to four game paddles, or two joy sticks to be connected to the system. The port is not limited to entertainment software. The port actually gives a value proportional to the resistance on the input, which allows your system to control industrial applications and CAD (computer aided design) software.

Real Time Clock/Calendar

• A real time clock/calendar with software to interface the clock hardware with the TIME and DATE functions of MS-DOS and PC-DOS. The clock continues to keep the correct time when the system is powered down by utilizing a replaceable lithium battery. The clock is based on the MM58274 CMOS chip and it is accurate to within seconds every year.

PentaRAM Board with 256K and all options \$299.00

PentaRAM Board with 512K and all options \$379.00

The BEST Parallel/ Game Card

\$69.00 (Cables Extra)



The BEST parallel/game card is an inexpensive addition to any BEST, IBM PC or compatible, which gives you the ability to connect almost any parallel printer or plotter to you system. The Parallel port is accessible through a DB25 connector located on the back of the card, which eliminates the need to disassemble the system case to connect the Interface cable to the card. Printers such as the Epson family and Stai Micronics, Toshiba and others work with the BEST printer card with no special hardware except the connecting cable.

The game port is compatible wr game paddles. Up to four game paddles or two loysticks can be connected via a 15-bin connector on the back of the card.

BEST 512K RAM Board

Switch selectable boundaries

Complete with 512K-\$229.00 With 64K-\$149.00

BEST PROTOTYPING BOARD

\$28.95

Mail Orders: 319 College Street, Toronto, Ontario, M5T 1S2

Apple Compatible Products

MULTIFLEX 128K MEMORY CARD. . \$99.00

(with 128K of RAM on board)

128K Card can be used to function as RAM disk with your Apple.

MULTIFLEX PARALLEL PRINTER INTERFACE CARD WITH CABLE \$59.00

This card plugs into any of the Apple II +, IIe, or workalike computers, and provides the user with a parallel interface capable of handling graphics and characters. Ideal for use with the Star Gemini and Eoson Printers.

MULTIFLEX 280-64K CARD

This spectacular card provides you with the functions of a 280 card along with giving you extra 64K of self contained memory, on top of the existing memory in your Apple computer. (Software not included)

\$149.00



128K RAM Card



Parallel Printer Card



780/84K Card



16K RAM Card



80 x 24 Card



Serial Card



EPROM Programmer



Z80 Card

MULTIFLEX SUPER SERIAL CARD....\$69.00

This card allows you to: • select desired baud rat. • connect to a serial RS-232 modem, terminal, or a serial printer port • for example, connect two Apple computers (using this card) to communicate with each other, through the RS-232 link over hundreds of feet.

MULTIFLEX NEW SUPER 80 X 24 VIDEO CARD WITH SOFTSWITCH \$69.00

This new Multiflex card features: • superb 80 columns by 24 lines display, with upper and lower case, reverse video • includes built-in soft switch, allowing you to switch between the Apple's 40 column and the video cards 80 column from the keyboard. • superb compatibility.

WIZARD IPI INTERFACE \$69.00

MULTIFLEX EPROM PROGRAMMER\$69.00

Features:

Eprom programmer for Apple computers • Programs 2716, 2732, 2732A, 2764 • ZIF socket for the EPROM
 Complete with software • Comes with a built-in programming voltage supply.

Included with the card is a disk full of software, which using menus allows the user to program or verify EPROMS, check if they are blank, set pointers anywhere in memory, and save or load memory ranges to/from the disk drive, making this unit a very versatile piece of hardware for the hardware developer or hobbylst.

MULTIFLEX 16K RAM CARD......\$43.00

Expand your 48K Apple II + to 64K. The Multiflex 16K Ram Card allows other languages to be loaded into your Apple from disk or tape.

MICROTEK SERIAL CARD FOR THE APPLE. . \$139.00

Similar functions to MULTIFLEX super serial card.

MULTIFLEX UPGRADED 280 CARD.....\$45.00

This card allows the user to run Z80/8080 programs on his Apple II + or IIe computer. Specifically, it allows him to run the CP/M operating system with all its attendant software such as word processors, accounting packages, etc. (CP/M software not included).

MULTIFLEX APPLE COMPATIBLE MODEM

EXCELLENT HAYES
MICROMODEM COMPATIBILITY



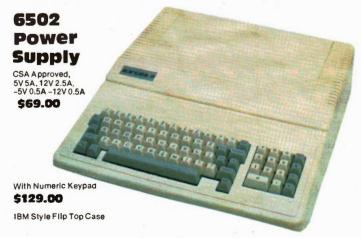
Plugs into your Apple or compatible computer, Direct connect, 300 Baud, Autodial, Autoanswer, Touch Tone/ Pulse Dial, complete with documentation.

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6502 Style Case



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Now available for Apple IIc



Features:

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 Attractively packaged
 Professionally built and tested
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 We believe that Multiflex put out more drives in the last year than all other Canadian manufacturers combined.

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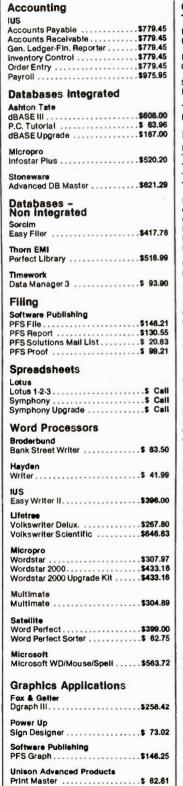
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Attractively packaged, Apple compatible, ultra reliable (90 day warranty).

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Keyboards



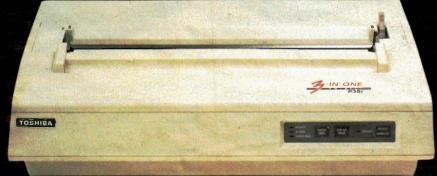
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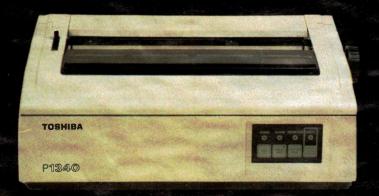
BA High Quality Printers

The Ultimate 3 in One Printer



Accepts font cartridges and downloadable font disks, Qume Sprint 11 emulation, IBM PC compatibility, and more.

- Letter quality at 100 cps
- High-speed drafts at 288 cps
- Superb graphics at 360 x 180 and 180 x 180 dots per inch.



3 in One printing in the economy size. Superb graphics at 180 x 180 dots per inch. Letter quality and draft speeds. **Oume Sprint 5 Emulation.**

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Specifications — P351

Print Method Print Head Print Speed

Impact wire dot matrix 24 pin; overlapping Letter quality: 100 CPS

Draft quality: 288 CPS (12 240 CPS (10 CPI)

Condensed print: 240 CPS

(16.7 CPI)

Graphic mode: 180 x 180 Resolution

dots/inch 180 x 360 dot/inch

Line Lengths: 226 Characters per line

(16.7 CPI)

163 Characters per line (12 CPI) 136 Characters per line

(10 CPI)

Character Set 96 standard ASCII characters

31 graphics characters Special character sets for USA, UK, Sweden, France, Germany, Norway, Spain and

IBM.

Character Fonts Letter quality courier,

prestige elite proportional spaced prestige elite are resident draft quality and condensed print are also resident. Downloadable fonts on disk and plug-in font cartridges are optional. To 1/120" under software

Horizontal Spacing

control 10 CPI, 12 CPI,

16.7 CPI, etc.

To 1/48" under software con-**Vertical Spacing** trol 10 CPI, 12 CPI, 16.7 CPI.

Line Spacing

6.25 IPS (continuous)

Speed Paper/Copies

From 4" to 15" wide 4 parts carbonless paper including

original.

Specifications — P1340

Printed Method Printed Head Print Speed

Impact wire dot matrix 24 pin: overlapping

Letter quality: 54 CPS Draft quality 144 CPS (12 CPI) 120 CPS (10 CPI)

Condensed print: 78 (16.7 CPI)

Line Lengths:

132 Character per line (16.7 CPI)

96 Characters per line (12 CPI)

80 Characters per line (10 CPI)

Character Set Character Fonts

96 standard ASCII characters Letter quality 24 x 36 dot

matrix (proportional spacing). Draft quality 24 x 9 dot matrix, condensed draft

quality 24 x 9 dot matrix. 10 CPI, 12 CPI, condensed

Horizontal Spacing 16.7 CPI, etc.

Proprotional spacing 6LPI, 8LPI, etc.

Vertical Spacing Paper/Copies

From 4.5" to 10" wide, 4 parts carbonless paper including original. Single

sheet or continuous form.

New from Star Micronics SG-10 Printer



ideal for Text & Graphics

• Dual Mode - NLQ/draft standard (NLQ = near letter quality) • 120 CPS and 20% faster throughput • Bidirectional, logic/seeking • 2K buffer (expandable to 6K with optional buffer interface) • 100% IBM PC or Star standard control codes-switch selected • Friction and tractor standard • full 1 year warranty • 10" wide carriage • Standard parallel interface (serial optional) \$379.00 SG-10C for Commodore \$399.00 (No Interface required)

SR-15

• 200 cps and 20% faster throughput • IBM PC or Star standard control codes switch selected • Dual Mode - NLQ/draft standard • Friction/tractor and automatic single sheet feed standard • 15" carriage • 16K buffer • Bidirectional, logic seeking • Price/performance leader · Parallel port standard, serial optional · Full 1 year warranty.

Star Micronics SG15



Same as SG10 except with 15" carriage and standard 16K buffer\$599.00

Radix

15" 200 cps, 100% duty cycle • 16k buffer • serial & parallel standard • proportional & downloadable characters • 240 x 144 Ultra High Res. • tractor & friction... Radix 15 PC (for IBM PC).....\$995.00

Star Printer Accessories

Printhead	 		\$80.00
Printwheel	 	, ,	\$18.00
Ribbons	 		 \$4.50
Paper (500 sheets) (81/2 x 11)	 		\$9.95
Paper (2,000 sheets) (81/2 x11)	 		\$32.00
Dust covers	 		\$8.50
Printer Stand (plastic) 10"	 		\$34.00
Printer Stand (plastic) 15"	 		\$38.00

Star Micronics — Power Type daisywheel printer **Ouality**

Print Speed: 18 c.p.s. bi-directional, logic seek-

Interface standard parallel (Centronics compatible) and serial RS232C-20mA current loop Paper Slew Speed: 12 l.p.s. @1/6" spacing Print Buffer: One line

Print Size: 10,12, 15 c.p.i and proportional spac-

ing

Number of Columns: 110,132, 165

Charecter Sets: over 100 Type fonts available. Special Features: proportional spacing; dual interface; standard printer mode and word processing mode; 32 easy access format switches reverse paper feed; short form tear-off;

Line Spacing: 3,4,6,8 lines/inch; switch and software selectable

Paper Handling: single sheet: 5.5" to 8.5" wide; sprocket 4" to 13" wide; copies 3 carbonless sheets

Ribbon standard cassette

OKIMATE 20 (IBM).....\$345.00 Colour



• Color Screen Print Software Package • 80 cps unidirectional printing • 40 cps correspondence quality • 24 element, long-life printhead with snap-in "no tool" replacement • IBM character sets 1 & 2 • High resolution 144 x 144 DPI graphics • Plain, thermal or acetate paper . Friction and pin feed paper handling (roll paper stand optional) . Variable width tractor feed.

ML182 \$390 IBM



• 120 cps bidirectional printing • Friction and adjustable pin feed paper handling • Parallel Interface (serial optional) • All Points Addressable graphics • 5 foreign lanuage sets. • IBM Graphics compatible.

Okidata ML192 \$699.00

(Apple Imagewriter or compatible, IIe, IIc, Mac)
• 120 cps • 2K Buffer, Serial Int., upgradeable to 10K

• Tractor and friction • 10" • 19.2K Baud max. • Cable

Okidata ML192 (IBM)\$679.00 • 160 cps • Parallel (optional Serial) • Correspondance

quality • 10".

Okidata ML193 \$1089.00

(Apple Imagewriter compatible IIe, IIc, Mac)
• 120 cps • 2K Buffer, Serial int., upgradeable to 10K

• Tractor and friction • 15" • 19.2K Baud max. • Cable

Okidata ML193 (IBM)\$1069.00

• 160 cps • Parallel (optional Serial) • Correspondance quality • 15".

Copal

SC55001 180cps, 132 column

\$699.00

Copal SC1500T.....\$499.00 180 cps, 80 column

Copal SC1200L \$329.00

120 cps, 80 column

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We also carry Toshiba Printers 1340 and 351 See page 11

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Cable Assemblies



DB25 Male to DB25 Female\$35.00
RS232 Cable (6ft of round conductor)\$39.00
RS232 other lengths and connector configurations available on request.
Parallel cable 36 nin Centronics type connectors male

joined by 6ft of ribbon cable to female\$35.00
Parallel Cable for IBM Interface DB25 through 6ft of rib-
bon to 36 pin centronics with appropriate
connections
Cable Assemblies for two 5-1/4in drives and controller
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Contains 3 BS232 connectors and a switch which switches all lines between A and output connector A and output connector C or connector B and output connector C. Example Applications. Switch between Serial printer and modern if you have only one serial port or switch on serial port or printer between two printers.

Zenith Data Systems



NEW

ZVM1220A

12in. diagonal screen • non-glare amber display • composite input • 25 lines x 40/80 characters

\$135.00

NEW

ZVM1230A

12in. diagonal screen . non-glare green display composite input
 25 lines x 40/80 characters

\$135.00

CV-2560 ● 25" diagonal screen ● RGB/composite input • 25 lines x 80 characters • sound capability • green screen only switch • video "loop thru" feature \$1049.00

ZVM 124 ● 12" diagonal screen ● non-glare amber display ● PC monochrome input (TTL) ● 25 lines x 80 characters • 720 x 350 pixels • IBM PC & compatibles \$229.00

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ZVM 135 ● 13" diagonal screen ● RGB/composite inputs ● 25 lines x 80 characters ● 640 x 240 pixels ● sound capability ● green screen only switch ● video "loop thru" feature \$787.00

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long persistence phosphors for interlaced applications • FOR BEST PRICE\$1195.00

Amdek

300A Amber		. ,												\$	2	1	8.	0	O)
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NEC Monitors now available — Call for price

Irwin 10 Meg Tape Drive Backup



Hooks up to your existing floppy controller. \$1199.00

5.25in. Disk Drives

. Shugart/Panasonic 5.25in, slimline, double sided, double density disk drive with 360K storage



Toshiba Disk Drives

ND-04D 360K DS/DD (black or grey) \$169.00 ND-08DE-G 1.2Mbyte AT Compatible, Grey\$299.00 For more details see page 14 Scorpion 20 Meg Tape Drive Backup and Controller.



Diskettes

Prices per box of 10 10% discount on 3 or more boxes.

10 / 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	
Dysan DS/DD	0
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BASF DS/DD\$29.95/1	0
BASFSS/DD\$22.95/1	0
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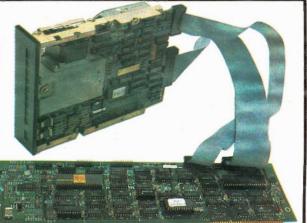
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10 MEG Seagate, slimline drive and hard disk controller. This controller can handle up to two-10 MEG hard drives. \$995.00

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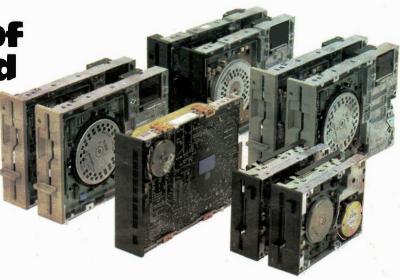
Quantity discounts available on two or more ee also Toshiba ard Drives on p.14.



TOSHIBA

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Upgrade your IBM PC/XT/AT or compatible with high quality Toshiba Drives.



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For your IBM PC/XT or compatible. Grey or black available. \$169.00

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Wide Selection of Hard Disk Drives

10 Megabyte (MMI) ... \$650.00 MK 53FA, 43.5 Meg \$2579 MK 54FA, 60.5 Meg \$2725 MK 56FA, 86.5 Meg \$2898 Controllers available.

Prices and specifications subject to change without notice.

SPECIFICATIONS' + CHARACTERISTICS

Unformatted Capacity:	MK-53FA	MK-54FA	MK-56FA
Mbytes per Drive (MFM)	43.2	60.5	86.5
Mbytes per Drive (RLL)	64.8	90.7	129.8
Bytes per Track (MFM)	10,416	10,416	10,416
Bytes per Track (RLL)	15,624	15,624	15,624
Heads per Surface	1	1	1
Data Surfaces	5	7	10
Cylinders per Drive	830	830	830

Data Transfer Rate (Mbits/sec) (MFM) 5.0
Data Transfer Rate (Mbits/sec) (RLL) 7.5
Access Time (ms) (includes settling):
Track-to-Track 8.0
Random Average 30.0
Maximum 55.0

Quantity Discounts available.

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Canada's MIDI Specialists

Presents: NEW DIRECTIONS FOR THE CREATIVE ARTIST.

THE YAMAHA CX5M MUSIC COMPUTER ATTENTION YAMAHA DX-7 OWNERS: K YOUR DX!

- Has the tedium of FM voice editing humbled you to

factory presets?

- With the CX5M's multi-menu graphic display, voice creation is a snap!

- Why purchase \$150 RAM packs to save 32 voices when you could save hundreds on a CX5M cassette?

- Plus you receive free: 200 voices for your DX-7! TAP YOUR DX-7'S POTENTIAL FOR ONLY \$675.00 (CX5M Computer, DX-7 Voicing Program, 200 Voices)

PERSONAL COMPOSER THE ULTIMATE STUDIO SYSTEM ON AN IBM PC/XT

- 32 trk sequencer (50,000 notes) w/full editing.
 Instant conversion of sequenced tracks to score including individual part extraction, multiple-format scoring and complete on-screen editing.
 Ultra high resolution graphics for copyright-quality printing.
 DX-7 graphic voicing program.

PROGRAM: \$595.00

Requires:
IBM compatible 320K min. (Our Loaded BEST w/512K & 2 Drives is \$1795.00)
Hercules compatible graphic card: \$380.00
Roland MPU-401 processor and interface: \$399.00
Monochrome monitor: \$229.00 (Zenith ZVM 122)
Epson FX-80 printer: \$649.00

THE COMPLETE PERSONAL COMPOSER PACKAGE: \$4049.00

- A dynamic DX-9 sound module on-board (96 voices, graphically programmable)

An efficient 8 part scoring system w/hardcopy printout.

PLAYS 8 VOICE MULTI-TYMBRAL

- Send your music out over 8 independent MIDI channels. - Produce audio-visual performances incorporating graphics and music.

- And the CX5M is a fully-fledged MSX computer for all your home and business needs.

THE COMPLETE CX5M PACKAGE IS ONLY \$825.00 (Computer, YK-01 Keyboard, FM Composing & Voicing Programs)

COMPUTER MUSIC CENTRE EXCLUSIVES: CMC 1 Voice Library (100 Voices: \$39.95) CMC 2 Song Library (15 Songs: \$39.95) CMC 3 DX-7 Voice Library (200 Voices: \$49.95) CMC 4 DX-7 Voice Library (500 Voices: \$99.95)

CX5M DISK DRIVES AND PRINTERS ARE HERE! Disk Drive \$395.00

Printer \$365.00

Now Available: 4 track real-time sequencer

RX drum machine rhythm editor Keyboard chord construction and

progression tutorials New! The MSX book - a must for CX5M owners. Complete

documentation: \$32.95

HOT MIDI PRODUCTS FROM THE NAMM SHOW

TEXTURE FROM CHERRY LANE ON THE IBM PC/APPLE 11

The most complete 'workhorse' professional recording software on the market! Complete real-time, step-time, and MIDI data editing. Link up to 64 8 track sequences to create songs or sets

PROGRAM PRICE: \$295.00

APPLE MACINTOSH MIDI HAS ARRIVED! Southworth TOTAL MUSIC SYSTEM 99 tracks, 50,000 notes, automatic transcriptions, full editing of recording and scoring, even visual editing! Fully mouse driven. SOFTWARE AND INTERFACE ONLY: \$649.00

IVL Technologies PITCHRIDER 7000: The affordable MIDI Guitar interface is here! Fully-polyphonic, velocity and pitch bend control. OUR PRICE: \$849.00 PITCHRIDER 2000: MIDI interface for brass/woodwinds. **OUR PRICE: \$425.00**

MAJOR REDUCTIONS ON MIDI DEVICES AND SIGNAL PROCESSORS

ROLAND MIDI:

SDE-2500 MIDI DIGITAL DELAY: \$765.00 MPU-101 MIDI to CV interface: \$315.00 MPU-103 MIDI Channel/Filter Converter: \$249.00 MPU-104 MIDI Output Selector: \$109.95 MPU-105 MIDI Input Selector: \$89.95 MM-4 MIDI thru box: \$74.95

NEW BOSS MICRO RACK SERIES: RDD-10 Digital Delay: \$315.00 RGE-10 Graphic Equalizer: \$219.00

Roland Compu-Music: complete system \$295.00

Passport compatible MIDI Interface: \$165.00

Boss DE-200 Digital Delay: \$395.00

Alpha Syntauri 5 octave System: complete with Mountain Music cards and software \$995.00

Yamaha DX-9 FM Synthesizer: \$1195.00 E-MU Systems Drumulator: \$749.00 Roland System 100M Analog Module Rack: \$1395.00

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COMPUTER MUSIC CENTRE Inc.

317 College Street, Toronto, Ontario M5T 1S2 Your centre for everything in MIDI keyboards, hardware, software, and accessories. The 8088 Controller and Trainer System

Based on the success of our Multiflex starter systems, we are proud to announce the arrival of the 8088 Controller and educational Trainer System. With the option to upgrade to a full IBM PC compatible, the starter system is the perfect education tool to learn 8088 based hardware and assembler code. It is also ideal for use as a complex, high speed industrial controller at an affordable price. This 8088 System consists of two boards. The first board (as seen in the picture) is the motherboard which can be used as a general purpose controller and contains the following:

- Socketed for 64K static RAM
- Socketed for 64K of EPROM
- RS232-C serial communications port
- Controller Port
- 300 baud modem
- 3 IBM PC compatible expansion slots (when the multifunction board is used)
- Wire Wrap area

The motherboard is a very versatile controller for which it is very easy to write software on the IBM PC/XT.

The second component is a console which connects to the motherboard via a ribbon cable. The console contains a display, hex keypad and another keypad containing function keys to perform memory block moves, register examination, the examination of I/O ports and a myriad of other functions. This board also contains an EPROM programmer.

A further multi-function board which has been designed specifically for the system to make it IBM PC compatible is available. This multi-function card contains a floppy diskette controller, DMA controller and up to 512K dynamic RAM.



Controller Board with 16K RAM (optionally expandable to 64K) \$250.00

Keyboard and Display Board with EPROM programmer and monitor software \$159.00

Multi-function Board with 64K RAM (expandable to 512K) \$250.00

SPECTACULAR GANG EPROM PROGRAMMER AND EMULATOR

Totally self-contained (has its own display, entry keypad and power supply).

Based on the Z-8 microprocessor.

Can program up to 8 EPROMs simultaneously (anywhere from one to 8 EPROMs at the same time with the information in its own memory or or master EPROM).

Each of the 8 EPROM programming sockets is individually buffered and isolated from one another providing protection in situations when there is a bad EPROM among the eight being programmed. Clearly indicates and singles out any defective or marginal EPROMs prior to or after programming.

After programming the unit does a full VERIFY routine of the EPROM (at a Max Vcc of 5.4V and at a Min Vcc of 4.5V) to ensure high reliability of your EPROMs. Very simple to use.

A standard unit contains 8x16K of on-board memory which is sufficient in most cases, but can easily optionally be upgraded to 8x64K of on-board memory.

The Gang Programmer can handle a wide selection of EPROMs: 2716. 2732, 2732A, P2732A, 2532, 2564, 2764, 27128,27128A and optionally upgradeable to handle 27256, 27512, 2758 and 2724.

Gives you option of entering the data which you want to be programmed on the EPROM through a built-in keypad and display into the EPROM programmer's built-in RAM or by downloading the data to be programmed by

RS232 interface (110 to 9600 Baud). The RS232 is standard — not optional!

Data can be checked or modified, since you can examine any memory location of the programmers built-in RAM, this holds true even after you have down-loaded through the RS232 from your computer; you can check or modify the memory before finally programming it on your EPROMs.

Read Master EPROMs; you can plug in a programmed EPROM, dump it into the programmers RAM, check the contents on display by stepping through the memory and, if you wish, you can alter any location before copying to other EPROMs.

EPROM Programmer can also be (optionally) used as an EPROM emulator, saving hours of frustration, reprogramming and waiting.

Using the Emulator option, you can enter via the keyboard or down-load through the RS232 from your computer or development system, the information which you think is right for whatever project you are building. This is the same Information which you would normally burn into an EPROM, plug into your new undebugged processor and moments later you realise that you forgot to enter a code or that you must add or delete some codes. This normally would mean waiting 20 minutes for erasing of the EPROM and reprogramming and wasting time.

Using the Emulator option, you simply plug in a 24 or 28 Pin buffered pod into the socket on your board where you would normally fit the



EPROM, the difference being now that you can have all the Information in the programmers RAM, connected to the pod by a ribbon cable and you can start your testing. If you wish to change, add, delete any codes, you can modify the contents of the programmers RAM using the keypad and display and continue testing moments later. Keep in mind that the RAM is protected from being accidentally altered.

Gang EPROM Programmer with 8 ZIF sockets, 16Kx8 RAM and RS232, without Emulator \$695,00

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4516 4518 4519 4520 4521	1 of 16 decoder/demultiplexer 1.8 Binary up/down counter	85 95 89 65 79	XR2206 XR2207 XR2208 XR2209 XR2211 XR2212	Monolitric function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision phase locked loop	2.75 2.99 3.25 6.99 8.99	74ALS11 74ALS12 74ALS15 74ALS20 74ALS21 74ALS22	Tinple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS AND gate OC Dual 4 input POS AND gate Dual 4 input POS NAND gate OC	.57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10	Quad 2 input NAND gate Quad 2 input NOR gate Hex inverter Quad 2 input AND gate Triple 3 input NAND gate	73 73 73 73 73
4516 4518 4519 4520 4521 4522	1 of 16 decoder/idemultiplexer	85 95 89 65 79 99	XR2206 XR2207 XR2208 XR2209 XR2211 XR2212 XR2242 XR2567	Monolithic function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision phase locked loop Long range timer Dual monolithic lone decoder	2.75 2.99 3.25 6.99 8.99 2.25 2.40	74ALS11 74ALS12 74ALS15 74ALS20 74ALS21 74ALS22 74ALS27 74ALS28	Tiple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Out POS NAND NAND gate	.57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20	Quad 2 Input NAND gate Quad 2 Input NOR gate Hex inverter Quad 2 Input AND gate Triple 3 Input NAND gate Triple 3 Input AND gate Dual 4 Input NAND gate	
4516 4518 4519 4520 4521 4522 4528 4527	1 of 16 decoder/demultiplexer	85 95 89 65 79 99 25	XR2206 XR2207 XR2208 XR2209 XR2211 XR2212 XR2242	Monolithic function generator yoftage controlled oscillator Operation multiplier Procision oscillator FSK demodulator/fone decoder Precision phase locked loop Long range timer Dual monolithic lone decoder hual low noise op amp	4 95 2.75 2 99 3 25 6 99 8 99 2 25 2 40 1 55	74ALS11 74ALS12 74ALS15 74ALS20 74ALS21 74ALS22 74ALS27 74ALS28 74ALS30 74ALS32	Tiple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Oual 4 input POS NOR gate Ouadruple 2 imput POS NOR Duffer 8 input POS NAND gate Ouadruple 2 imput POS NOR gate Ouadruple 2 imput POS NOR gate Ouadruple 2 imput POS NOR gate	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F74	Quad 2 Input NOR gate Hex inverter Quad 2 Input AND gate Triple 3 Input AND gate Triple 3 Input AND gate Dual 4 Input AND gate Quad 2 Input NOR gate	
4516 4518 4519 4520 4521 4522 4528 4527 4528	1 of 16 decoder/demultiplexer	85 95 89 65 79 99 25 10	XR2206 XR2207 XR2208 XR2209 XR2211 XR2212 XR2242 XR2567 XR4739	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSR demodulatorifone decoder Precision pales diocked loop Precision phase locked loop Dual monolithic tone decoder Dual low noise op amp FSK modem system.	4 95 2.75 2 99 3 25 6 99 8 99 2 25 2 40 1 55	74ALS11 74ALS12 74ALS15 74ALS20 74ALS21 74ALS22 74ALS27 74ALS28 74ALS30 74ALS32 74ALS33	Tiple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Ouadruple 2 input POS NOR buffer 8 input POS NAND gate Ouadruple 2 input POS NOR pate Ouadruple 2 input POS NOR buffer OC	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F74 74F86	Quad 2 Input NOR gate Hex inverter Quad 2 Input AND gate Triple 3 Input AND gate Triple 3 Input AND gate Dual 4 Input AND gate Quad 2 Input NOR gate	
4516 4518 4519 4520 4521 4522 4528 4527 4528 4527 4528	1 of 16 decoder/demultiplexer	85 95 89 65 79 99 25 10 10	XR2206 XR2207 XR2208 XR2209 XR2211 XR2212 XR2242 XR2567 XR4739 XR14412	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/tone decoder Precision passe locked loop Long range timer Dual monolithic tone decoder Dual tow noise op amp FSK modern system. 7400 SERIES TTL	2.75 2.75 2.99 3.25 6.99 8.99 2.25 2.40 1.55 8.75	74ALS11 74ALS12 74ALS20 74ALS20 74ALS20 74ALS27 74ALS27 74ALS30 74ALS32 74ALS33 74ALS33 74ALS35 74ALS37	Tiple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS AND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Ouad 4 input POS NAND gate Ouadruple 2 input POS NOR buffer 8 input POS NAND gate Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR D buffer Ouadruple 2 input POS NOR buffer OC	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F74 74F86 74F109	Quad 2 Input NOR gate Hex inverter Quad 2 Input AND gate Triple 3 Input NAND gate Triple 3 Input NAND gate Dual 4 Input NAND gate Quad 2 Input NAND gate Dual 5 Input NAND gate Quad 2 Input NOR gate Dual D type filtp flop Quad exclusive OR gate Dual IK PQS edge triggered flip flop	
4516 4518 4519 4520 4521 4522 4528 4527 4528 4529 4530 4531	1 of 16 decoder/demultiplexer 18 Blinary up/down counter 9.5 Dual BCD up counter 8.4 bit IA NID/DR select gate 6.6 Dual binary up counter 7.2 state frequency divider 1.5 BCD divide by N counter 1.2 bit binary divide by N counter 1.1 BCD rate multiplier 1.1 Dual retriggerable/mest table monostable 1.1 Dual 1 channel mux 1.1 Dual 1 singuit majority toglo gate 1.5 Dual 15 singuit majority toglo gate 1.5 Dia 1.5 Di	85 95 89 65 79 99 25 10 10	XR2206 XR2207 XR2208 XR2209 XR2211 XR2212 XR2242 XR2567 XR4739 XR14412	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder FSK demodulator/fone decoder Precision phase locked floop Long range filmer Dual monoille op amp FSK modern system 7400 SERIES TTL Quad 2 inout NAND gate	2.75 2.99 3.25 6.99 8.99 2.25 2.40 1.55 8.75	74ALS11 74ALS12 74ALS15 74ALS20 74ALS21 74ALS22 74ALS27 74ALS30 74ALS30 74ALS35 74ALS35 74ALS35 74ALS35	Triple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Ouadruple 2 input POS NOR buffer 8 input POS NAND gate Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR Duffer Ouadruple 2 input POS NOR Duffer Ouadruple 2 input POS NOR Duffer Ouadruple 2 input POS NAND Duffer Ouadruple 2 input POS NAND	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F34 74F86 74F109	Quad 2 Input NOR gate Mex inverter Quad 2 Input ND gate Triple 3 Input NAND gate Triple 3 Input NAND gate Triple 3 Input NAND gate Dual 4 Input NAND gate Quad 2 Input NOR gate Dual 4 Input NOR gate Dual 5 Input Input Inp	
4516 4518 4519 4520 4521 4522 4528 4527 4528 4529 4530 4531 4532 4532	1 of 16 decoder/demultiplexer Blans yup/down counter 9. Dual BCD up counter 8. Abit AND/DR select gate 6. Dual binary up counter 9. 24 state frequency divider 15. BCD divide by N counter 16. BCD divide by N counter 17. BCD rate multiplier 17. Dual refriggerable/meset/able monostable 18. Dual 4 channel mux 19. Dual 5 input majority togic gate 19. BCD trainer must 1	85 95 86 65 79 99 25 10 10 10 - 30 05 33 55	XR2206 XR2207 XR2208 XR2209 XR2211 XR2212 XR2242 XR2567 XR4739 XR14412	Monostrinc function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder FSK demodulator/fone decoder Precision pase locked floop Long range filmer Dual monoitable to decoder Dual flow noise op amp FSK modern system 7400 SERIES TTL Quad 2 input NAND gate Quad 2 input NAND gate Quad 2 input NAND oscie	2.75 2.99 3.25 6.99 2.25 2.40 1.55 8.75	74ALS11 74ALS12 74ALS15 74ALS20 74ALS21 74ALS21 74ALS28 74ALS33 74ALS33 74ALS33 74ALS33 74ALS35	Triple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Ouadruple 2 input POS NOR buffer 8 input POS NAND gate Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR Duffer Ouadruple 2 input POS NOR Duffer Ouadruple 2 input POS NOR Duffer Ouadruple 2 input POS NAND Duffer Ouadruple 2 input POS NAND	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F74 74F86 74F109 74F112	Quad Z Input NOR gate Hex inverter Quad Z Input NO gate Triple 3 input NAND gate Triple 3 input NAND gate Dual 4 input NAND gate Quad Z Input NOR gate Dual 4 input NAND gate Quad Z Input NOR gate Dual D type fillp flop Quad exclusive OR gate Dual J K POS edge triggered flip flop Dual J K NGG edge triggered flip flop	
4516 4518 4518 4520 4521 4522 4528 4527 4528 4529 4530 4531 4532	1 of 16 decoder/demultiplexer Blans yup/down counter 9.5 Dual BCD up counter 8.6 BUI AND/DR select gate 6.6 Dual binary up counter 9.7 24 state frequency divider 9.7 BCD divide by N counter 1.6 BCD rate multiplier 1.7 BCD rate multiplier 1.7 Dual 1 of inggreat bit Reset table monostable monostable 1.7 Dual 15 input majority togic gate 1.7 BUI generatorichecter 1.7 BUI priority encoder 1.7 Bual time 5 decade counter 1.7 Programmable timer 2.7 Dual 1 organisation monostable 2.7 Dual 1 organisation selection selec	85 95 86 65 79 99 25 10 10 10 10 05 35 35 50 37	XR2206 XR2207 XR2208 XR2208 XR2211 XR2212 XR2567 XR4 739 XR14412 7400 7402 7403 7404 7405	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision passe locked floop Long range filmer Dual monoithint one decoder Dual flow noise op amp FSK modern system 7400 SERIES TTL Quad Zinput NAND gate QUAD ZINPUT MEXITER FOR THE PROVINCE PROVINCE PROVINCE PROV	4.95 2.75 2.99 3.25 6.99 8.99 2.25 2.40 1.55 8.75	74ALS11 74ALS12 74ALS15 74ALS21 74ALS21 74ALS22 74ALS22 74ALS22 74ALS33 74ALS33 74ALS33 74ALS35 74ALS35 74ALS36	Tiple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Ouadruple 2 input POS NOR buffer 3 input POS NAND gate Ouadruple 2 input POS NOR gate Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NAND Dual 4 NDINNert gate Dual D type POS edge triggered FiF Ouadruple 1 poput sections of Positions of Po	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F32 74F36 74F109 74F112 74F113 74F114	Quad Z input NOR gate Hex inverter Quad Z input NO gate Triple 3 input NAND gate Triple 3 input NAND gate Triple 3 input NAND gate Quad Z input NOR gate Quad Six NOS dege triggered flip flop Qual JK NEG edge triggered flip flop	
4516 4518 4519 4520 4521 4522 4528 4527 4528 4530 4531 4532 4534 4536 4538	1 of 16 decoder/demultiplexer Blans yup/down counter 9.5 Dual BCD up counter 8.6 BUI AND/DR select gate 6.6 Dual binary up counter 9.7 24 state frequency divider 9.7 BCD divide by N counter 1.6 BCD rate multiplier 1.7 BCD rate multiplier 1.7 Dual 1 of inggreat bit Reset table monostable monostable 1.7 Dual 15 input majority togic gate 1.7 BUI generatorichecter 1.7 BUI priority encoder 1.7 Bual time 5 decade counter 1.7 Programmable timer 2.7 Dual 1 organisation monostable 2.7 Dual 1 organisation selection selec	85 95 86 65 79 99 25 10 10 10 10 05 35 35 50 37	XR2206 XR2207 XR2208 XR2208 XR2209 XR2211 XR2212 XR2567 XR4739 XR14412 7400 7402 7403 7404 7405 7406	Monotime function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision passe locked loop Long range timer Dual monotithic tone decoder Dual town one op amp FSK modern system 7400 SERIES TTL Quad 2 input NAND gate Quad 2 input NAND gate Quad 2 input NAND gate Ouad 2 input nand gate OC Max inverter Max inverter Max inverter Max inverter Max inverter Ouad 2 input nand gate OC	4 95 2.75 2.99 3.25 6.99 8.99 2.25 2.40 1.55 8.75	74ALS11 74ALS12 74ALS22 74ALS20 74ALS21 74ALS21 74ALS28 74ALS30 74ALS33 74ALS33 74ALS35 74ALS35 74ALS37	Tiple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Ouadruple 2 input POS NOR buffer 3 input POS NAND gate Ouadruple 2 input POS NOR gate Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR Duffer Ouadruple 2 input POS NAND Dual AND Invertigate Dual D type POS edge triggered Fir Ouadruple 2 input sectuative OR gate Ouadruple 2 input sectuative OR gate Dual JK positive edge triggered Fir	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F74 74F86 74F109 74F112 74F113 74F113 74F138 74F139	Quad 2 input NOR gate Hex inverter Quad 2 input NO gate Triple 3 input NAND gate Triple 3 input NAND gate Triple 3 input NAND gate Dual 4 input NAND gate Quad 2 input NOR gate Dual 4 input NAND gate Quad 2 input NOR gate Dual 5 input NOR gate Triple Dual 5 input NOR gate Triple Dual 5 input NOR gate Triple Signature Signatur	73 73 73 73 73 73 88 85 .99 1.99 1.79 1.79
4516 4518 4519 4520 4521 4522 4528 4527 4528 4528 4530 4531 4532 4534 4538 4538	1 of 16 decoder/demultiplexer Blans yup/down counter 9.5 Dual BCD up counter 8.6 BUI AND/DR select gate 6.6 Dual binary up counter 9.7 24 state frequency divider 9.7 BCD divide by N counter 1.6 BCD gate multiplier 1.7 BCD gate multiplier 1.7 Dual 1 of inggerable/meset table monostable 1.7 Dual 1.5 ingut majority togic gate 1.7 BUI generator/checker 1.7 BUI generator/checker 1.7 BUI generator/checker 1.7 Buil timer and timer and timer and timer 1.7 Buil timer and time	85 95 98 87 99 99 99 110 110 110 110 110 110 110 11	XR2206 XR2207 XR2208 XR2209 XR2211 XR2211 XR2242 XR2567 XR4739 XR14412 7400 7402 7403 7404 7405 7406 7407 7407 7408	Monotime function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision passe locked loop Long range timer Dual monotithic tone decoder Dual town one op amp FSK modern system 7400 SERIES TTL Quad 2 input NAND gate Quad 2 input NAND gate Quad 2 input NAND gate Ouad 2 input nand gate OC Max inverter Max inverter Max inverter Max inverter Max inverter Ouad 2 input nand gate OC	4 95 2.75 2.99 3.25 6.99 8.99 2.25 2.40 1.55 8.75	74ALS11 74ALS12 74ALS15 74ALS21 74ALS21 74ALS22 74ALS22 74ALS30 74ALS32 74ALS35 74ALS35 74ALS35 74ALS36 74ALS36 74ALS36 74ALS36 74ALS310 74ALS36	Triple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Cual 4 input POS NOR gate Cual 4 input POS NOR gate Cuadruple 2 input POS NOR buffer 08 input POS NAND gate Cuadruple 2 input POS NOR buffer 06 Underuple 2 input POS NOR buffer 06 Underuple 2 input POS NOR buffer 06 Underuple 2 input POS NOR buffer 07 Cuadruple 2 input POS NAND buffer OC. Dual 1 AND/invert gate Dual 0 type POS edge triggered F/F Cuadruple 2 input exclusive OR gate Dual 0 type POS edge triggered F/F Dual 1K NEG edge triggered F/F	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F32 74F36 74F109 74F112 74F113 74F114 74F138 74F139	Quad Z Input NOR gate Hex inverter Quad Z Input NND gate Triple 3 Input NND gate Triple 3 Input NND gate Triple 3 Input NND gate Dual 4 Input NAND gate Quad Z Input NOR gate Dual 4 Input NOR gate Dual 5 Input NOR gate Dual 5 Input NOR gate Dual J K NEG edge triggered flip flop Dual J K NEG edge triggered flip flop Dual J K NEG edge triggered flip flop Dual J K NEG edge ingered flip flop Dual J K NEG edge ingered flip flop Dual J K NEG edge ingered flip flop Dual J M NEG edge ingered flip flop Dual J M NEG edge ingered flip flop Dual I M NEG edge ingered flip flop	73 73 73 73 73 73 73 88 85 99 1.99 1.79 1.79
4516 4518 4519 4520 4521 4522 4528 4528 4528 4528 4530 4531 4532 4534 4538 4538 4538 4541 4543 4553	1 of 16 decoder/demultiplexer Binary up/down counter 9. Dual BCD up counter 8. Abit AND/DR select gate 6. Dual binary up counter 1. 24 state frequency divider 1. BCD divide by N counter 1. Abit binary divide by N counter 1. BCD cate multiplier 1. Dual reingerabiemsetable monostable 1. Dual 15 input majority togic gate 1. Builty generatorichecker 1. Both priority encoder 1. Real time 5 decade counter 1. Programmable timer 2. Dual schemel digital multiplexer 1. Quad 2 input analog mux 1. Sund 1 amend digital multiplexer 1. Quad 2 input analog mux 1. SCD to 7 ament latch/decoder/difver. 1. SCD to 7 ament latch/decoder/difver. 1. SCD to 7 ament latch/decoder/difver. 1.	85 989 989 665 79 925 110 10 10 30 05 335 335 50 37	XR2206 XR2208 XR2208 XR2208 XR2208 XR2211 XR2211 XR2212 XR2567 XR14412 7400 7402 7403 7404 7405 7406 7406 7407 7407 7408 7408 7408	Monotime function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision pass locked loop Long range filmer Dual monotithic tone decoder Dual flow nois eo p. amp FSK modern system 7400 SERIES TTL Quad 2 input NAND gate Quad 2 input NAND gate Quad 2 input NAND gate Oused 2 input NAND gate OCC Hest inverse gate OCC Hest inverse put lerddriver Hest Dufferddriver Quad 2 input AND gate	4 95 2.75 2.99 3.25 6.99 8.99 2.25 2.40 1.55 8.75 49 49 49 49 49 49 49 49 49 49 49	74ALS11 74ALS12 74ALS15 74ALS20 74ALS20 74ALS21 74ALS28 74ALS28 74ALS33 74ALS33 74ALS35 74ALS37 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31	Triple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS AND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Cual 4 input POS NOR buffer Briput POS NAND gate Cuadruple 2 input POS NOR gate Cuadruple 2 input POS NOR buffer OC Hex non Inverter with DIC output Quadruple 2 input POS NOR buffer OC Use Input NAND buffer Cuadruple 2 input POS NAND buffer OC. Dual 1 AND/Invert gate Dual 0 type POS edge triggered F/F Quadruple 2 input exclusive OR gate Dual 1 type POS edge triggered F/F Dual JK NEG edge triggered F/F Dual JK NEG edge triggered Flip flop with preset and clear Dual JK NEG edge triggered Flip flop with preset	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F20 74F32 74F32 74F36 74F109 74F112 74F113 74F114 74F138 74F139	Quad Z Input NOR gate Hex inverter Quad Z Input NND gate Triple 3 Input NND gate Triple 3 Input NND gate Triple 3 Input NND gate Dual 4 Input NAND gate Quad Z Input NOR gate Dual 4 Input NOR gate Dual 5 Input NOR gate Dual 5 Input NOR gate Dual J K NEG edge triggered flip flop Dual J K NEG edge triggered flip flop Dual J K NEG edge triggered flip flop Dual J K NEG edge ingered flip flop Dual J K NEG edge ingered flip flop Dual J K NEG edge ingered flip flop Dual J M NEG edge ingered flip flop Dual J M NEG edge ingered flip flop Dual I M NEG edge ingered flip flop	73 73 73 73 73 73 73 88 85 99 1.99 1.79 1.79
4516 4518 4518 4520 4521 4522 4528 4527 4528 4530 4531 4532 4534 4538 4538 4538	1 of 16 decoder/demultiplexer Binary upridown counter 9.5 Dual BCD up counter 4.5 Abit AND/DR elecit gate 6.6 Dual binary up counter 4.2 A state frequency divider 9.6 BCD divide by N counter 4.1 BCD rate multiplier 1.1 BCD rate multiplier 1.1 Dual 1 elinggreable/reset/table monostable 1.2 bit lang type electrone 1.2 bit lang type electrone 1.3 Bit lipriority encoder 1.3 Bit lipriority encoder 1.4 Bat limp 5 decade counter 1.7 Programmable timer 1.7 Dual 4 channel mux 1.7 Dual 5 decade counter 1.7 Dual 9 decision monostable multivibrator 1.3 Dual 4 channel digital multiplexer 1.3 Dual 4 channel digital multiplexer 1.3 Dual 1 generation for the decade counter 1.3 Dual 1 generation mux 1.3 BCD 10.7 segment last indexeder division 2.5 Dual 1 generation for the decader division 2.5 Dual 1 generation for the decader division 2.6 Dual binery 1 of 4 decoder	85 89 89 65 79 99 225 10 10 10 10 10 10 10 10 10 10 10 10 10	XR2206 XR2207 XR2208 XR2209 XR2208 XR2211 XR2211 XR2211 XR2242 XR247 XR4739 XR14412 7400 7402 7403 7403 7404 7406 7406 7406 7407 7408 7408 7408 7410 7411 7411	Monotime function generator voltage controlled oscillator Operation multiplier Precision oscillator Dual monotithic tone decoder Dual monotithic tone decoder Dual oscillator Dual oscillator Dual oscillator Dual oscillator Dual oscillator Dual oscillator PSK modern system 7400 SERIES TL Quad Zinput NAND gate Quad Zinput NAND gate Ousd Zinput NAND gate Mexinverter gate OIC Hexinverter but ferdirfiver Hex butferdirfiver Hex butferdirfiver Hex butferdirfiver Hex butferdirfiver Triple 3 input NAND gate Vitile 3 input NAND gate	4 95 2.75 2.99 3.29 3.29 3.29 5.69 8.99 8.99 2.245 2.25 2.240 1.55 8.75 4.9 4.9 4.9 5.5 6.5 6.5 6.5 6.5 6.5 6.5 6.5 6.5 6.5	74ALS11 74ALS12 74ALS20 74ALS20 74ALS20 74ALS27 74ALS28 74ALS30 74ALS37 74ALS38 74ALS37 74ALS38 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31	Tiple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS AND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Occupant POS NAND gate Occupant POS NAND gate Occupant POS NAND gate Ouddruple 2 input POS NOR gate Ouddruple 2 input POS NOR gate Ouddruple 2 input POS NOR buffer OC Hex non Inverter with DO'C output Ouddruple 2 input POS NAND Duffer OC Dual AND/invert gate Dual D type POS edge triggered F/F Ouddruple 2 input exclusive OR gate Ouddruple 2 input exclusive OR gate Dual JK ROS edge triggered F/F Dual JK ROS edge triggered F/F Dual JK NES edge triggered F/F	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F08 74F08 74F10 74F10 74F10 74F20 74F13 74F14 74F13 74F112 74F138 74F14 74F138 74F148 74F161 74F157 74F157 74F157	Quad Z Input NOR gate Mex inverter Quad Z Input AND gate Triple 3 Input AND gate Triple 3 Input AND gate Triple 3 Input AND gate Ouad Z Input AND gate Quad Z Input AND gate Quad Z Input NOR gate Quad X NOR Gate Gate Ingered Inp Input AND gate Input Mex Gate Ingered Inp Input AND gate Quad X NEG edge Ingered Inp Input AND gate Input AND gate Input AND gate Input In	73 73 73 73 73 73 73 73 73 88 88 85 99 1.99 1.79 1.79 1.79 1.79 1.79 1.79
4516 4518 4519 4520 4521 4521 4522 4528 4528 4528 4531 4531 4534 4536 4538 4539 4541 4541 4555 4556 4556	1 of 16 decoder/demultiplexer Binary upridown counter 9.5 Dual BCD up counter 4.5 Abit AND/DR elecit gate 6.6 Dual binary up counter 4.2 A state frequency divider 9.6 BCD divide by N counter 4.1 BCD rate multiplier 1.1 BCD rate multiplier 1.1 Dual 1 elinggreable/reset/table monostable 1.2 bit lang type electrone 1.2 bit lang type electrone 1.3 Bit lipriority encoder 1.3 Bit lipriority encoder 1.4 Bat limp 5 decade counter 1.7 Programmable timer 1.7 Dual 4 channel mux 1.7 Dual 5 decade counter 1.7 Dual 9 decision monostable multivibrator 1.3 Dual 4 channel digital multiplexer 1.3 Dual 4 channel digital multiplexer 1.3 Dual 1 generation for the decade counter 1.3 Dual 1 generation mux 1.3 BCD 10.7 segment last indexeder division 2.5 Dual 1 generation for the decader division 2.5 Dual 1 generation for the decader division 2.6 Dual binery 1 of 4 decoder	85 89 89 65 79 99 225 10 10 10 10 10 10 10 10 10 10 10 10 10	XR2206 XR2207 XR2208 XR2209 XR2211 XR2211 XR2242 XR2242 XR2242 XR4739 XR14412 7400 7402 7403 7405 7405 7407 7407 7407 7407 7407 7407	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision passe locked floop Long range filmer Dual monoitable to decoder Dual flow noise op amp FSK modern system 7400 SERIES TTL Quad Zinput NAND gate Quad Zinput NAND gate Quad Zinput NAND gate O/C Hex inverter Lex bufferfriver Quad Zinput AND gate O/C Hex inverter Quad Zinput AND gate Visit Province	4.95 2.75 2.99 3.25 6.99 2.225 2.40 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9	74AL511 74AL512 74AL515 74AL520 74AL520 74AL521 74AL527 74AL528 74AL530 74AL533 74AL533 74AL537 74AL531 74AL5114 74AL5114 74AL513	Tiple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NOR gate Ouadruple 2 input POS NOR buffer 8 input POS NAND gate Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR buffer OC Hex non inverter with O/C output Ouadruple 2 input POS NAND buffer Ouadruple 2 input POS NAND buffer Ouadruple 2 input POS NAND buffer OC Dual 1 NDInverti gate Dual Divpe POS adge triggered Fire Dual Divpe POS adge triggered Fire Dual I NEG edge triggered Fire Dual I NEG edge triggered Fire Dual I NEG edge triggered filp flop with preset and clear Dual JK NEG edge triggered Fire The State Post New Date See See See See See See See See See S	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F08 74F08 74F10 74F11 74F20 74F11 74F20 74F32 74F14 74F13 74F113 74F114 74F138 74F14 74F138 74F153 74F153 74F153 74F153 74F153	Quad 2 input NOR gate Hex inverter Quad 2 input NO gate Triple 3 input NAND gate Triple 3 input NAND gate Triple 3 input NAND gate Dual 4 input NAND gate Quad 2 input NOR gate Dual 4 input NAND gate Quad 2 input NOR gate Dual 5 input NATE gate Triple Dual 5 input NATE gate Triple Dual 5 input NATE gate Triple Stop 3 ine decoder/multiplexer Dual 4 input multiplexer Dual 5 input multiplexer Dual 5 input multiplexer Dual 5 input multiplexer Dual 5 input multiplexer Dual 6 input multiplexer Ouad 2 to 1 ine data selector Quad 2 input multiplexer Synch presettable BCD decade	
4516 4518 4519 4520 4521 4521 4528 4528 4528 4528 4530 4530 4530 4530 4530 4530 4530 4530	1 of 16 decoder/demultiplexer Binary up/down counter 9.5 Dual BCD up counter 8.6 Abit AND/DR select gate 9.6 Dual binary up counter 1.2 Select gate 1.3 BCD divide by N counter 1.3 BCD divide by N counter 1.4 BCD rate multipler 1.5 Dual 4 channel mux 1.5 Dual 4 channel mux 1.5 Dual 4 channel mux 1.6 1.2 Dual 5 ingury encoder 1.7 Real time 5 decade counter 7.7 Programmable time 1.0 Dual 4 channel digital multiplexer 1.3 Soli priority analog must gate 1.3 Dual 4 channel digital multiplexer 1.3 Soli procipi analog mux 1.3 Dual 4 channel digital multiplexer 1.3 Soli procipi analog mux 1.3 Dual 4 channel digital multiplexer 1.3 Soli procipi counter 1.4 BCD to 7 segment latch/decoder/driver 1.5 Soligl BCD counter 1.5 Dual 1 decoder 1.6 Dual binery 1.01 decoder 1.6 Soli procipi register 1.6 BCD to 7 segment decoder 1.7 BCD adder 1	85 89 89 89 89 99 99 25 10 10 10 10 30 53 53 53 73 30 53 75 89 89 89 89 89 89 89 89 89 89 89 89 89	XR2206 XR2207 XR2208 XR2209 XR2211 XR2211 XR2211 XR2242 XR2242 XR2242 XR4739 XR14412 7400 7402 7403 7404 7405 7406 7407 7408 7407 7409 7411 7414 7414 7416 7417 7416 7417 7416 7417 7416 7417 7416 7417 7416 7417 7416 7417 7416 7417 7416 7417 7416 7417 7416 7417 7416 7417 7417 7418 7417 7417 7418 7417 7417 7418 7417 7418 7417 7417 7417 7417 7418 7417 7417 7417 7417 7417 7418 7417 7417 7418 7417 7418 7417 7417 7418 7417 7420	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSR dem odulator FSR modern odulator Dual monolithic tone decoder Dual inwo noise op amp FSR modern system 7400 SERIES TTL Quad 2 input NAND gate Quad 2 input NAND gate Quad 2 input NAND gate OV Hex inverter Mex inverter gate OV Hex inverter Mex inverter in the inverter Mex inverter in NAND gate Quad 2 input AND gate Quad 2 input AND gate Quad 2 input AND gate FSR inverter Mex Dufferdriver Mex Schmitt in tigger Hex Schmitt in tigger Hex Schmitt in tigger Hex but ferdriver	4.95 2.75 2.99 3.25 6.99 2.225 2.40 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9	74AL511 74AL512 74AL521 74AL520 74AL520 74AL521 74AL527 74AL527 74AL532 74AL532 74AL533 74AL533 74AL534 74AL534 74AL534 74AL534 74AL534 74AL534 74AL534 74AL537 74AL536 74AL511 74AL5113 74AL5113 74AL5133	Tiple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Ouadruple 2 input POS NOR buffer 3 input POS NAND gate Ouadruple 2 input POS NOR buffer Ouadruple 2 input POS NOR buffer OC Hex non inverter with O/C output Ouadruple 2 input POS NAND buffer Dual D type POS edge triggered Fir Ouadruple 2 input invertix POS NAND buffer Dual JK NGE edge triggered Filp flop with preset Dual JK NGE edge triggered Filp flop with preset Dual JK NGE edge triggered Filp flop with preset Dual JK NGE edge triggered Filp flop with preset Dual JK NGE edge triggered Filp flop with preset Dual JK NGE edge triggered Filp flop with preset Dual JK NGE Ogge triggered Filp flop wi	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F08 74F08 74F10 74F10 74F10 74F20 74F13 74F14 74F13 74F112 74F138 74F14 74F138 74F148 74F161 74F157 74F157 74F157	Quad Z Input NOR pate Hex inverter Quad Z Input NO gate Triple 3 input NAND gate Triple 3 input NAND gate Triple 3 input NAND gate Dual 4 input NAND gate Quad Z Input NOR gate Dual 4 input NAND gate Quad Z Input NOR gate Dual J K PG Gedge triggered flip flop Dual J K NEG edge triggered flip flop Jual S NEG edge triggered flip flop Dual J K NEG edge triggered flip flop Jual 14 decoder demultiplexer Botal fine priority encoder Sinput multiplexer Dual 4 input multiplexer Dual 5 input multiplexer Ouad 2 to 1 item data selector Quad 2 to 1 item data selector	
4516 4518 4519 4520 4521 4521 4522 4528 4527 4528 4530 4531 4532 4534 4533 4533 4538 4538 4541 4641 4641 4643 4553 4555 4556 4556 4556 4556 4556	1 of 16 decoder/demultiplexer Binary up/down counter 9.5 Dual BCD up counter 8.6 Abit AND/DR select gate 9.6 Dual binary up counter 1.2 Select gate 1.3 BCD divide by N counter 1.3 BCD divide by N counter 1.4 BCD rate multipler 1.5 Dual 4 channel mux 1.5 Dual 4 channel mux 1.5 Dual 4 channel mux 1.6 1.2 Dual 5 ingury encoder 1.7 Real time 5 decade counter 7.7 Programmable time 1.0 Dual 4 channel digital multiplexer 1.3 Soli priority analog must gate 1.3 Dual 4 channel digital multiplexer 1.3 Soli procipi analog mux 1.3 Dual 4 channel digital multiplexer 1.3 Soli procipi analog mux 1.3 Dual 4 channel digital multiplexer 1.3 Soli procipi counter 1.4 BCD to 7 segment latch/decoder/driver 1.5 Soligl BCD counter 1.5 Dual 1 decoder 1.6 Dual binery 1.01 decoder 1.6 Soli procipi register 1.6 BCD to 7 segment decoder 1.7 BCD to 7 segment decoder	85 89 89 89 89 99 99 25 10 10 10 10 30 53 53 53 73 30 53 75 89 89 89 89 89 89 89 89 89 89 89 89 89	XR2206 XR2207 XR2208 XR2209 XR2211 XR2211 XR2212 XR2242 XR2242 XR2424 XR4739 XR14412 7400 7402 7403 7404 7405 7406 7407 7408 7407 7408 7411 7414 7414 7414 7414 7416 7417 7420 7423	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSR dem odulator FSR modern odulator Dual monosithic tone decoder Dual invonice op amp FSR modern system 7400 SERIES TTL Quad 2 input NAND gate Quad 2 input NAND gate Quad 2 input NAND gate OV Hex inverter gate OV Hex inverter Hex inverter put of the decoder Hex inverter in the decoder Hex butterdriver Quad 2 input AND gate withOVC Triple 3 input AND gate Quad 2 input AND gate Hex Schmitt intigger Hex Schmitt intigger Hex Schmitt intigger Hex Dutlerdriver Hex butferdriver Dual 4 input NAND gate Hex Dutferdriver Hex butferdriver Hex butferdriver Hex butferdriver Dual 4 input NAND gate Dual 2 input NAND gate	4.95 2.95 6.99 2.25 6.99 2.225 8.75 8.75 8.75 8.75 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9	74ALS11 74ALS12 74ALS21 74ALS20 74ALS20 74ALS27 74ALS26 74ALS27 74ALS27 74ALS37 74ALS37 74ALS31 74ALS313	Tiple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NOR Botter Dual 4 input POS NOR gate Ouadruple 2 input POS NOR gate Ouadruple 2 input POS NOR butter 0 input POS NOR butter 0 uadruple 2 input input POS NOR butter 0 uadruple 2 input pOS NOR butter 0 uadruple 2 input input pos NAND butter 0 uad input pos input inguiered Fir Filip (flow with preset inguered Fir Filip (flow with preset inguered Fir Filip (flow with preset inguered Fir Tiput POS NAND gate 0 uad Vates of the decement of the present common, clearic lock 1 input POS NAND gate 0 uad vactusive OR gate OC 1 input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS uad vactusive OR gate OC 1 input POS uad vactusive OR 1 input POS	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F10 74F20 74F32 74F74 74F109 74F113 74F113 74F13 74F13 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151 74F151	Quad Z Input NOR gate Nex inverter Quad Z Input NAND gate Triple 3 Input NAND gate Dual 4 Input NAND gate Quad Z Input NOR gate Dual 4 Input NAD gate Dual J K PEG Sedge triggered flip flop Dual J K NEG edge triggered flip flop Dual J K NEG edge triggered flip flop Dual J K NEG edge ingered flip flop Dual J K NEG edge ingered flip flop Dual J M neger flip flop	73 73 73 73 73 73 73 73 73 88 85 85 85 87 99 1.79 1.79 1.79 1.79 1.79 1.79 1.75 1.75 1.75 1.75 1.75 1.75 1.75 1.75
4516 4518 4519 4520 4521 4522 4528 4529 4530 4531 4532 4531 4532 4536 4531 4538 4531 4538 4531 4538 4539 4541 4541 4555 4565 4561 4561 4561 4561	1 of 16 decoder/demultiplexer Binary upridown counter 9.5 Dual BCD up counter 4.5 Abit AND/DR elect gate 6.6 Dual binary up counter 4.2 A state frequency divider 9.6 BCD divide by N counter 4.1 BCD rate multiplier 1.1 BCD rate multiplier 1.1 Dual 16 riggerable/reset/table monostable 1.2 bit langer greater and the state of the st	85 89 89 89 89 99 99 25 10 10 10 10 30 53 53 53 73 30 53 75 89 89 89 89 89 89 89 89 89 89 89 89 89	XR2206 XR2207 XR2209 XR2209 XR22111 XR22121 XR2242 XR2567 XR4739 XR14412 7400 7402 7403 7405 7407 7407 7407 7407 7407 7407 7407	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision passe locked loop Long range filmer Dual monosithic tone decoder Dual low nois eo pamp FSK modern system 7400 SERIES TTL Quad 2 input NAND gate Meximetriar bufferid/river Hex invertier gate O/C Hex invertier Dual 2 input AND gate withO/C Triple 3 input AND gate Hex Schmitt irtiger Hex invertier bufferid/river Hex Schmitt irtiger Hex invertier bufferid/river Hex Schmitt irtiger Hex invertier bufferid/river Hex bufferid/river Dual 4 input NAND gate Dual 2 input NAND gate Dual 4 input NAND gate Dual 4 input NAND gate	4.95 2.75 3.25 6.99 2.25 6.99 2.25 6.99 2.240 1.55 8.75 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9	74ALS11 74ALS12 74ALS21 74ALS20 74ALS20 74ALS27 74ALS26 74ALS30 74ALS37 74ALS37 74ALS37 74ALS38 74ALS31	Tiple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NOR Botter Dual 4 input POS NOR gate Ouadruple 2 input POS NOR gate Ouadruple 2 input POS NOR butter 0 input POS NOR butter 0 uadruple 2 input input POS NOR butter 0 uadruple 2 input pOS NOR butter 0 uadruple 2 input input pos NAND butter 0 uad input pos input inguiered Fir Filip (flow with preset inguered Fir Filip (flow with preset inguered Fir Filip (flow with preset inguered Fir Tiput POS NAND gate 0 uad Vates of the decement of the present common, clearic lock 1 input POS NAND gate 0 uad vactusive OR gate OC 1 input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS NAND gate 0 uad vactusive OR gate OC 1 in input POS uad vactusive OR gate OC 1 input POS uad vactusive OR 1 input POS	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F06 74F10 74F10 74F10 74F12 74F32 74F32 74F109 74F114 74F113 74F114 74F113 74F114 74F115 74F150 74F150 74F160 74F160 74F160 74F160 74F160 74F160 74F160 74F160 74F160 74F160 74F160 74F160 74F160 74F160 74F160	Quad 2 input NOR gate Meximenter Quad 2 input AND gate Triple 3 input AND gate Quad 2 input AND gate Quad 2 input AND gate Quad 2 input NOR gate Quad 2 input NOR gate Quad 3 input AND gate Quad 4 input AND gate Quad 5 input Gate Quad 5 input Gate Quad 5 input Multiplexer Quad 5 input multiplexer Quad 5 input multiplexer Synch presettable BCD decade Quad 1 in praile input Gate Serial in, parallel out shift register Mex D flip flop Quad 10 input flip	
4516 4518 4519 4520 4521 4522 4528 4528 4530 4531 4531 4532 4534 4536 4538 4539 4541 4543 4553 4564 4566 4566 4566 4566 4566	1 of 16 decoder/demultiplexer Binary upridown counter 9.5 Dual BCD up counter 4.5 Abit AND/DR elect gate 6.6 Dual binary up counter 4.2 Astate frequency divider 1.5 BCD divide by N counter 4.1 BCD rate multiplier 1.1 BCD rate multiplier 1.1 Dual 1 elinggreable/reset/able monostable 1.2 bit lang type electrone 1.2 bit lang type electrone 1.3 bit priority eneralorichecker 1.3 bit priority eneralorichecker 1.4 8 bit priority eneralorichecker 1.5 Real time 5 decade counter 1.7 Programmable timer 1.1 Dual 6 channel digital multiplexer 1.2 Dual 1 eliname digital multiplexer 1.3 Dual 4 channel digital multiplexer 1.4 Dual 1 binary 1 of 4 decoder 1.5 Dual binary 1 of 4 decoder 1.5 Dual binary 1 of 4 decoder 1.5 BCD 10 7 segment decoder 1.5 BCD 10 degment decoder 1.5 BCD 10 degment decoder 1.5 BCD 10 degment decoder 1.5 BCD 10 7 segment decoder 1.5 BCD 10 7 segment decoder 1.5 BCD 10 Temper 1.5 BCD 10 7 segment decoder 1.5 BCD 10 Temper 1.5 BCD 10 7 segment decoder 1.5 BCD 10 Temper 1.5 BCD 10 7 segment decoder 1.5 BCD 10 Temper 1.5 BCD 10 7 segment decoder 1.5 BCD 10 Temper 1.5 BCD 10 7 segment decoder 1.5 BCD 10 Temper 1.5 BCD 10 7 segment decoder 1.5 BCD 10 Temper 1.5 BCD 10 7 segment decoder 1.5 BCD 10 Temper 1.5 BCD 10 T	85 95 95 95 95 95 95 95 95 95 95 95 95 95	XR2206 XR2207 XR2209 XR2209 XR22111 XR22121 XR2242 XR2567 XR4739 XR14412 7400 7402 7403 7403 7404 7405 7407 7407 7407 7407 7407 7407	Monotime function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSK demodulator/fone decoder Precision passe locked loop Long range filmer Dual monotithic tone decoder Dual low noise op amp FSK modem system 7400 SERIES TTL Quad 2 Input NAND gate Quad 2 Input NAND gate Quad 2 Input NAND gate OCC Mex Inverter Louise Committee of the Committee	2.75 2.75 2.29 6.99 2.25 6.99 2.25 6.99 2.25 8.75 8.75 8.75 8.75 8.75 8.75 8.75 8.7	74ALS11 74ALS21 74ALS21 74ALS20 74ALS20 74ALS27 74ALS27 74ALS30 74ALS33 74ALS33 74ALS33 74ALS31 74ALS313 74ALS313 74ALS313 74ALS313	Triple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NOR NAND gate Ouadruple 2 mout POS NOR butler 8 input POS NAND gate Ouadruple 2 mout POS NOR Dutler Ouadruple 2 input POS NAND Dutler OC Dual AND/Invert gate Dual D type POS edge triggered FiF Ouadruple 2 input exclusive OR gate Dual JK Ros dege triggered Fip flop with preset Dual JK NEG edge triggered Fip flop with preset Dual JK NEG edge triggered Fip flop with preset Dual JK NEG edge triggered Fip flop with preset Dual JK NEG edge triggered Fip flop with preset Ouad Carpet OR AND Dutler OC Dual 4 inc POS NAND Dual 1 inc POS NA	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F11 74F20 74F32 74F32 74F113 74F114 74F113 74F114 74F139 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169	Quad Z Input NOR gate Nex inverter Quad Z Input NAND gate Triple 3 Input NAND gate Triple 3 Input NAND gate Triple 3 Input NAND gate Dual 4 Input NAND gate Dual 4 Input NAND gate Dual 4 Input NAND gate Dual 5 Input Inpu	73 73 73 73 73 73 73 73 73 88 85 85 89 1.99 1.79 1.79 1.79 1.75 1.75 1.75 1.75 1.75 1.75 1.75 1.75
4516 4518 4519 4520 4521 4522 4528 4528 4530 4531 4532 4534 4534 4536 4539 4541 4541 4541 4555 4566 4566 4566 4566	1 of 16 decoder/demultiplexer Binary upridown counter 9.5 Dual BCD up counter 4.5 Abit AND/DR elect gate 6.6 Dual binary up counter 4.2 Astate frequency divider 9.6 BCD divide by N counter 4.1 BCD rate multiplier 1.1 BCD rate multiplier 1.1 Dual 16 input majority logic gate 1.2 Dual 15 input majority logic gate 1.2 Divider by N counter 4.6 Bib lipriority encoder 1.6 Bib lipriority encoder 1.7 Beat lime 5 decade counter 2.7 Programmable timer 1.0 Dual 5 decade counter 2.7 Quad 2 input analog mux 8.0 Dual brained input analog mux 8.0 Dual brained input analog mux 8.0 Dual brained decader 1.6 Dual binary 1 of 4 decoder 1.7 BCD to 7 segment later hide-coder divier. 1.8 Dual binary 1 of 4 decoder 1.9 BCD to 7 segment decoder 1.1 BCD tates thift register 1.1 Brase comparatoriprogrammable 2.4 Brase comparatoriprogrammable 2.5 Brase Counter 2.5 Brase Counter 2.5 Brase Counter 2.5 Brase Counter 2.6 Brase Counter 2.7 Brase Comparatoriprogrammable 2.8 Bounter 3.6 Brase Counter 3.6 Brase Counter 3.7 Brase Counter 3.7 Brase Counter 3.7 Brase Counter 3.7 Brase Counter 3.8 Brase Counter 3.8 Brase Counter 3.9 Brase Counte	85 95 95 95 95 95 95 95 95 95 95 95 95 95	XR2206 XR2207 XR2209 XR2209 XR2211 XR2211 XR2211 XR2242 XR2567 XR4739 XR14412 7400 7402 7403 7407 7407 7407 7408 7407 7409 7411 7414 7414 7417 7417 7422 7425 7426 7427 7432	Monotime function generator Voltage controlled oscillator Operation multiplier Precision oscillator Investigation of the Controlled oscillator Operation multiplier Precision oscillator Operation multiplier Precision pales locked loop Long range timer Dual monotithic tone decoder Dual low noise op amp FSK modem system 7400 SERIES TTL Quad 2 Input NAND gate OCHex inverter but feld friver Hex bufferd friver Guad 2 Input AND gate Quad 2 Input AND gate Wilhold Controlled Input NAND gate Wilhold Shipput NAND gate Wes Schmitt (ligger Hex Schmitt (ligger He	4.95 2.75 2.99 3.25 6.99 2.25 6.99 2.25 6.99 2.25 8.75 8.75 8.75 8.75 8.75 8.75 8.75 8.7	74ALS11 74ALS12 74ALS21 74ALS20 74ALS21 74ALS27 74ALS28 74ALS30 74ALS33 74ALS33 74ALS33 74ALS31	Tiple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NOR buffer Ouadruple 2 input POS NOR buffer 8 input POS NAND gate Ouadruple 2 input POS NOR gate Ouadruple 2 input POS NOR buffer OC Idex non inverter with DIC output Ouadruple 2 input POS NOR buffer OC Dual 1 input P	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F11 74F20 74F32 74F32 74F113 74F114 74F113 74F114 74F139 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169 74F169	Quad Z Input NOR gate Nex inverter Quad Z Input NAND gate Triple 3 Input NAND gate Triple 3 Input NAND gate Triple 3 Input NAND gate Dual 4 Input NAND gate Dual 4 Input NAND gate Dual 4 Input NAND gate Dual 5 Input Inpu	73 73 73 73 73 73 73 73 73 88 85 85 89 1.99 1.79 1.79 1.79 1.75 1.75 1.75 1.75 1.75 1.75 1.75 1.75
4516 4518 4519 4520 4521 4521 4528 4528 4527 4530 4531 4533 4534 4533 4534 4534 4534 4541 4541	1 of 16 decoder/demultiplexer Binaryup/down counter 9.5 Dual BCD up counter 8.6 Abit AND/DR select gate 6.6 Dual binary up counter 9.7 24 state frequency divider 9.7 14 step to the select gate 9.6 15 binary up counter 1.7 15 binary up counter 1.7 15 binary up counter 1.7 15 binary divide by N counter 1.7 16 binary divide by N counter 1.7 16 binary divide by N counter 1.7 16 binary decoder 1.7 17 18 binary decoder 1.7 18 binary 1 of 4 decoder 1.8 2.9 2.9 3.0 complementer 1.9 3.0 counter 1.9 3.0 counter 1.9 3.0 counter 1.9 3.0 counter 3.0 counte	85 95 98 98 98 98 98 98 98 98 98 98 98 98 98	XR2206 XR2207 XR2208 XR2209 XR2211 XR2212 XR2242 XR2567 XR4739 XR14412 7400 7402 7402 7403 7404 7406 7407 7406 7407 7417 7417 7417 7417 7417 7417 7417	Monostrinic function generator Voltage controlled oscillator Operation multiplier Pickion oscillator Pickion	4 95 2.75 2.75 3.25 6.99 8.99 2.25 6.99 2.25 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9 4.9	74ALS11 74ALS21 74ALS21 74ALS20 74ALS20 74ALS27 74ALS28 74ALS30 74ALS33 74ALS33 74ALS33 74ALS31	Triple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS AND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate Occupant POS NAND buffer OC Hex non Inverter with DO'C output Occupant POS NAND buffer OC Dual AND/invert gate Dual D type POS edge triggered Fire Occupant POS NAND gate Occupant POS NAND gate Occupant POS NAND gate Occupant POS NAND gate Dual JK POS HOR gate Occupant POS NAND gate Occup	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F08 74F08 74F10 74F11 74F10 74F11 74F20 74F32 74F32 74F112 74F113 74F113 74F116 74F18	Quad Z Input NOR gate Mex inverter Quad Z Input AND gate Triple 3 Input AND gate Dual 4 Input NAND gate Quad Z Input NOR gate Dual 4 Input NAND gate Dual 5 Input In	
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4516 4518 4519 4520 4521 4521 4522 4528 4527 4528 4530 4530 4530 4530 4530 4530 4530 4530	1 of 16 decoder/demultiplexer Binaryup/down counter 9.9 Dual BCD up counter 8.8 Abit AND/DR select gate 8.6 Dual binary up counter 9.7 24 state frequency divider 9.7 Selection of the selection	85 95 88 95 85 55 79 99 99 99 99 99 99 99 99 95 55 55 55 55	XR2206 XR2207 XR2208 XR2209 XR22112 XR2212 XR2212 XR2242 XR2242 XR2567 XR4739 7403 7404 7406 7407 7408 7408 7408 7408 7408 7408 7408 7408 7407 7411 7412 7412 7412 7410 7411 7411 7412 7412 7413 7414 7416 7417 7417 7418 7418 7419 7429 7439 7439 7440 7441 7441 7441 7442 7442 7443 7444 7446 7447 7448 7449 7459 7	Monotime function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSR demodulatorifone decoder Precision passe locked loop Dual monotime of the decoder Dual monotime of the decoder Dual in monotime op amp FSK modern system 7400 SERIES TTL Quad 2 input NAND gate Quad 2 input NAND gate Quad 2 input NAND gate Occurred Rex inverter gate OiC Hex inverter gate OiC Hex inverter gate OiC Guad 2 input NAND gate OiC Hex inverter in the decoder Hex inverter butterdriver Hex butferdriver Quad 2 input NAND gate Quad 2 input NAND gate Dual 4 input NAND gate Hex Schmitt iftiger Hex inverter butferdriver Hex butferdriver Dual 4 input NAND gate Quad 2 input NAND gate Dual 4 input NAND gate Quad 2 input gate Dual 4 input NAND gate Dual 4 input NAND gate Dual 4 input NAND butfer Dual 4 input NAND butfer Dual 4 input NAND butfer But 4 input NAND butfer BCD-Good in an input gate gate BCD-Go 7 segment dieved erdriver BCD-Do 7 segment decoderdriver 2 and over gate A wide and/or input gate A wide and/or input gate	4 995 2 297 2 397 2 49 49 49 49 49 49 49 49 49 49	74AL511 74AL512 74AL521 74AL520 74AL520 74AL521 74AL522 74AL521 74AL532 74AL533 74AL533 74AL531 74AL531 74AL531 74AL531 74AL511 74AL511 74AL511 74AL511 74AL511 74AL511 74AL5136 74AL5137	Triple 3 input POS NAND gate Triple 3 input POS AND gate Triple 3 input POS AND gate Triple 3 input POS AND gate OC Triple 3 input POS AND gate OC Triple 3 input POS AND gate OC Dual 4 input POS AND gate Occupant POS AND buffer OC Her non Inverter with DOS AND Duffer OC Dual AND/invert gate Dual Dripe POS Gate (riggered Fife Occupant POS AND Duffer OC Dual JK POS Gate (riggered Fife Dual JK NEG edge triggered Fife Dual JK NEG edge t	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F11 74F11 74F11 74F12 74F12 74F13 74F14 74F13 74F15 74F16	Quad 2 input NOR gate Nex inverter Quad 2 input AND gate Triple 3 input AND gate Quad 2 input AND gate Quad 2 input AND gate Quad 2 input NOR gate Quad 2 input NOR gate Quad 2 input NOR gate Quad 3 input AND gate Ripf lop Quad 3 in AND gate Quad 3 input gate Ripf lop Quad 3 input gate Quad 3 input gate Ripf lop Quad 3 input gate Quad 4 input gate Quad 5 input gate Quad 5 input gate Quad 5 input gate Quad 5 input gate Quad 2	
4516 4518 4519 4520 4521 4522 4528 4530 4531 4532 4533 4534 4536 4536 4539 4541 4541 4541 4541 4555 4560 4560 4560 4560 4560 4560 4560	1 of 16 decoder/demultiplexer Binaryup/down counter 9.9 Dual BCD up counter 8.8 Abit AND/DR select gate 8.6 Dual binary up counter 9.7 24 state frequency glvider 9.7 Selection of the selection	855 9595 889 9595 9595 9595 9595 9595 9	XR2206 XR2207 XR2209 XR2209 XR2211 XR2211 XR2211 XR2211 XR2242 XR2567 XR4739 XR14412 7400 7402 7403 7403 7404 7405 7406 7407 7408 7408 7407 7408 7407 7408 7408 7407 7408 7408 7409 7417 7422 7427 7432 7433 7436 7437 7437 7438 7448 7448 7448 7448 7451	Monotime function generator Voltage controlled oscillator Operation multiplier Precision oscillator Precision oscillator PSR demodulator/fone decoder Precision pass locked floop Long rangel lime Precision pass locked floop Long rangel lime To the pass locked floop Long rangel lime Dual flow noise op amp PSK modern system 7400 SERIES TTL Quad 2 input NAND gate O/C Hex inverter Hex inverter but flordriver Hex but flordriver Quad 2 input AND gate with O/C Triple 3 input AND gate Quad 2 input AND gate Quad 2 input AND gate PRESIDENT STAND GATE Triple 3 input AND gate Mex Schmitt tripger Hex inverter but flordriver Mex but flordriver Dual 4 input NAND gate Dual 2 input NAND gate Quad 2 input NAND butfer But a input NAND butfer Bu	4 995 3 959 8 99 8 99 9 9	74ALS11 74ALS12 74ALS21 74ALS21 74ALS20 74ALS21 74ALS21 74ALS22 74ALS38 74ALS38 74ALS38 74ALS31 74ALS11 74ALS12 74ALS12 74ALS12 74ALS12 74ALS12 74ALS12 74ALS12 74ALS12 74ALS12 74ALS2 74ALS2 74ALS2 74ALS2 74ALS2 74ALS2 74ALS2	Triple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Occupant POS NAND potter OC Hear non Inverter with DCI Coutput Occupant POS NAND buffer OC Dual AND/invert gate Dual D type POS edge triggered Fire Occupant POS NAND buffer OC Ouddruple 2 input POS NAND buffer OC Ouddruple 2 input POS NAND buffer OC Dual AND/invert gate Dual J K POS HOR Gate Occupant POS NAND Duffer OC Dual AND/invert gate Dual JK ROS edge triggered Fire Dual JK NES edge triggered Fire Dual JK NES edge triggered Fire Dual JK NES edge triggered Filip flop with preset Dual JK NES edge triggered Filip flop with preset Occupant Pos Horse Occupant Pos NAND Dual JK NES edge triggered Filip flop with preset Dual JK NES edge triggered Filip flop with preset Dual JK NES edge triggered Filip flop with preset Dual JK Nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK Nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with preset Dual JK nes edge triggered Filip flop with pr	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F11 74F11 74F11 74F12 74F12 74F13 74F14 74F13 74F15 74F16	Quad 2 input NOR gate Nex inverter Quad 2 input AND gate Triple 3 input AND gate Quad 2 input AND gate Quad 2 input AND gate Quad 2 input NOR gate Quad 2 input NOR gate Quad 2 input NOR gate Quad 3 input AND gate Ripf lop Quad 3 in AND gate Quad 3 input gate Ripf lop Quad 3 input gate Quad 3 input gate Ripf lop Quad 3 input gate Quad 4 input gate Quad 5 input gate Quad 5 input gate Quad 5 input gate Quad 5 input gate Quad 2	
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4516 4518 4519 4520 4521 4522 4528 4528 4530 4531 4531 4532 4534 4538 4538 4538 4538 4538 4538 4538	1 of 16 decoder/demultiplexer Binaryup/down counter Dual BCD up counter	855 9595 889 9779 978 978 978 978 978 978 978 97	XR2206 XR2207 XR2209 XR2209 XR2211 XR2211 XR2211 XR2211 XR2211 XR2242 XR2567 XR4739 XR14412 7400 7402 7403 7403 7407 7405 7407 7407 7407 7407 7407 7407 7407 7407 7409 7411 7414 7417 7417 7417 7418 7422 7425 7426 7427 7430 7432 7447 7440 7447 7445 7447 7446 7447 7447 7446 7466	Monostrine function generator Voltage controlled oscillator Operation multiplier Percision oscillator FSR dem odulator FSR modern odulator Dual monosithic tone decoder Dual monosithic tone decoder Dual of monie op amp FSR modern system 7400 SERIES TTL Quad Zinput NAND gate Quad Zinput NAND gate Quad Zinput NAND gate Quad Zinput NAND gate OIC Hex inverter but ferdriver Hex inverter but ferdriver Hex inverter but ferdriver Quad Zinput AND gate Quad Zinput AND gate Dual Zinput NAND gate Cuad Zinput NAND gate Dual Zinput NAND gate Dual Zinput nAND gate Cuad Zinput NAND gate Cuad Zinput NAND gate Cuad Zinput NAND gate Cuad Zinput NAND gate Dual Zinput nAND gate D	4 997 2 279 2 279 2 279 2 279 2 279 2 289 8 8 99 8 8 99 49 49 49 49 49 49 49 49 49	74ALS11 74ALS12 74ALS21 74ALS22 74ALS22 74ALS27 74ALS27 74ALS23 74ALS33 74ALS33 74ALS35 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS11 74ALS12 74ALS2	Triple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Occupant POS NAND gate Occupant POS NOR buffer Occupant POS NOR Duffer Occupant POS NAND Duffer OC Ouddruple 2 input POS NAND Duffer OC Ouddruple 3 input POS NAND Sync. upfdown Qual clock Counter Octabuffer Ine Offiverre	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F08 74F08 74F10 74F11 74F10 74F11 74F20 74F32 74F32 74F112 74F113 74F113 74F114 74F16 74	Quad 2 input NOR gate Nex inverter Quad 2 input AND gate Triple 3 input NAND gate Quad 2 input NOR gate Quad 2 input NOR gate Dual 3 input NOR gate Dual 4 input multiplexer Quad 5 input multiplexer Synch presettable BCD decade Counter Serial in, parallel out shift register Nex D ilig flop Dual 4 input multiplexer Quad 5 input multiplexer Quad 5 input multiplexer Didown decade counter Upidown decade counter Upidown decade counter Quad out in a input in	
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4516 4518 4519 4520 4521 4522 4528 4528 4530 4531 4532 4536 4531 4536 4536 4536 4536 4536 4536 4536 4541 4543 4555 4566 4566 4566 4566 4566 4566	1 of 16 decoder/demultiplexer Binaryup/down counter 9.9 Dual BCD up counter 4.8 Abit AND/DR elecit gate 6.0 Dual binary up counter 4.2 Astate frequency divider 9.1 BCD divide by N counter 1.2 Astate frequency divide by N counter 1.2 BCD rate multiplier 1.1 Dual 4 chemel mux 1.2 Dual 5 input majority logic gate 1.2 Dual 5 input majority logic gate 1.2 Dual 1 chemel mux 1.2 Dual 5 input majority logic gate 1.2 Dual 5 input majority logic gate 1.3 Real time 5 decade counter 7.7 Programmable timer 2.2 Dual precision monostable multiplier 1.3 BCD 10 7 segment latch/decoder/divier 1.4 BCD 10 7 segment latch/decoder/divier 1.5 3 digit BCD counter 1.8 Dual binary 1 of 4 decoder 1.8 Dual binary 1 of 4 decoder 1.8 Dual binary 1 of 4 decoder 1.8 Dial shift register 9.9 Scomplimenter 1.9 Segment decoder 1.8 Dias shift register 1.9 Phase comparator/programmable counter 1.8 Dual Schmitt lift ges 1.9 Dual Schmitt unit gate 1.9 Dual Schmitt in base generator 1.8 Dias of the segment decoder 1.8 Dias of the segment decoder 1.8 Dias of the segment decoder 1.9 Dual binary 1 of 4 decoder 1.1 Dual binary 1 of 4 decoder 1.1 Dual binary 1 of 4 decoder 1.5 Dual binary 1 of 4 decoder 1.6 Dual binary 1 of 4 decoder 1.7 Dual binary 1 of 4 decoder 1.8 Dual binary 1 of 4 decoder 1.9 Dual binary 1 of 4 decoder 1	85 95 98 99 99 99 99 99 99 99 99 99 99 99 99	XR2206 XR2207 XR2208 XR2209 XR2211 XR2242 XR2242 XR2242 XR2242 XR2441 XR2441 XR2442 XR2567 XR4730 7403 7404 7405 7406 7407 7406 7407 7408 7409 7408 74	Monostrine function generator Voltage controlled oscillator Operation multiplier Precision oscillator Precision oscillator Precision passe locked loop Precision passe locked loop Dual monostrine Dual of the precision oscillator Dual oscillator Dual oscillator Precision passe locked loop Dual monostrinic loop decoder Dual low noise op amp FSK modern system 7400 SERIES TTL Quad 2 Input NAND gate Quad 2 Input NAND gate Quad 2 Input NAND gate Occurred Use kinverter Hex kinverter gate O/C Hex inverter gate O/C Hex inverter loop Quad 2 Input AND gate Triple 3 Input AND gate Precision Dual 4 Input NAND gate Quad 2 Input RAND gate Quad 2 Input RAND gate Quad 2 Input RAND gate Dual 4 Input NAND gate Dual 4 Input RAND gate Dual 4 Input	4 997 2 279 2 279 2 279 2 279 2 289 8 8 99 8 99 9 49 9 49 9 49 9 49 9 49 9 49 9 49 9 49 9 55 9 55 9 55 9 56 9 57 9 58 9 59 9 59	74ALS11 74ALS12 74ALS21 74ALS22 74ALS22 74ALS27 74ALS27 74ALS23 74ALS33 74ALS33 74ALS35 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS31 74ALS11 74ALS12 74ALS2 74ALS2 74ALS2 74ALS2 74ALS2 74ALS2 74ALS2 74ALS2 74ALS2	Triple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Dual 4 input POS NAND gate OC Triple 3 input POS NOR gate Ouddruple 2 input pos NAND Duffer OC Dual I No State Ouddruple 2 input pos NAND Duffer OC Dual I No State Gate Gate Gate Fill plop with preset and clear Dual IX Ros Gate Gate Fill plop with preset and clear Dual IX Ros Gate Gate Fill plop with preset and clear Ouddruple 2 input pos NAND Dual IX Ros Gate Gate Fill plop with preset and clear Oudd Value Code Oudd Value Ouddruple Sate Oudd Value Ouddruple Oud	.5.7 .5.7 .5.7 .5.7 .5.7 .5.7 .5.7 .5.7	74F02 74F08 74F08 74F11 74F11 74F11 74F12 74F12 74F13 74F13 74F13 74F13 74F13 74F18	Quad 2 input NOR gate Nex inverter Quad 2 input AND gate Triple 3 input AND gate Unad 2 input AND gate Quad 2 input NOR gate Quad 3 input Sed get inggered Inpi low Kedge triggered flip inp Qual 3 input Sedge triggered Inpi low Kedge triggered flip inp Qual 3 input Sedge triggered Inpi low Kedge triggered Inpi low Kedge triggered Inpi low Kedge triggered Inpi low Kedge triggered Inpi low Law Sedge triggered Inpi l	
4516 4518 4519 4520 4521 4522 4528 4528 4530 4531 4532 4536 4531 4536 4536 4536 4536 4536 4536 4536 4536	1 of 16 decoder/demultiplexer Binaryup/down counter 9.9 Dual BCD up counter 4.8 Abit AND/DR elecit gate 6.0 Dual binary up counter 4.2 Astate frequency divider 9.1 BCD divide by N counter 1.2 Astate frequency divide by N counter 1.2 BCD rate multiplier 1.1 Dual 4 chemel mux 1.2 Dual 5 input majority logic gate 1.2 Dual 5 input majority logic gate 1.2 Dual 1 chemel mux 1.2 Dual 5 input majority logic gate 1.2 Dual 5 input majority logic gate 1.3 Real time 5 decade counter 7.7 Programmable timer 2.2 Dual precision monostable multiplier 1.3 BCD 10 7 segment latch/decoder/divier 1.4 BCD 10 7 segment latch/decoder/divier 1.5 3 digit BCD counter 1.8 Dual binary 1 of 4 decoder 1.8 Dual binary 1 of 4 decoder 1.8 Dual binary 1 of 4 decoder 1.8 Dial shift register 9.9 Scomplimenter 1.9 Segment decoder 1.8 Dias shift register 1.9 Phase comparator/programmable counter 1.8 Dual Schmitt lift ges 1.9 Dual Schmitt unit gate 1.9 Dual Schmitt in base generator 1.8 Dias of the segment decoder 1.8 Dias of the segment decoder 1.8 Dias of the segment decoder 1.9 Dual binary 1 of 4 decoder 1.1 Dual binary 1 of 4 decoder 1.1 Dual binary 1 of 4 decoder 1.5 Dual binary 1 of 4 decoder 1.6 Dual binary 1 of 4 decoder 1.7 Dual binary 1 of 4 decoder 1.8 Dual binary 1 of 4 decoder 1.9 Dual binary 1 of 4 decoder 1	85 95 98 99 99 99 99 99 99 99 99 99 99 99 99	XR2206 XR2207 XR2209 XR22109 XR22109 XR22112 XR2242 XR2267 XR4739 XR14412 7400 7402 7403 7403 7404 7405 7406 7407 7408 7408 7409 7410 7422 7425 7426 7427 7438 7439 7439 7448 7448 7451 7468 7469 7490	Monotithic function generator Voltage controlled oscillator Operation multiplier Precision oscillator FSR demodulator/fone decoder Precision pass locked loop Long range limer Dual monotithes a locked loop Long range limer Dual flow notities a locked loop Long range limer Dual flow notities a loop amp FSR modern system 7400 SERIES TTL Quad 2 input NAND gate Quad 2 input NAND gate Quad 2 input NAND gate o//C Hex inverter a loop loop loop loop loop loop loop lo	4 992 2 993 2 995	74ALS11 74ALS21 74ALS21 74ALS20 74ALS20 74ALS21 74ALS22 74ALS27 74ALS32 74ALS33 74ALS31 74ALS32	Triple 3 input POS NAND gate Triple 3 input POS NAND gate Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate OC Triple 3 input POS NAND gate Dual 4 input POS NAND gate Occupant POS NAND buffer OC Hex non Inverte with DOS NAND Duffer OC Dual AND/invert gate Dual D type POS edge triggered FiF Occupant POS NAND Duffer OC Dual JK NGE edge triggered FiF Dual JK NGE edge triggered Fip flop with preset and clear Dual JK NGE edge triggered Fip flop with	.5.7 .5.7 .5.7 .5.7 .5.7 .5.7 .5.7 .5.7	74F02 74F08 74F08 74F11 74F11 74F11 74F10 74F11 74F10 74F11	Quad 2 input NOR gate Nex inverter Quad 2 input AND gate Triple 3 input AND gate Quad 2 input AND gate Quad 2 input NOR gate Quad 2 input NOR gate Quad 2 input NOR gate Quad 3 input NOR gate Quad 4 input NOR gate Quad 4 input NOR gate Quad 4 input More Quad 5 input input gate Quad 5 input input gate Quad 5 input multiplexer And 1 input multiplexer Quad 5 input multiplexer And 1 input multiplexer Quad 5 input multiplexer Quad 6 input multiplexer Quad 1 input multiplexer Quad 2 input multiplexer Quad 3 input multiplexer Quad 4 input m	
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4516 4518 4519 4520 4521 4521 4528 4527 4528 4530 4541	1 of 16 decoder/demultiplexer Binaryup/down counter 9.9 Dual BCD up counter 4.8 Abit AND/DR elect gate 6.9 Dual binary up counter 7.2 Satate frequency divider 8.0 Dual binary up counter 8.0 Sub binary up counter 8.0 Sub divide by N counter 8.0 Sub divide by N counter 8.0 Dual reinggarable/mesetable monotable sub priority encoder 1.2 Sub ip parity generator/checker 8.3 Bib priority encoder 9.1 Real time 5 decade counter 9.7 Programmable timer 9.7 Programmable timer 9.7 Dual 1 channel digital multiplexer 9.7 Dual 1 channel digital multiplexer 9.7 Sub ip 10 of 3 segment latch/decoder/diver 9.7 Sub ip 10 of 3 segment latch/decoder/diver 9.7 Sub ip 10 of 3 segment latch/decoder/diver 9.7 Sub ip 10 of 3 segment latch/decoder 9.7 Sub ip 10 of 3 segment latch/decoder 9.7 Sub ip 10 of 10 segment latch/decoder 9.7 Sub ip 10 of 10 segment latch/decoder 9.7 Sub ip 10 of 10 segment latch/decoder 9.7 Sub ip 10 segment latch/de	85 95 98 98 99 99 99 99 99 99 99 99 99 99 99	XR2206 XR2209 XR2209 XR2209 XR2211 XR2211 XR2211 XR2211 XR2242 XR2242 XR2567 XR4739 XR14412 7402 7403 7403 7404 7405 7406 7407 7408 7408 7407 7408 7408 7407 7408 7408 7408 7408 7409 7400 740	Monotime function generator Voltage controlled oscillator Operation multiplier Persision oscillator Fish dem odulator Fi	4 995 3 959 8 99 8 99 9 99	74ALS11 74ALS21 74ALS20 74ALS20 74ALS20 74ALS20 74ALS21 74ALS20 74ALS31 74ALS331 74ALS331	Triple 3 input POS NAND gate OC Triple 3 input POS NAND gate OC Dual 4 input POS NAND gate OC Dual 4 input POS NAND gate Dual 4 input POS NAND gate Occupant POS NOR gate Occupant POS NOR potential Occupant POS NOR POS N	.57 .57 .57 .57 .57 .57 .57 .57 .57 .57	74F02 74F04 74F08 74F10 74F11 74F10 74F11 74F10 74F11 74F10 74F112 74F112 74F113 74F113 74F113 74F116 74F116 74F116 74F118 74F119 74F116 74F118 74F18	Quad 2 input NOR gate Nex inverter Quad 2 input AND gate Triple 3 input NAND gate Triple 3 input NAND gate Triple 3 input NAND gate Quad 2 input NAND gate Quad 2 input NAD gate Quad 2 input NOR gate Quad 2 input NOR gate Quad 3 input NOR gate Quad 4 input NAEG adge triggered (Inplice) Quad 5 NEG adge triggered flip flop Quad 6 NEG adge triggered flip flop Quad 6 NEG adge triggered flip flop Quad 6 NEG adge triggered flip flop Quad 7 input multiplexer Serial in, parallel out shift register Mex D flip flop Quad 6 Neg flip flip flop Paralled 6 Neg flip flop Quad 1 Neg flip flip Quad 6 Neg flip flip flip Quad 7 Neg flip flip flip Quad 7 Neg flip flip flip Paralled 6 Neg flip flip Quad 7 Neg flip flip flip Paralled 6 Neg flip flip Quad 7 Neg flip flip flip Paralled 6 Neg flip flip A Dil ALU 4 Neg flip flip flip flip A Dil Alu 4 Neg flip flip flip flip A Dil Alu 4 Neg flip flip flip flip A Dil Alu 4 Neg flip flip flip flip A Dil Alu 4 Neg flip flip	

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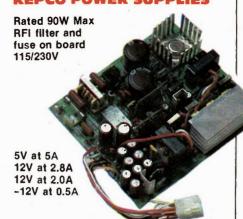


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Now available from MULTIFLEX is an economy video display terminal. Originally designed as a low cost access unit for our mail-ordering and buffetin board system, this terminal is a semi-intelligent. system which is controlled by a Z80A microprocessor and a 6845 CRT controller chip. The keyboard is fully ASCII encoded and the character generator contains the full 128-character set as well as a 128-character alternate set both of which are in the 5x7 dot matrix format. The screen display is 80 characters by 24 lines if the unit is hooked to an external monitor. (Monitor not included). There are 3 software selectable attributes (dim. reverse video, and alternate character set) which can be chosen one at a time for the whole screen. The attribute can then be switched on and off for each individual character. A 2K buffer is provided for normal operation. However when the optional 6K memory upgrade is purchased, 4 screen pages can be loaded from the host machine, edited, locally, and then downloaded back to the host again saving on connect time and phone line bills. Also included are 2 RS232 ports: one for a modem and one so that a printer can be attached to the terminal. The baud rates on these ports are software programmable and can range from 110 to 9600 baud. With all these features, you would expect to pay a lot for this system, but all this

A&T board with keyboard (as pisture top right) with one RS232 and 2K buffer \$169.00

is available to you, complete with an attrac-

tive case, for an extremely low price.



Terminal Complete: Tested and 90 days warranty with 2 RS232 ports, 2K buffer case and power supply (Hydro approved)

5299.00



Multiflex Terminal

U of T **6809 Single Board** Computer

The 6809 Single Board Computer, designed at the University of Toronto and distributed exclusively by EXCELTRONIX, is a compact hardware unit which was designed originally as a lab board for teaching students about microprocessor systems. Its many features, however, make it an ideal unit for stand-alone control applications or software development systems as well.

The system is designed around the Motorola MC6809 microprocessor. This is an 8-bit processor with full 16-bit Internal architecture, 2 index registers, 2 stack pointers, 28-bit or 116-bit accumulators, a direct page register and a wide range of addressing modes, including a programcounter-relative mode. This mode allows the user to write completely position independent software, important in systems software development.

There is provision for up to 48K bytes of dynamic RAM on-board. The refreshing of this RAM is controlled by an 8202 Dynamic RAM Controller. This chip allows for completely transparant refreshing of the RAM (ie. no wait states to slow the system down). There is also provision for up to 12K of EPROM using 2532 chips.

There are 4 complete I/O circuits built onto the board. 2 of them are serial (RS232); one is used for a terminal (which is required for use of the board with the supplied monitor software), and the other one is user defineable, but it is set up to communicate with either a modem or a printer. Also on-board are 2 6522 VIA chips. These provide 2 parallel ports per chip along with 2 16-bit timer/counters. One of the parallel ports and one of the timers are use by the monitor software to provide a cassette interface (which operates at 300 baud). The second parallel port on that chip is wired into a connector which is ideal for interfacing a parallel printer or keyboard. The 2nd VIA is not used at all and is completely free for the user. For further expansion of the system, a fully buffered version of the CPU signals (data, address, control lines and a signal indicating whether or not the current address is located on the board) is available at a cable connector.

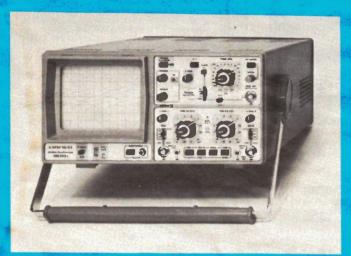
The software provided with the system is in a 2532 EPROM and allows the user to: test the memory; dump blocks of memory; examine and modify single memory locations; read or write from the cassette port; set and examine breakpoints; single step and/or execute machine language programs and set and examine the processor registers. All this is accomplished through a 9600-baud terminal interface (one of the serial ports) Included is a full screen editor/assembler which allows the user to work in 6809 assembly language rather than machine language. All this makes this board an ideal trainer, control unit or software development unit for just about anyone.

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A&T with 48K \$299

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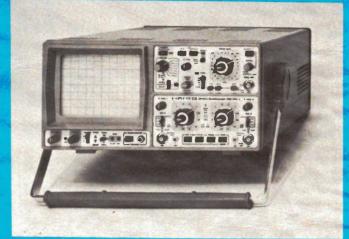


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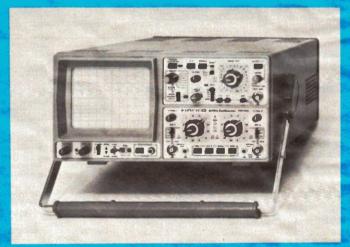


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You've just completed a microprocessor system, and it doesn't work. What next? You can use an oscilloscope to check for clock signals and the like, but if everything appears to be in order you can't go much further without sophisticated equipment. In these situations, professionals turn to their logic state analyzers, each of which cost thousands of dollars. MULTIFLEX has the answer for all those people who don't want to take a mortgage on their house just to get a computer working. The MULTIFLEX Logic State Analyzer has all the essential features of those more expensive units at a fraction of the cost. This is a high-quality piece of test equipment, suitable for industrial or scientific use, but its price is well within the price range of a hobbylst.

Easy to understand and operate, the Logic State Analyzer allows you to monitor 16 points in a digital system (ie. data and/or address bus, or control lines) which carry continually changing signals. You can select a bit pattern you expect will appear at these points. Once the pattern appears the Analyzer will trigger and record ("freeze") the next 1023 bit patterns so that they can be examined step by step even though data is no longer available in the unit being examined. For software development the Analyzer is invaluable, especially in dedicated systems. If you design a microprocessor system for a specific function, and you have no monitor, assembler or other such software, the best and often only way to debug the system is to use a logic analyzer. It will let you look closely at the data flow as a program is executing, or monitor the address lines to make sure that the instructions are being executed in the proper sequence. The various control lines such as memory read and write, DMA, interrupts, or enable and disable signals can also be examined. You can, of course, monitor any combination of these signals, such as the data bus and half of the address bus, or half of each plus 4 control lines. The combinations are endless.

Complete, assembled and tested \$369



Note from Industry to **Educational Institutions:**

At Multiflex we interview many technicians each year, from a variety of Colleges. Only a few applicants know what a Logic State analyzer is and even fewer know how to use one

Yet In our industry, it is almost as important to know how to operate logic analyzers as it is to use an oscilloscope since the technician will need to use a logic or timing analyzer to trouble-shoot complex equip-

We have spoken to many other companies and found that they are experiencing the same problems with technicians coming fresh fromCollege. So, we asked educational institutions why they don't teach this aspect of electronic engineering. The teachers are fully aware of the problem but explained that they cannot afford the high cost of logic analyzers; even those institutions which have them can afford only one or two which gives the students little chance to learn them.

Our LSA is a time-proven product which is considerably less expensive than the alternatives.

Here is your chance to prepare technicians for the real world!

If you are a computer designer who values your time, you can't afford to be without this!

Did you ever write a piece of code, burn it into an EPROM, plugged it in and it didn't work? Did you then go through the code (using an analyzer or your brain power) and then discover you left out some

crucial Byte which caused the processor go the point of no return?

If the above holds true, how many EPROMs have you reprogrammed, erased and damaged? More important

how many hours have you wasted?

Put an end to all the above problems and save time, money and frustration: Buy an EPROM Emulator.

It allows you to download over RS232 (at 300 to 9600 Baud) a program from your computer into the Emulator's memory (16Kx8) and then simply plug a 24 or 28 Pin header connected via ribbon cable to the Emulator in place of your EPROM and you have successfully emulated an EPROM.

If you need to change your code, simply change it on

your computer, download to the Emulator's memory and you are back in business in seconds.

This stand-alone product emulates the following EPROMs: 2716, 2732, 2764 and 27128. Can be used with any computer with an RS232 interface.

This product is a must for any hardware development since it allows the user to test and modify EPROM data roughly 20 times faster than conventional methods.

The Emulator normally comes attractively packaged and contains its own power supply. However, to make it more affordable for beginners, we have separated the price Into several categories:

- 1. Complete Emulator with 16Kx8 memory, attractively packaged with power supply. Fully assembled and tested with warranty..... ······\$189.00
- 2. As above but with 8Kx8 of memory.

.....\$159.00

3. Emulator with 4K RAM, no housing or power supply (requires +5V at 1.5A Max, +/- 12V at 0.03A. Fully



5100 Starter System

Complete, **Assembled** and Tested

Options: 64K Dynamic PAM and

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\$100 CPU Board with 64K . . . \$149

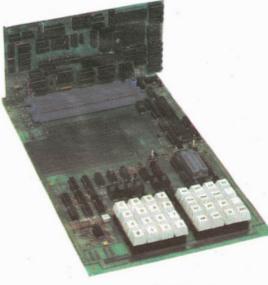
MULTIFLEX's Z80 computer is a versatile and expandable stand-alone computer system designed and built right here in Canada. It uses the newest technology to provide the user with the most capabilities for the smallest price-tag. Its adaptability to any situation and extremely low cost allow it to be used in many applica-tions ranging from a trainer to a complete CP/M-based computer comparable to the best on the market, at a

The actual layout of the system is a two board design. One board (the "motherboard") contains a 24-line parallel I/O chip for interfacing to the external world, an RS232C serial port with board rates selectable from 110 to 9600 baud, a hex address and data display, a

hex keypad, 14 monitor function keys, 2 user definable keys, a 40-chip wire wrap area with full access to all the bus signals, on-board provision for regulators so that the board can be supplied with standard S-100 voltages, an EPROM programmer which will handle 2708 (1Kx8), 2716 (2Kx8), 2732 (4Kx8), 2532 (4Kx8), 2764 (8Kx8) and the brand new 27128 (16Kx8) EPROMs, a DC-to-DC converter to supply the programming voltage to the EPROM programmer and four (4) slots for IEEE S-100 compatible boards for further expansion. This is an extremely useful and important feature as It allows expansion of the system with all boards using this industry-standard bus structure, which are available from MULTIFLEX, as well as from hundreds of manufacturers worldwide

The other board is the CPU card. This card plugs into one of the S-100 slots on the motherboard and is IEEE 696/S-100 compatible with the full 24-bit address path to allow up to 16 megabytes of memory to be addressed. The processor used is the Z80 (running up to 6 MHz) and there is provision on-board for 64K of dynamic memory (using 4164 chips) which will operate without walt states. Provided for as well is a 2K to 32K (selectable in 2K blocks) common resident area in memory for use with multiple memory banks. There are also 4 sockets on board which will handle 2732 (4Kx8) or 2764 (8Kx8) EPROMs or the new 6116/2016 (2Kx8) static RAMs (all of which can be software deselected if desired) to allow the user complete versatility in setting up the board to meet his own specifications. Also on board is 1 parallel port with 24 lines of I/O and 3 16-bit counter/timers for applications which require the unit to keep track of real time. Another feature of the CPU board is that it was designed by our engineers to run the CP/M 2.2 disk operating system so that if a floppy disk controller board is added to the system a fully configured CP/M machine can be set up for a very low cost

The monitor software that comes with the kit is a well-written extensive package which allows the user to have complete versatility in machine language programming and execution as well as control of all the features on the board. The monitor functions include: ex-amine/modify memory locations, memory block moves, compare 2 blocks of memory, examine CPU register, ex



amine I/O ports, load and save from cassette calculate relative branch offsets, set breakpoints single step programs, execute programs, and program EPROMs. Each of these process is invoked by a single keypress. Also available to the use are 2 spare keys definable for special functions a required by specific applications and applicator programs.

The standard kit includes the CPU board with a Z80A (4HMz) processor, 2K of RAM (a 6116), and 4K of EMPROM (a 2732) as well as the motherboard with all the features mentioned above except the RS232C port and the DC-toDC converter. Also supplied are sockets for all IC's and 1 S-100 connector.

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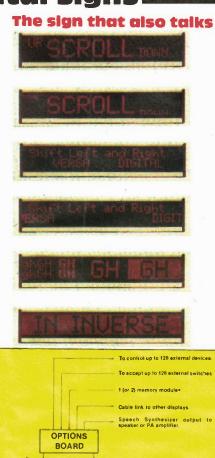
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face allows up to 128 switches to be connected to the Display, enabling customers to select specific messages without having to wait for the sign to cycle through its repertoire.

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VERSADIGITAL DISPLAY

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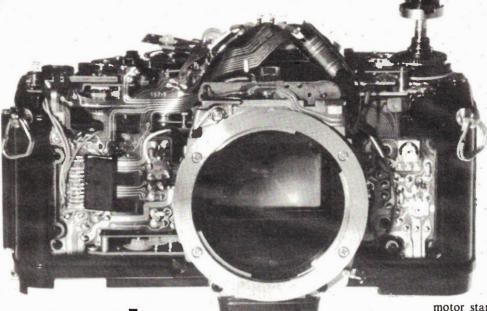
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Electronics in Photography

The ubiquitous microprocessor finds its way into photo gear to eliminate number-crunching and make battery manufacturers happy.

By Bill Markwick

WHEN I bought my first camera in the 60s, it had a selenium meter on top which read out an exposure value. You then matched this value by turning the shutter and/or aperture rings. The selenium cell wasn't sensitive enough to respond to anything but daylight, but I thought it was great.

About the same time, cameras began to appear with through-the-lens, batterypowered meters coupled to the shutter. Then you could add filters or go from sunlight to candlelight, all without fiddling with exposure controls. These cameras, in varying degrees of sophistication, held sway until recently.

Once microprocessors and dedicated ICs became available for next to nothing. gadget designers went crazy designing them into everything. All sorts of features now became possible with the new lowcost control circuitry.

Cameras

Once designers decided to replace the transistor with the microprocessor, they were faced with the dilemma of finding something for it to do. After all, if you've got all this computing power sitting there, it seems a shame to have it do nothing but calculate the exposure and then just sit on

At the moment, the Minolta Maxxum 7000 is the best example of the full-blown microprocessor camera. Everything but the shutter button is under electronic control, and you can even get a remote control for that. Here's a tour of the Maxxum, chosen because it has just about every possible microelectronic feature in one camera body.

The first thing you'll notice if you pick it up and touch the shutter button lightly is that is has autofocus. The lens will turn automatically to focus on the area inside the tiny focussing rectangle. The next thing you'll notice is that the camera suddenly takes a picture with an accompanying whine from the motor drive. Everybody around here did that: focus and jump when they held the button down too long. The autofocus has its own IC, a small surface-mount, one of eight on the flexible PC board that wraps around the innards.

The Minolta autofocus uses a rangefinder approach rather than sending out beams of infrared or ultrasonic sound. The image is split through two tiny lenses which project it onto a CCD array; the two signals produced are checked for phase difference, and the resulting output is converted to micromotor rotation signals by the autofocus IC. The lens is then rotated until the images are coincident; the time required for the entire procedure depends on the lens in use: the normal 50mm lens takes 300mS to focus from 1m to infinity, a speed typical of most of the Minolta AF lenses. The micromotor, incidentally, uses four speeds. If the lens has to be turned most of the way, the

motor starts at the highest speed and is switched to slower and slower speeds as the lens approaches proper focus. This prevents overshooting and hunting for the correct stop point.

Like any rangefinder, the AF system prefers to look at vertical lines rather than horizontal, and pointing it at a blank wall confuses it. Under most conditions, however, it's very difficult to fool. It even focussed accurately on a ceiling tile covered with those random holes for sound absorption. The AF can be set to manual focussing if you prefer; if you don't like the split-second lag while the motor runs, Minolta makes a continuousfocus version, the Maxxum Professional,

Now to get some film in it. You don't have to fiddle and fumble with the film leader and spools any more; just lay the film in and close the back. Cameras such as the Maxxum, the Canon T70, the Konica FT-1, the Nikon 2000 and others feature autoloading, not necessarily an electronic feature, but certainly a mechanical marvel. The electronics comes into it with the contact pins for Kodak's DX film encoding. The subject was covered in a previous article (Kodak DX Coding, August, 1985), so we'll limit it to saying that the camera will set the ISO speed automatically and also optimize exposure for negative or positive colour films; it does this by contacting conducting or non-conducting patches on the cassette. If there are small contacts in the right-hand side of the well that holds the film cassette, the camera has DX coding.

The next major use of the microelectronics is in controlling the exposure. First of all, most cameras these days use the silicon blue cell for light sensing; it's a photodiode with a flat response to visible light. The diode produces a current proportional to the light intensity and is connected to a current-to-voltage converter such as a high-gain amplifier with resistive feedback from output to input. The output voltage of the converter can be used directly, as in simpler match-the-needle metering, or converted to a digital code to light the LEDs or run the LCD display in

electronic cameras.



Top view of a naked electronics camera supplied by Yashica. Unless noted, photos for this article were taken by Ed Zapletal using either a Pentax ME Super or a Maxxum.

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Top view of the Canon T80 showing the LCD program display and an add-on autofocus lens.

The great advantage to the silicon cell is a nearly instant response to changes in light intensity, unlike the older cadmium sulphide cells which had slow attack and release times as well as a "memory" effect (they take a while to adjust their resistance levels after being in bright light). Not only can the camera respond instantly to changing light, but the light from dedicated flash units can be controlled by interrupting the flash duration when the through-the-lens cell tells the controlling IC that enough light has happened.

But that's nothing; controlling the exposure value is the simple part. The designers used some of that CPU potential by coming up with different exposure modes. The Minolta boggles the mind. You can have shutter-priority to hold a selected shutter speed and vary the aperture, aperture-priority for the other way around, a locking mode to hold a desired reading, and manual. In addition, when you change lenses a ROM in the lens tells the CPU that it's just arrived and to reset the program; long lenses get a faster shutter speed to prevent shake, and so on. Besides that, you can step any program in 1/2 stop increments. All the various gyrations display on an LCD window as well as in a tiny frame in the viewfinder. Gadzooks. What would George Eastman say?

The major camera manufacturers all have some version of the gee-whiz electronic camera: Canon, Nikon, Pentax, Vivitar, etc. We only used the Minolta as an example of what can be done.

You might wonder whether you should take out a second mortgage to buy batteries to run all these dooies. Actually, the cameras are remarkably efficient in turns of power consumption; the Maxxum spec sheet claims 25 rolls of 24-exposure film from its four AAA cells. There's also a tiny lithium battery with a 10-year life; it keeps the memory alive if you have to change batteries in the middle of things.

The most remarkable achievement in batteries is from Kodak: their 4000 disk camera has a lithium battery that runs the motor drive, the flash and the electronics, and it lasts so long it isn't even user-

replaceable.

Before leaving cameras, we should point out that you don't have to buy a top-of-the-line camera to get electronic control. You've probably noticed the proliferation of pocket cameras with autoeverything. The Vivitar TEC45 on our cover, for instance, is a compact 35mm with a CPU that controls DX coding, infrared autofocus, autoload, autowind, exposure, and flash. Like all similar pocket cameras in this price range, it's designed for point-and-shoot only; there are no controls to play with. There's one serious omission from the hi-tech of the simpler cameras: there are some people who don't like gadgetry of any kind and when they take a picture, they flinch as if they were firing a shotgun. The only answer is an IC-controlled 50-pound flywheel to lock the camera onto the subject and prevent heads from being cut off.

Flash Units

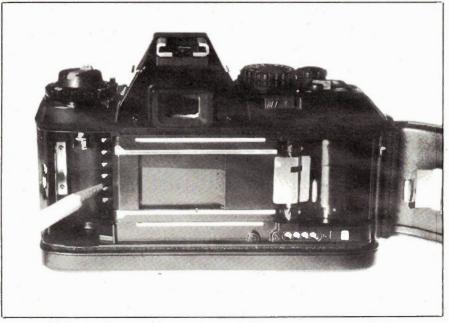
All major manufacturers supply flash units dedicated to their particular camera model: pins on the hotshoe or extension cord let the camera and flash coexist peacefully, and some flash units have settings to suit different cameras. When set for the Leica R4, for instance, the presence of a 1.3V signal on the extra flash pin tells the CPU that the flash is present and to set the shutter to the flash sync speed (to make sure that the shutter is fully open during the flash duration). When the flash reaches full charge, the 1.3V increases to 2.5V and begins to pulse between these limits, lighting the indicator in the viewfinder that says the flash is ready. Most cameras have a similar system.

For some years now, the old full-power, adjust-the-aperture flash method has been replaced with the thyristor system. The flash fires, a photocell in either the camera or the flash body measures the light, and when the proper exposure is reached, an SCR cuts off the flashtube. This has the advantage of conserving battery power and shortening recycle times as well as eliminating that ir-

ritating aperture twiddling.

Enter the microprocessor once again. A good example of what can be done is the Vivitar 5200. First, the bottom part that mates to the camera is actually a small module available in versions to suit the requirements of most popular cameras. The flash unit itself has the usual 4-stop automatic control range plus 12 manual stops, but the electronics really come into it with the LCD readout. This window displays the ISO speed, the maximum distance, the zoom head setting, and the f-stop. If you're on manual, there's no more measuring and calculating. If you're on automatic, there's nothing to do at all.

Electronics Today December 1985



The pencil points to the contacts that touch conductive patches on DX-coded film cassettes. The four circles under the takeup spool are databack contacts.

continued on page 65



Electronics Today

LITERATURE REVIEWS



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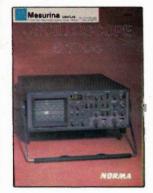
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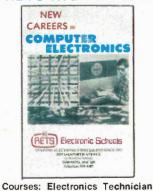


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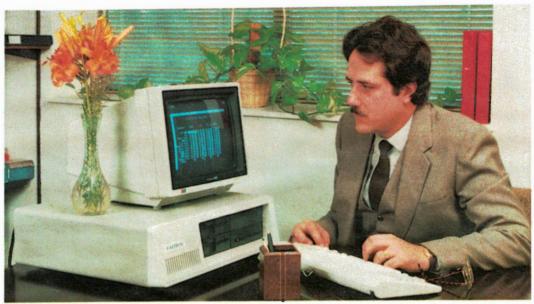
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Heat Pen Project

Measure temperature directly with your digital voltmeter; build this simple add-on.

By Geoff Phillips

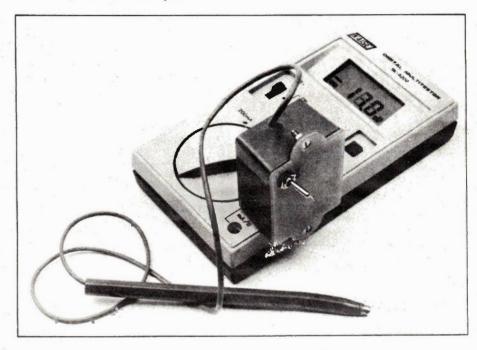
THE HEAT PEN is a low cost temperature probe that transforms a standard DVM into a digital thermometer. Just plug the Heat Pen into any digital voltmeter, place the tip onto a surface, and the DVM shows its temperature directly in degrees C. Its range is from -50 to +150 degrees C.

Thermocouples are messy: they require cold junction compensation and scale conversion. Stick-on labels have their uses but they are expensive and can only be used once. The Heat Pen is an inexpensive solution to your temperature measurement problems.

Temperatures of power transistors can be measured easily. Balance your central heating equipment by measuring inlet and outlet temperatures. Take your own temperature by placing the Heat Pen under your tongue. The uses are endless.

A semiconductor temperature sensor is used as the probe tip. It gives a nominal luA per degree Kelvin. This is converted to 10mV per degree Kelvin. A bandgap voltage reference is amplified to 2.73V; this is subtracted from the voltage signal derived from the probe tip so that the remaining voltage is equivalent to 10mV per degree C. Low power semiconductors are used, making the quiescent current drain of the Heat Pen less than 1mA.

Nearly all DVMs are fitted with 4mm input sockets which are pitched 3/4' apart. The Heat Pen's PCB, as well as



housing the circuitry, also has two 4mm plugs firmly fitted at the 3/4' pitch. The PCB, along with a 9V battery, fits neatly into a plastic utility box. The probe is mounted in a ball point pen casing and is connected to the PCB via a shielded cable.

Fig. 1 Circuit diagram of the heat pen. Electronics Today December 1985

Construction

Fit the resistors, capacitor, then IC1 and ZD1 to the PCB. No special precautions are required. Remove the plastic casing from the two 4mm terminals and using a small hacksaw, cut 1mm of the hexagonal sections of the terminals so that approximately 12mm remains. The terminals already have one hole drilled in the hexagonal section. Ideally a second hole should be drilled 8mm from the first. If you have metric taps, drill these holes for an M3 tap and then tap out the holes. Secure the two 4mm terminals to the PCB with M3 x 6mm screws. If you cannot lay your hands on metric taps then the terminal may be fixed to the PCB by passing short lengths of heavy gauge copper wire through the holes and soldering the wires in place. The wires are then passed through the holes in the PCB and soldered in place.

Solder the -ve lead of the 9V battery clip to the 0V terminal of the PCB and solder a 2' lead to the +9V terminal. Solder the conductor of the shielded lead to the PCB and the screen to +9V terminal. The case must now be prepared for the fitting of the PCB.

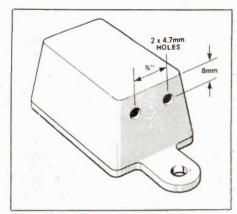


Fig. 2 Case details for the heat pen.

First of all it is necessary to make a cover for the box. This may be made from glass fibre sheet or plastic sheet. Use the box as a template and draw around its shape on the plastic sheet with a scriber. Cut out the shape with a hacksaw. After dressing up the cover with a file, temporarily clamp it to the box and drill two mounting holes through the lugs of the box and cover. Drill and file a hole in the cover for the on/off switch.

The hole will have to be carefully positioned so that the switch does not foul the 9V battery when the unit is assembled. Fit the switch to the cover. Drill two 4.7mm holes in the side of the box (Fig. 2) to allow the 4mm terminals to protrude from the box and one small hole in the opposite end of the box for the shielded cable.

Tie a knot in the shielded cable about 25mm away from the PCB and then pass the cable through the small hole in the box. Pass the two 4mm terminals on the PCB through the two holes in the box and continue to pull the shielded cable through the hole until the PCB is positioned at the bottom of the box.

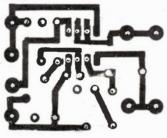
Pass the shielded cable through the empty ball point pen casing and solder it carefully to the Intersil temperature sensor. The AD590JH has an accuracy of 5 degrees, the AD590IH is 10 degrees, and the AD590KH is 2.5 degrees. The 590-type ICs will be about five dollars, except for the KH version, which will be about ten. The Intersil ICL8069 is available in various tolerances, with the suffix DCZR indicating .01 percent and a price of about two dollars.

Connect the shield to the +ve lead and the case lead of the sensor. Connect the core to the -ve lead of the sensor. Insulate the leads from each other with sleeving; then the sensor can be positioned at the tip of the pen casing and secured with adhesive. Solder the +ve lead of the battery clip and the +ve lead from the PCB to the two switch terminals. The Heat Pen is now ready for calibration.

Calibration

A crude but effective way of calibrating the Heat Pen is in iced water. Ideally the water should be distilled and free from contaminants which may alter the freezing point temperature. It is important to ensure that water does not penetrate the leads of the temperature sensor as it will cause a leakage current to flow and thus give an erroneous reading. Therefore place the heat pen probe in a plastic bag and place in a vessel of iced water. Switch on the Heat Pen and with your DVM monitor the voltage at pin 7 of ICI with respect to OV. Adjust RV2 for 2.73V.

Now plug the Heat Pen into the DVM. Adjust RV1 until 0.00V is obtained. The unit is now calibrated to 0 degrees C. Cut out a piece of foam rubber to fit on top of the PCB in the box. This is to prevent the battery casing from short circuiting the components, and also to prevent everything from rattling around inside the box. Fit the battery on top of the foam rubber and fit the cover with its switch to the box and secure with two nuts and bolts.



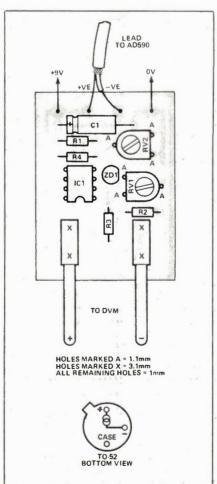


Fig. 3 Overlay diagram and pin-out of the AD590 temperature transducer.

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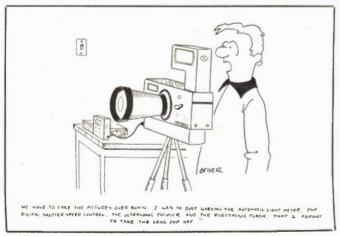
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FET Power Am

A simple design for a 50 watt amp using complementary power FETs.

By Jeff Macauley

WHEN power MOSFETs were first introduced a few years ago, they were heralded as the audio designer's dream device. Nevertheless, despite several designs in popular magazines, they haven't yet fulfilled their promise of causing the demise of normal transistors.

This writer is sure that one of the reasons for this may simply be that the designs published so far have all been rather complicated. Normally several transistors are used in cascaded Darlington and differential pairs. It is not necessary to have a complex circuit to adequately exploit the potential of FETs.

The circuit described here uses a pair of complementary FETs in a six-transistor circuit. As it stands, the circuit will deliver 50 watts into 8 ohms and about 75 watts into 4 ohms. Capacitor coupling to the speaker avoids the usual problem of blown speakers in the unlikely event of complete circuit failure. FETs will withstand a certain amount of abuse during an output short circuit, though the latter is not highly recommended.

Read the specifications of the devices, and you'll see that the input impedance is thousands of megohms. From this it would seem that almost anything would be capable of driving them, but this is not so because there is a gate to source capacitance that has to be charged and discharged by the driver circuit.

With the devices chosen, this capacitance is on the order of a nanofarad. The maximum rate at which this capacitance can be charged depends on the current available from the driver. The highest audio frequency encountered is 20kHz. To enable the amplifier to deliver this a slew rate of 2V/uS is required. With a 5mA driver current a slew rate of 5V/uS is obtained, a comfortable enough margin.

The other peculiarity of power FETs is their temperature characteristic. In complete reverse to normal transistors, they have a negative temperature coefficient. This means that as the device gets hotter it attempts to cut itself off and delivers less current for a given bias

voltage.

With normal bipolar devices, the hotter they get the harder they turn on. This effect, called thermal runaway, can lead to disastrous consequences. As a result, the bias arrangements found in power amps are often highly complicated, using transistors to compensate for temperature rises in the output. Without this sophistication, bipolar devices would get hotter and pass more current, a vicious circle that can destroy the outputs.

Because of the more civilized behaviour of power FETs, all that is needed for bias is a trimpot.

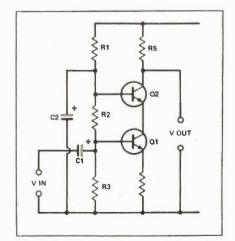


Fig. 1 The circuit of a simple cascode amplifier: our circuit is opposite.

The Cascode

Fig. 1 shows a simple cascode amplifier. The two active devices, in this case bipolar transistors, are in a 'totem pole' arrangement. To understand it properly it must be broken down into two sections.

O1 is used in the emitter follower mode. Base bias for both transistors is obtained from the resistive divider formed by R1, R2 and R3. The junction of R1 and R2 is decoupled to ground by C2, removing any supply line noise and producing smooth, quiet DC on Q2's base. Consequently a smooth DC appears on Q2's emitter. Thus Q1 operates from a low impedance, low noise supply: Q2's emitter.

As I mentioned previously, Q1 is operated as an emitter follower. In this configuration the input impedance is high, equal to the transistor's current gain multiplied by the transistor's current gain in ohms. In a practical circuit, the input impedance is shunted by the parallel resistance of R2 and R3. R1 doesn't enter the picture because it is effectively shunted to ground by C2. The signal that appears at the emitter of O1 is very nearly equal to the input signal, but the current gain is high. That is, the input voltage effectively appears across the emitter resistor, causing large current variations

The overall effect of Q1 is producing a varying current drive for Q2. This kind of circuit, a voltage to current converter, is known as a transconductance amplifier.

Q2 is employed as a common base amplifier. This configuration is rarely seen, but has advantages over the common emitter stage. Chief among these is high linearity; the major disadvantage is the high output impedance and low input resistance. In our application these are not important considerations.

The cascode will normally be biased so that the emitter current of Q1, flowing through R5, sets the collector of O1 at half the supply voltage. Under these circumstances, the voltage gain is equal to R5 divided by the emitter resistor.

Thus the stage is a high impedance, highly linear voltage amplifier with considerably better performance than can be obtained with other configurations.

The Circuit

The full circuit of the amplifier is shown in Fig.2. With reference to this, signals are coupled into the base of Q1A/1B which are connected as a Darlington pair which acts as a single very high gain transistor.

The pair is the transconductance part of a cascode driver, the rest of the circuit being completed by a second Darlington pair, O2 and O3. To obtain maximum open loop gain, no emitter resistor is used. The current provided by the cascode generates a highly amplified voltage at Q3's collector.

Drive for the FETs is taken from this point in the circuit. As Q4 and Q5 are complementary, a small amount of bias is required to prevent crossover distortion. Crossover distortion is produced when the output transistors don't conduct soon enough as the signal rises from zero; the voltage across trimpot RV1 provides a small forward bias so the outputs will follow the signal at low levels.

C3 is called the bootstrapping capacitor. Its function is to ensure that Q4 can turn on hard enough during positive cycles. Imagine that Q1 and Q3 are turned off; all that remains to turn Q4 and pull it up to the positive supply is the resistive bias divider. However, one end of C3 is charged by the output line, and when the output goes in the positive direction, it dumps the charge on C3 into the bias resistors, providing an extra voltage boost

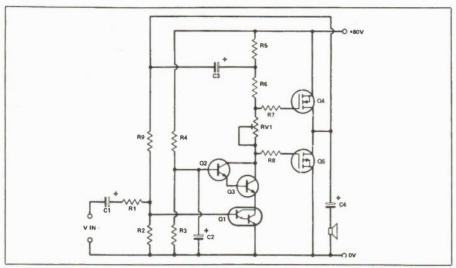


Fig. 2 The circuit of our cascode amplifier. Q1 contains in effect two transistors connected as a Darlington pair.

for Q4. C3 also provides the same signal at R5/R6 as is present on the collector of Q3, isolating the driver from power supply noise.

R7 and R8 provide stability by forming a lowpass filter in conjunction with the FET input capacitance. The output is connected to the speaker via a large electrolytic capacitor, isolating the speaker from DC; this is particularly important should a transistor fail. The low frequency cutoff point is determined by this capacitor. Using the value specified gives the amp a -3dB point of 10Hz.

Overall shunt feedback is applied from the output stage to the base of Q1 via R9 and R2. Together they form a potential divider to bias the amplifier so that the output is at half the supply voltage. R1 defines both the input impedance and the voltage gain of the amplifier. With 27k for this resistor the sensitivity is 600mV for full output. The voltage gain is R9/R1, assuming that the amp is being driven by a fairly low impedance source (less than 2000 ohms). R1 can be reduced to about 10k if more gain is required.

The amp can be powered by a simple fullwave supply consisting of a transformer, a rectifier, and a large

capacitor. The transformer should be 50 to 55 volts, with a current rating of 2A or more.

Construction

Most of the components are mounted on the board as shown in Fig.3. Q4 and Q5 are mounted on a heatsink with the usual mica washers, nylon bushings, and silicone grease for electrical isolation and good heat conduction.

All that is required for setting up is a multimeter. Set it on a range that will indicate 50mA DC and connect it in series with the power supply lead. Set RV1 for minimum resistance and apply power. If the needle bangs over, shut the power off immediately and check for faults, particularly components with wrong polarity or solder bridges. If everything is OK, the meter will read something like 5mA. Gradually turn RV1 until the reading is 30mA. Reconnect the power lead and measure the voltage at the positive side of the output capacitor C4 (i.e., the junction of the two FETs). It should be about half the supply voltage, give or take a few

If these tests are OK, all you need is a speaker and a signal source.

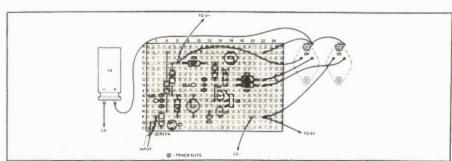
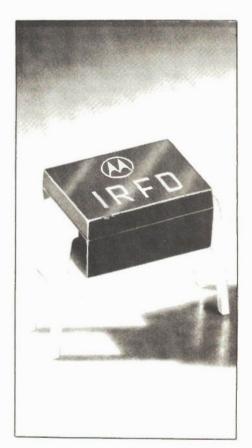


Fig. 3a. The component layout on Veroboard: construction is simple, but pay attention to the track cuts and components orientation.



given the current state of computing power and mathematical theory. These functions can form the basis of a public

key encryption process.

Imagine for a moment that you are a cryptanalyst in the pay of Universal Generality Inc, and you have just intercepted a message intended for International Monolithic Co (IMC). The message is the encrypted form of a single ASCII character and looks like this: 0110010.

From a public directory, you obtain IMC's encrypting keys which are: a) 5 and

b) 221.

You also know that IMC uses a standard form of encryption which is: raise the message to the power of the first key, 5, and express the answer modulo the second key (mod. 221).

It seems as if you have plenty to work with; you know how the encrypted message was produced from the plaintext and you also know the keys used to generate it. Surely you can just reverse the process to get back to the original

A little experiment will quickly convince you that this is indeed a one-way function; you can't go back by the same route that you took on the way out. But where does that leave IMC? Surely they can't get the plaintext message back

either? Here we have the essence of the Public Key system: yes, IMC can recover the message because there is a trapdoor in the encryption process which allows them to go back by a different route. IMC can use this trapdoor because they have the deciphering key, UGI can't because they haven't.

As an expert cryptanalyst you will have even more information at your disposal than I have already given you. You will know how IMC's secret key can be generated. IMC's second public key is the product of two prime numbers, 'p' and 'q'. These were selected at random by IMC when they were first setting up the system. The product of the primes (221) is published, but the primes themselves are known only to IMC.

The secret key was generated as follows: one was subtracted from each of the primes 'p' and 'q' and another number 'r' was formed by multiplying together the results, so $r = (p-1) \times (q-1)$. The secret key 's' was then formed by taking the multiplicative inverse modulo r of the first public key (5), in other words a number s was found such that s is less than r and s x 5 = 1 modulo r. Decryption is then carried out in the same way as the encryption process, with the secret key substituted for the first public key.

Your main task as cryptanalyst to UGI is to factor IMCs second public key. If you can do this, you will be able to generate their private key and decipher their message. In this case it is not difficult, but suppose the second public key had been 6533431. Can you find the factors of this? (It can be done.)

Finding primes and testing for primality is a fairly fast process: to test a 130 digit number for primality would take a matter of minutes on an average minicomputer. On the other hand, to find the prime factors of a 130 digit composite formed by multiplying together two 65-digit primes would take quadrillions of years with the fastest computers and most efficient factorization algorithms available today. It is this vast difference in computing times that allows the system to be set up, yet makes it unfeasible to break.

Before getting too carried away by big numbers, it's worth remembering that a public key system devised by mathematicians at Stanford University in the USA, using a different process, has already been broken, and that was just for a \$1000 bet. If anyone can find a factorization algorithm that will operate in a time comparable with finding primes the system will be useless; it would take as long to set up as it would to break.

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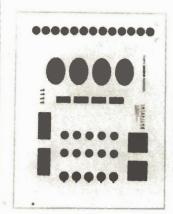
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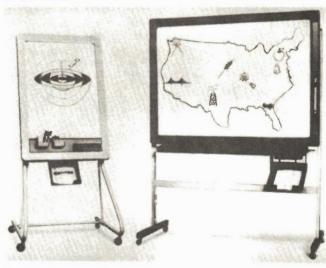


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clude low-cost switching devices, Force Sensing Resistor. The device motor controls, electronic drum pads, etc. A sample sent to ET worked as claimed; now we have to find something to make with it. From Interlink Electronics, 331 Palm Avenue, Santa Barbara, CA 93101, (805) 965-5155.

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The Oki Copyboard consists of a white writing surface similar to a blackboard, connected elec-tronically to a thermal printer. It allows each member of the audience to receive an 8 1/2 by 11 copy of whatever appears on the board. There are two versions; the

largest is 58 by 75 inches. After the print is made (about 10 seconds), the board can be erased for the next presentation. From Office Equipment of Canada Inc., 525 Denison St., Markham, Ontario, (416) 491-9330.

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NEXT MONTH Electronics Soldering Tools Today

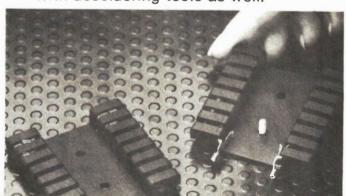
Heatsinks

How to find out what power your semiconductors are dissipating and how to select a heatsink for any given temperature rating.

More On Transformers

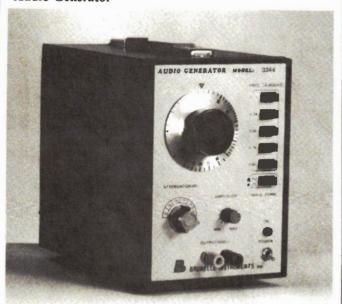
Delayed from last month, we present the continuation of October's Guide To Transformers; find out how to select transformers for unusual applications such as autotransforming and DC conversion.

Using a proper soldering station is a delight compared to the pencil types; we cover some of the units available, along with desoldering tools as well.



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Audio Generator

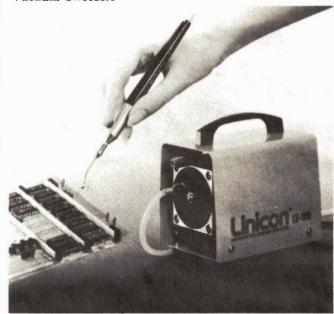


The BTL audio frequency generator features 5 ranges from 10Hz to 1Mhz, a 7V RMS no-load output, and a 6-step 10dB attenuator; sine waves or square waves are switch-selectable.

Amplitude stability is within 0.5dB. From Brunelle Instruments, 73 — 6th Range South, St. Elie d'Orford, Quebec JOB 2S0, (819) 563-9096.

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Vacuum Tweezers



Pick up small objects or vacuum tiny carpets. The MH-100 vacuum tweezers feature 0.02 to 0.06 pounds of suction, depending on the pad used. Its pump is oil-free,

ideal for clean rooms. Medo USA 1nc., 808-C North Central Avenue, Wood Dale, IL 60191, (312) 860-0500.

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This Month in



RTTY For The Apple

Takes radio-teletype code from a shortwave receiver and converts it to an ASCII display on your screen.

I Ching For The Mac

High tech meets mysticism with a program to display and interpret hexagrams.

Personal Composer Review

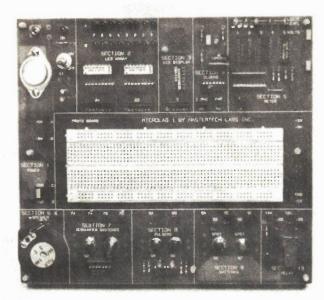
We check out this remarkable music software for the PC.

Video Cards For The PC

We discuss the various video display cards that are available for the PC; find the optimum card for your text and/or graphics applications.

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Microlab



knowledge. The 40 topics covered BC V8X 3X1, (604) 652-1544. by the course include logic gates, flip-flops, dividers, counters, Circle No. 44 on Reader Service Card.

The Microlab 1 Digital Electronics various types of shift registers, Course consists of a solderless multiplexers and more. All the bits breadboard with accessories and a of hardware needed, such as switmanual with theory, explanations, ches, a speaker, a regulator, LEDs, and self-tests. It's designed to etc., are included on the board teach digital techniques to those itself. From Mastertech Labs Inc., with little or no electronics 6792 Kirkpatrick Cres., Victoria,

Data Recorder



4-channel and the MR30 7-channel. Both are compact enough to be easily portable, and feature microprocessor controlled 851-8871. self-test, counter search functions,

TEAC now has data recorders us- dual meters, and complete front ing Philips cassettes, the MR10 panel operation. From Metermaster Division of R.H. Nichols Co. Ltd., 80 Vinyl Court, Woodbridge, Ontario L4L 4A3, (416)

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was dismantled in Mill Village, Steve Rimmer, may recall when he Nova Scotia. The Mill Village 1, ran one of his unique ad spoofs, built by the federal Department of "Big Round Things". That was Transport in 1964, ushered Mill Village 1. The Big Round Canada into the era of satellite Thing has been dismantled and cut telecommunications; it is now down. replaced by the new 32 metre Mill Village 1, dish Village 4 dish.

This July, Canada's first telecom-munications satellite earth station Electronics Today's former editor, was dismantled in Mill Village, Steve Rimmer, may recall when he

New DMM



Canadian. Peak Hold is the ability to detect, store, and display voltage or current peaks as short as 6ms. When used with a proper current clamp, the meter will detect

Beckman has introduced their new starting surges in AC or DC DM50 digital multimeter that systems. Other features are 0.5 features Peak Hold for \$149.95 percent accuracy and 10M input impedance. Lenbrook Electronics, Unit 1, 111 Esna Park Drive, Markham, Or (416) 477-7722. Ontario L3R 1H2.

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Programmable Thermometer

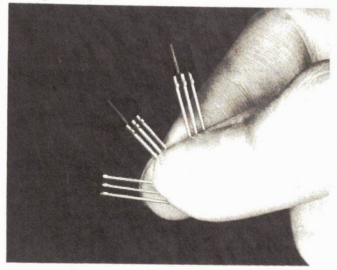


The Keithley Instruments Model 740 Programming Scanning Thermometer can linearize the outputs from the seven most popular ther-mocouple types, and includes a separate mV range for non-standard types requiring a microvolt sensitivity. It supports up to 81 channels, scans at 20

channels/sec and includes a nonvolatile memory. It can send data to a printer, and a 24-hr clock can time stamp each measurement. Keithley Instruments, Inc., 28775 Aurora Rd., Cleveland, Ohio 44139, (216) 248-0400.

Circle No. 47 on Reader Service Card.

Voltage Reference



plus or minus 2 percent and a 3791 Victoria Park Avenue, temperature coefficient of 60 parts per million per degree C. Only a

Ferranti's low power reference in a single resistor is needed to operate TO-92 package, the REF25Z, of- it from a DC supply. Ferranti is fers an output voltage accuracy of stocked by Carsten Electronics,

Circle No. 48 on Reader Service Card.

New Compatible



The Heath HF-148 is an IBM- pansion slot. A serial and a parallel compatible computer kit, featuring an 8MHz clock, one or two drives, 256K expandable to 640K onboard, a redesigned IBM-compatible keyboard, and one ex-

port are standard. Contact the Heath Company, 1020 Islington Avenue, Toronto, Ontario M8Z 5Z3, (416) 232-2686.

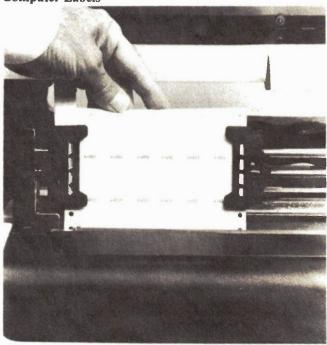
Circle No. 49 on Reader Service Card.

Sci-fi buffs have a picture of what a space station looks like engraved on their minds: a donut-shaped rotating platform, like the one in "2001: A Space Odyssey". Unfortunately, this shape isn't going to happen just yet. Aslde from the fact that the artificial gravity would be stronger at your feet than at your head, it doesn't lend itself to what space station planners have in mind. The one planned by the US for the early 1990s looks more like a highway bridge with huge solar panels sticking out the sides. This girder format allows living quarter, communication, and shuttle docking modules to be

placed for best access.

According to the National Research Council, Canada is in on the station program, and we're developing several options for participation. One is a robotic servicer and Integrated Servicing and Test Facility for space assembly and testing. Second would be the solar arrays which provide primary power for the man-tended platforms and auxiliary power for the station. Third would be a remote sensing facility. In January, 1986, NASA will hold a review to discuss Canadian hardware design and development.

Computer Labels





Labelling of wires, control panels. harnesses, etc., can be done using a computer/printer and 3M Scotchcode SCS vinyl labels. They're self-adhesive and can be fanfolded; each .8 by 1.44 inch label has a transparent tail which is

wrapped around the wire and the printing itself for protection. From 3M dealers, or contact 3M Canada Inc., Electrical Products, PO Box 5757, London, Ontario N6A 4T1, (519) 451-2500.

Circle No. 50 on Reader Service Card.

In the August issue we reported that Mitsubishi has produced a prototype flat-panel TV. Philips Research Labs in Redhill, England, have come up with one of their own. The monochrome CRT measures 12 inches diagonally by three inches thick; a colour version is said to be feasible. The unit uses a very low power electron beam (1uA, 400eV) which, because of its low energy, can be deflected in an unorthodox way. The beam travels down the back of the panel and Is deflected 180 degrees into

the front section. There, more deflection plates turn the beam onto an electron multiplier; this amplifies the beam several hundred times before it reaches the screen. Vertical scan is achieved by progressively ramping the poten-tlals on the screen plates. The resolution and grey scale capability is said to be "appropriate for TV applications". So far, the panel exists only as a lab prototype; no production plans were announced.

One of the biggest problems in editing Electronics Today is getting enough technically-oriented manuscripts. If you have an idea or circuit or project ranging in depth from beginner to expert, chances are close to 100 percent that we'll publish you. It doesn't

matter about literary ability; we'll even write the article if you'll supply the ideas in point form. Write to the Editor, Electronics Today, at the address shown on the contents

Designer's Notebook

GETTING TO KNOW diodes requires an understanding of the basic P-N junction (Fig. 1). These are created by taking a rod, approximately 2-4 inches in diameter, of very highly purified silicon or other semiconductor material in single crystal form, and cutting it into slices with a diamond edged circular saw. The very thin discs formed are cleaned and polished, and finally heated in a vacuum oven with a carefully chosen atmosphere so that selected impurities will diffuse into the semiconductor material to a precisely controlled depth.

Silicon is tetravalent, which means that the atomic structure is such that there are four surplus valency electrons present in the outermost electron shell within the atom. If one diffuses in a trace quantity (one or two parts per million) of an impurity such as arsenic which is pentavalent (it has five outer orbital electrons), the net result will be that there are some 'spare' electrons floating around in the crystal structure.

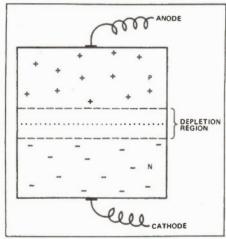


Fig. 1 A basic PN junction.

We call such a material an N doped silicon, or simply N type. If the material is heavily doped, we call it N+, if it is lightly doped we call it N- and so on.

Similarly, if we diffuse in boron which is trivalent (it has only three outer valency electrons), the result will be a number of holes where electrons should be, but are not. These holes behave like positive electrons, but are a bit more sluggish. This is because their movement takes place only as a result of an electron coming from somewhere else to fill the gap, leaving another hole where it had been, and so on. Think of it as a kind of electronic leap-frog.

A leak at diodes, including some unusual types and their uses.

By John Linsley Hood

When a P and an N doped semiconductor material are in contact, usually as a result of deliberately contrived impurity zones within the single crystal slice, a diffusion of these 'spare' electrons and holes occurs across the junction. This leaves a depletion zone on either side of the junction region which is completely stripped of both holes and electrons, as shown in Fig. 1.

This depletion region is, therefore, effectively a non-conductor, so even in the conducting direction of the diode it is necessary to apply a certain forward voltage before any current will flow, to give the electrons enough kinetic energy to traverse the potential gap. The effect of this is to make the depletion zone appear to decrease in width, to the point at which it disappears when the forward conduction potential for the junction is reached. The converse is true for a reverse biased junction.

This results in the voltage/current graph shown in Fig.2. A very important characteristic to note is that the forward voltage drop, Vf, for a diode connected in its forward biased mode, is one of pure conduction and is therefore not very noisy, whereas operation in the reverse conduction mode is very noisy.

So, when you need a voltage drop in low noise circuitry, use a string of forward diodes or an 'amplified diode' as shown in Fig.3 rather than a zener diode, in which the conduction occurs as a result of reverse breakdown.

A consequence of the greater mobility of electrons and holes as a result of thermal excitation is that the forward voltage drop of a PN diode decreases with temperature. This seems to contradict the concept that the depletion region arises as the holes and electrons migrate across the junction, so that greater mobility of these should cause a wider depletion zone, but in reality the increased carrier mobility simply acts to lessen the forward bias which needs to be applied before conduction occurs.

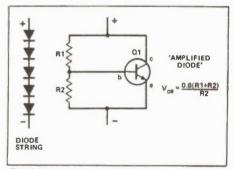


Fig. 3 Arrangements which can be used instead of a zener diode where a voltage drop is required in low noise circuitry.

However, an important feature of doped regions is that the width of the depletion zone at the junction decreases as the impurity concentration, and the consequent number of holes and electrons, is increased. This phenomenon is used in tailoring device characteristics, as shown later.

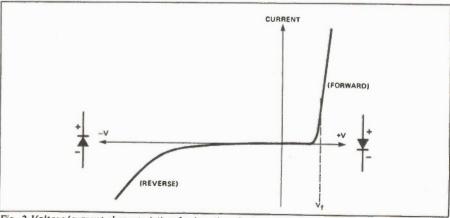


Fig. 2 Voltage/current characteristic of a junction diode.

Small Signal Diodes

There are three different types in general use:

Germanium point-contact diodes, useful for very low level signals in radio applications, but otherwise becoming obsolete.

Germanium diffused junction diodes, similar in characteristics to silicon ones, but with a lower forward voltage drop (0.15V typically, as compared with 0.55V for silicon), worse reverse leakage current (by a factor of about 1500x), and worse temperature coefficient and maximum working temperature.

Silicon diffused junction diodes, such as the 1N4148. These are inexpensive and very reliable (if from a good manufacturer) and can be used as low power rectifiers up to about 30V RMS and 50mA. Their four nanosecond recovery time (the length of time which it takes for the electrons and holes generated by current flow to recombine, so that the diode would be nonconducting in the reverse biased direction) limits their use to about 100MHz. For higher frequency use more suitable diode types as available.

Power Rectifier Diodes

These are basically similar to the small signal diode. However, the power handling capability of the diode is determined by the maximum junction temperature, which in turn depends on the conducting resistance of the diode which determines the power dissipation for any given current and the ease with which the heat generated in the junction can diffuse away. These will usually have a large junction area to minimize the conduction resistance, in good thermal contact with a metal plate or stud whereby the heat can be taken to some kind of heat sink.

Also, such diodes must be able to withstand a high reverse voltage. This is achieved partly by their junction geometry, as shown in Fig.4. In early designs of the rectifier diode, it was noted that failure almost always took place at the edge of the junction area. If the sides of the chip are etched away at an angle, the electrical stress at the edge of the junction can be reduced so that this kind of breakdown is prevented.

The other technique employed is to keep the doping impurity levels relatively low, so that the depletion region is wider and the stress, per unit thickness, is consequently less. This unfortunately, increases the resistance of the silicon per unit area with a consequently higher forward voltage drop; hence more thermal dissipation for a given current. In lower voltage rectifier diodes relatively high doping levels will be employed, simply to reduce the forward conduction power losses.

Avalanche Diodes

A further technique which is used in power rectifiers is to tailor the diffusion process and the doping levels of the P and N regions on either side of the junction so that the depletion region is very uniform in thickness. Then, provided that the doping levels are not too high, any carrier (electron or hole) entering the depletion region under conditions of reverse bias will be so accelerated that impacts with atoms will generate further electron-hole pairs. These, in turn, will be accelerated by the applied electric field and will collide with other atoms, giving rise to a situation very similar to that of an avalanche of rocks falling down a sloping hillside.

This process is known as ionization but is most commonly seen only in gases, such as neon signs or sodium vapour street lamps. The purpose of the avalanche diode approach is to avoid destructive damage to the rectifier occurring as a result of very short duration high voltage spikes. These arise all too frequently on power lines. A straightforward rectifier diode could break down under these conditions, and the very high temperatures generated by even small local current flow at high reverse voltages could fuse portions of the junction, leading to a short-circuit.

A typical voltage/current graph for an avalanche diode is shown in Fig. 5. In a well designed device, the reverse turn on is very abrupt and conduction will be distributed uniformly across the whole of the junction area.

Although a maximum static thermal dissipation of some tens of watts might be permitted for such a diode, this could well absorb a spike energy equivalent to tens of kilowatts for a duration of only a few microseconds without any harm.

Zener Diodes

These are very heavily doped diodes, with, in consequence, a very thin depletion region between the PN junction. The reverse bias electrical stress across this causes ionization of the semiconductor material in the depletion zone, and consequent current flow. Beyond about 7V, zener diodes are not used. All of the so-called zeners above this voltage will, in

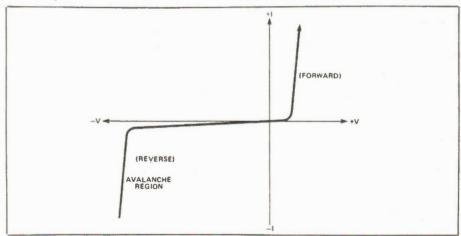


Fig. 5 Voltage/current characteristics of a typical avalanche diode.

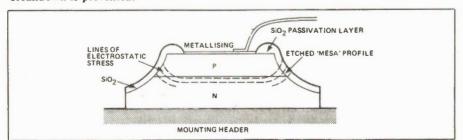


Fig. 4 The sides of a high voltage rectifier chip are etched away at an angle to deflect electrostatic stress away from the edge of the junction.

fact, be avalanche diodes. As mentioned above, reverse leakage current is noisy, and a zener diode will make quite a good wide-band noise source.

The fact that zeners are all very highly doped tends to give them a low and fairly sharp turn-on characteristic in the forward direction, which can be useful.

High Frequency Diodes

The major requirements in this application are high carrier mobility and low junction capacitance. These requirements

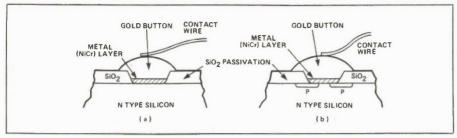


Fig. 6 Cross-sectional views of a) a normal Schottky diode and b) a hybrid Shottky device.

are met fairly well by the old point contact or gold-bonded germanium diodes, but the most commonly employed type nowadays is the Schottky diode, shown in Fig. 6a. This relies only on majority carrier action (electron flow), and is, in consequence, fast in action. The snag is that there are sharp corners where the metal inlay abuts on the N type silicon slice, and the electrical stress at these points leads to a low reverse breakdown voltage which can be as little as 5V. The advantages of this construction are that the forward voltage drop is reduced to some 180-220mV, and that the operating frequency can be as high as 18-20GHz.

The electrical stress at the edges of the metallic layer in a Schottky diode can be lessened by the inclusion of an annular ring of P-type silicon under the edge, as shown in Fig. 6b. However, although it can increase the reverse breakdown voltage to 60-70V, the maximum operating frequency is reduced to about 4GHz. THEse are sometimes called hybrid Schottky diodes.

Depending on the construction employed, the junction capacitance can be as low as 1pF - compared with 5-50pF for a standard small-signal silicon junction diode and 500-5000pF for a rectifier diode.

An important characteristic of diode'

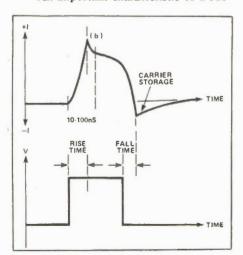


Fig. 7 Carrier storage following a reversal of the applied voltage in a PN junction diode.

behaviour, which influences pulse and switching performance in addition to RF behaviour, is the transient response of the diode junction. This is determined by a variety of phenomena such as:

Carrier storage, due tot he minority carriers (holes) still left uncombined at the conclusion of a forward conduction period. THis causes conduction to continue for a short period following a reversal of potential, as shown in Fig. 7a. The stored charge can be expressed in picocoulombs (1 coulomb is the charge stored in a 1 farad capacitor at an EMF of 1 volt), and typical values are 100-10,000pC.

Turn-on transient, due to the time taken for conduction to settle down to a steady value. Typically 10-100ns for a small signal silicon junction diode.

Voltage dependence of junction capacitance is a characteristic which is exploited in varicap diodes, but occurs in all reverse-biased PN junctions and has some of the characteristics of inductance.

Varicap Diodes

The capacitance of a reverse biased PN junction is, roughly, inversely proportional to the square of the voltage across it. Such a diode appears to consist of two conducting regions separated by the depletion layer between them, which acts as a dielectric. Typical devices have capacitance values in the range 3-50pF, depending on device and applied voltage.

The way in which they can be used as a remote voltage-controlled tuning element is shown in Fig. 8. Back-to-back connection is frequently employed where large signal levels are likely, to prevent the

signal voltage itself from modulating the capacitance.

A similar type of construction is employed in the varactor diode, in which the diode is connected across an oscillator coil and the dependence of the capacitance on instantaneous voltage is used to generate harmonics of the signal.

Step Recovery Diode

This is a device which is designed, by geometry and doping levels, to have a very abrupt switching characteristic when the applied potential is reversed. It can be used to shock excite a coil into oscillation at a much higher frequency (up to the sixth harmonic) than the input voltage. This is useful for microwave signal generation, as is the impatt diode.

Impatt Diode

This is a device whose design is deliberately contrived to give a very wide depletion layer, assisted by the inclusion of a layer of intrinsic (un-doped) silicon, in the form shown in Fig. 9a. When this is used in the type of circuit shown in Fig. 9b., quite useful amounts of microwave power (up to 1W CW at 50GHZ, or 50W pulsed) can be generated. The trepatt diode is a structural modification of this to cause bunching of the electrons, which allows some increase in power levels.

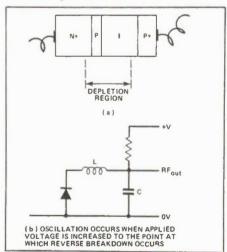


Fig. 9 The construction of an impatt diode and b) a microwave power oscillator using the device.

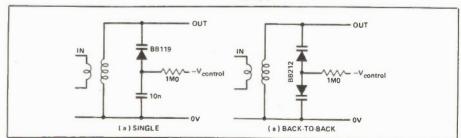


Fig. 8 The use of varicap diodes to provide remote voltage controlled tuning.

Gunn Diode

In spite of its name, this isn't really a true diode at all. Made from N-type gallium arsenide, it is what is known as a two-valley semiconductor in which the conduction band (that energy level in which the electrons can move as in a normal conductor) has two different levels, with different mobilities.

When current is caused to flow through a slice of this material between two ohmic contacts, the fast electrons overtake the slow electrons to form a bunch which travels through the slice. The result of this is an accumulation of charge at the cathode, until it neutralizes the field due to the applied voltage at the contact. Charge accumulation then stops, and the charge domain then travels through the semiconductor slice in the form of a sharp spike of current at a speed determined by the applied voltage. This process then repeats to generate a rapid series of such spikes.

Such devices are often used as the microwave power sources in such things as microwave Doppler intruder and fire alarms.

Tunnel Diodes

A decade or so ago, these devices were seen as the bright new hope for simple RF oscillator circuits. Unfortunately, their price never became low enough for them to achieve popularity, and they may soon become just a historical curiosity. They are based on the use of a very highly doped junction, with a consequent very thin depletion layer.

known as a negative resistance characteristic

In due course, the leakage current begins to increase once more lading to the type of reverse voltage/current graph shown in Fig 10. If this is connected in series with a coil as shown in Fig. 11, a simple HF oscillator circuit with a stable output voltage is produced. However, tunnel diodes are quite easily damaged by excessive currents.

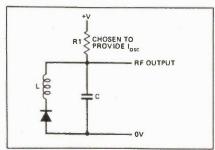


Fig. 11 A simple HF oscillator circuit using a tunnel.

Light Emitting Diodes

Apart from the ubiquitous transistor, this is, I think, the bit of modern semiconductor technology which has made the biggest impact on the public at large, as a long-life replacement for filament indicator bulbs.

These work because radiation is emitted by an atom when an electron, having been excited into a higher energy level by some input of energy such as a current, falls back into its original rest level; a similar mechanism operates when an excited electron falls into a hole. The pro-

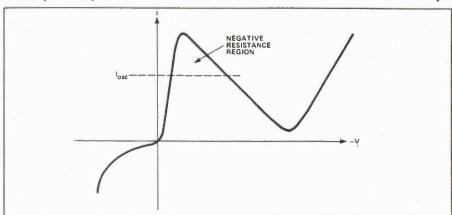


Fig. 10 Voltage/current characteristics of a typical tunnel diode.

At very low reverse bias levels, the thermal energy of the electrons in the semiconductor is high enough for them to 'tunnel' through this depletion layer, and the junction conducts. However, as the reverse voltage is increased the thickness of the depletion layer increases, so the tunneling effect, even with an increased potential difference, begins to lose ground. The current then begins to fall as the potential is increased, giving what is

cess is known as electroluminescence.

Since the light is emitted from the junction, the diode must be designed so that the light can escape, and usually they are encapsulated in a plastic moulding so designed that it acts as a magnifying glass with the junction at its focus. In order to get radiation emitted in the visible part of the spectrum, it is necessary to have a material with a large energy band gap, such as gallium arsenide (red), gallium

arsenide phosphide (amber and yellow), gallium phosphide (yellow or green, depending on oping).

Early LEDs were not very efficient in terms of the light output for current input, efficiencies of the order of 0.005% being not untypical. However, more modern 'high-brightness' LEDs can reach 3% efficiency, especially in the red colours where they are beginning to compete with filament bulbs. Also, by tailoring the geometry of the device, semiconductor lasers are possible and these are used as the 'reading' device in compact disc players.

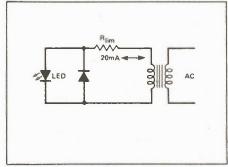


Fig. 12 Driving an LED from an AC supply.

Care must be taken not to reverse bias an LED (light is emitted when current flows in the forward direction) since reverse breakdown will damage the device. An LED can be used on an AC source if it is shunted by an ordinary silicon diode as shown in Fig. 12.

Reverse Leakage

This is one of the major problems with semiconductor diodes, apart from reverse breakdown, and is strongly dependent on temperature. Such leakage currents increase by 10% for each I degree C rise in temperature, which means that the leakage current will double every 8 degree C. This sets an upper limit for the use of germanium diodes at about 70 degrees C and for silicon at about 160 degrees C.

Although diodes do not appear to have a lot to do with ICs, most microcircuits are made on a substrate of silicon with all the bits of circuitry isolated from the substrate simply because they are sitting on top of a reverse-biased diode junction.

As I mentioned above, such leakage currents are noisy, and this was (and to a lesser extent, still is) the reason why low noise circuits built up from discrete semiconductors would often be better than their op-amp equivalents. However, technology improvements have lessened this penalty, and nowadays, for most practical purposes, if an IC is available to do the job it is not sensible to do it any other way.

Electronics from the Start Part 6

This month we're starting on circuits which use capacitors, specifically electrolytic capacitors.

By Keith Brindley

CAPACITANCE VALUES are measured in a unit called the farad (named after the scientist Faraday) and given the symbol F. We'll define exactly what the farad is later; suffice to say here that it is a very large unit. Typical capacitors have values much, much smaller. Fractions such as a millionth of a farad (i.e., one microfarad: 1uF), a thousand millionth of a farad (i.e., one nanofarad: 1nF) or one million millionth of a farad (i.e., one picofarad: 1pF) are common.

The circuit for our first experiment is shown in Fig. 1. You should see that it bears a striking resemblance to the voltage divider resistor circuits we looked at some time ago, except that a capacitor has taken the place of one of the resistors. Also included in the circuit of Fig. 1 is a switch. These circuits have a number of dynamic properties which occur for only a while after the circuit is connected. We can use the switch to make sure that we see all of these dynamic properties, from the moment electric current is allowed to flow into the circuit.

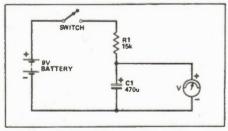


Fig. 1 A simple resistor and capacitor circuit.

A breadboard layout for the circuit is shown in Fig. 2. You'll notice from the diagram that the meter is connected across the capacitor using alligator clips. These are useful additions to buy for your meter, as they allow you to do other things while taking measurements.

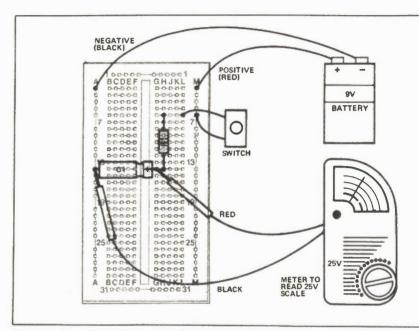


Fig. 2 A breadboard layout of the circuit in figure 1.

As the switch is off, no current should flow and the meter reading should be OV. If you've accidentally had the switch on and current has already flowed, you'll have a voltage reading other than this, even after the switch is consequently turned off. In such a case, get a short length of wire and touch it to both leads of the capacitor simultaneously, for a few seconds, so that a short circuit is formed. The voltage displayed by the meter should rapidly fall to OV and stay there. Now you're ready to start, but before you switch on, read the next section!

Getting Results

As well as watching what happens when the circuit is switched on, you should make a record of the voltages displayed by the meter, every few seconds. To help you do this a blank table of results is given in Table 1. All you have to do is carefully fill in the voltages you have measured, at the times given. You will find that the voltage will increase, rapidly at first but slowing down to a snail's pace at the end. Don't worry if your measurements of time and voltage aren't too accurate; we're only trying to prove the principle of the experiment, not the exact details. Besides, if you switch off and then short circuit the capacitor, as described above, you can

repeat the measurements as required.

When you've got your results transfer them as points onto the blan graph of Fig. 3 and sketch in a curv which goes approximately through all th points. Table 2 and Fig. 4 show the result we obtained when we performed the experiment.

What we have seen is that the voltag across the capacitor (any capacitor, in fact) rises, not instantaneously as with the voltage across a resistor, but gradually following a curve. Is the curve the same do you think, for all capacitors? Chang the capacitor for one with a value of 220uF (about half the previous one). Using the blank table of Table 3 and blank graph in Fig. 5, perform the experiment again, to find out.

Table 4 and Fig. 6 show the results of our second experiment (yours should be similar) which shows that although the curves of the two circuits aren't exactly the same as far as the time axis is concerned, they are exactly the same shape.

This proves that the rising voltage across a capacitor follows some sort of law. It is an exponential law, and the curves you obtained are known as exponential curves. These exponential curves are related by their time constants. We can calculate any rising exponential

Electronics Today December 1985

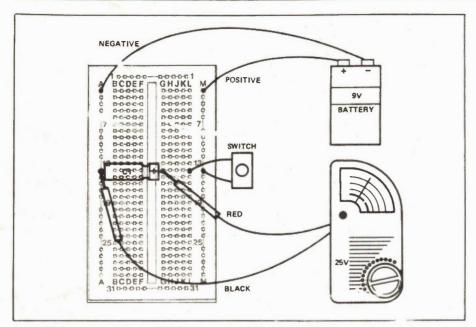


Fig. 9 The breadboard layout for the experiment in figure 8.

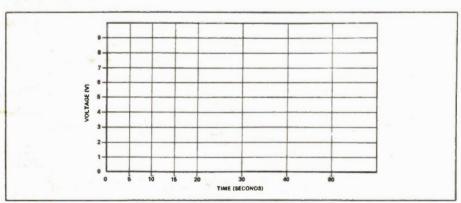


Fig. 10 A blank graph to fill in.

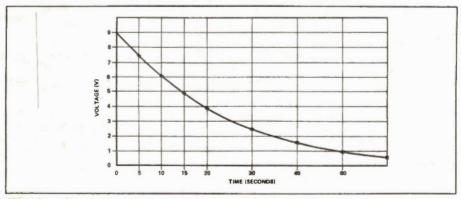


Fig. 11 The graph showing our own results.

see that the curve obtained is similar to the curves we got earlier, but are falling instead of rising. Like the earlier curves, this is an exponential curve, too.

If you wish, you can calculate the time constant here in the same way, but remember that it must now be the time at which the voltage falls to 0.63 of the total voltage drop, i.e.,

when
$$V = 9 - (0.63 \times 9)$$

= 3.3 V

Theoretical Aspects

So what is it about a capacitor which causes this delay in voltage between switching on or off the electricity supply and obtaining the final voltage across it? It's

almost as if there's some mystical time delay inside the capacitor. To find the answer we'll have to consider a capacitor's innards again.

Fig. 12 shows the capacitor we first saw in Fig. 1. A number of electrons gather on the capacitor plate connected to the battery's negative terminal, which in turn repel any electrons on the other plate towards the battery's positive terminal. A deficiency in electrons causes molecules of this capacitor plate to have a positive charge, so the two plates of the capacitor now have equal, but opposite, electric charges on them.

Current only flows during the time when the charges are building up on the capacitor plates, not before and not after. It's also important to remember that current cannot flow through the capacitor; a layer of insulator (known correctly as the dielectric) lies between the plates. Remember: current only flows in the circuit around the capacitor.

If we now completely disconnect the charged capacitor from the battery, the equal and opposite charges remain, in theory indefinitely. In practice, charge is always lost due to leakage current between the plates. You can try this for yourself, if you like: put a capacitor into the breadboard then charge it up by connecting the battery directly across it. Now disconnect the battery and leave the capacitor in the breadboard for a time (overnight, say). Then connect your meter across it to measure the voltage. You should still get a reading, but remember that the resistance of the meter itself will always drain the charge stored.

The size of the charge stored in a capacitor depends on two factors: the capacitor's capacitance (in farads) and the applied voltage. The relationship is given by:

$$Q = C \times V$$

where Q is the charge measure in coulombs. C is the capacitance and V is the voltage. From this we can see that a charge of one coulomb is stored by a capacitor of one farad when a voltage of one volt is applied.

Alternatively, we may define the farad (as we promised we would) as being the capacitance which will store a charge of one coulomb when a voltage of one volt is applied.

We can now understand why it is that changing the capacitor value changes the associated time delay in the changing voltage across the capacitor. Increasing the capacitance, say, increases the charge stored. As the current flowing is determined by the resistance in the circuit, and is thus fixed at any particular voltage, this increased charge takes longer to build up or longer to decay away. Reducing the capacitance reduces the charge, which is therefore more quickly stored or more quickly discharged.

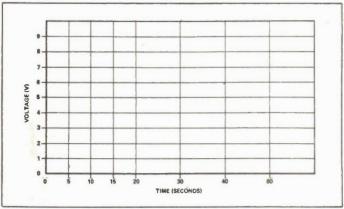


Fig. 3 Blank graph to fill in: see text.

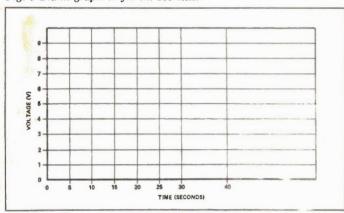


Fig. 5 Another blank graph to fill in: see text.

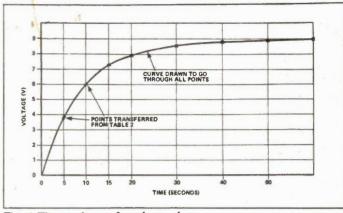


Fig. 4 The results we found ourselves.

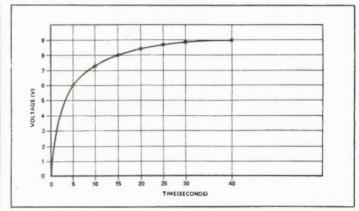


Fig. 6 Our own results.

curve's time constant, as shown in Fig. 7, where a value of 0.63 times the total voltage change cuts the time axis at a time equalling the curve's time constant, (the Greek letter 'tau').

In a capacitor circuit like that of Fig. 1. the exponential curve's time constant is given simply by the product of the capacitor and resistor value. So, in the case of the first experiment, the time constant is:

 $t1 = 470 \times 10t \times 15,000$

= 7 seconds

and, in the case of the second experiment:

 $t2 = 220 \times 20t \times 15,000$

= 3.3 seconds

You can check this, if you like, against your curves, or those in Fig.s 4 and 6, where you will find that the times when the capacitor voltage is about 5V7 (ie. 0.63 times the total voltage change of 9V) are about 8 seconds and 4 seconds. Not bad when you consider possible experimental errors, the biggest of which is the existence of the meter resistance.

One final point of interest about exponential curves, before we move on, is that the measurement denoted by the curve can be taken to be within about 18 of its final value after a time corresponding to five time constants. Because of this, it's taken as a rule-of-thumb that the changing voltage across a capacitor is complete after 5 time constants, or 5 x R x C seconds.

FINAL 0.63 FINAL VALUE

Fig. 7 A graph showing an exponential curve, related to the time constant.

The Other Way

During these experiments you should have noticed how the voltage across the capacitor appeared to stay for a while, even after the switch had been turned off. How could this be so?

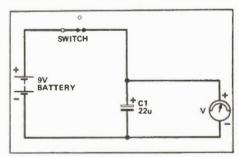
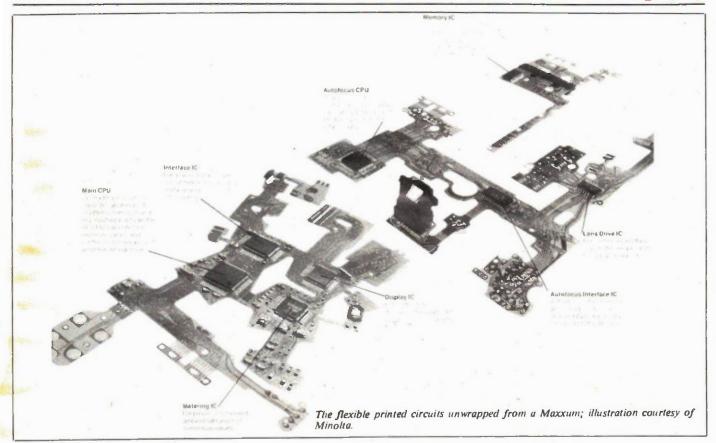


Fig. 8 An experimental circuit to demonstrate how a capacitor stores voltage (or 'charge').

Fig. 8 shows the circuit of an experiment you should now do, which will help you to understand this unusual capability of capacitors to apparently store voltage. Build the circuit up as in the breadboard layout of Fig. 9, with the switch in its on position.

Now, measure the voltages at selected time intervals, and fill in the results into Table 5, then transfer them to form a curve in Fig. 10, after switching the switch

The results we obtained are tabulated in Table 6 and plotted in Fig. 11. You can



Other models include photocells that can be removed and placed near the subject for precise control, remote triggers that set off a slave flash by detecting the fast risetime of the main flash, high voltage (510V) rechargeable battery packs to speed up recycling by eliminating the step-up oscillator, and many more. The flash for the Maxxum, for instance, even sends out a quick burst of red light to aid the autofocus mechanism, making autofocussed flash possible in complete darkness, should you want to do such a thing.

Meters

As cameras get smarter, the market for separate exposure meters dwindles. However, they're indispensable in still photography and film work. Despite the small demand, the manufacturers haven't been idle; meters are now far more than the former cell-plus-movement. The Gossen Mastersix and the Minolta Flashmeter can read ambient or electronic flash or both, and can accumulate the total effect of multiple flashes. Both display their information on an LCD readout.

cessories, you can read colour temperature, make measurements off the groundglass, do densitometry or darkroom work, and convert to a narrowangle spot meter. Of course, the price of these systems is fairly high because of the small market; a top-of-the-line LCD exposure meter probably costs more than

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The Vivitar Model 5200 flash. The processor module is tailored to suit popular camera electronics and fastens to the flash via a multipin connector.

Other Equipment

Other equipment? It's a gadget freak's delight out there. We'll just look at a few of the trainload of things that eat batteries and bank accounts.

If you're dying to have autofocus on your standard camera, Vivitar makes a series of autofocus lenses for all popular 35mm lens mounts. The 200mm unit shown on the cover takes 3 AAA batteries and weighs 750g (26.4 oz). It's huge; the camera is sort of an accessory to the lens. You'll have to really want autofocus. Maybe it's good for remote control stuff, photographing the neighbours or something.

Camera backs have sprouted brains, if only electronic ones. If your camera can take a special recording back, you can probably tell by a row of contacts just below where the pressure plate would be. They let you imprint the date on the film, and some of them are right smart, letting you program all the camera's functions and run them unattended via an interval timer.

The darkroom is a natural place to put CPUs. The Durst AC707 and AC650, for example, have electronic control of filtering and exposure to minimize test strips and aid in uniformity of colour. Colour analyzers may not be inexpensive, but then, neither is colour paper. The added through features the microprocessor should make the equipment pay for itself, if only because you don't have to throw so many prints in the garbage.

But...

There are certain tasks that deserve to be taken over by microprocessors. Who really needs to stand in the darkroom timing the flow of liquids? If you're not interested in freaky colour effects, wouldn't you rather have perfect consistent filtration figured out by a machine? Flash calculation is a chore best left to circuitry.

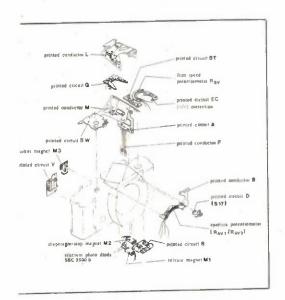
But... I don't want to sound like a fuddy, and I know that this is not a photography review, but this does bear on the philosophy of microelectronics in consumer products. I don't take to automation just for the sake of doing it. I don't like the myriad of symbols and numbers and little pictures that flash at me. If you're doing still photography, you don't need automation, and when you're shooting under rapidly changing conditions, it's unnerving to have to decipher some arbitrary program symbol in a display: is the camera at 125th at f8 or isn't it?

Good photography has nothing to do with how smart the CPU is. I think that automation has its place in the point-andshoot cameras with no controls on them, great for people who just want a picture. As far as creative photography goes, I think the new designs amount to photographic overkill.

There. So much for that. On the other hand, if you're enchanted with gadgets and having the camera do lots for you, the new electronic units are a miracle of economical miniaturization.



The Vivitar self-contained autofocus 200mm lens. Just add batteries and a camera.



The basic construction of an electronic camera, the Leica R4 (courtesy of Wild Leitz).



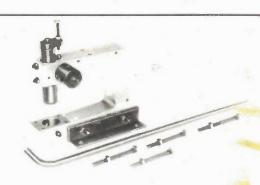


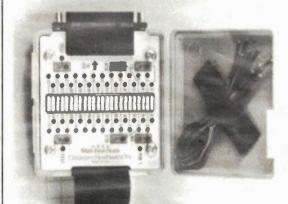
Manupress

Manupress
Prototype developers and small-run manufacturers will be interested in the hand-operated, bench mounted Lever Press from Manupress of the UK. Over 100 standard tools are available to punch holes for components such as "D", BNC and DIN connectors, switches and fuseholders. The range includes nibbling tools, bending and forming tools

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Mini Breakout

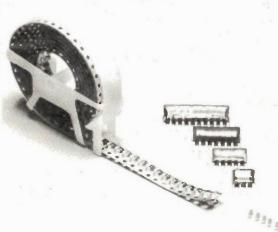
The Datacom Model 15 is a communications breakout box tha allows line jumpering, monitoring and cross connecting of the line between a terminal and a moden or other communications equipment. Small enough to fit in a shir pocket, the box allows fas troubleshooting or modifications. Contact Allan Crawford Associates Ltd., Test and Measurements Division, 583; Coopers Ave., Mississauga, On tario L4Z 1Y2, (416) 890-2010.

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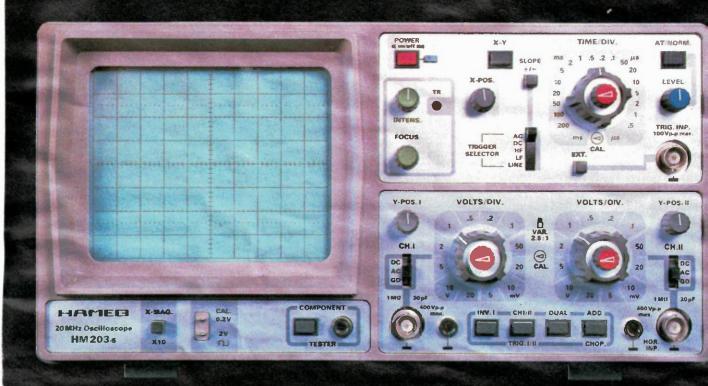
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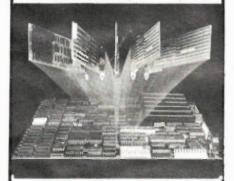
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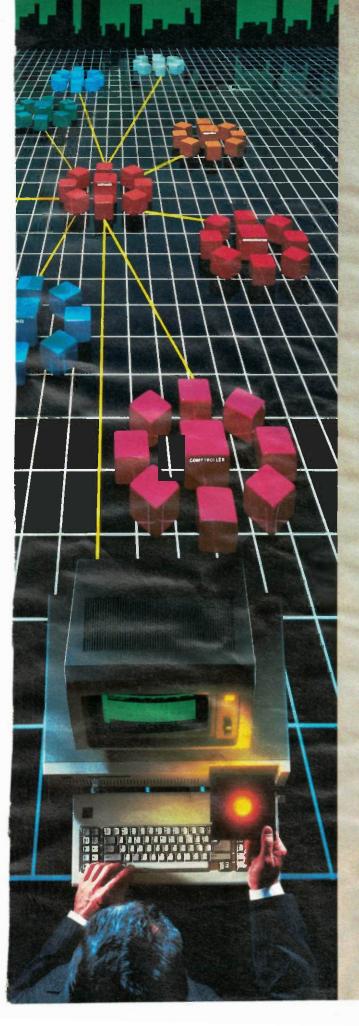
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