



# "attractive to those seeking a less fraught introduction to pcb cad"

Electronics World Dec 96

For over 5 years, Quickroute has been a solution for those seeking easy to use Schematic & PCB design software. Users from all walks of life have found Quickroute's simple approach a refreshing alternative.

*"probably the most intuitive of the integrated schematic-drawing and autorouter products"*  
Electronics World Dec 96

For example, because Quickroute integrates both schematic & PCB design into one package, you only have to learn how to use one set of tools!

*"The drawing facility is logical, sequentially organised and really user-friendly"*  
Educational Computing & Technology Oct/Nov 96

Furthermore, with schematic capture available at the touch of a button, there is no need to mess with net-lists or files.

We haven't forgotten value for money either.

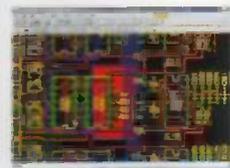
*"extremely good value for money for such a comprehensive package"*  
Practical Wireless July 96

Quickroute 3.6 Designer costs just £149 and includes "one click" schematic capture to turn your circuit into a PCB rats-nest, auto-routing, design checking and support for drill & B.O.M files. For those seeking more power, we have Quickroute PRO+, together with a range of plug in modules and extended libraries. Phone us now or fill in the coupon for a free demonstration pack.

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## Tell me more!

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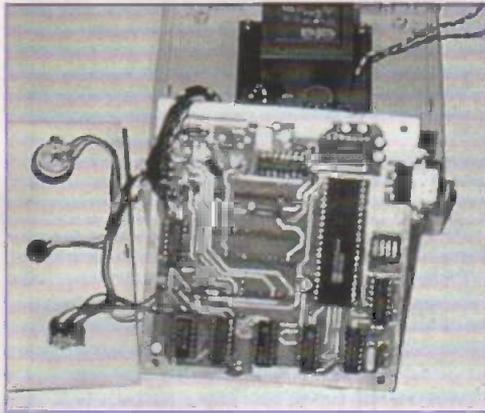


# Contents

Volume 26 No.7

## & Features & Projects

Next Issue 18th July 1997



### **Catching The Universal Serial Bus**

11

The Universal Serial Bus SB is a new single-interface protocol standard for PC computer peripherals giving unlimited expansion capability at transfer rates of more than 1 megabyte per second. Robin Abbott reports.

### **Switch Tuned AF generator**

27

Robert Penfold's design for an audio frequency signal generator covering a range of 10 Hertz to 100 kiloHertz has switch controlled tuning rather than the continuous type. No calibration is needed once the unit is finished - you just switch on and dial up.

### **The New Generation of PC processors**

35

A report on the new processors from Intel and AMD that will be driving PCs into the millenium.

### **Are Your Lights On?**

41

Driving away without activating your headlights can be embarrassing and dangerous. Terry Balbirnie's circuit is designed to give you an audible warning.

### **"Eprommer" Eprom Programmer**

47

Keith Wardill's new Eprom Programmer connects to a computer serial port, and will program and read most eproms now available up to 64KByte now available. A special feature is a plug-in 'Personality Module' for each type of eprom.

### **Peak Reading VU Meter**

58

Tony Sercombe's peak-reading VU meter is designed to have a faster response and greater linearity than many moving-coil VU meters, giving better peak reading against short overloads.

### **Spiced Circuits (Part 3) - More About Frequency**

61

Using SpiceAge simulator and an experimental filter circuit, Owen Bishop examines frequency response, phase, square waves and Fourier analysis.

### **Book Reviews**

65

The Electronic Pathway in Advanced GNVQ Engineering and Satellite Projects Handbook.

### **\*New This Month\***

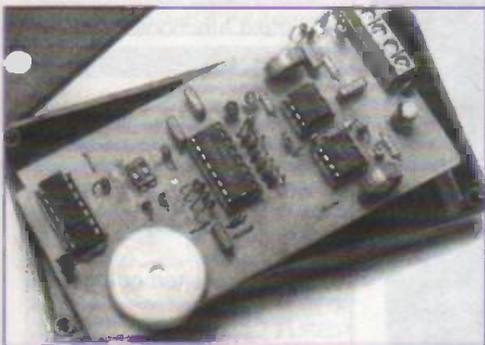
71

The ETI PCB Service returns - buy your ready made boards here.

### **Ham Radio Today**

73

Good news for Radio Amateurs



## Regulars

News

8, 9

Practically Speaking

72

Terry Balbirnie looks at measurements and abbreviations for electronics

PCB foils

68,70

Round the Corner

74



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page 46

## DIGITAL MULTIMETERS

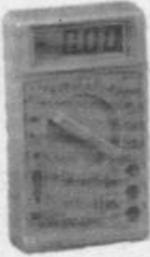
### CM2300 DIGITAL MULTIMETER



- FEATURES:**
- 3.5 LCD DISPLAY
  - HEIGHT 12mm
  - MAX READING 1999
  - HV INDICATION FOR HIGH VOLTAGE
  - SINGLE MANUAL ROTARY SWITCH FOR FUNCTION AND RANGE OPERATION
  - ALL RANGES OVERLOAD PROTECTED
  - 10A DC CURRENT TEST
  - DC VOLTAGE 2V/20V/200V/500V
  - AC VOLTAGE 200/500V
  - DC CURRENT 200mA
  - RESISTANCE 2kΩ /20kΩ /200kΩ /2MΩ
  - SUPPLIED WITH TEST PROBES

**ORDER CODE: CM2300**  
**PRICE: 975p**

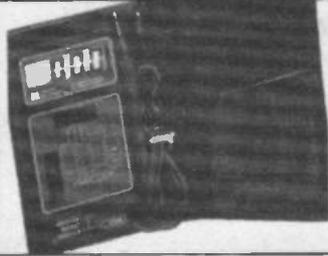
### CM2400T DIGITAL MULTIMETER WITH TEMP MEASUREMENT



- FEATURES:**
- 3.5 LCD DISPLAY
  - HEIGHT 12mm
  - MAXIMUM READING 1999
  - 10A DC CURRENT TEST
  - DC VOLTAGE 200mV/2V/20V/200V/1000V
  - AC VOLTAGE 200/750V
  - DC CURRENT 0.2mA/200mA/20mA/200mA/20A
  - RESISTANCE 200Ω /2KΩ /20KΩ /200KΩ /2MΩ
  - SUPPLIED WITH TEST PROBES
  - TEMPERATURE MEASUREMENT
  - CONTINUITY TEST
  - DIODE TEST & CONTINUITY CHECK
  - ALL RANGES OVERLOAD PROTECTED

**ORDER CODE: CM2400T**  
**PRICE: 1450p**

### CM2900 PACKET DIGITAL MULTIMETER



- FEATURES:**
- 3.5 LCD DISPLAY
  - COMPACT AND LIGHTWEIGHT POCKET SIZE
  - MAXIMUM READING 1999
  - DC CURRENT 7 RESISTANCE OVERLOAD PROTECTED
  - SLIDE SWITCHES FOR FUNCTION AND RANGE OPERATION
  - SUPPLIED IN WALLET WITH TEST PROBES
  - DC VOLTAGE 2V/20V/200V/500V
  - AC VOLTAGE 200V/500V
  - DC CURRENT 200mA
  - RESISTANCE 2KΩ /20KΩ /200KΩ /2MΩ

**ORDER CODE: CM2900**  
**PRICE: 1150p**

### CM3900A DIGITAL MULTIMETER



- FEATURES:**
- LARGE LCD DISPLAY
  - HEIGHT 18mm
  - MAXIMUM READING 1999 + UNIT
  - SINGLE MANUAL ROTARY SWITCH FOR FUNCTION AND RANGE OPERATION
  - AUTO POWER OFF (APPROX 15 mins)
  - DIODE TEST FUNCTION
  - ALL RANGES OVERLOAD PROTECTED
  - SUPPLIED WITH TEST PROBES
  - DC VOLTAGE: 200mV/2V/20V/200V/700V
  - 700V ACCURACY ±0.5%
  - AC VOLTAGE: 200mV/2V/20V/200V/700V
  - DC CURRENT A: 200μA/20mA/200mA/2A/20A
  - AC CURRENT A: 200μA/20mA/200mA/2A/20A
  - RESISTANCE: 200Ω/2kΩ/20kΩ/200kΩ/2MΩ/20MΩ

**ORDER CODE: CM3900A**  
**PRICE: 2900p**

### CM3920 DIGITAL METER WITH TEMP MEASUREMENT



- FEATURES:**
- TEMPERATURE MEASUREMENT
  - DIODE & TRANSISTOR HFE TEST
  - LARGE LCD DISPLAY
  - HEIGHT 18mm
  - MAXIMUM READING 1999 + UNIT
  - SINGLE MANUAL ROTARY SWITCH FOR FUNCTION AND RANGE OPERATION
  - AUTO POWER OFF (APPROX 15 mins)
  - DIODE TEST FUNCTION
  - ALL RANGES OVERLOAD PROTECTED
  - SUPPLIED WITH TEST PROBES
  - DC VOLTAGE: 200mV/2V/20V/200V/1000V
  - 1000V ACCURACY ±0.5%
  - AC VOLTAGE: 200mV/2V/20V/200V/700V
  - DC CURRENT 2mA/20mA/200mA/20A
  - AC CURRENT A: 200mA/20A
  - RESISTANCE: 200Ω/2KΩ/20KΩ/200KΩ/2MΩ/20MΩ
  - CAPACITANCE: 2nF/20nF/200nF/2μF/20μF

**ORDER CODE: CM3920**  
**PRICE: 4100p**

### CM2700 AUTORANGING DIGITAL MULTIMETER



- FEATURES:**
- 3.75 LCD DISPLAY WITH DECIMAL POINT
  - 33 SEGMENT BARGRAPH DISPLAY
  - OVERRANGE INDICATION
  - ROTARY SWITCH FOR FUNCTION SELECTION
  - AUTO POWER OFF (APPROX 15 mins)
  - AUTO POLARITY WITH INDICATION
  - DIODE TEST & CONTINUITY TEST WITH BUZZER
  - ALL RANGES OVERLOAD PROTECTED
  - LOW BATTERY INDICATION
  - SUPPLIED WITH TEST PROBES
  - DC VOLTAGE: 320mV/3.2V/32V/320V/600V
  - AC VOLTAGE: 320mV/3.2V/32V/320V/600V
  - DC CURRENT A: 320μA/3200μA/32mA/320mA/10A
  - AC CURRENT A: 320μA/3200μA/32mA/320mA/10A
  - RESISTANCE: 320Ω/3.2KΩ/32KΩ/320KΩ/3.2MΩ/32MΩ

**ORDER CODE: CM2700**  
**PRICE: 4050p**

### CM3230 DIGITAL CAPACITANCE METER



- FEATURES:**
- 3.5 LCD DISPLAY
  - HEIGHT 18mm
  - MAXIMUM READING 1999
  - CAPACITANCE 9 RANGES FROM 200pF - 20000μF
  - MEASURING FROM 10F - 20000μF
  - SINGLE MANUAL ROTARY SWITCH FOR FUNCTION AND RANGE OPERATION
  - ZERO ADJUST KNOB

**ORDER CODE: CM3230**  
**PRICE: 3950p**

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A single remote control to operate Television, Videos and Satellite Receivers. Plus Auxiliary Options!!

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<b>ALBA</b>	<b>CODE</b>	<b>ECHOSTAR</b>	<b>CODE</b>	<b>MIMTEC</b>	<b>CODE</b>
SAT660	SATPSU2	SR5500 EARLY PSU WITH ADJ 6500, SR7700, SR8700	SATPSU12 SATPSU13	SOPRENSON TYPE PSU ONLY	SATPSU15
<b>AMSTRAD</b>	<b>CODE</b>	<b>FERGUSON</b>	<b>CODE</b>	<b>NETWORK</b>	<b>CODE</b>
SRD510, SRD520, SRD540, SRD550 SRDR45 SRD500 SRX320, SRX340, SRX345, SRX350 SRX100 SRD600 SAT250, SR950, SRD700, SRD950, SRX1002, SRX2001, SRX301, SRX501, SRX502 SRD2000	SATPSU3  SATPSU4 SATPSU5 SATPSU6 SATPSU14 SATPSU16  SATPSU18	SRD 5, SRD16 SRV1 SRDE4	SATPSU1 SATPSU2 SATPSU11	9000, 9200	SATPSU2
<b>BRITISH TELECOM</b>	<b>CODE</b>	<b>FINLUX</b>	<b>CODE</b>	<b>NOKIA</b>	<b>CODE</b>
SVS300	SATPSU17	SR5700	SATPSU12	SAT1500	SATPSU2
<b>BUSH</b>	<b>CODE</b>	<b>GOODMANS</b>	<b>CODE</b>	<b>PACE</b>	<b>CODE</b>
IRD150 IRD155	SATPSU12 SATPSU19	ST700	SATPSU1	PRD800, PRD900, PSR800, PSR900 MRD920, SS9000, SS9010, SS9200, SS9210, SS9220 D100, D150, MSS100 APOLLO, MSS200, MSS300 MSS500, MSS1000	SATPSU1 SATPSU2  SATPSU6 SATPSU8 SATPSU9 SATPSU10
<b>CHURCHILL</b>	<b>CODE</b>	<b>GRUNDIG</b>	<b>CODE</b>	<b>PHILIPS</b>	<b>CODE</b>
D3MAC DECODER	SATPSU7	STR1 GIRD200, FIRD3000	SATPSU1 SATPSU2	STU802/05M STU801	SATPSU1 SATPSU2
		<b>MANHATTAN</b>	<b>CODE</b>	<b>THOMSON</b>	<b>CODE</b>
		850, 950	SATPSU1	SRS4	SATPSU2
		<b>MASPRO</b>	<b>CODE</b>	<b>TOSHIBA</b>	<b>CODE</b>
		SRE250S/1, SRE350S/1 SRE250S, SRE350S, SRE450S	SATPSU1 SATPSU2	SAT99, TU-SDU200	SATPSU1

CODE	PRICE	CODE	PRICE	CODE	PRICE	CODE	PRICE
SATPSU1	650p	SATPSU6	650p	SATPSU11	835p	SATPSU16	730p
SATPSU2	650p	SATPSU7	650p	SATPSU12	1735p	SATPSU17	850p
SATPSU3	650p	SATPSU8	730p	SATPSU13	3125p	SATPSU18	1175p
SATPSU4	650p	SATPSU9	900p	SATPSU14	3135p	SATPSU19	650p
SATPSU5	650p	SATPSU10	1230p	SATPSU15	77.5p		

### PACE SATELLITE TUNERS

MODELS	CODE	PRICE
PRD800, MSS200 (2GHZ) (221-2077062)	TUNER01	1650p
PRD900, MSS500, MSS1000 (2GHZ) (221-2177012)	TUNER02	1650p

### PACE SWITCH MODE TRANSFORMERS

MODELS	CODE	PRICE
PACE9000	PACE9000	800p
PACEPRD800, PRD900	PRD800	550p

### SATMETER

THE SATMETER IS A PROFESSIONAL PORTABLE SATELLITE STRNTH METER DESIGNED FOR THE INSTALLATION AND MAINTENANCE OF SATELLITE TV SYSTEMS. THE SATMETER CAN BE USED AS STAND ALONE METER WITH POWERING THE LNB AS WELL AS IN LOOP. THROUGH OPERATION WITH SATELLITE RX POWERING THE LNB.

ACOUSTICAL SIGNAL: ON SIGNAL STRENGTH  
INPUT IMPEDENCE: 75 Ohm  
MAX.INPUT SIGNAL: -10 DBM

LED INDICATOR: VERTICAL/HORIZONTAL  
POWER AMPLIFIER: 18 DB

FREQUENCY RANGE: 900 TO 2050 MHZ  
DETECTION RANGE: -60 TO -10 DBM

**ORDER CODE: TOOL 22      PRICE: 8500p**

### SATELLITE LNB'S

MAKE & MODEL	ORDER CODE	PRICE	MAKE & MODEL	ORDER CODE	PRICE
Cambridge AE22/AE5 0.8dB standard 10.95-11.70 GHz Gold Range	LNB1	2160p	Cambridge AE7 Twin O/P H+V Both Enhanced	LNB7	4000p
Cambridge AE14 Universal LNB 10.7-11.7/11.7-12.75 GHz	LNB2	2500p	Cambridge AE2 Dual O/P H-V Separate Enhanced	LNB8	3550p
Cambridge AE21/AE5 Single O/P Switching LNB 1.0dB Standard	LNB3	2050p	Grundig Super Universal 'Anis' 10.7-12.75 GHz 0.8dB	LNB9	2600p
Cambridge AE19/AE6 Single O/P Switching LNB 1.0dB Enhanced	LNB4	2050p	Grundig Universal 'Anis' 10.7-12.75 GHz 1.0dB	LNB10	2250p
Cambridge AE23/AE12 0.8dB Enhanced 10.7-11.8GHz Gold Range	LNB5	2160p	Cambridge AE1 Twin O/P H+V Both Standard	LNB11	4000p
Cambridge AE8 Dual O/P H-V Separate Enhanced	LNB6	4000p			

### FUSES

CURRENT RATING	TIME LAG (20MM)		QUICK BLOW (20MM)	
	ORDER CODE	PRICE	ORDER CODE	PRICE
100mA	FUSE36	75p	FUSE37	60p
160mA	FUSE01	75p	FUSE17	60p
250mA	FUSE02	75p	FUSE18	60p
315mA	FUSE03	75p	FUSE19	60p
400mA	FUSE04	75p	FUSE20	60p
500mA	FUSE05	75p	FUSE21	60p
630mA	FUSE06	75p	FUSE22	60p
800mA	FUSE07	60p	FUSE23	60p
1A	FUSE08	60p	FUSE24	60p
1.25A	FUSE09	60p	FUSE25	60p
1.6A	FUSE10	60p	FUSE26	60p
2A	FUSE11	50p	FUSE27	60p
2.5A	FUSE12	50p	FUSE28	60p
3.15A	FUSE13	55p	FUSE29	50p
4A	FUSE14	55p	FUSE30	50p
5A	FUSE15	60p	FUSE31	50p
6.3A	FUSE16	60p	FUSE32	50p

### CERAMIC PLUG TOP

CURRENT RATING	ORDER CODE	PRICE
3A	FUSE33	100p
5A	FUSE34	100p
13A	FUSE35	100p

### 20mm CERAMIC TIME LAG

CURRENT RATING	ORDER CODE	PRICE
6.3A	FUSE38	100p
8A	FUSE39	100p
10A	FUSE40	100p
3.15A	FUSE41	85p
4A	FUSE42	85p
5A	FUSE43	85p

### 38mm CERAMIC TIME LAG

CURRENT RATING	ORDER CODE	PRICE
10A	FUSE48	815p

### 32mm CERAMIC SLOW BLOW

CURRENT RATING	ORDER CODE	PRICE
8A	FUSE44	185p
10A	FUSE45	185p
15A	FUSE46	185p
20A	FUSE47	210p

NB.

ALL FUSES ARE MADE IN THE UK AND FULLY MEET BS4265 & BS1362 SAFETY STANDARDS AND SHOULD NOT BE COMPARED WITH CHEAP IMPORTED TYPES.

**\*\*ALL THE ABOVE PRICES ARE FOR PACKS OF 10 FUSES\*\***

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PART	PRICE	PART	PRICE	PART	PRICE	PART	PRICE	PART	PRICE	PART	PRICE
AC125	30P	BD647	50P	BU409	85P	BUX48A	150P	MPSA14	15P	2N3533	100P
AC126	30P	BD649	50P	BU412	175P	BUX55	800P	MPSA20	15P	2N3585	650P
AC127	30P	BD675	40P	BU413	175P	BUX80	180P	MPSA42	15P	2N3702	9P
AC128K	40P	BD676	40P	BU414B	250P	BUX81	160P	MPSA43	15P	2N3703	9P
AC141K	45P	BD677	38P	BU415A	170P	BUX84	50P	MPSA44	40P	2N3704	9P
AC176	22P	BD678	40P	BU426A	70P	BUX85	50P	MPSA45	12P	2N3705	9P
AC18	48P	BD679	40P	BU433	120P	BUX86	30P	MPSA50	15P	2N3706	9P
AC19	48P	BD680	40P	BU500	100P	BUX87	50P	MPSA70	15P	2N3707	9P
AD146	60P	BD681	45P	BU500D	225P	BUX98A	350P	MPSA92	20P	2N3710	12P
AF125	50P	BD682	45P	BU505	90P	BUY18S	150P	MPSA93	20P	2N3711	12P
AF139	30P	BD705	50P	BU505D	90P	BUY47	150P	MPSU10	200P	2N3771	85P
BC107	8P	BD707	50P	BU505DF	90P	BUY57	125P	MPSU45	550P	2N3772	90P
BC108	8P	BD709	50P	BU506	100P	BUY69A	200P	MPSU56	400P	2N3773	100P
BC109	8P	BD711	50P	BU506D	120P	BUY71	250P	MPSU60	350P	2N3792	150P
BC109C	10P	BD736	50P	BU506DF	120P	BUZ10	65P	MR510	35P	2N3799	18P
BC140	20P	BD826	50P	BU508	70P	BUZ11	200P	MR856	36P	2N3819	29P
BC142	20P	BD828	50P	BU508AF	95P	BUZ11A	175P	OC28	350P	2N3820	70P
BC143	20P	BD839	55P	BU508APH	80P	BUZ14	550P	OC29	250P	2N3823	40P
BC147	8P	BD897	50P	BU508D	90P	BUZ20	225P	OC35	350P	2N3866	110P
BC149	8P	BD899	50P	BU508DF	115P	BUZ21	250P	OC36	250P	2N3903	11P
BC159	8P	BD977	50P	BU508DR	130P	BUZ24	350P	OC45	50P	2N3906	11P
BC160	30P	BDX33	60P	BU508V	110P	BUZ25	450P	OC200	180P	2N3924	375P
BC171	10P	BDX37	100P	BU508VF	100P	BUZ32	125P	R2008R	100P	2N3958	375P
BC172	10P	BDX44	100P	BU526	75P	BUZ36	800P	R2010E	100P	2N4031	25P
BC177	14P	BDX47	75P	BU536	100P	BUZ44A	525P	S2000A3	175P	2N4033	25P
BC178	14P	BDX54C	75P	BU546	125P	BUZ45A	800P	S2000AF	175P	2N4036	25P
BC179	14P	BDX62C	150P	BU603	125P	BUZ50B	600P	S2055A	175P	2N4039	29P
BC182	7P	BDX63C	175P	BU606D	225P	BUZ51	800P	S2055AF	200P	2N4347	130P
BC182L	7P	BDX64C	175P	BU626	120P	BUZ71AF	100P	S2300A	100P	2N4391	60P
BC183	7P	BDX65	80P	BU626	120P	BUZ71AF	100P	S2800M	72P	2N4392	50P
BC183L	7P	BDX66C	175P	BU705	130P	BUZ72AF	100P	TIP29	15P	2N4393	55P
BC184	7P	BDX67C	275P	BU706DF	175P	BUZ72AF	100P	TIP29A	22P	2N4399	200P
BC184L	7P	BDX71	70P	BU706F	150P	BUZ73A	150P	TIP29C	25P	2N4401	12P
BC212	7P	BDX77	175P	BU724A	100P	BUZ76A	110P	TIP29E	40P	2N4403	12P
BC212L	7P	BDX87C	175P	BU801	70P	BUZ80	200P	TIP29F	25P	2N4416	120P
BC213	7P	BDX88C	150P	BU805	70P	BUZ90AF	200P	TIP30C	25P	2N4420	75P
BC213L	7P	BDW24	55P	BU807	55P	BUZ83	200P	TIP31A	22P	2N4427	75P
BC214	7P	BDW93	50P	BU807F	75P	BUZ90A	180P	TIP31C	27P	2N4920	50P
BC214L	7P	BDW94	50P	BU808DF	300P	BUZ91A	400P	TIP32	24P	2N4922	30P
BC237	7P	BDY29	225P	BU810	110P	BY448	20P	TIP32A	21P	2N4923	30P
BC238	7P	BDY56	225P	BU824	450P	BYT11	25P	TIP32C	35P	2N5038	175P
BC239	7P	BDY58	500P	BU826	120P	C106D	28P	TIP33	20P	2N5061	20P
BC300	20P	BDY90	125P	BU826A	100P	CGY80	40P	TIP33C	60P	2N5088	20P
BC301	20P	BDY92	100P	BU902	110P	IRF120	225P	TIP34	65P	2N5109	100P
BC302	20P	BF137	35P	BU903	110P	IRF130	475P	TIP34C	60P	2N5116	175P
BC303	20P	BF167	30P	BU910	80P	IRF140	550P	TIP35C	65P	2N5154	150P
BC304	25P	BF181	18P	BU912	100P	IRF230	550P	TIP36C	65P	2N5192	600P
BC327	7P	BF183	20P	BU920	100P	IRF240	425P	TIP42A	20P	2N5193	40P
BC328	7P	BF195	8P	BU922	110P	IRF250	375P	TIP41C	22P	2N5190	50P
BC337	7P	BF199	8P	BU930	130P	IRF330	600P	TIP42A	20P	2N5241	500P
BC338	7P	BF200	16P	BU932	175P	IRF330	325P	TIP42C	22P	2N5245	45P
BC441	28P	BF225	30P	BU941	250P	IRF350	750P	TIP47	40P	2N5294	30P
BC446	8P	BF240	16P	BU941	130P	IRF450	650P	TIP48	40P	2N5296	30P
BC477	18P	BF245	25P	BU2508A	130P	IRF10	150P	TIP50	60P	2N5320	50P
BC516	22P	BF254	15P	BU2508DF	130P	IRF520	150P	TIP51	80P	2N5322	55P
BC537	25P	BF255	12P	BU2508DF	150P	IRF530	150P	TIP52	80P	2N5401	10P
BC546	8P	BF256	18P	BU2520AF	225P	IRF540	200P	TIP54	85P	2N5416	40P
BC547	8P	BF257	18P	BU2520DF	225P	IRF540	150P	TIP102	70P	2N5448	12P
BC548	8P	BF259	18P	BU2525AF	325P	IRF611	150P	TIP105	65P	2N5457	45P
BC549	8P	BF262	25P	BU2525AF	325P	IRF620	160P	TIP106	65P	2N5458	55P
BC550	8P	BF270	18P	BU2527AF	400P	IRF630	150P	TIP107	65P	2N5460	55P
BC556	8P	BF273	15P	BU2528DF	200P	IRF640	350P	TIP107	65P	2N5461	75P
BC557	8P	BF311	21P	BUF405A	200P	IRF642	200P	TIP111	40P	2N5462	45P
BC558	8P	BF336	20P	BUH15D	250P	IRF650	200P	TIP112	35P	2N5484	55P
BC559	8P	BF337	20P	BUH515	200P	IRF710	150P	TIP112H	50P	2N5551	12P
BC560	8P	BF338	20P	BUH515D	250P	IRF720	150P	TIP115	30P	2N5671	350P
BC637	20P	BF362	30P	BUH517	275P	IRF730	150P	TIP116	30P	2N5672	400P
BC639	20P	BF367	13P	BUH517D	175P	IRF740	150P	TIP117	30P	2N5680	55P
BC640	20P	BF371	17P	BUH715	425P	IRF750	150P	TIP120	37P	2N5884	175P
BCY33	200P	BF421	18P	BUV93	175P	IRF830	160P	TIP121	35P	2N5886	325P
BCY34	200P	BF422	21P	BUK444	200P	IRF840	150P	TIP122	30P	2N6031	250P
BCY70	16P	BF423	25P	BUK444/500B	200P	IRF9140	1000P	TIP125	30P	2N6049	55P
BCY71	16P	BF455	12P	BUK444/800B	200P	IRF9510	150P	TIP126	40P	2N6059	150P
BCY72	16P	BF458	19P	BUK445/800B	200P	IRF9511	150P	TIP127	50P	2N6086	55P
BD115	30P	BF462	20P	BUK446/800B	200P	IRF9520	150P	TIP130	30P	2N6090	45P
BD124P	50P	BF471	28P	BUK446/800B	200P	IRF9530	400P	TIP131	30P	2N6107	40P
BD131	25P	BF472	28P	BUK446/800B	200P	IRF9540	300P	TIP132	30P	2N6109	40P
BD132	25P	BF479	30P	BUK455/600B	200P	IRF9541	200P	TIP136	40P	2N6211	400P
BD133	50P	BF494	16P	BUK456/600B	200P	IRF9610	150P	TIP137	65P	2N6248	150P
BD135	20P	BF495	16P	BUW51A	200P	IRF9620	150P	TIP162	150P	2N6284	225P
BD136	20P	BF595	16P	BUW51B	150P	IRF9622	200P	TIP141	65P	2N6287	225P
BD137	20P	BF596	16P	BUW51C	150P	IRF9630	325P	TIP143	75P	2N6292	40P
BD138	20P	BF615	30P	BUW51D	1900P	IRF9640	375P	TIP145	50P	2N6385	120P
BD139	20P	BF617	30P	BUW51E	1900P	IRF9640	375P	TIP146	50P	2N6403	160P
BD140	20P	BF760	40P	BUS11A	200P	IRFD9220	100P	TIP147	80P	2N6427	25P
BD144	90P	BF763	40P	BUS12A	200P	IRF8C30	200P	TIP150	90P	2N6476	250P
BD157	38P	BF870	22P	BUS14A	500P	IRFBC40	400P	TIP151	100P	2N6488	30P
BD166	30P	BF871	22P	BUS23	225P	IRFP140	250P	TIP2955	50P	2N6491	30P
BD175	30P	BF960	38P	BUS24	175P	IRFP150	300P	TIP3055	60P	2N6547	300P
BD177	30P	BF961	38P	BUS25	175P	IRFP240	300P	TIP1760	100P	2N6609	375P
BD179	32P	BF984	35P	BUT11AF	55P	IRFP250	400P	TIP1762A	200P	2N6666	375P
BD181	45P	BFQ232	75P	BUT12	80P	IRFP350	325P	TIP1763A	200P	2N6675	175P
BD182	60P	BFQ252A	60P	BUT13	310P	IRFP450	400P	TIP1791A	80P	2N6678	225P
BD184	60P	BFQ90	85P	BUT18	80P	IRFP460	775P	TS81	15P	4N35	50P
BD187	30P	BFQ91	99P	BUT18AF	80P	IRFP140	1450P	TS80	15P		
BD201	32P	BFQ43	30P	BUT30V	1700P	IRFP240	500P	TS83	20P	RECTIFIER DIODES	
BD202	38P	BFQ29	28P	BUT36A	100P	IRFP350	600P	ZTX107	11P		
BD203	42P	BFX84	20P	BUT78A	80P	IRFRC20	250P	ZTX108	11P		
BD204	42P	BFX85	20P	BUT90	1300P	IRFZ20	65P	ZTX109	12P	BY127	8P
BD222	31P	BFX87	15P	BUT92	1200P	IRFZ42	275P	ZTX122	20P	BY133	8P
BD225	31P	BFX88	15P	BUV18	650P	IRFZ44	275P	ZTX300	10P	BY179	35P
BD232	31P	BFX89	60P	BUV20	650P	MJ906	16P	ZTX302	200P	BY184	32P
BD233	30P	BFY50	14P	BUV21	400P	MJ1001	200P	ZTX303	20P	BY206	11P
BD234	32P	BFY51	14P	BUV23	475P	MJ1001	200P	ZTX304	10P	BY207	20P
BD235	28P	BFY52	14P	BUV24	350P	MJ2501	100P	ZTX320	20P	BY227	19P
BD236	30P	BFY56	25P	BUV25	110P	MJ2955	55P	ZTX320	20P	BY228	28P
BD237	21P	BFY64	25P	BUV26	150P	MJ3000	100P	ZTX501	10P	BY298	15P
BD238	24P	BFY90	45P	BUV27	125P	MJ3001	100P	ZTX502	17P	BY299	18P
BD239	30P	BLY48	85P	BUV28	110P	MJ4032	175P				

## TRANSISTORS

PART	PRICE	PART	PRICE	PART	PRICE	PART	PRICE	PART	PRICE
<b>I.C. SOCKETS</b>		1A/50V		TIC116C	59p	8156	300p	4075	13p
8 PIN	4P	W01	18p	8A/300V		8224	240p	4076	42p
14 PIN	5P	1A/100V		TIC116D	70p	8225	240p	4077	13p
16 PIN	6P	W02	19p	8A/400V		8250	750p	4078	13p
18 PIN	9P	1A/200V	21p	TIC126D	75p	8251	200p	4081	13p
20 PIN	10P	W04		12A/400V		8253	160p	4082	13p
22 PIN	12P	LA/400V	23p	TIC126M	90p	8257	220p	4085	36p
24 PIN	13P	W06		12A/600V		8271	340p	4086	30p
28 PIN	15P	LA/600V	28p	C106D	28p	8279	270p	4089	75p
32 PIN	17P	W08		4A/400V		8283	400p	4093	18p
36 PIN	19P	1A/800V	28p	BR103	37p	8284	440p	4094	44p
40 PIN	15P	BR81D	33p	BR303	85p	8287	260p	4094	58p
<b>ZENER DIODES</b>		2A/100V		BT106	180p	8288	650p	4098	50p
400m	WATT	BR82D	33p	BT119	100p	82C206PLCC	500p	4099	42p
2V7 TO 39V	5P	2A/200V		17088	200p	8748	700p	4501	26p
1.3	WATT	BR84D	37p	17127	200p	8755	800p	4502	36p
2V7 TO 39V	9P	2A/400V	43p	15/80H	200p	8726	110p	4504	35p
<b>VOLTAGE REGULATORS</b>		BR88D	43p	15/85R	230p			4505	80p
		2A/800V		SG 264	800p			4506	58p
		BR32	43p	SG613	1500p			4507	30p
		2A/200V						4508	67p
		BR34	43p	<b>COMPUTER IC's</b>		4000	13p	4510	32p
7805	25P	2A/400V		Z80ACPU	100p	4001	13p	4511	30p
7806	25P	BR36	44p	Z80ADMA	200p	4002	13p	4512	38p
7808	25P	2A/600V		Z80ADCTC	140p	4006	34p	4514	65p
7812	25P	BR62	80p	Z80ASIO-1	210p	4007	13p	4515	65p
7815	25P	6A/200V		Z80ASIO-2	210p	4009	20p	4516	36p
7818	25P	BR64	72p	75107	55p	4010	21p	4517	100p
7824	25P	6A/400V		75110	75p	4011	13p	4518	56p
7825	25P	BR251	150p	75112	110p	4012	13p	4519	23p
7826	30P	25A/100V		75122	110p	4014	32p	4520	36p
7828	30P	BR252	165p	75154	100p	4018	18p	4521	86p
7912	30P	24A/200V		75162	700p	4019	28p	4526	38p
7915	30P	BR254	185p	75182	95p	4020	33p	4527	41p
7918	30P	25A/400V		75183	95p	4021	36p	4528	39p
7924	30P	BR2156	200p	75195	185p	4022	36p	4529	65p
79L05	24P	25A/600V		2114	150p	4023	13p	4532	48p
79L08	24P	BR258	240p	2532	200p	4024	25p	4533	36p
79L12	24P	25A/800V		26LS32	75p	4025	13p	4534	140p
79L15	24P	BR351	185p	2716	100p	4026	60p	4535	60p
79L18	24P	35V/100V		2732	200p	4027	18p	4536	30p
79L24	24P	BR352	200p	2732A	220p	4028	29p	4537	40p
79L05	35P	35V/200V		2764	150p	4029	34p	4538	40p
79L08	35P	BR354	220p	27C64	200p	4030	17p	4539	120p
79L12	35P	35V/400V		27128	150p	4032	52p	4540	140p
79L15	35P	BR356	230p	27256-25	150p	4033	52p	4541	35p
LM309K	100P	35V/600V		27512	300p	4034	60p	4542	50p
LM317T	100P	BR358	260p	4116	40p	4035	76p	4543	170p
LM323K	350P	35V/800V		4164-15	80p	4038	42p	4544	180p
78H08KC	800P	BY164	40p	4164-12	90p	4040	46p	4545	55p
78H12KC	700P	1.5A/100V		41256-15	80p	4041	36p	4546	55p
78HGKC	800P	BY176	40p	41256-12	100p	4042	30p	4547	48p
		1.5A/800V		41256-10	110p	4043	36p	4548	48p
				41464-12	150p	4045	72p	4549	58p
<b>L.E.D.'s 3mm</b>		<b>TRIACS</b>		6116	80p	4046	42p	4550	120p
RED	5p	TIC206D	60p	6254-10	210p	4047	45p	4551	120p
YELLOW	8p	4A/400V		62256-12	300p	4048	26p	4552	120p
GREEN	8p	6502A	360p	6502A	360p	4049	18p	4553	120p
		TIC225D	69p	65C02	930p	4050	20p	4554	20p
5mm		6A/400V		6522	280p	4051	38p	4555	15p
RED	5p	TIC226D	68p	6800	210p	4052	35p	4556	18p
YELLOW	8p	8A/400V		6802	220p	4053	35p	4557	20p
GREEN	8p	TIC235D	85p	6803	500p	4054	53p	4558	35p
<b>RECTANGULAR LED's</b>		12A/400V		6808	500p	4055	52p	4559	10p
5mm x 2.5mm		TIC246D	105p	6809	500p	4056	52p	4560	30p
RED	5p	16A/400V		6810	150p	4060	40p	4561	30p
YELLOW	8p	TIC263D	190p	6818	380p	4063	52p	4562	25p
GREEN	8p	20A/400V		6821	130p	4066	20p	4563	20p
<b>BRIDGE RECTIFIER</b>		TIC263D	205p	6840	200p	4067	120p	4564	30p
W005	16p	2N5061	20p	6845	200p	4068	13p	4565	45p
		0.8A/60V		6850	90p	4069	13p	4566	32p
				74F244	35p	4070	13p	4567	32p
				8085A	300p	4071	13p	4568	20p
				8086	500p	4072	13p	4569	25p
				8088	480p	4073	13p	4570	15p
								4571	15p
								4572	15p
								4573	15p
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								4638	15p
								4639	15p
								4640	15p

## Mobile Phone Call for Recycling

The Association of European Telecommunications and Professional Electronics Industry (ECTEL) in conjunction with British Telecommunications (BT) is running a pilot scheme to encourage the recycling of mobile phones on the basis of "take-back", where manufacturers offer to take back and recycle items once users have finished with them.

The idea is that technology is moving so fast that new "state of the art" equipment can be overtaken quickly by newer ones, creating obsolescence and a problem of waste management. The issue of waste electronic equipment is gradually being considered by Governments and industry is being encouraged to take a lead in establishing what is "technically possible and economically sensible". A Government spokesman said: "We need to establish how much electronic equipment is going into the waste stream and the extent of its potential environmental impact. We must also define the equipment to be considered and decide which categories should be earmarked as priorities for recovery. This should help to establish a methodology for the recycling of electronic equipment."

Under the scheme, owners of old mobile phones will be invited to return them to their nearest BT communication centre, on the understanding that they will be disposed of in an environmentally friendly way. The aim is to establish if there is a



real consumer interest in the recycling of phones and whether "take-back" is an environmentally and economically viable solution for small electronic products.

Users can take used mobile phones and batteries to the following contact points:

Atlas (Northern Ireland, Cumbria, South and West Scotland) 01387 251010; Tyne Tees Telecom (North East) 0191 232 4536; JWE Foneshops (Yorkshire and North Midlands) 0990 130130; Landalow (East and North Scotland) 01738 633674; Midland Phones (West Midlands) 01384 572144; Phoneline (Thames Valley) 01753 553553 Crown Talk (Midlands) 01203 447844; Keeping You In Touch (Greater Manchester, Lancs and North Cheshire) 0161 848 0599; Uplands (South Wales and South West) 0800 240000 Phone The World (South Coast) 01489 577770; Radiophone (Cambs and Norfolk) 01603 434000; National Radiophone (Milton Keynes and Northants) 01933 402202; Moco (Kent and West London) 0800 371214; Straight Talk (South London) 0800 171819. ECTEL can be contacted at 01753 500074.

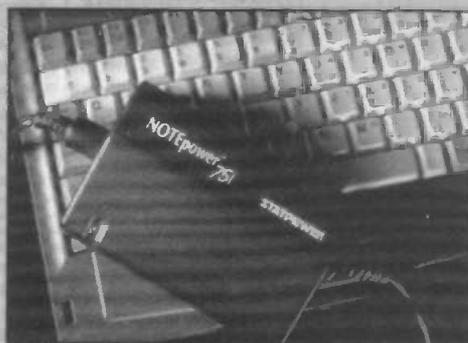
Users interested in the scheme can contact their nearest BT shop for information.

In April, leading recycling experts from the Department of the Environment, industry and local government have addressed a conference on the implications for industry of recent packaging recovery legislation. Legislation covering a gamut of materials, from card to batteries, is gradually placing greater obligations on manufacturers and users to recycle used materials.

## Matchbox-sized recharger for in-car use

Travelling users of computer and other equipment are constantly on the guard against unwanted power-failures caused by their batteries running out while in use. Small chargers that can be connected via a car's 12V supply are becoming more popular. The NOTEpower 75i inverter by Merlin Equipment (shown) converts the car's 12 volts to 230 VAC mains. Specifically designed by a computer manufacturer for operating small appliances, the NOTEpower 75i plugs into that mainstay of portable electrical appliances, the car's cigar lighter socket. Output is sent to the appliance's own power supply "brick" using a standard cable provided.

The NOTEpower 75i can also be used to power peripherals such as bubblejet printers for a complete mobile workstation. Designed to be more than 90 percent efficient, it is also fully overload and overheat protected, and stated by its makers to be smaller, lighter and easier to use than any other on the market at present. It also features a battery alarm to warn if the vehicle battery is low and turn off the inverter to allow the car to re-start. So you will not have to substitute a dead computer with an immobile car.



Other useful applications include running chargers for camcorders, small power tools and other equipment that can run

on mobile 230 volt AC power. The NOTEpower 75i is CE approved for electrical interference and safety. The retail price is £86 ex VAT and carriage. This is a top-end example of a useful class of power add-on.

For information contact Merlin Equipment, Unit 1, Hithercroft Court, Lupton Road, Hithercroft Industrial Estate, Wallingford, Oxon OX10 9BT. Tel. 01491 824333 fax 01492 824466.

# Rockwell bring in 56 Kbps analogue modem chip



The K56Plus modem technology from Rockwell provides data transfer rates of up to 56,000 bps across standard telephone lines. Faster transfer rates are now in greater demand, particularly from Internet users wanting high speed connection.

One advantage of the K56Plus technology is the

availability of higher speed of transmission without the extra expense of ISDN installation. The Rockwell technology has already received the support of over 300 Internet Service Providers (ISPs) and communications equipment manufacturers. K56Plus crosses the gap between analogue and digital comms; where existing analogue modems normally assume an analogue connection at both ends of the communications, K56Plus assumes that one end is a purely digital connection - which tends to be the case for corporate users and ISPs.

For the user accessing the Internet or a large central computer site from a K56Plus modem, the upstream part of the dialogue, mainly keystrokes and mouse commands, flows at the conventional rate of 33.6 Kbps, and the downstream conversation, frequently Web pages loaded with graphics, sound, video and other large data files, can be downloaded at the higher 56.6 Kbps rate. Time and cost are reduced, a significant factor as the Net access grows more congested.

Rockwell have used a new encoding method that reduces the analogue noise that has hitherto restricted data rates to 33.6Kbps when employed with a K56Plus modem to dial into a digital connection. K56Plus products will be upgradeable to the expected new ITU standard, say distributor Telecom Design Communications, and compatibility should be ensured by Rockwell's very large existing modem manufacturer and user base.

This may be as fast as analogue modems get - until technology springs another surprise.

Information from TDC Ltd., Stroudley Road, Basingstoke, Hants RG24 8FN. Tel 01256 332800 Fax 01256 332810.

## OVERSEAS READERS

To call UK telephone numbers, replace the initial 0 with your local overseas access code plus the digits 44.

## MODSMODSMODSMODSMODSMODS

**CampLight (October 1996):** for future constructors: Electrovalue, who supply the magnetic core, have moved. The new address is Unit 5, Beta Way, Thorpe Industrial Park, Egham, Surrey TW20 8RE Tel 01784 433604. Overseas mail order customers please write to Electrovalue for ordering information. (And the coil-winding wire is 0.20 mm insulated copper.)

**PIC Programmer (Vol 26 issue 6):** for a non-DIY copy of the 'PIC devices programmed' table on page 40 of last month's issue, please send your address to Lynn at Nexus House (see page 74).



# Test and measurement catalogue

Keithley Instruments have published their 1997 test and measurement catalogue and reference guide. The 300-page catalogue includes DMMs, electrometers, precision sources, voltmeters, picoammeters, ohmmeters, source-measure units, power supplies, switch systems, semiconductor characterisation systems, a new line of miniaturised measurement modules and more. The guide includes application examples to help design a test system, selector guides to compare important specifications and complete product specifications.

Specially featured in the colour section is the model 2400 digital sourcemeter, the fastest system instrument offered by Keithley to date, a high-speed test solution for large volume component manufacturers.

The catalogue is available in print or on CD rom from Keithley Instruments, The Minster, 58 Portman Road, Reading, Berks RG30 1EA. Tel 0118 9575666 fax 0118 9596469.

# Nevada Catalogue



Nevada's 1997 hobby communications catalogue is available now to anyone who sends an A4 stamped self-addressed envelope to Nevada, 189 London Road, North End, Portsmouth, Hants PO2 9AE. Radio enthusiasts will find antenna equipment, scanning receivers, transceivers, airband and marine radios, filters, packet radio

equipment, TVI filters, Morse equipment and many more things for the radio shack. Their showroom in Portsmouth is also open to the public.

**DIFFERENTIAL THERMOSTAT KIT** Perfect for heat recording, solar systems, boiler efficiency etc. Two sensors will operate a relay when a temp difference (adjustable) is detected. All components and pcb. £29 ref L0T93

**MAGNETIC RUBBER TAPE** Selfadhesive 10 metre reel, 8mm wide perfect for all sorts of applications! £15 ref L0T87

**MAINS POWER SAVER UK** made plug in unit, fitted in seconds, can reduce your energy consumption by 15%. Works with fridges, soldering irons, conventional bulbs etc. Max 2A rating. £9 each ref L0T71, pack of 10 £69 ref L0T72

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# The Universal Serial Bus

**The USB is a new standard providing a single interface protocol for PC peripherals. It could take over from RS232 and other standards - if it gets support from hardware manufacturers. Will it be a long wait, or will there be three along in a minute? Asks Robn Abbott.**

**T**he Universal Serial Bus (USB) is a new standard for interfacing peripherals to a computer at transfer rates in excess of 1 megabyte per second. It is intended to be flexible, inexpensive, easy to use, easily expandable and sufficiently defined to allow any kind of peripheral to be connected and remain compatible with other devices connected to the bus. The bus standard has been sponsored by Compaq, Digital Equipment Corporation, IBM, Intel, Microsoft, NEC and Northern Telecom - an impressive list of names whichever way you look at it!

In this article we will look at the USB bus from the viewpoint of its capability and benefits to the user, and we will also look in some detail at its operation, although it is worth noting that much of the functionality of the bus at the lower level protocols - electrical and signalling - will be available in integrated form and therefore removed from the direct concern of anyone actually implementing it.

## The need for USB

Why do we need a new protocol for addressing peripherals? Let us consider a typical medium- to high-end PC. For the sake of example I have chosen to consider my own PC, which is used for software and hardware development as well as word processing, data analysis, accounting, and games. This PC is Pentium-based and has the following peripherals:

- A keyboard and mouse for input
- A gamepad/joystick for games
- A colour inkjet printer.
- A laser printer which is used in place of the ink jet for high speed output.
- A scanner, used for OCR, photocopying and incorporating photographs in documents.
- Speakers used for sound output.
- An internal modem.

- An internal ZIP drive used for backup
- An internal CD-ROM
- External development peripherals, including a PIC programmer, an eprom programmer, and the FED PIC Basic development system.

This PC is shown in figure 1, which includes a view of the internal cards required to support all these devices. Note that the following physical data standards are used in this system:

- An RS232 serial interface (COM1) is used for the mouse.
- An RS232 serial interface (COM2) is used for the development peripherals, which are connected through a simple data switch
- The joystick is connected on a standard PC games port.
- A SCSI interface is used for the scanner.
- A slightly different SCSI interface is used for the ZIP drive. Note that two SCSI cards are required, because the particular scanner in use does not operate correctly on a standard SCSI interface.
- A parallel printer port is used for the inkjet and laser printers. As the PC has only one printer port, and as no further slots are available for additional printer port expansion cards, an external printer switch is required.
- The modem is internal, emulates a standard serial port (COM4) and, because of the way interrupts are used in the PC, cannot be used at the same time as the development peripherals.
- The keyboard is connected to the PC on a standard PC keyboard interface.
- The speakers are connected on a standard analogue output from an internal sound card.
- The CD-ROM is connected on an internal E-IDE interface.

Once the power cables are thrown in, this adds up to a grand total of 18 different cables and 9 different physical interface

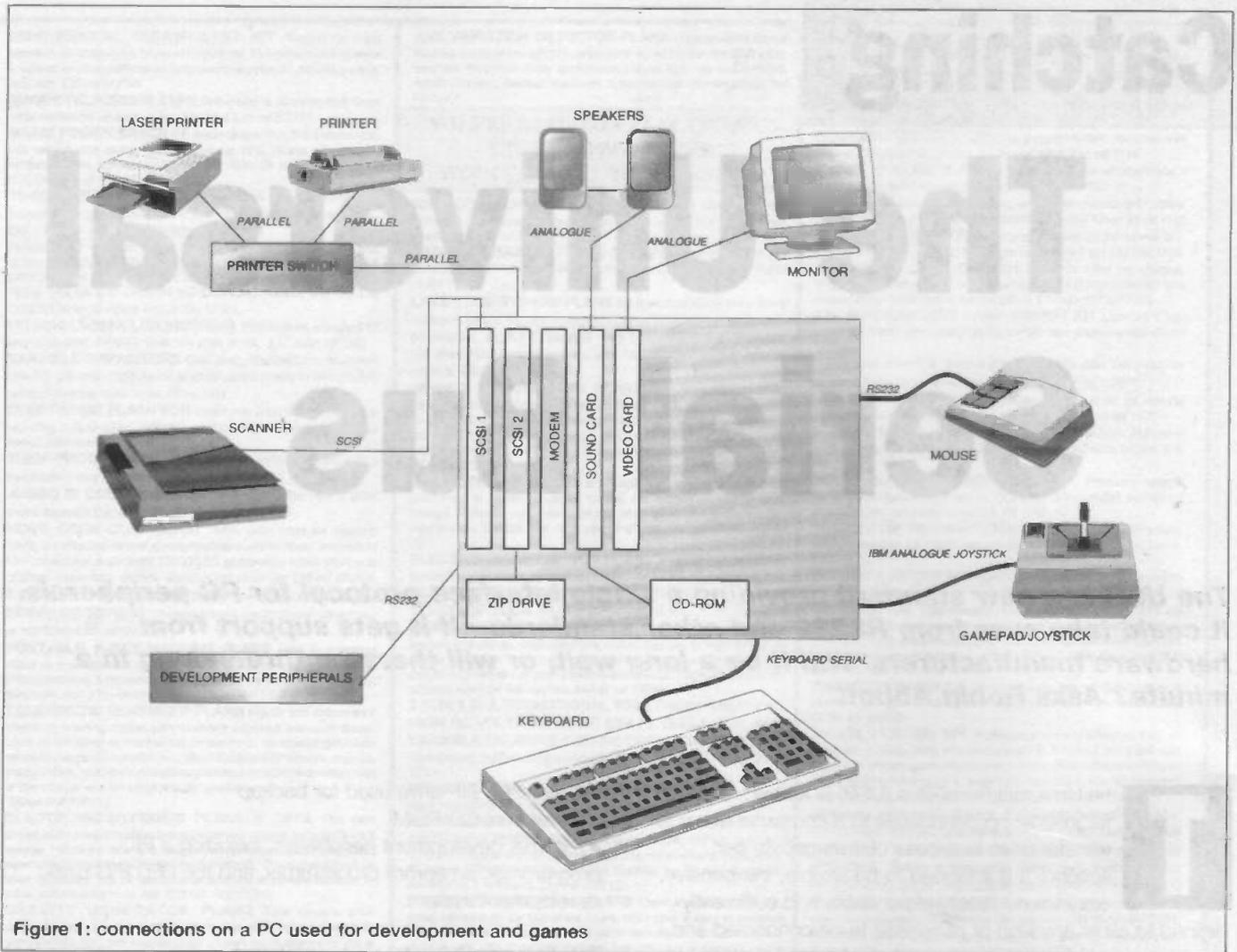


Figure 1: connections on a PC used for development and games

standards required to connect and power all the peripherals - which are a fairly standard set for a busy professional or semi-professional personal computer.

Switching between printers requires not only a selection on the PC, but also manual switching. If this is forgotten, the current print command has to be canceled and submitted again once the printer switch is in the correct position.

There are four different expansion cards required (two SCSI, a modem and a sound card), which occupy all the ISA slots on the motherboard, so that future expansion using this type of PC bus slot is not possible. Setting up these cards to ensure that there was no clash on the I/O port addresses used by the sound card and the second SCSI interface caused considerable problems, as they both clashed with the first SCSI card which was already installed. If you are not yourself a PC owner, ask anyone who is - they will almost certainly have a similar story to tell.

Another issue is that of interrupts. In a typical PC there are 15 interrupts available for peripheral devices, a number of which are reserved for internal operation, and in my PC all the remaining interrupts have been used. Although well-behaved applications and cards can share interrupts, this is not always possible, and in practice I have rarely found it possible to install two peripherals on one interrupt.

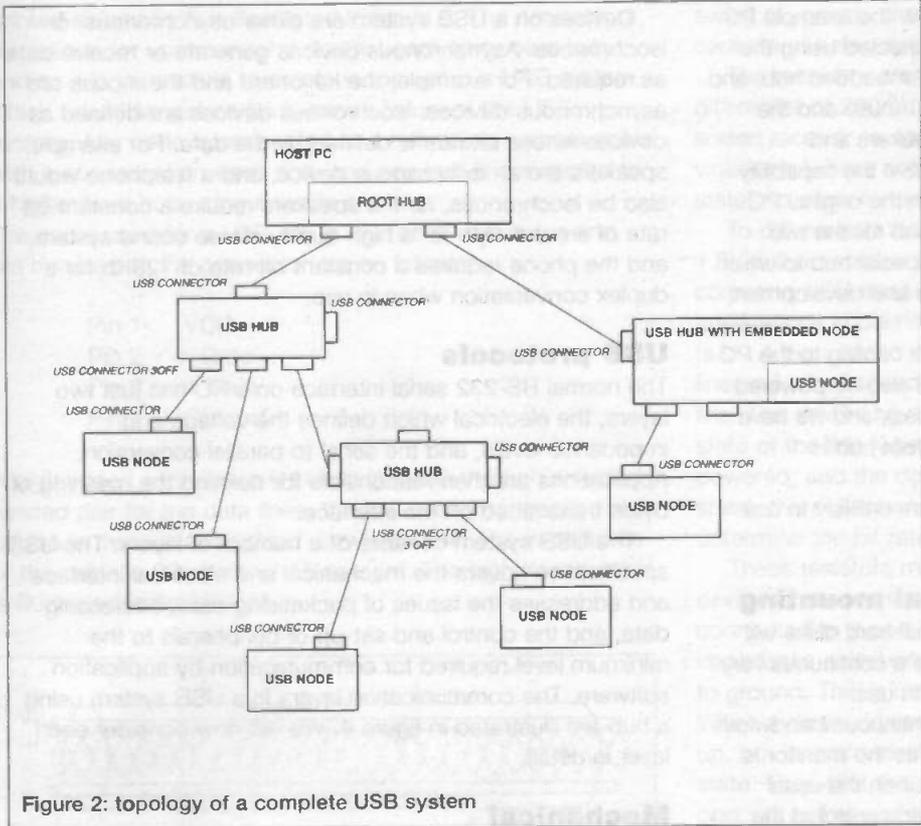
### The Bus

The Universal serial bus (USB) system is intended to overcome some of these problems. The USB is a new interface standard which provides the following features:

- Removal of the limit to the expansion capability of a PC due to a limited number of expansion slots.
- Allows simple expansion of a PC with common low-level software drivers.
- Powering of peripheral devices via the USB cable.
- Up to 127 devices may be served per USB host connection.
- "Hot swapping" - devices may be connected, disconnected, reset, and power cycled while the host PC is in use.
- Operation for simple low bandwidth devices at 1.5Mb/s.
- Operation at up to 12Mb/s for other devices, including audio and compressed video.
- Reduction in cabling.
- Reduction in different type of cables.
- Cabling is simple, physically flexible, low cost 4-core cable.
- Up to 5m distance per cable run.
- The electrical signalling allows the PC to place all peripherals on the USB into a suspended low power state. This is used for "green" energy saving systems.
- Automatic assignment of addresses to devices, together with software interrupt polling requires no user intervention for device set-up.

The universal serial bus is not intended as a LAN, or to provide networking services, but purely as a local connection facility to a single host PC.

The topology of a complete USB system is illustrated in figure 2. The root hub is the host PC. All data transfers are performed between the root hub and devices on the USB



network, and devices on the USB network do not communicate directly with each other. There is only ever one root hub. A USB node (also known as a function) is a device which uses the USB to communicate to the host PC, so our USB nodes are the keyboard, the mouse, the printer, and so on.

The devices shown as hubs are special USB equipments which provide one port towards the host PC, but several downstream ports to which further hubs or USB nodes may be connected. This allows a tree-like connection of USB nodes, and has the advantage that unplugging a device from the hub does not affect any other devices connected to it. It is envisaged that in practice hubs will normally form part of an equipment. For example, a keyboard may contain a hub device as well as a USB node for the keyboard itself. The hub on the keyboard now allows a mouse and gamepad to be directly plugged into it. This is called an embedded hub.

Figure 2: topology of a complete USB system

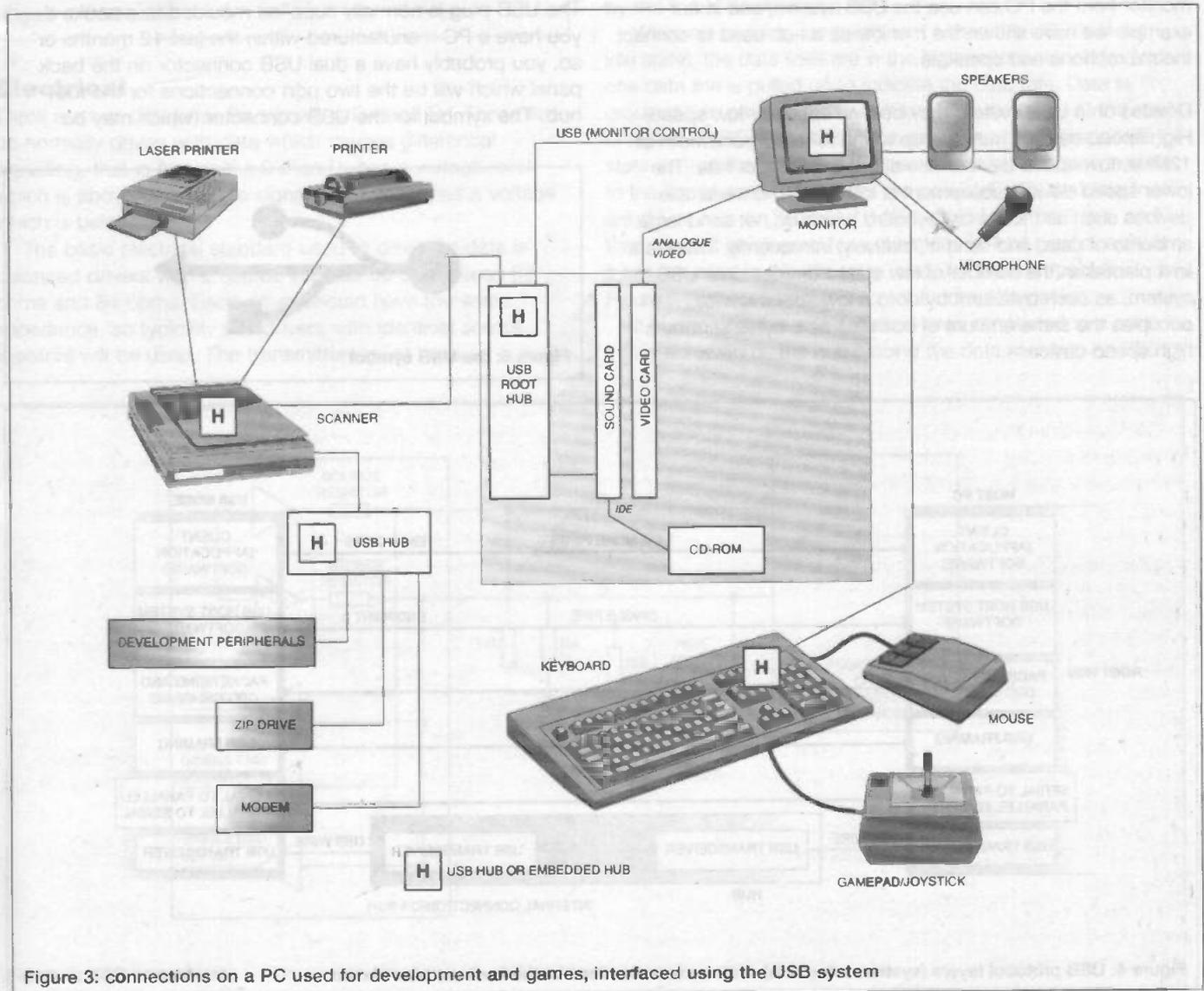


Figure 3: connections on a PC used for development and games, interfaced using the USB system

We can now redraw figure 1 to show how the example PC and all its connections can now be interconnected using the USB system (figure 3). The keyboard is an embedded hub, and the other hub connections are used for the mouse and the game pad. The monitor is a hub for the speakers and microphone (note that in figure 3 we now have the capability for monitor control which was not present in the original PC configuration). The scanner operates as a hub for the two printers, and also operates as a hub for a special hub to which the other peripherals (the modem, ZIP drive and development peripherals) are connected.

Note that we now have considerably less cabling to the PC (as well as this, many of these functions will also be powered from the USB, eliminating some power cables), and we have removed three expansion cards - the USB root hub is implemented on the motherboard.

There are a number of peripherals that are unlikely to use the USB system:

### CD-ROMs Intended for internal mounting

Disk drives - internal disk drives, and external hard disks will not use the USB system because they have a continuous very high bandwidth requirement when they are in use. Monitor video data. This has a very high continuous bandwidth requirement (up to tens of megahertz), and as the monitor is usually located close to the PC system unit then the usual SVGA connector can be employed. However, control of the monitor from the PC can use the USB system, and in our example we have shown the monitor as a hub used to connect the microphone and speakers.

Devices on a USB system may be high speed or low speed. High speed devices may use up to the full USB bandwidth of 12MHz, low speed devices use a 1.5MHz transfer rate. The lower speed device requirement is intended to drive simple devices such as mouse or keyboard which do not send large amounts of data, and send it (relatively) infrequently. There is a limit placed on the number of low speed devices on a USB system, as each byte sent by or to a low speed device occupies the same amount of bus time as 8 bytes sent by a high speed device.

Devices on a USB system are either asynchronous, or isochronous. Asynchronous devices generate or receive data as required. For example, the keyboard and the mouse are asynchronous devices. Isochronous devices are defined as devices whose bit rate is defined by the data. For example, speakers are an isochronous device, and a telephone would also be isochronous, as the speakers require a constant bit rate of around 1Mb for a high quality stereo sound system, and the phone requires a constant bit rate of 128Kb for a duplex conversation when in use.

### USB protocols

The normal RS-232 serial interface on a PC has just two layers, the electrical which defines the voltage and impedance levels, and the serial to parallel conversion. Applications are then responsible for defining the meaning of bytes transmitted on the interface.

The USB system consists of a number of layers. The USB specification covers the mechanical and electrical interface and addresses the issues of packetising data, addressing data, and the control and set-up of peripherals to the minimum level required for communication by application software. The communication layers in a USB system using a hub are illustrated in figure 4. We will now consider each layer in detail.

### Mechanical

The USB plug is normally supplied moulded to a cable. If you have a PC manufactured within the last 12 months or so, you probably have a dual USB connector on the back panel which will be the two port connections for the root hub. The symbol for the USB connector (which may be

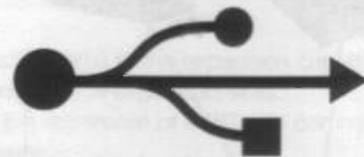


Figure 5: the USB symbol

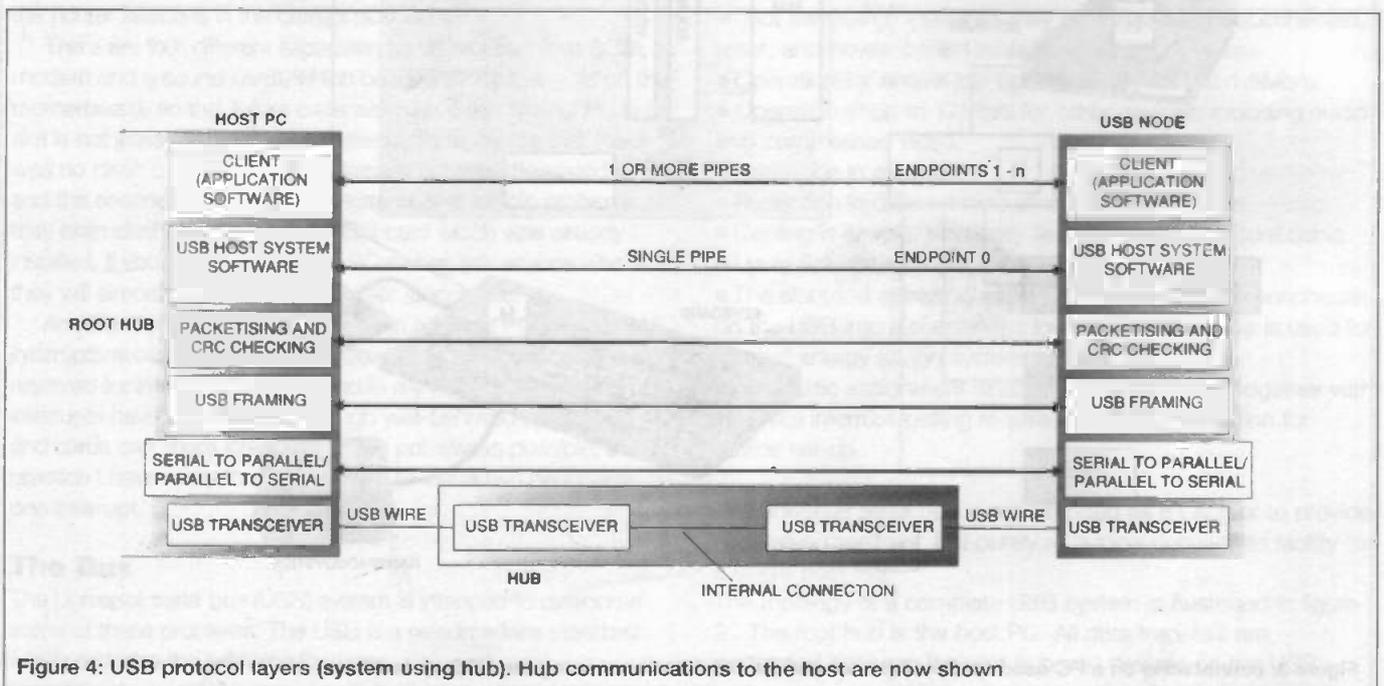


Figure 4: USB protocol layers (system using hub). Hub communications to the host are now shown

printed alongside it) is illustrated in figure 5. The connectors are unique for upstream and downstream ports on a hub, and so it is impossible to create loops within a USB system which would almost certainly cause failure of the USB system. Any node is connected to a hub using a single cable with a connector on each end, and any hub is connected to a higher level hub using a single cable.

The cable used for a USB link is four-core. The cores and pin numbers in the connector are as follows:

- Pin 1 VCC
- Pin 2 - Data
- Pin 3 + Data
- Pin 4 Ground

A high speed connection (12MHz) is made through shielded twisted pair for the data lines, the low speed connection may be made through unshielded untwisted pair. The impedance of the cable is 90 ohms +/-15 percent. The high speed cable is illustrated in figure 6.

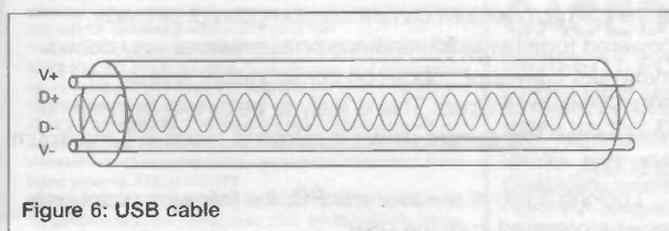


Figure 6: USB cable

## Electrical

There are two data lines for signalling D+ and D-. These are normally driven with data which causes differential signalling, that is to signal a 0 then D- has a voltage level which is above D+, and to signal a 1 then D- has a voltage level which is below D+.

The basic electrical standard used to drive the data is balanced drivers with a source impedance of between 58 ohms and 84 ohms. Each driver should have the same impedance, so typically two drivers with identical source resistors will be used. The transmitters must have an output

swing of greater than 2.8V into a 15k load, and must be capable of being switched off.

The receivers are balanced and must switch with an input differential of 200mV. Each data line must also have a single ended receiver to enable the device to detect differential violations (where both data lines are switched to the same state), to allow for additional switching.

To differentiate between a high and low data rate device 1.5k pull-up resistors are used. See figure 7 which shows complete USB transceivers for high speed and low speed applications implemented in a USB node, or in a port on a hub. A high speed device has a pull-up resistor on the D+ line and a low speed device has a pull-up resistor on the D- line. The drivers have pull-down resistors of 15k. In the idle state of the line (when the USB peripheral device is not powered, and the downstream driver is in a high impedance state), the pull-down and pull-up resistors may be used to determine the bit rate by the hub.

These resistors may also be used to determine when a device is connected to a hub port. With no device connected to a port, its transmission drivers will be in a high impedance state, and the two data lines will both be pulled to ground. This is called a 'single ended 0' state (or SE0). When a device is connected to the port, it will be powered up, but its transmission drivers will be in a high impedance state. Therefore the pull-up resistor on the device's upstream port will pull one of the data lines high. This may be detected by the hub's downstream port.

When no packets are being sent (when the USB is in the idle state), the data lines are in the high impedance state, and one data line is pulled up to indicate the data rate. Data is grouped into packets for transmission on the USB. At the start of a packet (SOP), the data lines are driven into the opposite state so that the first bit of the packet holds the opposite state to the idle state. At the end of the packet (EOP), the data lines are driven to a single ended 0 for a period of 2 bit times, and then the bus is released for a minimum of 1 bit period, so that it has returned to the idle state ready for the next packet. Figure 8 illustrates transmission of a complete packet.

It is also possible to reset all devices connected to a port; this is achieved by the hub placing the data lines into an SE0

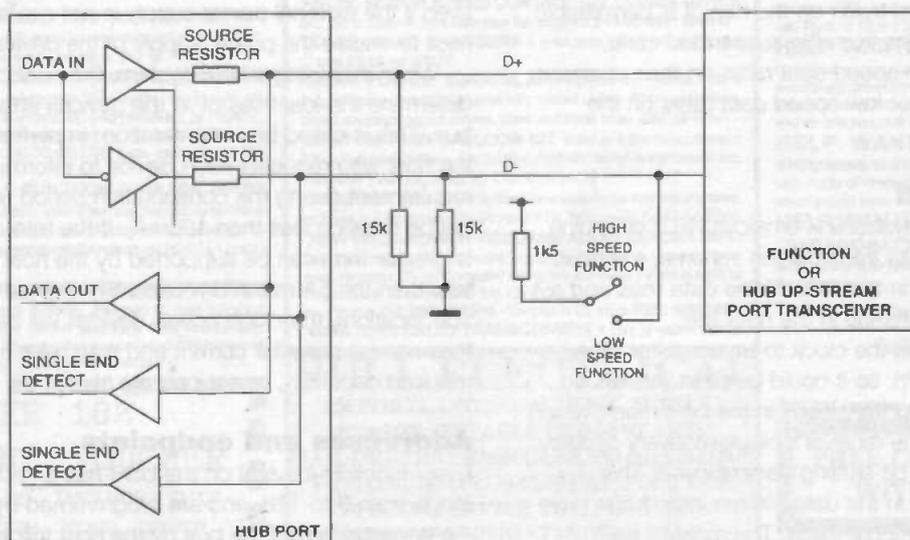


Figure 7: USB transceivers

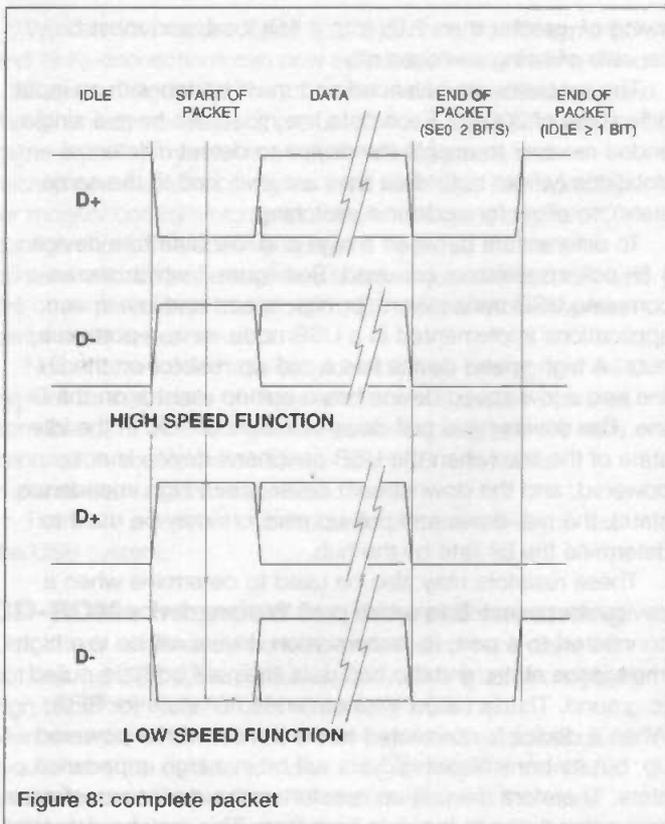


Figure 8: complete packet

state for a period of 10ms. All devices which see this state should then act as if they have just been connected to the bus.

Normally at least one packet will be sent on the bus every millisecond. If the USB is idle for more than 3ms, all devices may enter a suspend state. In the suspend state devices should power down and use less than 500uA current. However they do not have to do this, and may continue to draw power as required for monitoring functions. For example, if the host PC enters a low power state due to lack of keyboard or mouse activity, then it may suspend the bus by placing it into the idle state for more than 3ms. However, the keyboard and mouse must remain fully available to detect user key presses or mouse movements to wake the PC from its low-power state: To do this they may draw the current they require to perform this task.

Once suspended, devices may be woken by a reset, by a resume state, or by normal traffic on the bus. It is permissible to take up to 20ms to wake from the suspended state.

All hubs must use high speed data rates on their upstream port, and may allow high or low speed data rates on the downstream port.

### Data transmission

The data sent over the physical link employs NRZI encoding with bit-stuffing. In this data transmission scheme, a 0 bit is represented by a change in the state of the data lines and a 1 bit is represented by no change in the data line.

The receiver determines the clock to be used from the transitions in the bit stream, so it could be seen that a long series of 1s will result in no transitions in the bit stream, which would be disastrous for the receiver's clock-recovery circuitry. To prevent this problem, "bit stuffing" is employed. This technique inserts a '0' bit in the data stream if six 1 bits have been seen, prior to the NRZI process. The receiver then removes the 0 bit after six 1s. This means that when transmitting a long series of 1s, there is a potential inefficiency

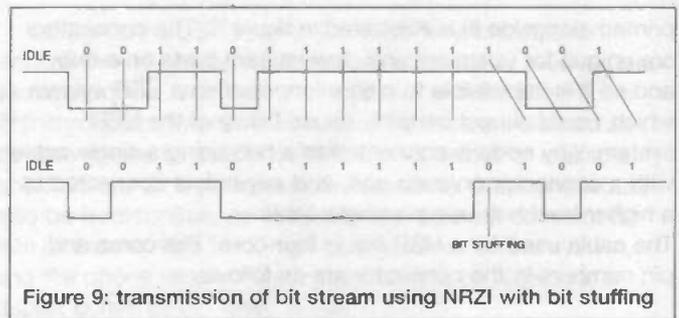


Figure 9: transmission of bit stream using NRZI with bit stuffing

of 14 percent in the data, however, if this is a problem then higher levels of the software are expected to employ data compression to optimise out this inefficiency.

The complete transmission process is shown in figure 9 which illustrates the transmission of a bit stream.

### The USB power distribution system

The USB systems allows distribution of power to attached peripherals. The nominal supply voltage is 5V, but devices must operate with a power supply as low as 4.75V when drawing full current. The maximum current consumption of all loads powered together is 5A, and any one peripheral may draw a maximum current of 100mA on configuration, and up to 500mA maximum load. This is likely to be ample for nearly all peripherals. The current drawn in suspend mode is a maximum of 500uA.

Looking again at our example PC, the following peripherals may be powered from the USB:

- Keyboard.
- Mouse
- Gamepad
- Speakers.
- Modem.
- High capacity floppy drive
- External development peripherals.

Clearly the scanner and printers will require their own power supplies, as power drawn by these devices may be tens of watts. This type of device is known as a self powered device. It is expected that even self powered devices will still power their USB circuitry from the USB power distribution system. This allows the host to determine that these devices are present even if the external power supply is not available, or allows the host to enable the power supply of the device remotely.

When initialising a USB system, the host controller must determine the identities of all the devices attached to the USB bus. This is called bus enumeration. In performing this function, the host also requests each device to inform it of its current requirement during the configuration period (when each device will be drawing less than 100mA). If the total requested current is greater than can be supported by the host (which may be less than the 5A maximum capability defined by the USB specification), then high power devices will be informed that they cannot draw full current and may have to operate with reduced capability, or not operate at all.

### Addresses and endpoints

Every function (device) on the USB has an address. Addresses range from 0 to 128, and are programmed by the host during the configuration of the bus by the host following power up, up to 127 devices may exist on the USB system without the necessity for each to have a different address set up by the

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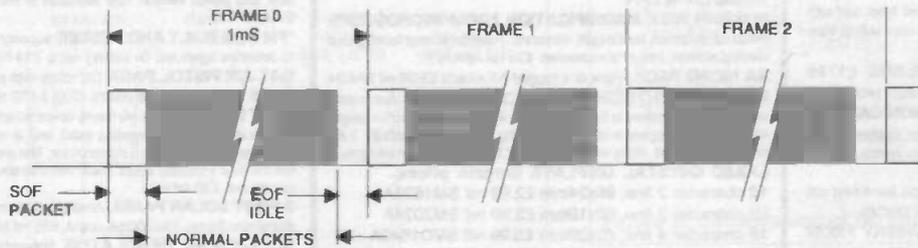


Figure 10: USB 1 millisecond frame structure

user - anyone who has tried to set up expansion cards in a PC and has to worry about setting the address and interrupt number of the card will appreciate how much of a benefit this can be!

A function may have a number of endpoints. An endpoint is an internal address within the function which allows the controlling software to address packets to different internal functions of a single USB function. For example, a combined scanner/printer may have an endpoint for the scanner and an endpoint for the printer within a single USB address to allow the host software to treat the single device as two independent functions. A device may have up to 16 endpoint addresses. Endpoint address 0 is the controlling endpoint which is used by the host software to set up all devices on the USB system, and cannot be used by the application.

### Packetising

Data to be sent on to the USB is divided into packets, the maximum size of which is around 8kbits - at 12MHz this takes around 700us to transmit on the bus. Data is transmitted on

the bus Least Significant Bit (LSB) first. Most data transfers are in bytes, however, some bit fields transmitted in headers are not aligned on a byte boundary.

The USB host divides time into 1ms frames, Every 1ms the host sends a special packet, the start of which (the SOP bit) is accurately timed. At the end of each frame there is a guard time during which no data is sent, and during which time hubs on the USB system electrically disconnect any active transmitters. This ensures that the bus is idle for the SOF. This is illustrated in figure 10. These frame packets are not sent when the bus is suspended.

A packet is delimited by the SOP and EOP markers on the data lines as described above.

A packet always starts with a synchronising pattern which is sent as seven 0s followed by a 1 - this represents the byte 80 hex. This is used to enable the receiver's clock synchronisation circuitry to recover the clock. Following the synchronisation pattern the first bit of the packet is sent on the bus.

The format of the various USB packets is shown in figure 11.

	FRAME SYNC	PID - IN	ADDRESS	ENDP	CRC	
IN PACKET	00000001	10010110	7 BIT	4 BIT	5 BIT	32 BITS
OUT PACKET	00000001	10000111	7 BIT	4 BIT	5 BIT	32 BITS
SETUP PACKET	00000001	10110100	7 BIT	4 BIT	5 BIT	32 BITS
SOF PACKET	00000001	10100101	FRAME NUMBER	CRC		32 BITS
			11 BIT	5 BIT		
DATA0 PACKET	00000001	11000011	DATA	CRC		32 - 8216 BITS
			0 - 1023 BITS	16 BIT		
DATA1 PACKET	00000001	11010010	DATA	CRC		32 - 8216 BITS
			0 - 1023 BITS	16 BIT		
HANDSHAKE PACKET - ACK	00000001	01001011				16 BITS
HANDSHAKE PACKET - NAK	00000001	01011010				16 BITS
HANDSHAKE PACKET - STALL	00000001	01111000				16 BITS

ADDRESS IS THE 7 BIT ADDRESS OF THE USB NODE. THE ENDPOINT ADDRESS IS THE ADDRESS OF THE ENDPOINT WITHIN THE NODE. FOR OUT AND SETUP THESE ADDRESSES ARE THE ENDPOINT WHICH SHOULD RECEIVE DATA. FOR IN PACKETS THESE ARE THE ENDPOINT ADDRESS WHICH SHOULD TRANSMIT THE PACKET

THE FRAME NUMBER CYCLES FROM 0 TO 7FF Hex AND INCREMENTS ON EACH FRAME

NOTE:  
ALL FIELDS ARE TRANSMITTED LSB FIRST  
BIT FIELDS ARE SHOWN LSB FIRST AS THEY WOULD BE TRANSMITTED

THERE IS AN ADDITIONAL PREAMBLE PACKET TYPE USED FOR LOW SPEED DEVICES

Figure 11: packet formats

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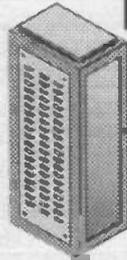
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OUT	First packet of a host to function transaction. Packet includes address of function and the address of the endpoint.
IN	First packet of a function to host transaction. Packet includes address of the function and the address of the endpoint which is generating data.
SOF	Start of frame, followed by the 11 bit frame number. This is the packet sent every 1ms.
SETUP	This is used in setting up the function by the host bus enumeration software.
DATA0	Data packet. Followed by up to 1023 bytes of data in 8 bit form.
DATA1	Data packet. (See below for details of how DATA0 and DATA1 packets are used). Followed by up to 1023 bytes of data in 8 bit form.
ACK	Acknowledgement by receiver that the packet was received without errors.
NAK	Receiver cannot accept data.
STALL	Indicates that an endpoint is stalled, for example this may be sent to indicate that a device has been unable to process all the data sent to it as yet.
PRE	Preamble - used for communication to low speed devices.

Note that the fields of the packet do not line up on 8-bit boundaries; for example, the 7 bits of the address in an OUT packet are followed immediately by the 4 bits of the endpoint address. These packets are also known in the specification as tokens.

The first four bits sent are the PID. This stands for packet identity, and is a 4-bit code (the upper four bits of the PID byte are the same 4-bit code, but inverted providing a simple check on the code). The types of PID available are shown in the table above.

Addresses are seven bits in length, endpoint addresses are four bits in length.

CRC checking is applied to the longer of the packets. The CRC check is either five bits long or 16 bits long dependant on the packet type. The CRC check is intended to ensure that the packet has been received correctly without errors.

### Sending and receiving data

Data transfers always occur between the host and an endpoint on a function with a specific address. This unique address to which data is transferred is called a pipe. A pipe may be a stream pipe or a message pipe. A message pipe contains data in a defined structure, a stream pipe contains unstructured data such as sound information for speakers or from a microphone.

Data transfers are always based on a multi way packet transfer and are always initiated by the host - it is not possible for any function on a USB system to send data autonomously. The direction of transfer is indicated by the host sending either an IN or an OUT packet. The IN packet is a request to the function from the host to send the host some data in one or more packets, the OUT packet is a request to the function to send the host some data in one or more packets. Following the IN or OUT packet the function or the host respectively send a DATA packet, following each data packet the receiving end of the USB transaction sends an ACK (or NAK or STALL if there is a problem), and then the next packet may be sent. The initial packet sent is a DATA0 packet, the next packet is a DATA1 packet and then back to DATA0, the packet types toggle between DATA0 and DATA1 throughout the data transfer.

When the function is receiving data it may return an ACK packet if the data was received without error, a NAK packet if the function was temporarily unable to receive data, and a STALL packet if there is a problem with the function which requires resetting from the host.

An example of an occasion when a STALL packet may be sent would be if a watchdog circuit timed out. A watchdog circuit in a microprocessor based system is usually based on a resettable monostable, the software must keep resetting the monostable, if it fails to do so due to a software failure or unexpected external event then the monostable times out and produces a hardware reset or non-maskable interrupt. If a watchdog time-out occurred in the function the function may send STALL packets in response to any query by the host, and may then be reset and initialised by the host, and can continue operation.

An interrupt transaction occurs to a special type of endpoint called an interrupt endpoint. Interrupt endpoints are normally polled by the host on a regular basis. If the endpoint has data then it sends it using the interrupt endpoint when it is polled. It is usual that there is only a small amount of interrupt data, if larger amounts are to be sent the interrupt packet may identify the need for the large data transfer which can be transferred at the request of the host at a later time.

An example of interrupt data may be a keyboard which returns a keycode when a new key is pressed. Thus the endpoints on a keyboard may be:

Endpoint 0, which is always used for USB management control.  
Endpoint 1 which may be used to control the state of the keyboard - for example in setting LED's on the keyboard.  
Endpoint 2 an interrupt endpoint used to send the keycode of a key which is pressed.

Transfer to an isochronous endpoint is achieved without the handshake packet, the host issues an IN or OUT packet and then the DATA packet is sent. Only DATA0 packets are used. As an illustration of bus bandwidth required by an isochronous device, consider a stereo microphone which employs audio compression to achieve a bandwidth requirement of 50Kbits/s per channel. This will require a packet size of 12.5 bytes per millisecond. By the time that the size of the IN packet, the header and CRC on the DATA packet, and the SOP and EOP states, are taken into account this device will use about 170bits during the frame, or around 1.5 percent of the USB bandwidth at 12MHz.

### Connecting and disconnecting USB functions

When a USB function is attached to a hub the hub detects its presence by the change in electrical levels (as described above in the section on the electrical interface of the bus).



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The hub notifies the host of the attachment (recall that the host PC has a hub itself - the root hub). The host then enables the port on the hub and addresses the peripheral on that port using the USB default address, the host then assigns an address to the peripheral and establishes control pipe 0. From this point onwards the peripheral can be set up from the host and then used by application software. It is likely that in most implementations of the host, then the host will set up all USB devices during the boot up sequence of the host, applications may then run at a later time and will have the USB peripherals which they address immediately available.

Connection of a hub requires the hub to be initialised, and then each port of the hub to be initialised in order.

Disconnection of a function or hub is also recognised by the change in electrical levels. The hub notifies the host which removes all records referring to the hub or function, and notifies any application software of the removal.

### Implementing the USB interface

Those readers who have studied this article in detail will probably have realised that providing a USB interface on a peripheral is considerably more complex than interfacing to earlier standards, such as RS232 or the parallel port. On the other hand, the problem of interfacing to USB is likely to become crucial if it becomes widely accepted, and in this case it is likely that microcontrollers will increasingly become available with USB ports, and with USB application software provided as standard.

The first decision to be made is whether the peripheral will contain an embedded hub, or whether it will simply consist of a USB function. It is likely that most non-commercial users will implement peripherals without embedded hubs, and rely on the commercial manufacturers to produce either dedicated hubs, or peripherals with embedded hubs and multiple downstream ports.

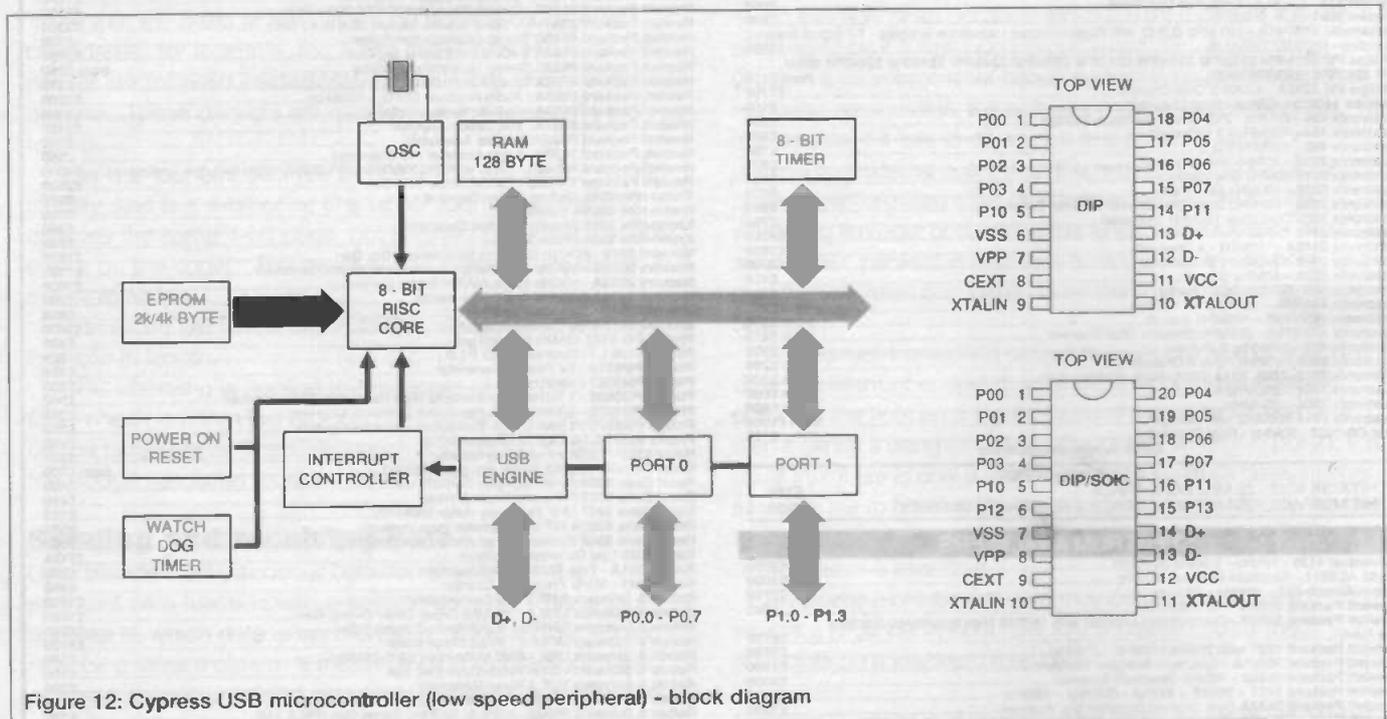


Figure 12: Cypress USB microcontroller (low speed peripheral) - block diagram

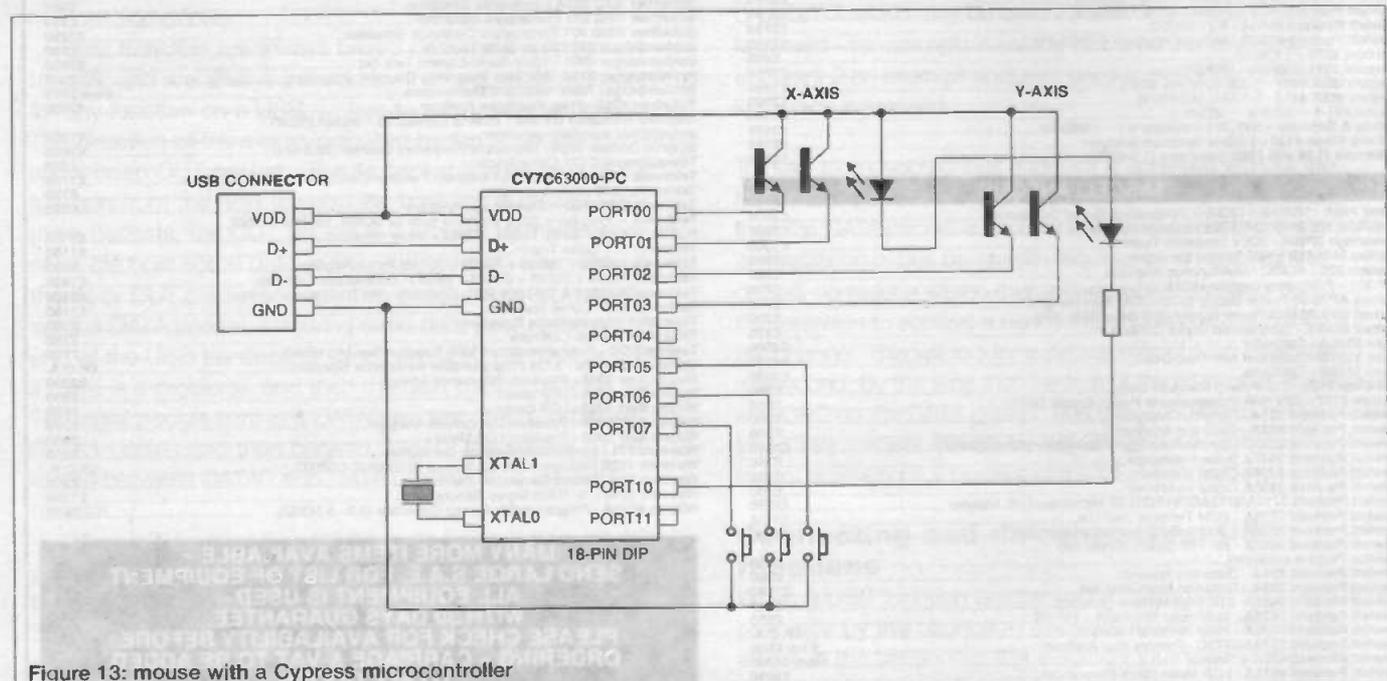


Figure 13: mouse with a Cypress microcontroller

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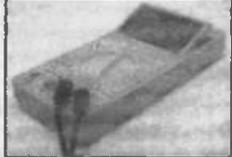
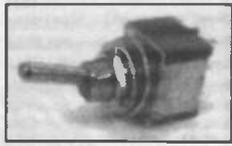
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Given the relative complexity of the USB it is hardly surprising that the only support chips currently available or planned are based on microcontrollers, we will take a look at two series of planned microcontrollers from Intel and from Cypress. It is likely that mask programmed microcontrollers like these will become available for specific applications such as keyboard and mouse functions, these chips are likely to be available at very low cost.

### Implementing a peripheral device

The CY7C63x00 series are USB microcontrollers designed for peripherals operating using low speed USB. The series is intended to be fully available by the end of 1997 with full production now. The block diagram of this device is shown in figure 12. The smaller devices offer 10 in/out pins, and the larger devices offer 16 in/out pins. The USB transceiver is on chip, and the chips offer 128 byte of ram and 2K or 4K of eeprom. As an example of the use of one of these microcontrollers, figure 13 shows a mouse implemented using one of the Cypress ics using Cypress's promotional literature. As can be seen in this circuit, there is very little to a USB device implemented using a controller such as this.

Anyone used to using the PIC series of microcontrollers would have little problem using these devices. There is an on chip timer generating 128us and 1.024ms interrupts. The device supports USB suspend mode directly, which can be woken up by USB interrupt or external interrupts. Interrupt sources are the timer, external action, or USB endpoint actions. The device has two endpoints, endpoint 0 is capable of receive and transmit, endpoint 1 is capable of transmit only, data is loaded into the endpoint FIFO register, and will be transmitted automatically by the microcontroller on receipt of an IN packet. The microcontroller will generate an interrupt on receipt of a packet to endpoint 0. The microcontroller has a risc core, and supports Immediate, direct, and indexed addressing modes.

### Implementing a Hub

The 8x930Hx microcontroller from Intel is a complete hub peripheral controller which offers one upstream port, an internal hub, and three external downstream ports. All the ports have internal USB transceivers. The internal embedded controller has three endpoint buffers of 16 bytes, and a configurable 1Kbyte buffer, this will allow endpoint 0 and control functions on the 16 byte buffers with large scale data transfer on the 1K buffer. The device has 1Kbyte of internal ram, and up to 16Kbytes of rom, although it is possible to use external eeprom/ram up to a total of 256Kbytes. This particular device offers operation at up to 12Mbps. The controller is available in a 64 pin package - the pin out is shown in figure 14, and this is clearly a non-trivial device!

This device is code compatible with the 8051 series of microcontrollers.

### The future of USB

The USB is a new interface standard. To become accepted and widely used a standard must be supported by suppliers of equipment which may use it. One only has to look at the Betamax video tape standard, or the IBM Micro-Channel architecture for bus interfacing, to see that good ideas can fail to conquer the market if they have insufficient third party support.

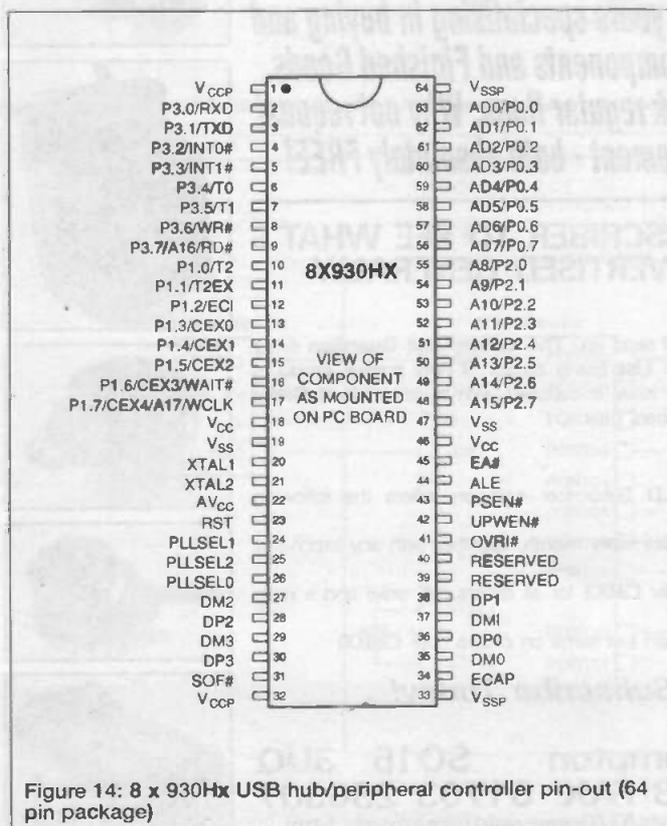
It seems unlikely that USB will be supported until it is widely available on host PCs. This is because suppliers of peripherals will want a market which is not limited to people who have purchased new PCs in the last year or so. This could be achieved if add-on USB host cards become widely available for those that do not have USB on the motherboard, but a quick scan of the computer magazines showed a distinct dearth of such cards available at time of writing. A number of PCs are advertised with dual USB ports, and I found that many new PCs which are not advertised with USB ports are in fact supplied with them. However, when I phoned three PC suppliers, only one could actually tell me what USB stood for and for what it is used!

The first peripherals likely to use USB are the higher bandwidth complex devices such as printers and scanners where the cost of the USB interface is not significant in comparison to the overall cost of the device. For simpler devices such as keyboards and mice, it seems unlikely that USB will be cost competitive with the cheapest devices which are currently available, and it will take some time for USB chipsets for these applications to be widely available and cost competitive. I spent some time scanning advertisements in several PC magazines, but I was unable to find a single peripheral which used the USB.

In conclusion, the attractions of USB are such that if the system starts to become widely available on peripherals as well as on PCs, then it will snowball, becoming the de facto interfacing standard. In the longer term as the application ics become more widely available I hope to be able to bring the readers of ETI an example interface using the USB.

### The USB standard

The current issue of the USB standard is available on the internet from the USB implementors forum at: <http://teleport.com/~USB>, or on email at [USB@FES.FM.INTEL.COM](mailto:USB@FES.FM.INTEL.COM). The author is prepared to supply copies of the USB specification in Microsoft Word 6.0 (and above) format. Please send an SAE and £3.00 to Robin Abbott, 60 Walkford Way, Christchurch, Dorset, BH23 5QG mail order only.



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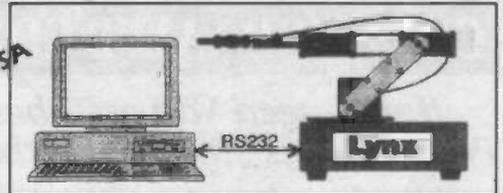
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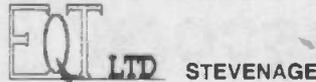
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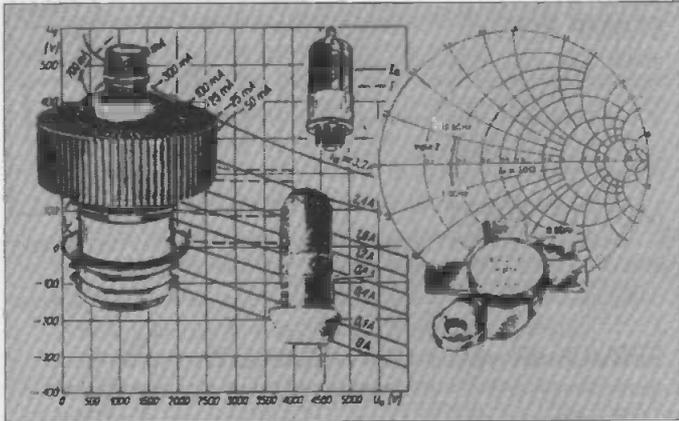
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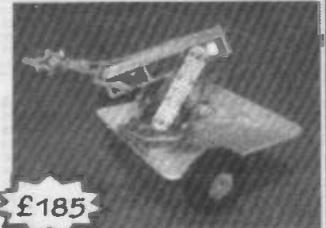
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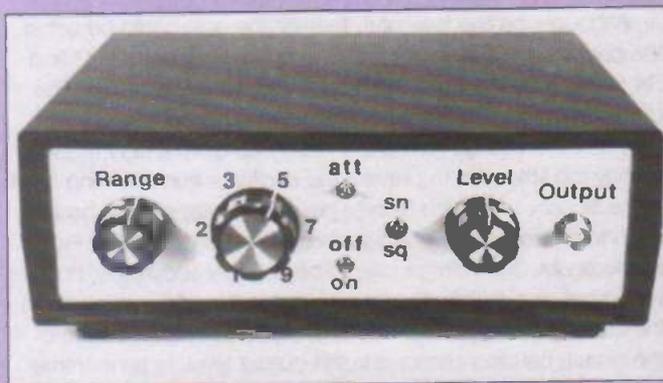
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# AF generator Switch tuned

**Robert Penfold's design for an audio frequency signal generator has switch controlled tuning rather than the continuous type. Covering a range of 10 hertz to 100 kilohertz, no calibration is needed once the unit is finished.**

**A** conventional audio frequency signal generator typically covers a range of 20 Hertz to 200 kilohertz in four ranges, with continuously variable tuning over each range. A unit of this type represents a fairly straightforward project for the home conductor to build, but it can be difficult to get the finished generator accurately calibrated. Matters are made more difficult by the fact that the scaling is decidedly non-linear unless a difficult to obtain anti-log potentiometer is used for the tuning control. Even then, accurate linear scaling is not guaranteed, and producing an accurate scale can be difficult. In fact it is not possible at all, unless you have access to either a frequency meter or another signal generator that is accurately calibrated.

This audio generator covers a range of 10 Hertz to 100 kilohertz, but it has switch controlled tuning rather than the continuous variety. On the lowest of its four ranges it provides output frequencies of 10, 15, 20, 25, 30, 40, 50, 60, 70, 80, 90, and 100 Hertz. The other three ranges provide output frequencies at 10, 100, and 1000 times these frequencies. The advantage of using switch controlled tuning is that there is no calibration to contend with once the unit has been completed. You just switch on and dial up the required frequencies using the two frequency control switches. Close tolerance components in the critical parts of the circuit ensure that the accuracy of the output frequencies is reasonably good. The actual error will vary somewhat from one frequency to the next, but in general the output frequency will be within five percent of the required figure, and in many cases the error will be under one percent. This is



more than adequate for most purposes, including most frequency response testing.

Obviously it is not possible to set any desired frequency using this method of frequency control, but in practice you do not often need to set odd output frequencies such as 876 Hertz. However, it is only fair to point out that this generator is not suitable for testing filters that have either a very narrow passband or provide a very narrow "notch." It is well suited to most general audio testing, such as checking the frequency responses of tone controls and RIAA preamplifiers, voltage gain measurement, and so on.

Good quality sine and squarewave output signals are provided at a maximum output voltage of about three volts peak-to-peak. The squarewave signal has rise and fall times of well under one microsecond, and is free from overshoot and ringing effects. An attenuator switch can be set to reduce the output level by about 40dB, and a continuously variable attenuator is also provided. Power is obtained from an internal nine volt battery and the unit is therefore fully portable.

## Sinewave Generation

Audio signal generators are almost invariably based on a Wien oscillator which produces the sinewave signal. The squarewave signal is then produced from the sinewave signal using a clipping amplifier. The basic circuit for a Wien oscillator is shown in figure 1. The Wien network consists of R1, R2, C1, and C2, and it is connected so that it provides positive feedback over the operational amplifier.

At a certain frequency the Wien network will produce a phase shift of zero degrees, and the circuit will oscillate at this frequency provided the voltage gain of the amplifier fractionally more than compensates for the losses through the Wien network. Conventionally,  $C1=C2$  and  $R1=R2$ , but the circuit

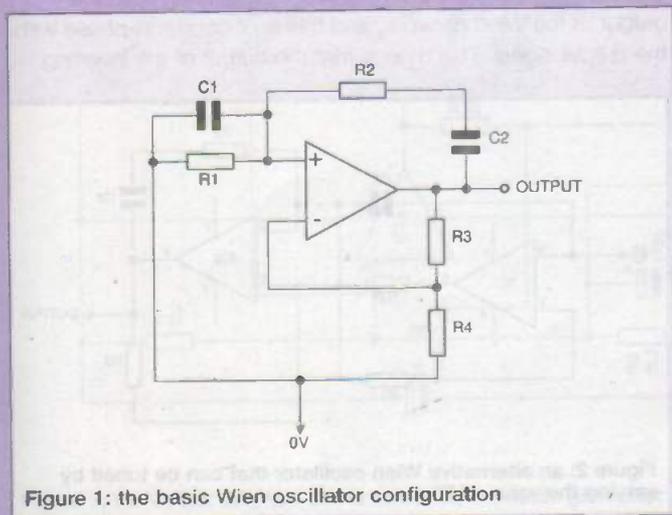
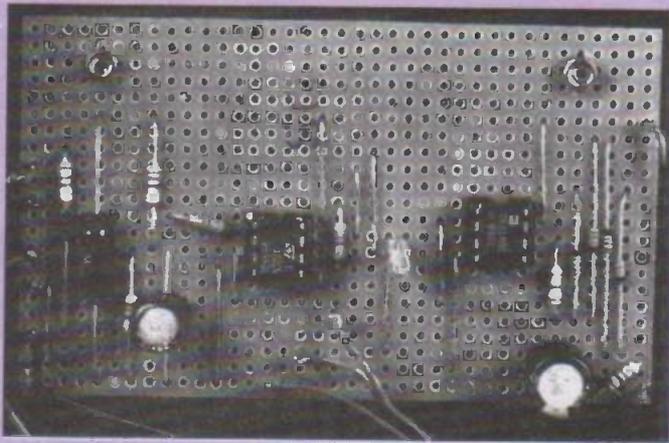


Figure 1: the basic Wien oscillator configuration



will operate if the two resistors and the two capacitors are not of equal value. Assuming that they are, a voltage gain of approximately three must be provided by the amplifier in order to produce oscillation.

Although, on the face of it, the voltage gain could be set at the correct level by giving negative feedback resistors R3 and R4 suitable values, in practice this will not work properly. The circuit will provide a high quality sinewave output signal provided the voltage gain is set very accurately. It only requires slightly too little gain to prevent the oscillator from working at all, or fractionally too much gain to produce a clipped and badly distorted output signal. Using a preset resistor for R3 or R4 would enable the voltage gain to be set very accurately, but this would not give satisfactory results in practice. Any variations in the loading on the output would tend to upset the balance of the circuit, causing changes in the output level. In an extreme case the oscillator could stall completely. Where a variable output frequency is required it is normally obtained by using a dual gang potentiometer for R1 and R2. The matching of the two gangs is never perfect, and this means that the precise voltage gain needed to sustain oscillation will vary somewhat from one setting of the tuning control to another.

The only practical way of stabilising the output level is to use some form of automatic gain control (agc) circuit. There is more than one way of achieving this, but by far the most common is to use one of the self-heating thermistors that are designed specifically for this application. Unlike an ordinary thermistor, a self-heating type is contained in an evacuated glass envelope that largely isolates it from the ambient temperature. Passing a small current through one of these components results in it rising in temperature, which in turn causes its resistance to fall. In a Wien oscillator the thermistor is connected in place of R3.

Initially the resistance of the thermistor is quite high, which gives high voltage gain through the amplifier and strong oscillation. This strong oscillation produces a heavy flow of current through the thermistor, which results in the thermistor heating up. The thermistor's resistance therefore drops and produces weaker oscillation. In fact it normally results in oscillation ceasing, and the thermistor starting to cool off again. Its resistance then rises again and quite strong oscillation occurs once more. This process continues a few times with the circuit gradually settling down and oscillating gently.

As with any form of automatic gain control, this system relies on a form of negative feedback action. If the circuit oscillates more strongly for some reason, the amount of current through the thermistor increases and the gain of the circuit is reduced. Conversely, if the level of oscillation reduces, the current flow through the thermistor reduces and the gain of the circuit is increased. Any change in output level is therefore counteracted

by a suitable change in the voltage gain of the amplifier. Obviously some variations in the output level will occur, since the thermistor will not quite fully compensate for any change in the amplitude of the output signal. However, under normal loading the circuit will keep the output level within a fraction of a decibel of its unloaded level. In the case of a variable frequency Wien oscillator the stabilisation also maintains a virtually constant output amplitude over the full audio range.

### Single resistor tuning

Life would be easier if the frequency of a Wien oscillator could be varied via just one of the resistors in the Wien network. A switch tuned audio generator would then only need one set of precision resistors. Also, an ordinary single pole switch would suffice, which would give a considerable cost saving over a multi-way two pole type.

It is actually possible to tune a Wien oscillator via one of the resistors in the Wien network, but it produces practical difficulties. The gain needed to just sustain oscillation varies greatly if the value of one resistor is altered. Although one might expect the automatic gain control to compensate for these variations in gain, in reality the variations are so great that normal thermistor stabilisation can not cope adequately. In fact it is difficult to produce an output stabilisation circuit that can cope adequately with both variations in loading and changing losses through the Wien network.

One way around this problem is to use the alternative Wien oscillator configuration of figure 2 (the "Brokaw" configuration). In this circuit the positive feedback is once again provided by a Wien network which consists of R1, R2, C1, and C2. This time there is a slight change in that R1 is returned to the virtual earth formed at the non-inverting input of IC1, rather than to the zero volt earth rail. Whether it is returned to the true earth or a virtual earth makes no difference to the operation of the Wien oscillator, and the circuit therefore oscillates normally. The negative feedback circuit of this design is much more complex than the simple two resistor network of the basic Wien oscillator. R4 and R5 form what at first appears to be an ordinary negative feedback network, but R4 is returned to the output of IC1 and not to the earth rail. IC1 operates as a non-inverting amplifier, and its voltage gain is controlled by the ratio of R3 to R1. The input signal for this amplifier comes from the output of the Wien network, and as we have already seen, R1 is actually part of this network.

When considering the operation of this circuit you need to bear in mind that any increase in the value of R1 will reduce the output frequency, but it will also produce stronger oscillation. The input signal to the inverting amplifier is taken from the output of the Wien network, and this is of course in-phase with the output signal. This means that the output of the inverting

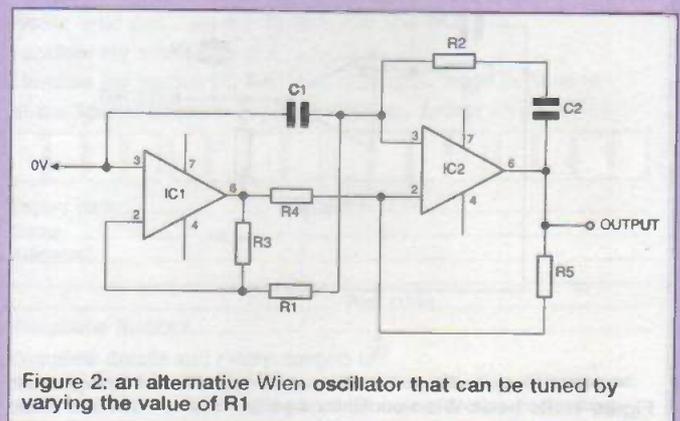


Figure 2: an alternative Wien oscillator that can be tuned by varying the value of R1

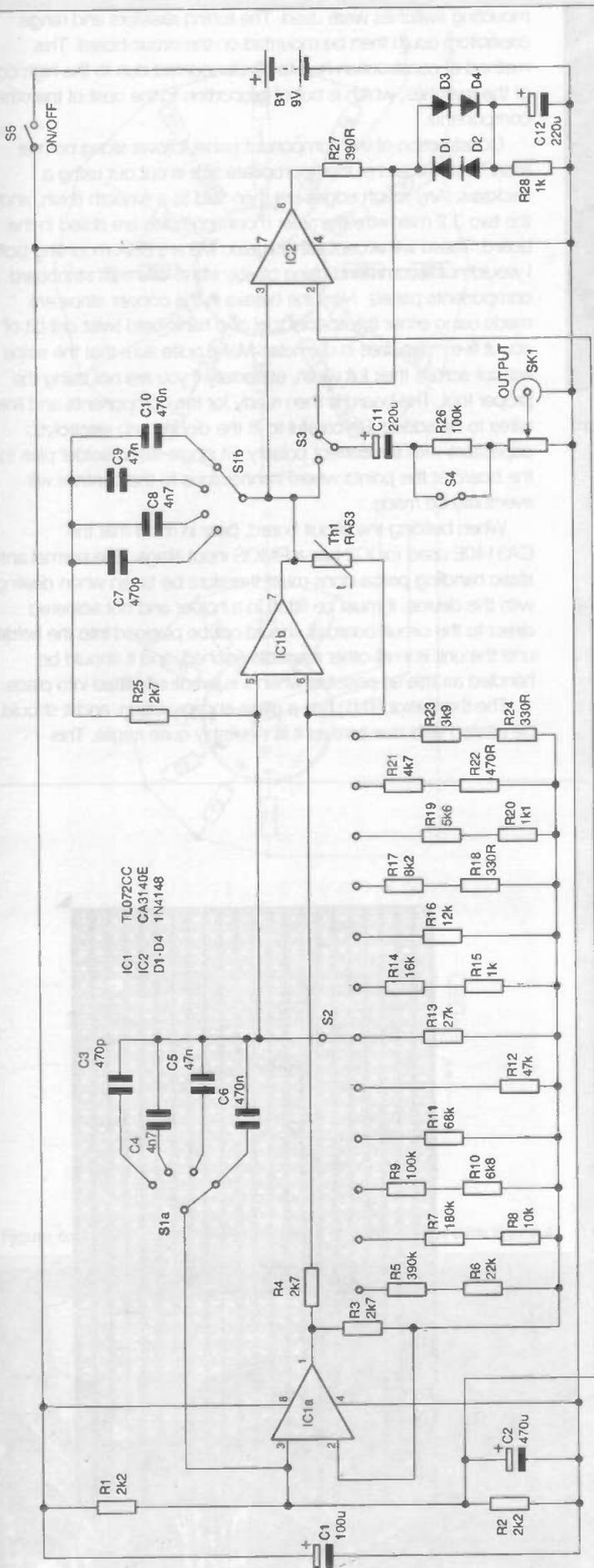


Figure 3: the circuit diagram of the switch tuned audio generator

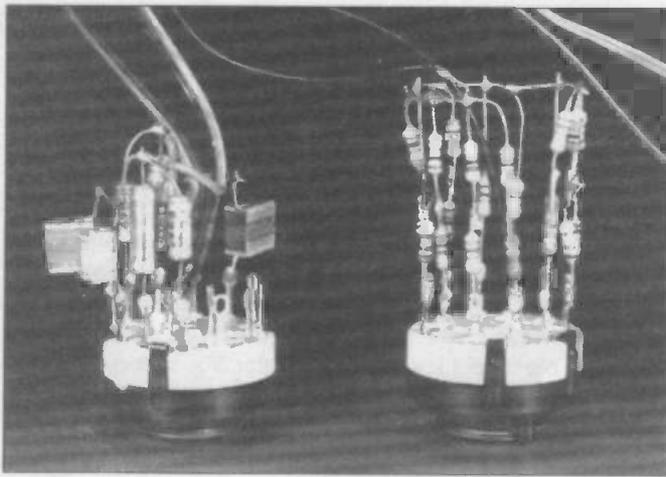
amplifier is out-of-phase with the output signal. IC2 operates as an inverting amplifier to this signal, with R4 and R5 acting as the negative feedback network. This further inversion means that the output signal from IC1 boosts the output signal from IC2. The stronger the output from IC1, the greater the boost.

Increasing the value of R1 produces stronger oscillation, but it also produces reduced voltage gain from IC1 and a weaker output signal from this amplifier. This produces a reduction in the boost to the output level of the oscillator. The stronger oscillation is therefore counteracted by the reduced boost to the output level, and wide variations in the value of R1 produce no significant change in the output level. The circuit still requires automatic gain control to compensate for variations in output loading, etc. This can be provided in the normal way by using a thermistor in place of R5. It is possible to cover a wide frequency span by altering the value of R1, but the scaling is extremely non-linear. This is not really of any consequence in the present application where R1 is a series of fixed resistors rather than a potentiometer.

### Circuit operation

The circuit diagram for the switch tuned audio generator appears in figure 3. IC1 is used in the Wien oscillator, and with the only exception that the circuit is powered from a single supply rail, it uses exactly the same configuration as the circuit of figure 2. R1, R2, and C2 produce a centre-tap on the supply rails that is used for biasing purposes. R5 to R24 are the range resistors which give the circuit its basic repertoire of 12 output frequencies. It is inevitable that some rather odd values are required, and most of the tuning resistances are made up from two resistors connected in series. There are four sets of capacitors in the Wien network (C3 to C10) and these provide the circuit with its four frequency ranges. S1 is the frequency control and S2 is the range switch. Th1 is the self-heating thermistor which stabilises the output level.

Sinewave to squarewave conversion is provided by IC2 which is an operational amplifier used open-loop and in the non-inverting mode. The high voltage gain of IC1 results in a heavily clipped output signal that is a squarewave of reasonable quality. However, the wave shape is improved by the diode clipping circuit based on D1 to D4, and this circuit also reduces the peak-to-peak output level of the squarewave generator to one that is comparable to the output level of the sinewave oscillator. The required output signal is selected via S3, and C11 couples the output from S3 to the attenuator circuit. VR1 is the variable attenuator, and S4 is the attenuator switch. VR1 receives the full output level when S4 is closed, but R26 introduces losses of about 40dB (that is, it reduces the output level by a factor of 100) when S4 is open. It is easier to set very low output levels when S4 is set to attenuate the output signal. Due to the 20 percent tolerance rating of VR1, the attenuation provided by R26 may not be precisely 40dB.



Power is obtained from a nine volt battery (B1), and the current consumption of the circuit is about 10 milliamps. A PP3 size battery is just about adequate to power the circuit, but a higher capacity battery would be a better choice if the unit is likely to be used for long periods.

### Construction

Details of the stripboard component panel are provided in figure 4 (component layout) and figure 5 (copper side view). The board measures 37 holes by 22 copper strips. Construction of the board is very straight forward since the majority of the components are hard wired to the switches. Construction would be slightly easier and the finished unit would be somewhat neater if printed circuit

mounting switches were used. The tuning resistors and range capacitors could then be mounted on the circuit board. This method of construction had to be discounted due to the high cost of the switches, which is out of proportion to the cost of the other components.

Construction of the component panel follows along normal lines. First a board of the appropriate size is cut out using a hacksaw. Any rough edges are then filed to a smooth finish, and the two 3.2 millimetre diameter mounting holes are drilled in the board. These will accept either metric M3 or 6BA mounting bolts. I would not recommend using plastic stand-offs with stripboard components panels. Next the breaks in the copper strips are made using either the special tool or a hand-held twist drill bit of about five millimetres in diameter. Make quite sure that the strips are cut across their full width, especially if you are not using the proper tool. The board is then ready for the components and link-wires to be added. Be careful to fit the diodes and electrolytic capacitors with the correct polarity. Fit single-sided solder pins to the board at the points where connections to the controls will eventually be made.

When building the circuit board, bear in mind that the CA3140E used for IC2 has a PMOS input stage. The normal anti-static handling precautions must therefore be taken when dealing with this device. It must be fitted in a holder and not soldered direct to the circuit board. It should not be plugged into the holder until the unit is in all other respects finished, and it should be handled as little as possible when it is eventually fitted into place.

The thermistor (TH1) has a glass encapsulation, and it should be treated with due care as it is inevitably quite fragile. This

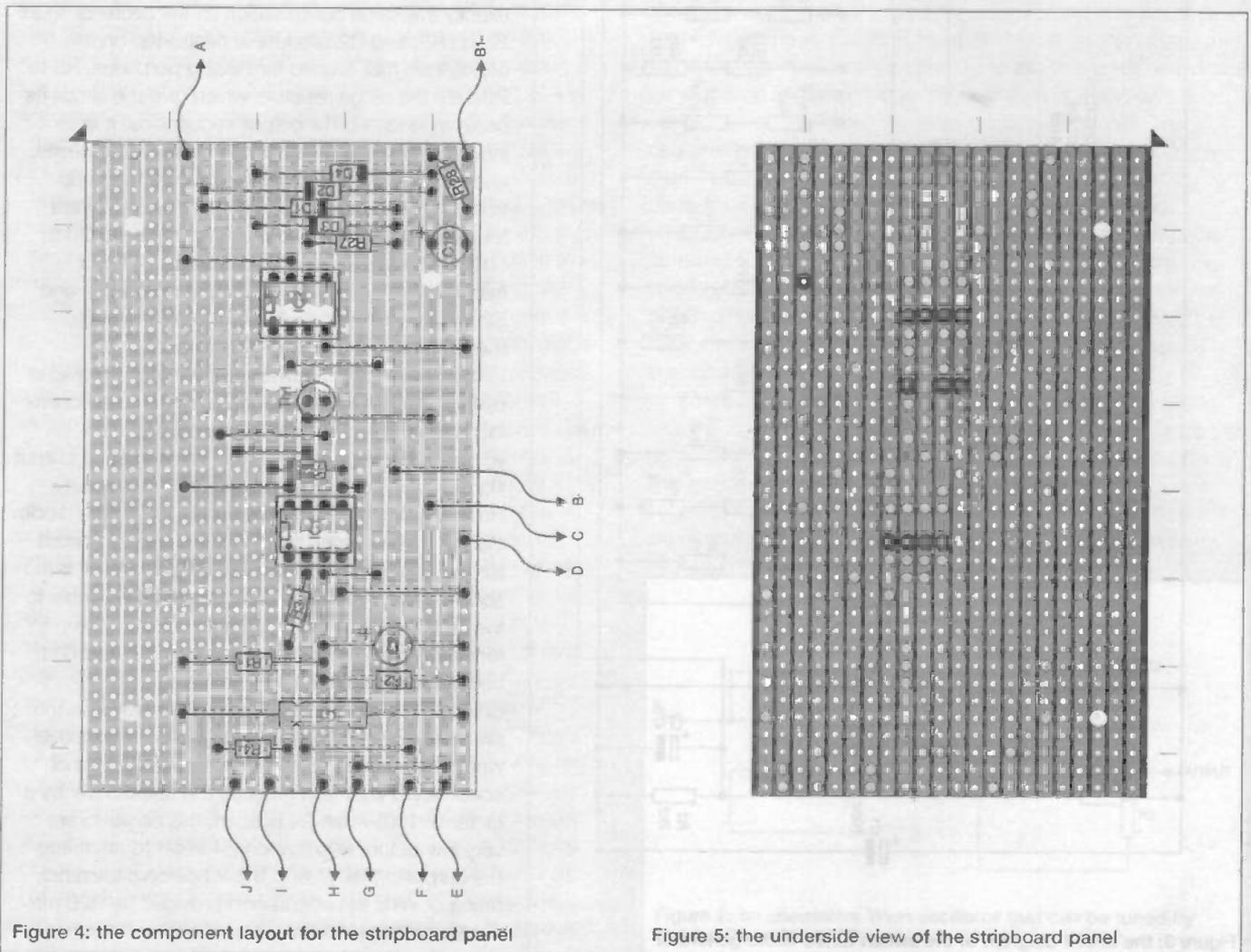


Figure 4: the component layout for the stripboard panel

Figure 5: the underside view of the stripboard panel

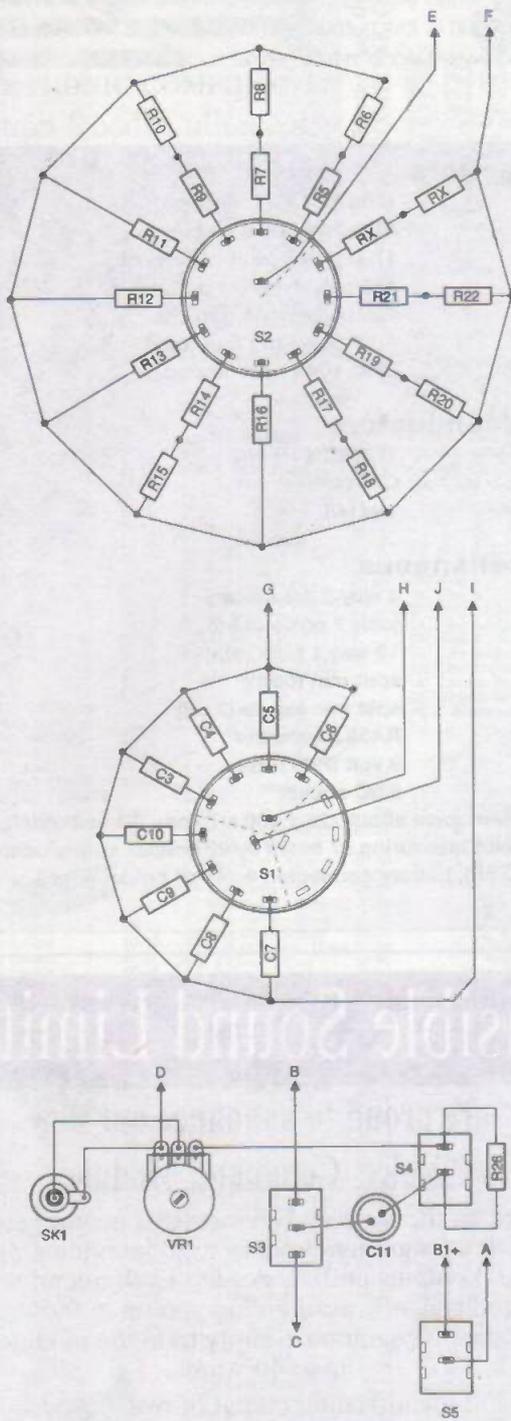
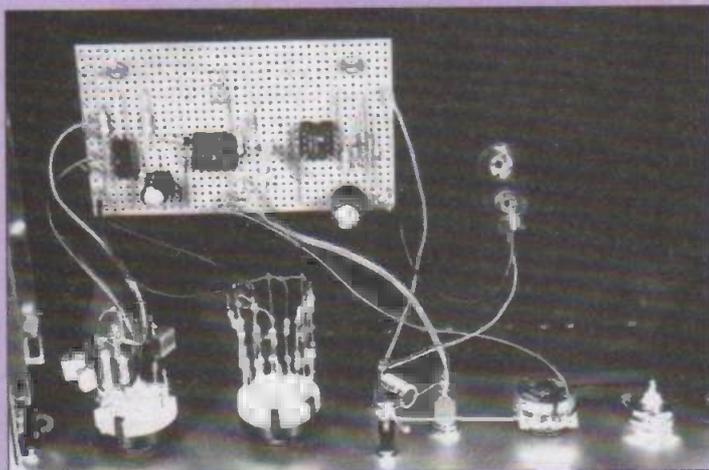


Figure 6: details of the hard wiring (use in conjunction with figure 4)



component seems to be sold as both the RA53 and as the R53, but it is suitable for use in this circuit in either guise. It is quite an expensive component, and it is worth checking in a few component catalogues to find the best price as there seems to be significant variations from one catalogue to another. Inexpensive bead and rod thermistors are not suitable for use in this circuit; only a self-heating type will work in this application.

### Hard wiring

As already pointed out, a substantial number of the components are mounted on the controls. Details of all the hard wiring are shown in figure 6, which should be used in conjunction with figure 4. Provided you go about things in the right way this wiring is reasonably easy to complete. S1 is a standard four way three pole rotary switch, but in this case one pole is left unused. It is a good idea to fit the range capacitors to S1 and the tuning resistors to S2 before these two switches are mounted on the front panel of the case. Start by doing any necessary trimming of the leadout wires, and then "tin" the ends of the leadout wires and the tags of the switches. Before adding the components to a switch it must be fixed in place on the work-top. All that is needed is a generous blob of Bostik Blu-Tack or Plasticine. The switch is then easily repositioned as you work your way around the tags.

In order to obtain good frequency accuracy the tuning resistors must be close tolerance components, as must most of the other resistors in the circuit. In fact I would recommend using 1 percent tolerance resistors throughout the circuit. Ideally the range capacitors would also have a tolerance rating of 1 percent, but it might be difficult to obtain suitable components having values of 47n and 470n. Capacitors having a tolerance of 5 percent are readily available in these values though, and should give good results. The tolerance rating indicates a maximum error, and in practice the actual error is likely to be considerably less. Due to stray capacitance in the circuit the output frequencies might be fractionally low on the highest frequency range. Perfectionists can replace each 470pF range capacitor with a 390pF capacitor in parallel with a 68pF type.

It is a good idea to connect long flying leads to the pole tags of S1 and S2 before mounting these switches in the case. Once they are in position it can be difficult to gain access to the pole tags as they are shielded by the tuning resistors and range capacitors. The remaining hard wiring is very straight forward and should present no real difficulties. The general layout used for this project is not critical, but it is always a good idea to avoid too much criss-crossing of the connecting wires. The prototype is housed in a plastic instrument case which has aluminium front and rear panels, but a much smaller case could be used if greater portability is required. However, make sure that you choose a case that can accommodate all the controls and the output socket on its front panel.

### Testing, testing...

If you have access to suitable test equipment, use this to check that the unit is providing the correct output waveforms, frequencies, and output levels. Note that the sinewave peak-to-peak output voltage will be slightly higher than the squarewave peak-to-peak output potential. The quality of the squarewave output signal inevitably reduces slightly at high frequencies, but it should be reasonable even at the highest output frequency of 100 kilohertz. In the absence of suitable test equipment the output can be monitored using an amplifier and loudspeaker, or even a crystal earphone will suffice. This will enable the

basic function of the unit to be verified, but bear in mind that apart from 10 kilohertz, the output frequencies on the highest range are beyond the limits of human hearing. The sinewave signal has a very distinctive "pure" sound, whereas the squarewave signal is

more of a rough buzzing sound. The difference between the two is more obvious at low output frequencies where the harmonics of the squarewave signal are largely at audible frequencies.

# PARTS LIST

for the Switch Tuned AF Generator

## Resistors

(All 0.6W 1 percent metal film)

R1,2	2k2 (2 off)
R3,4,25	2k7 (3 off)
R5	390k
R6	22k
R7	180k
R8	10k
R9	100k
R10	6k8
R11	68k
R12	47k
R13	27k
R14	16k
R15	1k
R16	12k
R17	8k2
R18, 24	330R
R19	5k6
R20	1k1
R21	4k7
R22	470R
R23	3k9
VR1	1k lin carbon rotary

## Capacitors

C1	100u 10V axial electro
C2	470u 10V radial electro
C3,7	470p polystyrene 1 percent
C4,8	4n7 polystyrene 1 percent
C5,9	47n polyester 5 percent
C6,10	470n polyester 5 percent
C11,12	220u 10V radial electro

## Semiconductors

IC1	TL072CN
IC2	CA3140E
D1 - D4	1N4148

## Miscellaneous

S1	4 way 3 pole rotary (only 2 poles used)
S2	12 way 1 pole rotary
S3	spdt min toggle
S4,5	spst min toggle (2 off)
TH1	RA53 thermistor
B1	9 volt (PP3 size)
SK1	BNC socket

Instrument case about 225 x 180 x 70mm, 0.1 inch pitch stripboard measuring 37 holes by 22 copper strips, control knob (3 off), battery connector, 8-pin diil holder (2 off), wire, solder, etc.

AN5521	1.35	STK73410/2	5.95	TEA2026C	4.50
AN5732	1.40	STK73605	4.50	TEA5170	1.40
AN6327	9.85	STR441	18.99	TUA2000-4	4.25
AN6677	8.50	STR451	29.99	U884B	2.35
BA5114	1.55	STR3125	5.50	UAA1008	3.00
BA6218	1.85	STR4211	5.50	UPC1178	1.05
BA6219	1.20	STR4090	11.15	UPC1182H	5.15
HA11423	1.65	STR20005	5.00	UPC1278H	2.20
HA13119	2.50	STR40090	4.00	UPC1420	4.50
KA6210	4.99	STR501037A	3.85	UPD1937	3.00
LA3220	0.60	STR54041	3.75	25A814	0.71
LA4183	1.35	STR58041	3.75	25A839	1.40
LA4445	1.90	STR80001	6.00	25A1062	1.00
LA4495	1.40	STR1706	4.75		
LA4588	2.55	STRD1806	4.50		
LA7835	2.35	STRD6008	10.00		
LB1415	2.25	TA227	1.85		
LM301	0.25	TA7271	2.50		
LM317T	1.50	TA7280	2.25		
M4918BI	4.75	TA7281	2.20		
M498BI	6.75	TA7698	5.00		
M51393	5.95	TA8200	3.50		
M58655	3.30	TA8210	3.00		
MB3730	1.70	TA8214	3.00		
MB3756	8.00	TA8215	3.00		
STK078	6.00	TA8205	3.95		
STK435	4.00	TA8659	13.00		
STK461	10.50	TA75339	..		
STK2250	7.45	TDA3500	4.99		
STK4121/2	7.00	TDA3645	8.00		
STK4141/2	5.50	TDA3650	8.99		
STK4142/2	6.50	TDA3850	18.99		
STK4162/2	6.25	TDA4400	1.75		
STK4171/2	8.10	TDA4500	3.50		
STK4191/2	8.50	TDA4505A	4.10		
STK4352	6.20	TDA4505B	4.10		
STK4372	5.65	TDA4505M	5.25		
STK4803	7.05	TDA4505K	6.15		
STK4843	7.05	TDA4680	4.50		
STK5315	5.85	TDA4950	1.40		
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STK5361	4.15	TDA8370	14.00		
STK5372	2.85	TDA8405	8.00		
STK5372H	4.15	TDA8732	5.95		
STK5412	3.75	TEA2018A	1.50		
STK5471	3.85				
STK6732	14.00				
STK7226	7.50				
STK7308	4.05				
STK7308	4.05				
STK7348	4.05				
STK7356	4.75				
STK7004	6.50				
STK73410	5.15				

Please phone us for the types not listed. Please add 60p post & packing and then add 17.5% VAT to the total. Callers by appointment only.

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With this CD a technician can control & adjust the electrical data of CD players in a minimum of time will improve the repair results in adjusting cd player after changing the laser unit. £20.99 each

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8 Pin DIL 0.3"	£0.06
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24 Pin DIL 0.6"	£0.13
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40 Pin DIL 0.6"	£0.64

## SIL Header Strip

1 x 36 Way Straight	£0.38
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T018-4 Base Socket	£0.24
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## IDC Cable Sockets

10 Way Socket	£0.23
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16 Way Socket	£0.31
20 Way Socket	£0.26
26 Way Socket	£0.30
34 Way Socket	£0.42
40 Way Socket	£0.66
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16 Way Straight	£0.36
20 Way Straight	£0.41
26 Way Straight	£0.49
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40 Way Straight	£0.96
50 Way Straight	£0.96
10 Way 90°	£0.36
16 Way 90°	£0.47
20 Way 90°	£0.53
26 Way 90°	£0.58
34 Way 90°	£0.72
40 Way 90°	£0.83
50 Way 90°	£0.99

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16 Way Straight	£0.70
20 Way Straight	£0.78
26 Way Straight	£1.00
34 Way Straight	£0.86
40 Way Straight	£1.56
50 Way Straight	£1.29
10 Way 90°	£0.58
16 Way 90°	£0.78
20 Way 90°	£0.82
26 Way 90°	£1.06
34 Way 90°	£1.14
40 Way 90°	£1.26
50 Way 90°	£1.74

## DIL Headers

14 Way DIL	£0.54
16 Way DIL	£0.59
24 Way DIL	£0.90
40 Way DIL	£1.02

## Transition Headers

10 Way Transition	£0.49
14 Way Transition	£0.47
16 Way Transition	£0.47
20 Way Transition	£0.54
26 Way Transition	£0.62
34 Way Transition	£0.67
40 Way Transition	£0.90
50 Way Transition	£1.02

## D Type Connectors

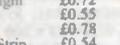


9 Way Male Plug	£0.29
9 Way Female Socket	£0.30
15 Way Male Plug	£0.39
15 Way Female Socket	£0.39
15 Way H.D. Plug	£0.49
15 Way H.D. Socket	£0.78
23 Way Male Plug	£0.49
23 Way Female Socket	£0.49
25 Way Male Plug	£0.48
25 Way Female Socket	£0.50

## IDC Ribbon Mounting

9 Way Male Plug	£1.08
9 Way Female Socket	£1.08
25 Way Male Plug	£1.26
25 Way Female Socket	£1.26

## Right Angled PCB



9 Way Male Plug	£0.37
9 Way Female Socket	£0.35
15 Way H.D. Socket	£0.77
25 Way Male Plug	£0.53
25 Way Female Socket	£0.51

## Plastic D Covers

9 Way Cover - Grey	£0.30
9 Way Cover - Black	£0.30
15 Way Cover - Grey	£0.33
23 Way Cover - Grey	£0.36
23 Way Cover - Black	£0.36
25 Way Cover - Grey	£0.36
25 Way D Cover - Black	£0.36
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25 to 25 Cover / Case	£0.96

## Audio Connectors

2.5mm Jack Plug	£0.21
2.5mm Line Socket	£0.16
2.5mm Chassis Socket	£0.09
3.5mm Mono Plug	£0.24
3.5mm Mono Line Skt	£0.30
3.5mm Mono Chassis Skt	£0.14
3.5mm Stereo Plug	£0.33
3.5mm Stereo Line Skt	£0.37
3.5mm Stereo Chassis Skt	£0.34
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1/2" Mono Line Socket	£0.35
1/2" Mono Chassis Socket	£0.40
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1/2" Stereo Chassis Skt	£0.44

## DIN Series

2 Pin Line Plug	£0.21
2 Pin Chassis Socket	£0.15
3 Pin Line Plug	£0.27
3 Pin Chassis Socket	£0.28
4 Pin Line Plug	£0.24
4 Pin Chassis Socket	£0.26
5 Pin Line Plug 180°	£0.30
5 Pin Chassis Skt 180°	£0.32
5 Pin Line Plug 240°	£0.32
5 Pin Chassis Skt 240°	£0.32
5 Pin Line Plug 360°	£0.24
5 Pin Chassis Skt 360°	£0.32
6 Pin Line Plug	£0.27
6 Pin Chassis Socket	£0.32
7 Pin Line Plug	£0.34
7 Pin Chassis Socket	£0.34
8 Pin Line Plug	£0.45
8 Pin Chassis Socket	£0.36

## Phono Series

Red Line Plug	£0.20
Black Line Plug	£0.20
Yellow Line Plug	£0.20
White Line Plug	£0.20
Red Line Socket	£0.20
Black Line Socket	£0.20
Yellow Line Socket	£0.20
White Line Socket	£0.20
Red Chassis Socket	£0.20
Black Chassis Socket	£0.20
Gold Plated Plug - Red	£0.76
Gold Plated Plug - Black	£0.76

## XLR Series - metal

3 Pin Line Plug	£1.50
3 Pin Line Socket	£1.82
3 Pin Chassis Plug	£1.50
3 Pin Chassis Socket	£1.70

## Solder Bucket



9 Way Male Plug	£0.29
9 Way Female Socket	£0.30
15 Way Male Plug	£0.39
15 Way Female Socket	£0.39
15 Way H.D. Plug	£0.49
15 Way H.D. Socket	£0.78
23 Way Male Plug	£0.49
23 Way Female Socket	£0.49
25 Way Male Plug	£0.48
25 Way Female Socket	£0.50

## DC Low Voltage

DC Plug 0.7ID, 2.35OD	£0.46
DC Plug 1.3ID, 3.4OD	£0.32
DC Plug 1.7ID, 4.0OD	£0.46
DC Plug 1.7ID, 4.75OD	£0.46
DC Plug 2.1ID, 5.0OD	£0.24
DC Plug 2.5ID, 5.0OD	£0.24
DC Plug 3.1ID, 6.3OD	£0.46
DC Line Socket 2.1mm	£0.56
DC Line Socket 2.5mm	£0.56
DC Chassis Skt 2.1mm	£0.46
DC Chassis Skt 2.5mm	£0.46

## IEC Mains 6A 250V ac



3 Pin IEC Line Socket	£1.14
3 Pin IEC Line Plug	£1.83
3 Pin Chassis Plug	£0.56
8 Way Buitgen	£0.72

## Toggle Switches

8 Pin Line Plug	P551 £3.99
8 Pin Chassis Skt	P552 £1.39

## Sub-Minature

3A 125V 1A 250V	£0.70
5mm Ø Mounting Hole	£0.58
SPST 5 x 10mm	£0.60
SPDT 5 x 10mm	£0.60
SPDT C/O/F 5 x 10mm	£0.86
DPDT 9.2 x 10mm	£0.66

## Miniature

6A 125V 3A 250V	£0.70
6.2mm Ø Mounting Hole	£0.70
SPST 8 x 13mm	£0.60
SPDT 8 x 13mm	£0.60
SPDT C/O/F 8 x 13mm	£0.64
SPDT C/O Biased 2 way	£1.34
SPDT C/O Biased 1 way	£1.04
DPDT 12 x 13mm	£0.72
DPDT C/O/F 12 x 13mm	£0.76
DPDT C/O Biased 2 way	£1.28
DPDT C/O Biased 1 way	£1.28

## Standard

10A 250V Push on terminals	£1.14
11mm Ø Mounting Hole	£1.14
SPST 18 x 30mm	£1.28
SPDT 18 x 30mm	£1.52
DPDT 21 x 30mm	£1.78
DPDT C/O/F 21 x 30mm	£1.78

## Slide Switches

Miniature	£0.15
300mA 125V	£0.15
7 x 15mm Mounting Hole	£0.15
DPDT 7 x 23mm	£0.15

## Rotary Switches

1A 125V	£0.24
DPDT 12.5 x 35mm	£0.24
DPDT C/O 12.5 x 35mm	£0.27

## RF Connectors

BNC Plug 50Ω Solder	£0.93
BNC Plug 50Ω Crimp	£0.56
BNC Plug 75Ω Solder	£0.95
BNC Plug 75Ω Crimp	£0.70
BNC Chassis Socket	£0.80
F Plug - Twist	£0.27
F Plug - Crimp	£0.20
TNC Plug Solder	£0.78
TNC Plug Crimp	£0.78
UHF Plug 5mm Cable	£0.72
UHF Plug 11mm Cable	£0.66
UHF Chassis Skt-Sqr	£0.45
UHF Chassis Skt-Rnd	£0.58

## Terminals

All Available in - Red, Black, Green, Blue, White or Yellow	
2mm Solder Plugs	£0.18
2mm Chassis Sockets	£0.20
4mm Solder Plugs	£0.30
4mm Chassis Sockets	£0.40
4mm Binding Posts	£0.23
4mm Crocodile Clips	£0.54
Power Connectors	£0.13

## Push Switches

DC Plug 0.7ID, 2.35OD	£0.46
DC Plug 1.3ID, 3.4OD	£0.32
DC Plug 1.7ID, 4.0OD	£0.46
DC Plug 1.7ID, 4.75OD	£0.46
DC Plug 2.1ID, 5.0OD	£0.24
DC Plug 2.5ID, 5.0OD	£0.24
DC Plug 3.1ID, 6.3OD	£0.46
DC Line Socket 2.1mm	£0.56
DC Line Socket 2.5mm	£0.56
DC Chassis Skt 2.1mm	£0.46
DC Chassis Skt 2.5mm	£0.46

## Relays

PCB Mounting	
1A 24Vdc DPDT 5V	£1.44
1A 24Vdc DPDT 12V	£1.44
3A 110V SPDT 6V	£0.58
3A 110V SPDT 12V	£0.58
5A 110V SPDT 6V	£0.72
5A 110V SPDT 12V	£0.72
5A 110V DPDT 6V	£0.93
5A 110V DPDT 12V	£0.93
5A 240V DPDT 6V	£1.76
5A 240V DPDT 12V	£1.76
10A 240V SPDT 6V	£1.25
10A 240V SPDT 12V	£1.44
10A 240V SPDT 24V	£1.44

## Computer Accessories

Adaptors	
9M Gender Changer	£2.18
9F Gender Changer	£2.29
25M Gender Changer	£2.64
25F Gender Changer	£2.80
9 Male - 25 Female	£2.02
9 Female - 25 Male	£2.09
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9F - 6 Mini Din Female	£2.54
5M Din - 6F Mini Din	£2.64
5F Din - 6M Mini Din	£2.28

## Testers / Patch Boxes

Mini Tester 7 LEDs	£6.68
Check Tester 18 LEDs	£7.11
Enhanced LED Switches	£15.25
2J Jumper Box M-F	£2.90
2SD Patch Box M-F	£7.32
Anti-Static Wrist Strap	£5.30
RS232 Surge Protector	£5.43
Mains Surge Protector	£11.99

## Leads & Cables

1.5m Printer Lead	£3.40
5m Printer Lead	£9.38
10m Printer Lead	£12.38
Serial Printer 25M-9F	£4.20
Serial Printer 25M-25F	£4.45
Null Modem 9F-9F	£3.45
Null Modem 25F-25F	£4.63
Null Modem 9E25-9E25	£5.54
Modem Lead 25M-9F	£4.08
Modem Lead 25M-25F	£4.75
Interlink Lead 25F-9F	£6.50
Interlink Lead 25F-25F	£6.50
Interlink Lead 25M-25M	£5.70
Patch Lead 25M-25M	£4.66
Patch Lead 36M-36M	£5.90
Floppy Drive Cable A/B	£4.50
Hard Disk Cable 2XIDE	£2.90
Power Cable 3/2-2 x 3/4	£1.88
Power Cable 3/4-2 x 3/4	£1.84
Power Cable 5/4-2 x 3/4	£2.24
Power Cable 5/4-3/4 x 5/4	£2.24

## Networking

BNC T Piece FMF	£2.40
BNC T Piece FFF	£2.40
BNC Coupler F	£1.02
BNC Coupler M	£1.65
BNC Ratchet Crimper	£17.44
RJ45 IDC Plug	£0.39
Thinnet Cable per m	£0.44

## Push Switches

Miniature Round	
250mA 125V 28 x 10mm	£0.26
7mm Ø Mounting Hole	£0.26
Non Latching Push to Make	£0.25
Black PTM	£0.25
Red PTM	£0.25
Yellow PTM	£0.25
Green PTM	£0.25
Blue PTM	£0.25
White PTM	£0.25
Non Latching Push to Break	£0.25
Black PTB	£0.25

## Standard Square

1A 250V	£0.60
39 x 15MM	£0.60
12mm Ø Mounting Hole	£0.60
Non Latching Push to Make	£0.60
Black PTM	£0.60
Red PTM	£0.60
Blue PTM	£0.60
White PTM	£0.60
Latching	£0.63
Black	£0.63
Red	£0.63
Blue	£0.63
White	£0.63

## Rocker Switches

Miniature	
6A 250V Solder Tags	£0.62
SPST 21 x 14 x 16mm	£0.63
DPDT 21 x 24 x 22mm	£0.96

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R.M.S. into 4 ohms, frequency response 1Hz - 100KHz  
-3dB, Damping Factor >300, Slew Rate 45V/uS.  
T.H.D. typical 0.002%, Input Sensitivity 500mV, S.N.R.  
-110 dB. Size 300 x 123 x 60mm.  
PRICE £40.85 + £3.50 P&P



**OMP/MF 200 Mos-Fet Output power 200 watts**  
R.M.S. into 4 ohms, frequency response 1Hz - 100KHz  
-3dB, Damping Factor >300, Slew Rate 50V/uS.  
T.H.D. typical 0.001%, Input Sensitivity 500mV, S.N.R.  
-110 dB. Size 300 x 155 x 100mm.  
PRICE £64.35 + £4.00 P&P



**OMP/MF 300 Mos-Fet Output power 300 watts**  
R.M.S. into 4 ohms, frequency response 1Hz - 100KHz  
-3dB, Damping Factor >300, Slew Rate 60V/uS.  
T.H.D. typical 0.001%, Input Sensitivity 500mV, S.N.R.  
-110 dB. Size 330 x 175 x 100mm.  
PRICE £81.75 + £5.00 P&P



**OMP/MF 450 Mos-Fet Output power 450 watts**  
R.M.S. into 4 ohms, frequency response 1Hz - 100KHz  
-3dB, Damping Factor >300, Slew Rate 75V/uS.  
T.H.D. typical 0.001%, Input Sensitivity 500mV, S.N.R.  
-110 dB, Fan Cooled, D.C. Loudspeaker Protection, 2  
Second Anti-Thump Delay. Size 385 x 210 x 105mm.  
PRICE £132.85 + £5.00 P&P



**OMP/MF 1000 Mos-Fet Output power 1000 watts**  
R.M.S. into 2 ohms, 725 watts R.M.S. into 4 ohms,  
frequency response 1Hz - 100KHz -3dB, Damping  
Factor >300, Slew Rate 75V/uS, T.H.D. typical  
0.002%, Input Sensitivity 500mV, S.N.R. -110 dB, Fan  
Cooled, D.C. Loudspeaker Protection, 2 Second  
Anti-Thump Delay. Size 422 x 300 x 125mm.  
PRICE £259.00 + £12.00 P&P

NOTE: MOS-FET MODULES ARE AVAILABLE IN TWO VERSIONS:  
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  - 12" 100 WATT R.M.S. ME12-100LE GEN. PURPOSE, LEAD GUITAR, DISCO, STAGE MONITOR. RES. FREQ. 49Hz, FREQ. RESP. TO 6KHz, SENS 100dB. PRICE £35.64 + £3.50 P&P
  - 12" 100 WATT R.M.S. ME12-100LT (TWIN CONE) WIDE RESPONSE, P.A., VOCAL, STAGE MONITOR. RES. FREQ. 42Hz, FREQ. RESP. TO 10KHz, SENS 98dB. PRICE £36.67 + £3.50 P&P
  - 12" 200 WATT R.M.S. ME12-200 GEN. PURPOSE, GUITAR, DISCO, VOCAL, EXCELLENT MID. RES. FREQ. 58Hz, FREQ. RESP. TO 6KHz, SENS 98dB. PRICE £46.71 + £3.50 P&P
  - 12" 300 WATT R.M.S. ME12-300GP HIGH POWER BASS, LEAD GUITAR, KEYBOARD, DISCO ETC. RES. FREQ. 47Hz, FREQ. RESP. TO 5KHz, SENS 103dB. PRICE £70.19 + £3.50 P&P
  - 15" 200 WATT R.M.S. ME15-200 GEN. PURPOSE BASS, INCLUDING BASS GUITAR. RES. FREQ. 46Hz, FREQ. RESP. TO 5KHz, SENS 99dB. PRICE £50.72 + £4.00 P&P
  - 15" 300 WATT R.M.S. ME15-300 HIGH POWER BASS, INCLUDING BASS GUITAR. RES. FREQ. 39Hz, FREQ. RESP. TO 3KHz, SENS 103dB. PRICE £73.34 + £4.00 P&P

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# PC New generation processors

## FROM MAJOR MANUFACTURERS

### Intel introduces "next level" with the Pentium II processor

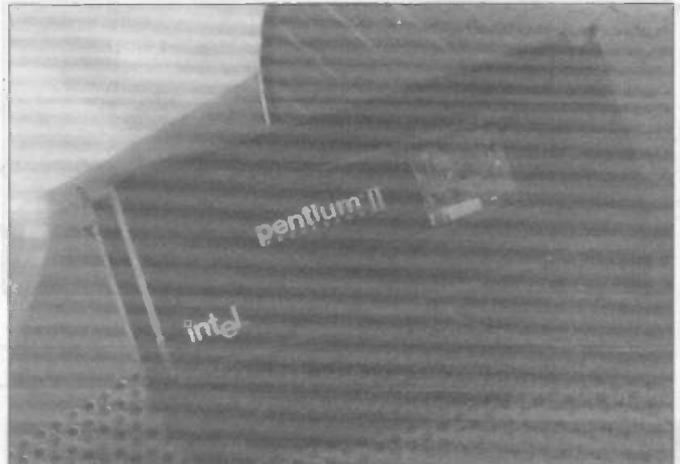
The Intel Corporation has introduced a new generation of Pentium processors, the Pentium II processor. The new device, which has been introduced at speeds of 300, 266 and 233 megahertz, combines Intel's advanced Pentium Pro processor with the capabilities of its MMX (tm) media enhancement technology. MMX, which has been added to the Pentium II to enhance performance for audio, video and graphics applications as well as speed up data encryption and compression, was originally developed primarily for the games market (which has driven so much popular computer development) but, to give an example, can also be used to allow a PC to do most of the work of a modem so that the modem itself becomes just a low cost interface to the telephone line. (Though the PC cannot do much else at the same time.) The 7.5-million transistor processor core is based on Intel's P6 architecture, and is manufactured on .35-micron process technology with a new packaging method.

The new processor is provided in a new style of package, the Single Edge Contact cartridge. This incorporates the processor itself, plus 512KB of level 2 cache unified for code and data. The L2 cache is made from industry-sourced static ram, and runs at half processor speed on its own bus, while the system bus (used for memory of I/O requests) runs at 66MHz. Consequently, the Pentium II at 266 MHz can achieve around three times the peak bus bandwidth of the highest speed Pentium processor system, which runs one bus at a peak of 66MHz. (This feature is referred to as dual independent bus architecture).

L2 cache performance has a major impact on overall system performance, and running the L2 cache bus at half processor speed would be difficult to achieve if the cache and the processor itself were both separately mounted on the motherboard. No doubt Intel intend to use this new packaging method to permit further advances in power for further processor generations. Another advantage for Intel may be that the cartridge processor makes a break from standard motherboards, compelling users to choose between Intel-specific motherboards and competing types.

The processor core has 32KB of non-blocking L1 cache running at processor speed, divided as 16KB data and 16KB instructions, also a vital feature for performance.

The new processor also has the Pentium Pro's features of dynamic execution, which executes instructions ahead of time when possible. A 12 stage pipeline is used, with a two level adaptive branch prediction mechanism to reduce the probability

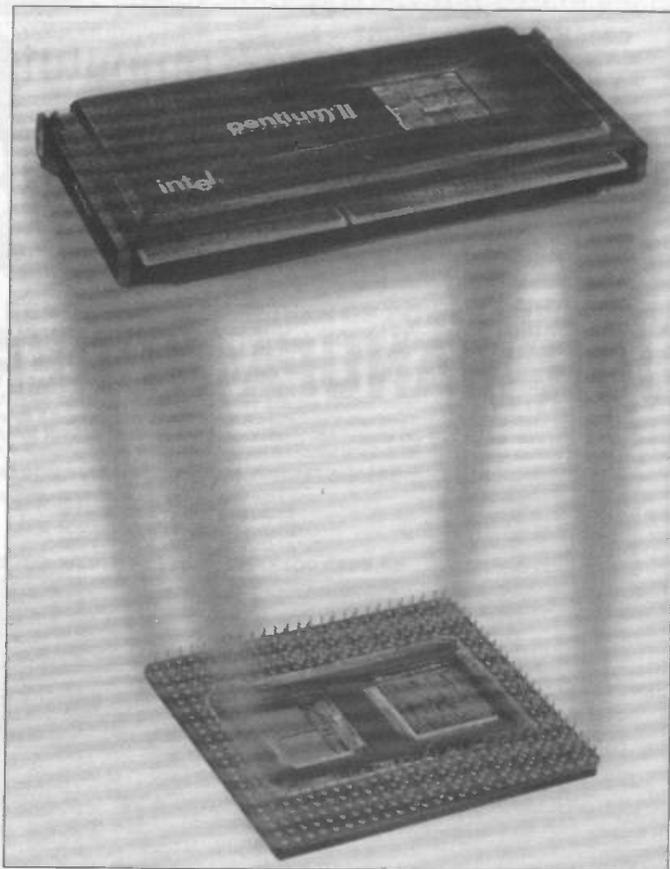


of there being a clock cycle when the processor is wasting time.

Dynamic Execution incorporates the two concepts of "out of order" and "speculative execution", effectively eliminating the constraint of linear instruction sequencing between the traditional "fetch" and "execute" phases of instruction execution. Up to three instructions can be decoded per clock cycle. Considered conceptually, the decoded instructions are put into a dataflow graph, which can hold up to 40 instructions; these are executed from the graph when their operands are available (versus instruction order). Up to four instructions can be executed per clock cycle.

Intel's MMX Technology incorporates 57 instructions oriented to highly parallel operations with multimedia and communications type data. These instructions use a technique known as SIMD (single instruction, multiple data) to crunch several bytes in the same way, delivering better multimedia and comms performance. The Pentium II processor can execute two MMX instructions at a time.

Multi processor systems are also catered for. The Pentium II system bus supports up to eight outstanding bus requests (four per processor), allowing more parallel operation between processors and I/O, as well as supporting smooth performance scaling to a two-processor system. The GTL+ electrical signalling of the system bus will assist the migration of this bus to higher frequencies as DRAM technologies with higher performances reach the market. The GTL+ bus will provide "glueless" (no additional chip logic required) support for two processors, giving cost-effective SMP (symmetric multi-processor) solutions.



The Pentium II's write-combining technology, which combines multiple writes to one region of memory (for example, a video controller frame buffer) declared as WC type in a single burst write operation, can be used to get very high graphics I/O performance.

Testing and fault-monitoring features include built-in self-test, providing single stuck-at fault coverage of the microcode and large PLAs, as well as testing of the instruction cache, data cache, Translation Lookaside Buffers and ROMs. There is a standard test access port and boundary scan architecture mechanism (a growing test technique which is becoming a new industrial standard).

The 266- and 233-MHz Pentium II processors are already available in desktop systems from more than a dozen leading computer manufacturers, and the 300-MHz version will be appearing in workstations after the middle of 1997.

### A new high end processor from AMD

Flash memory leaders AMD are also releasing a new PC microprocessor in 1997. In April AMD introduced the AMD-K6(tm) 233, 200 and 166 MHz processors, its 6th generation processors with MMX technology. AMD's view is that Intel's product strategy is to encourage PC makers away from the current motherboard and processor technology (Socket 7) to its unique daughter board technology, or single edge card (SEC). "These new technologies do not offer direct benefits to the PC manufacturer, or necessarily to the user. This strategy ties manufacturers deeper into Intel, as it moves to propriety chip sets for the Pentium II. Intel's market strategy also requires different processors for 16-bit and 32-bit software."

The AMD-K6 is a single chip solution for 16-bit and 32-bit software. Importantly, the AMD-K6 maintains Socket 7 compatibility and therefore allows manufacturers to develop

new, more powerful PCs using the current infrastructure, (motherboards, casing, power supplies etc), allowing cost saving.

Perhaps because AMD are not the company setting the standard for PCs, their strategy seems to be to aim for technological advantages in certain areas. For example, comparing the cache size, the level 1 cache on the AMD-K6 processor is 32K instruction and 32K data. AMD say that they have cut the number of transistors required for random logic compared with the Intel Pentium processors, and have used the space on the chip thus saved to incorporate a larger level 1 cache. In addition, they have concentrated on maximising the cache hits by using a very large branch prediction table.

The relevance of the branch prediction table is that if the instruction cache is loaded with a series of instructions from memory, these will be able to be accessed very rapidly for as long as the software does not branch outside the instructions in the cache. As soon as there is a branch requiring an instruction not in the cache, the processor has to wait while the extra instructions are fetched. The branch prediction table is a means of determining the likely direction of the branch, and thus having the instructions ready when they are required. As a comparison, the AMD-K6 has 8192 branch history table entries, as compared with 512 in the Intel Pentium Pro.

There are several other areas in particular where AMD's top of the range processors are aiming to offer features that will be an advantage to some users. One of these is that the AMD-K6 processor will plug into the widely-used Socket-7, often referred to as the Pentium bus connection. By comparison, Intel's Pentium II processor plugs into a proprietary slot, which will mean that new motherboard designs will be required. Some PC manufacturers may find it advantageous to offer comparable performance without changing motherboard designs.

The other competitive edge offered by the AMD-K6 processor is that the delay when switching from 32-bit to 16-bit instructions is less than with the Pentium Pro. What this means in practice is that when running an operating system such as Windows 95, which incorporates 16-bit code as well as 32-bit code, the AMD-K6 processor has significant speed advantages. An operating system such as Windows NT, which is exclusively 32-bit, will not show such differences between the two processors.

Nevertheless, a comparison between an AMD-K6 processor running at 200 MHz and an Intel Pentium Pro processor running at 200 MHz, both running the Windows

	Market Segment	Operating System	Applications
Pentium® II processor	Business Desktop, Uni/Dual Workstation, Uni/Dual Server	Windows NT*, Windows® 95, UNIX*	Business Apps, Multimedia & Communications Workstation Apps
Pentium® Pro processor	Business Desktop, Workstation, Server	Windows NT, Windows 95, UNIX	32-Bit Business Apps, Professional Apps, Workstation Apps
Pentium® processor with MMX™ Technology	Home, Mobile, Business Desktop	Windows 95	Business Apps, Consumer Apps, Multimedia & Communications
Pentium® processor	Home, Mobile, Business Desktop	Windows 95, Windows 3.1	Consumer, 16-Bit Business Apps

\*Other brands and names are the property of their respective owners.

NT operating system, shows according to AMD's figures, a speed advantage to the AMD-K6. It is therefore possible that PC manufacturers offering machines specifically for use with Windows '95 may prefer to use the AMD-K6 processor for this purpose.

The AMD-K6 processor is almost a risc machine in some respects. x86 instructions are translated into risc-86 instructions then executed. In some cases the x86 instruction results in zero risc-86 instructions. This raises the question as to whether a K6 native code running even faster would be possible.

Another interesting technological step taken in the manufacture of the AMD-K6 processor is that instead of

connecting to the chip by using bond-wires welded to pads round the edge of the chip, solder bumps on the chip are used to bond it upside-down into the package, taking connections from all over the chip. This saves some chip area, gets the signals in and out faster, and improves thermal performance.

*Below: A selection from comparison figures issued to AMD in May 1997. \*Data shown are estimates based on industry publications and derived from reports that Pentium II is based on Pentium Pro. Pentium II is not publicly available at time of writing. Final product features may vary from estimates shown.*

Processor features	AMD-K6 200MHz	*Pentium II	Pentium Pro	Pentium with MMX (P556)
x86 decoders	2 sophisticated 1 long, 1 vector	1 sophisticated 2 simple	1 sophisticated 1 simple	1 sophisticated 1 simple
Branch history table entries	8192	512	512	258
Branch prediction accuracy	95 percent	90 percent	90 percent	75-80 percent
L1 instruction and data cache	32k + 32K	16K + 16K	8K + 8K	16K + 16K
Processor bus	Socket 7 66MHz	Pentium Pro 66MHz	Pentium Pro 66MHz	Socket 7 66MHz
Latency (smaller is better)	2 clock	5-7 clock	5-7 clock	2 clock

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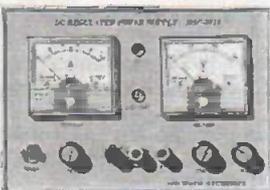
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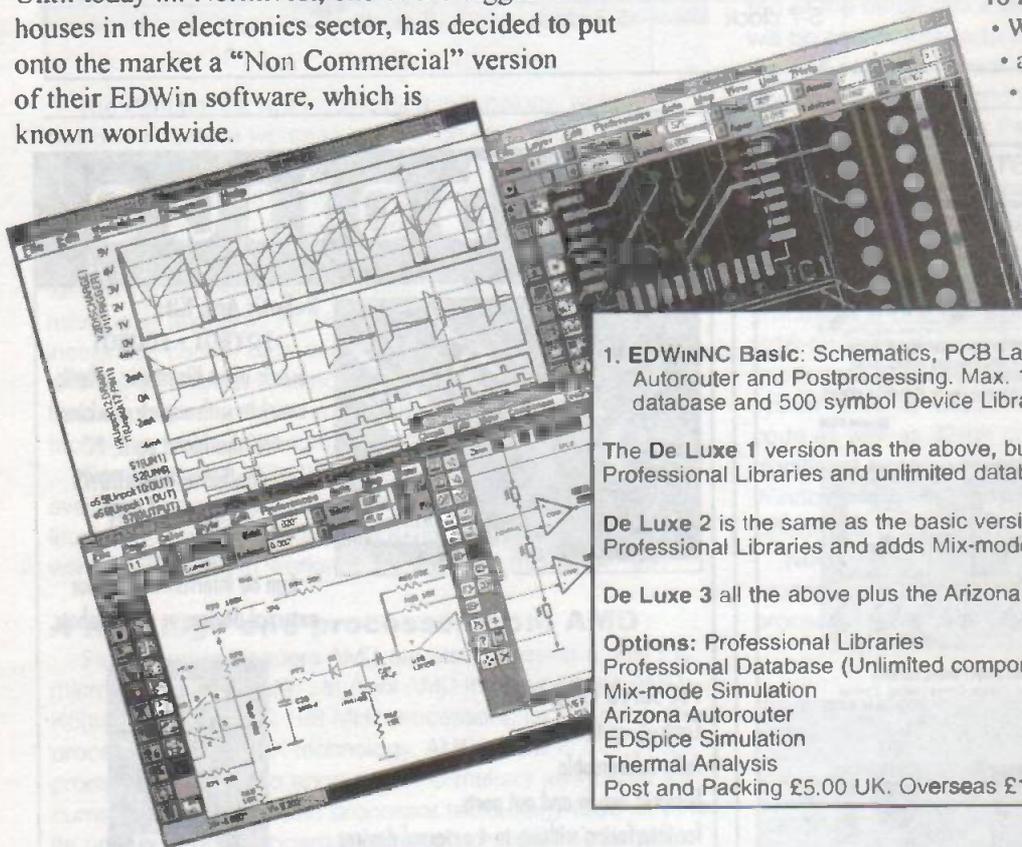
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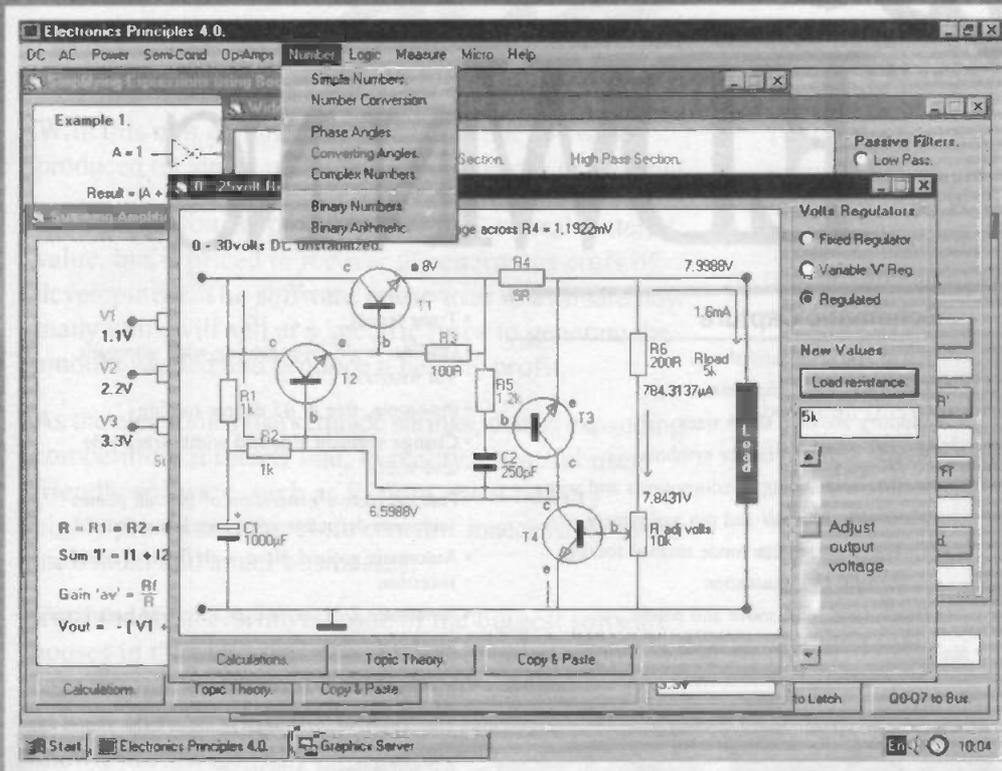


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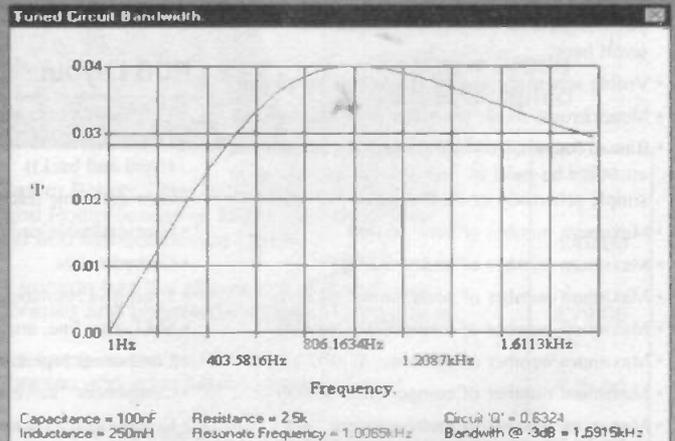
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$$I = \sqrt{.5^2 + (1.570796 - .3183099)^2} = 1.3486 = 1.3486\text{A}$$

$$\phi = \tan^{-1} \frac{1.570796 - .3183099}{.5} = 68.2378^\circ$$

$$Z = \frac{100 \times 157.0796 \times 31.83099}{\sqrt{157.0796^2 \times 31.83099^2 + 100^2 \times (157.0796 - 31.83099)^2}} = 37.0755\Omega$$

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## Overview

This circuit provides an audible warning when the headlights need to be switched on. The sound is only given while the ignition key is turned and will stop as soon as the headlights are operated. The warning takes the form of two rapid high-pitched bleeps given every so often. The signal is discrete and enables the motorist to disregard it when, for example, the engine is left running with only the sidelights on. It is also distinctive and will be easily heard above other noises inside the car.

At the end of construction, the light level at which the unit needs to operate will be set to individual requirements. The time between sets of bleeps is also adjustable to one of three periods - 1 minute, 2 minutes and 4 minutes approximately.

Providing it is dark enough, the first signal will be given as soon as the ignition is switched on. This ensures that a warning is obtained at the first opportunity. Of course, it may be dark enough inside the garage or in an enclosed car park to operate the unit while it is still light outside. Although a signal would be given under such circumstances, this is not thought to be a problem and the effect is simply ignored.

The circuit comprises two parts. The first is housed in a small plastic box and contains the light sensor. This is placed so as to respond to the light outside. The other section contains the rest of the circuit including the buzzer. This will probably be mounted out of sight under the dashboard. The connection between the two parts is made

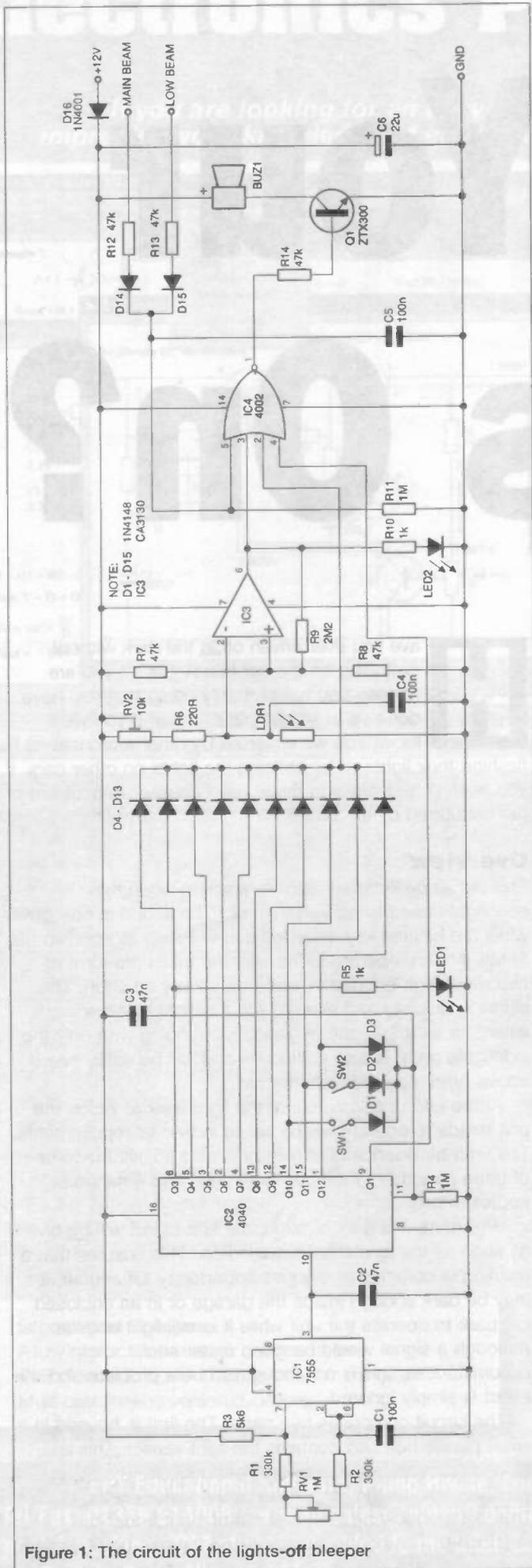


Figure 1: The circuit of the lights-off bleeper

using light-duty twin wire. Connections will also need to be made to the 12V supply and to the lighting circuits. Details for doing this are given later. The unit draws current only while the ignition is switched on and since it requires only a few milliamps, it imposes a negligible load on the car system.

### How it works

The circuit for the Lights Needed Bleeper is shown in figure 1. IC1 is a timer integrated circuit configured as an astable. Thus, as long as it receives a supply, it will provide a string of pulses at the output, pin 3. The rate at which these are given depends on the values of resistors R1, R2, R3, preset potentiometer RV1 and capacitor C1. With those specified, RV1 may be used to adjust the rate between about 4 and 11 per second. For the purpose of explanation, assume that it has been set to provide eight pulses per second.

These pulses are applied to the clock input, pin 10, of 12-stage binary counter, IC2. Its outputs, Q1 to Q12, then go high in various combinations to record, in binary, the total number of pulses registered. Table 1 shows the state of IC2 outputs over the first six and last six pulses. It would be very difficult showing the entire truth table because it would have 4096 lines! It will be seen that output Q1 goes high every second pulse, Q2 every four pulses and so. It will also be noted that, starting from the beginning, output Q3 goes high on the fourth pulse, Q4 would do so on the eighth one, etc. By the same reasoning, the final output, Q12 would go high on 2048 counts and all outputs would end up high on 4095. On the next pulse, all outputs would return to zero and the whole process repeat. In this circuit, a whole cycle is not allowed to happen because the i.c. is reset to begin a further one prematurely by making pin 11 (reset input) high momentarily. More will be said about this later. The reset pin is normally kept low via resistor, R4 to prevent possible false resetting. Additionally, capacitor, C3 makes pin 11 go high for an instant when the supply is connected (that is, when the ignition is switched on) and this ensures that the count will always begin at zero.

Since output Q1 goes high every two pulses, it will operate at half the astable frequency - that is about 4 per second. This output is connected to one of the inputs, pin 4, of 4-input NOR gate, IC4. (in fact, this ic contains two identical gates but only one of them is used here). A NOR gate output is high only when all of its inputs are low. If any input is high, the output must therefore be low and will have no further effect. Referring to the truth table, it will be seen that when Q1 has gone through two low states (shown by asterisks), outputs Q3 to Q12 go high in various ways until the device has performed a complete cycle or has been reset. Note that output Q2 is not used. A high condition at any output Q3 to Q12 will allow this state to pass via one or more of diodes D4 to D13 and hence to another NOR gate input, IC4 pin 2. The output, pin 1 will therefore be kept low. Assume for the moment that the remaining NOR gate inputs, pins 3 and 5, are also low (this will be the case when it is sufficiently dark and when the lights are switched off). IC4 output, pin 1, will then give two pulses (shown by the asterisks) at the beginning of the cycle then remain off until the next one.

### Seeing the light

The light sensor comprises light-dependent resistor, LDR1, and this works in conjunction with operational amplifier, IC3.

The resistance of an LDR rises as the amount of light falling on its sensitive surface falls. The LDR forms the lower arm of a potential divider with resistor R6 and preset RV2 in the upper one. The voltage at IC3 pin 2 (the inverting input) will therefore rise as the light level falls. If the voltage at the inverting input is less than that at the non-inverting one (pin 3) the output, pin 6, will be high. In other cases it will be low. The non-inverting input, pin 3, receives a fixed voltage equal to one-half that of the supply (nominally 6V) due to the further potential divider consisting of equal-value resistors, R7 and R8. With suitable adjustment to RV2, the voltage at pin 2 will be less than that at pin 3 during daylight hours and pin 6 will be high. As it becomes darker, the voltage at pin 2 will rise above that at pin 3 so pin 6 will go low. Note that variations in supply voltage will have no effect on the operating point since the voltages at both inputs will rise or fall in sympathy. Their relative conditions will therefore remain unchanged. Resistor R9 applies a little positive feedback to the system and sharpens the switching action. The logic state of IC3 pin 6 is connected to another NOR gate input, IC4 pin 3. During daytime, therefore, IC4 output can never be high.

### Beam me up

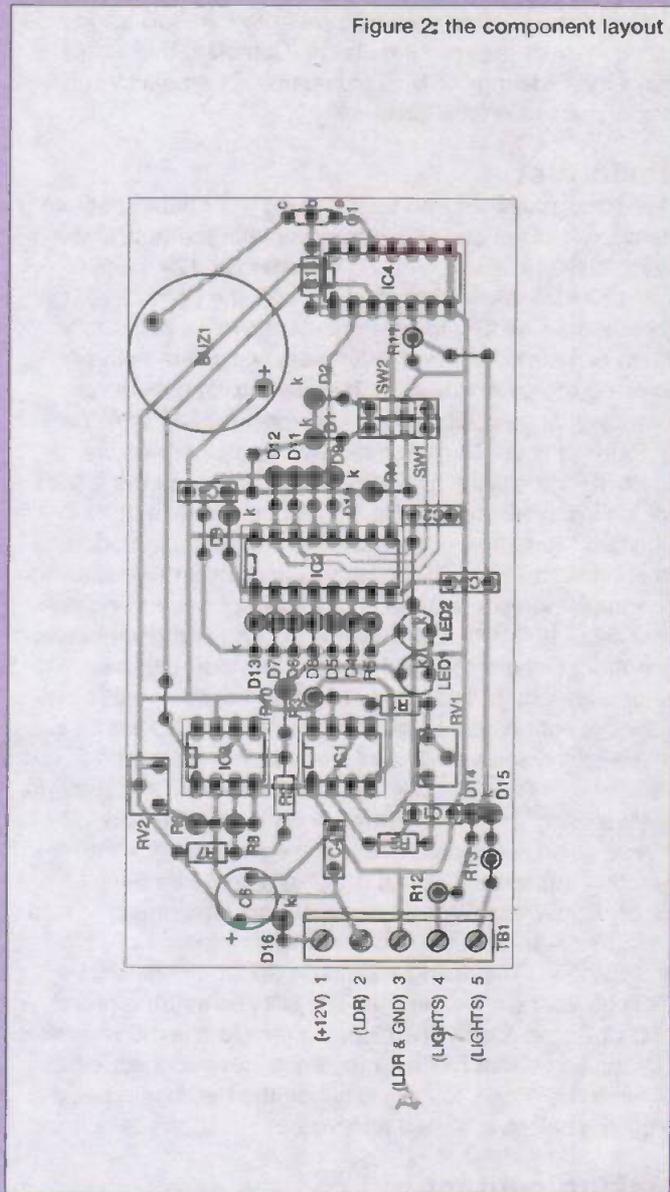
The fourth NOR gate input, pin 5, receives a feed from the headlight circuit. Due to the logistics of wiring this part up later, it will probably be easier to gain access to the main and low-beam circuits separately. If either headlamp filament is on, a high state will pass via either R12 and D14 or R13 and D15. As the dlp switch is moved between the positions in the course of driving, there could be a brief period when neither filament is on. This would present a momentary low input to IC4 and allow the output to go high if there happened to be low states at the other three inputs. To prevent this effect, capacitor C5 will be charged when either filament is on. When the dip-switch changes over, the capacitor will maintain the high state for sufficient time to allow the other filament to take over. With the headlights on, it is therefore impossible for IC4 output to be high. During periods when all IC4 inputs are low (dim light conditions, headlights off and at the times shown by the asterisks in Table 1), pin 1 will go high. Current will then flow into the base of transistor, Q1, with current limited by resistor R14. The buzzer will then bleep.

Q12	Q11	Q10	Q9	Q8	Q7	Q6	Q5	Q4	Q3	Q2	Q1
0	0	0	0	0	0	0	0	0	0	0	0*
0	0	0	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	0	0	0	0	0	0*
0	0	0	0	0	0	0	0	0	0	1	1
0	0	0	0	0	0	0	0	0	1	0	0
0	0	0	0	0	0	0	0	0	1	0	1
1	1	1	1	1	1	1	1	1	0	1	0
1	1	1	1	1	1	1	1	1	0	1	1
1	1	1	1	1	1	1	1	1	1	0	0
1	1	1	1	1	1	1	1	1	1	0	1
1	1	1	1	1	1	1	1	1	1	1	0
1	1	1	1	1	1	1	1	1	1	1	1

### Time settings

Imagine both switches SW1 and SW2 are off for the moment. Counting will continue until pin 1 (Q12) goes high whereupon diode D3 will apply a reset pulse to IC2 pin 11 and a new cycle will begin. This takes some 4 minutes.

Figure 2: the component layout



Switches SW1 and SW2 provide options to reset IC2 when pin 14 (Q10) or pin 15 (Q11) goes high by allowing a reset pulse to flow through diode D1 or D2 as appropriate. The timings will then be about 1 or 2 minutes.

A signal is derived from IC2 pin 6 (Q3 output) for testing and setting-up purposes. This allows current to flow through red light emitting diode LED1 each time it goes high with the current limited by resistor R5. Since this output pulses at one-eighth clock frequency, the LED will flash at about once per second which is easy to monitor. A signal is also obtained at the output of op-amp, IC3. This causes green light-emitting diode LED2 to glow under the "bright" condition. Here, R10 limits the operating current. This LED simplifies the setting-up of the operating light level at the end of construction.

### Construction

The PCB component layout is shown in figure 2. Begin by soldering the four ic sockets in position. Add the 5-section piece of PCB screw terminal block, TB1, the four link wires and the twin dip switches SW1/SW2. Solder the resistors and capacitors. C6 is an electrolytic type - check the polarity. Add the diodes and transistor - there are 16 diodes and care must be taken to observe the orientation

of all of them. Solder the LEDs close to the PCB using minimum heat to prevent damage. Complete the circuit panel by soldering the buzzer in place - the polarity is clearly marked on the body.

### Basic test

The circuit panel may be tested using a 9V battery before installing it in the car. Note, however, that the buzzer will be much quieter than when used with the car 12V system. Insert all the ics into their sockets with the correct orientation. CMOS can be static-sensitive - it helps to touch something earthed (such as a water tap) before handling the pins. Adjust both presets to approximately mid-travel and switch SW1 on (for one-minute timing).

Refer to figure 3. Attach the 9V battery connectors to positions 1 (positive) and 3 (negative) and secure the LDR wires temporarily to terminal block positions 2 and 3. Connect the battery - the buzzer may make a sound. Check that the red LED is flashing and adjust RV1 until it operates once per second. This does not need to be done very accurate but a timing of one second will result in the operating times mentioned earlier. Note also that this setting also alters the buzzer on time. Providing there is sufficient light reaching the LDR, the green LED will be on. If necessary, adjust RV2 until it is. Place a piece of Plasticine or Blu-Tak over the LDR window so that the LED switches off. The buzzer should now sound with double bleeps given every one minute or so (with a clock rate of exactly 1 pulse per second it would actually be 64 seconds). Switch SW1 off and SW2 on - the timing should now double to about 2 minutes. Finally, switch both SW1 and SW2 off. The timing should be about 4 minutes.

Remove the covering from the LDR so that the green LED operates again. The bleeping should stop. Cover the LDR again so that bleeping resumes. Inter-connect either of terminals 4 or 5 to 1 (to simulate the headlights being on) - the bleeping should again stop.

### Making contact

Before mounting the circuit panel inside the box, drill two holes in the side close to the terminal block position. These should be large enough to accommodate all six wires passing through (terminal 3 carries two wires). If the circuit panel is a tight fit inside the box, no mounting holes are needed. If you need to file off corners so that it will go inside, do this after having removed the ics. Also, take care not to damage any tracks on the underside. To prevent vibration, place thin pieces of plastic foam on the base of the box and above the circuit panel avoiding the area of the buzzer. Measure the position of the hole in the buzzer and drill a hole in the lid to correspond with it so that the sound will pass through.

Prepare the sensor box by drilling a hole of sufficient size in the lid for a large grommet through which the LDR will be secured (see photograph). Alternatively, a tight hole could be made in the plastic and the LDR held in position using a little quick-setting epoxy resin adhesive. If this method is used, the window of the LDR should be made level with the face of the case. Mount a piece of tag strip on the lid inside and solder the LDR wires to this. If there is any chance of them touching, sleeve them using short pieces of insulation stripped from connecting wire. Drill a hole for the twin wire which will lead from the light sensor to the main unit. Measure sufficient wire to interconnect the

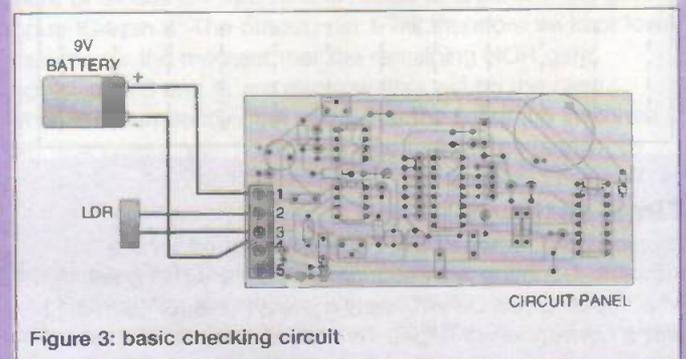
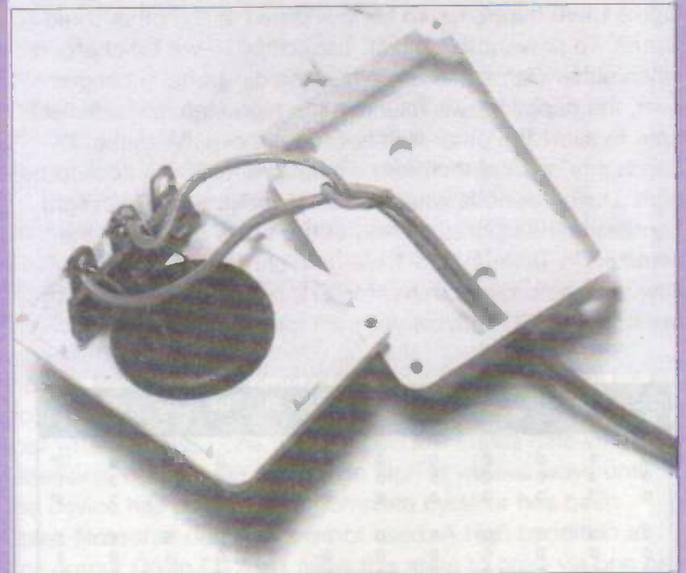
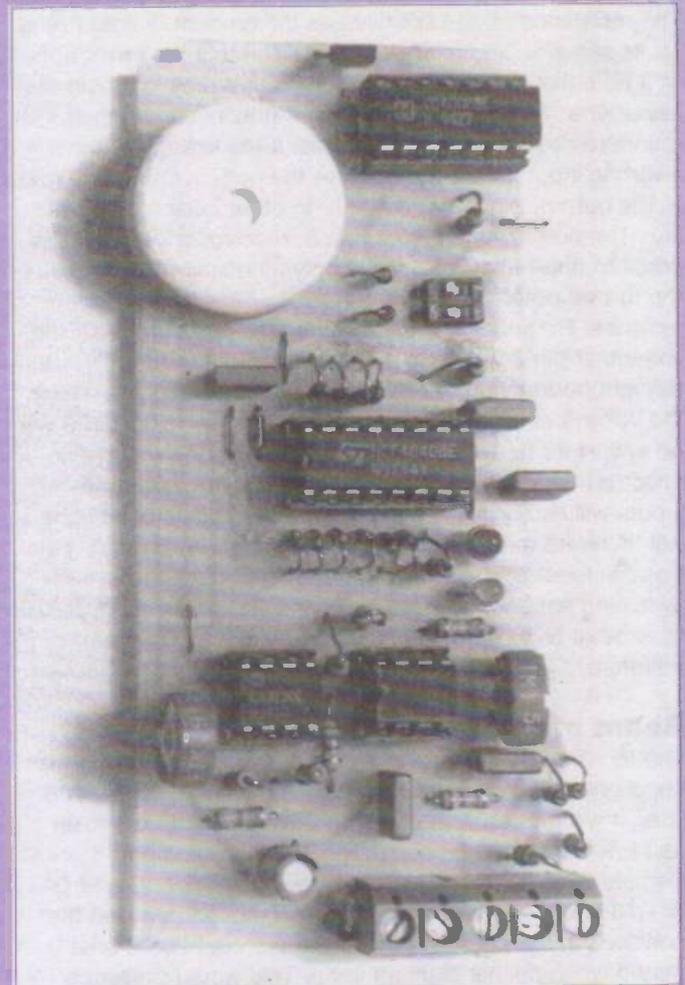


Figure 3: basic checking circuit

## Resistors

R1, R2	330k
R3	5k6
R4, R11	1M
R5, R10	1k
R6	220R
R7, R8, R12, R13	47k
R9	2M2
RV1	1M
RV2	10k
LDR1	ORP12 light dependent resistor

## Capacitors:

C1, C4, C5	100n min. metallised polyester, 5mm pin spacing
C2, C3	47n min. metallised polyester, 5mm pin spacing
C6	22u 16V radial electrolytic

## Semiconductors:

D1-D15	1N4148
D16	1N4001
LED1	3mm red LED
LED2	3mm green LED
Q1	ZTX300
IC1	7555 low-power timer
IC2	4040 12-stage counter
IC3	CA3130 op-amp
IC4	4002 dual quad NOR gate

## Miscellaneous

SW1, SW2	Dual dil SPST switches
BUZ1	PCB mounting buzzer 12V 5mA dc operation.
TB1	5-way PCB mounting screw terminal block - 5mm spacing.

2 off 8-pin dil sockets, 14-pin dil socket, 16-pin dil socket. Tag strip. Plastic box for circuit panel, small plastic box for light sensor. 9V battery and connectors for testing. Auto-type wire and snap-lock connectors as required.

two sections. Light-duty twin wire such as loudspeaker type would be suitable. Pass one end through the hole and, leaving some slack, tie a knot in it to provide strain relief. Solder the wires to the LDR tags. Route the wire back to the main unit position but do not connect it up yet.

## Car connections

Check how you will gain access to the connections which need to be made to the car wiring. One must be made to a wire which is live only when the ignition is switched on. It is essential that this is on the outlet side of a low-value fuse. A ground (earth) connection (which may usually be made to an existing earth point) and connections to the wires leading to both filaments of one of the headlights. In some older cars, the headlights are not fused. If this is the case, include line fuse holders in each wire close to the connections and insert a low-value (say 3A) fuse in each. Connections to existing wires may be conveniently made using "snap lock" connectors which avoids having to break wires.

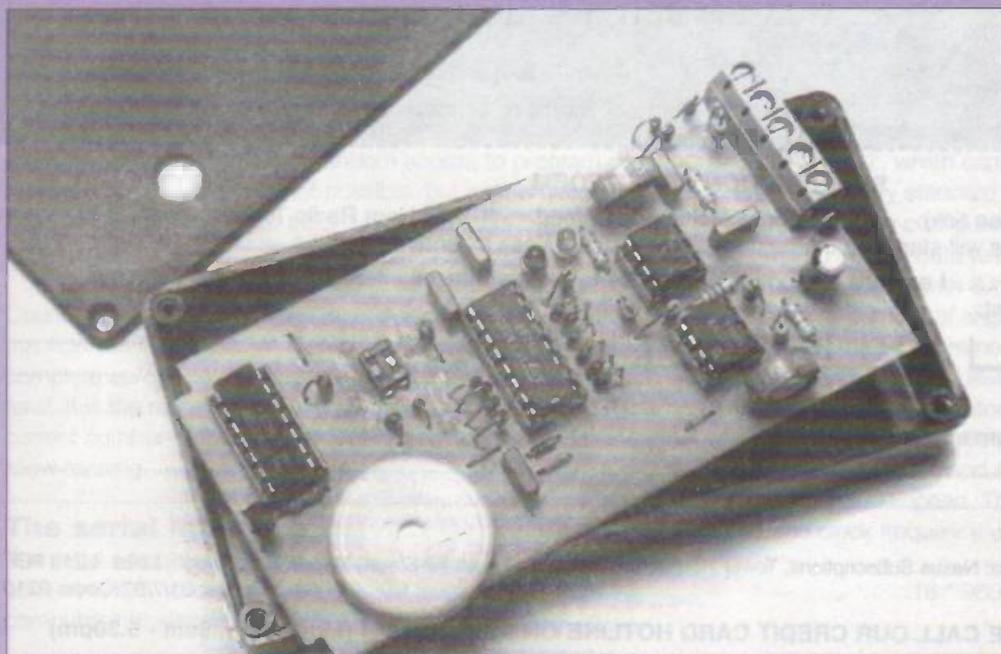
**This additional wiring must of proper automotive type and a rubber grommet must be used wherever it needs to pass through a hole in metal.**

Run the wires back to the unit and connect them to the terminal block in the following way: Terminal 1 to the +12V feed, Terminal 2 to one sensor wire, Terminal 3 to the earth connection and the other sensor wire, Terminals 4 and 5 to the lighting circuits. Attach the lid on the main unit box and secure it in position.

Assemble the sensor unit and decide on a suitable location for it. Some experimentation will be needed to find the best one. Make checks in lighting conditions rather brighter than the required switching point. If the sensor is placed so that direct light falls on its sensitive surface, such as in the corner of the windscreen, it will be found that RV2 needs to be adjusted almost to the zero resistance end of its track. If such adjustment is difficult, it will be found helpful to restrict the amount of light reaching the LDR by, for example, using translucent plastic or by partially covering it with PVC tape. If no rubber grommet was used, the sensor box could be stuck to window glass using two self-adhesive pads of the type normally used to attach the interior mirror, making sure, of course, that it is clear of your

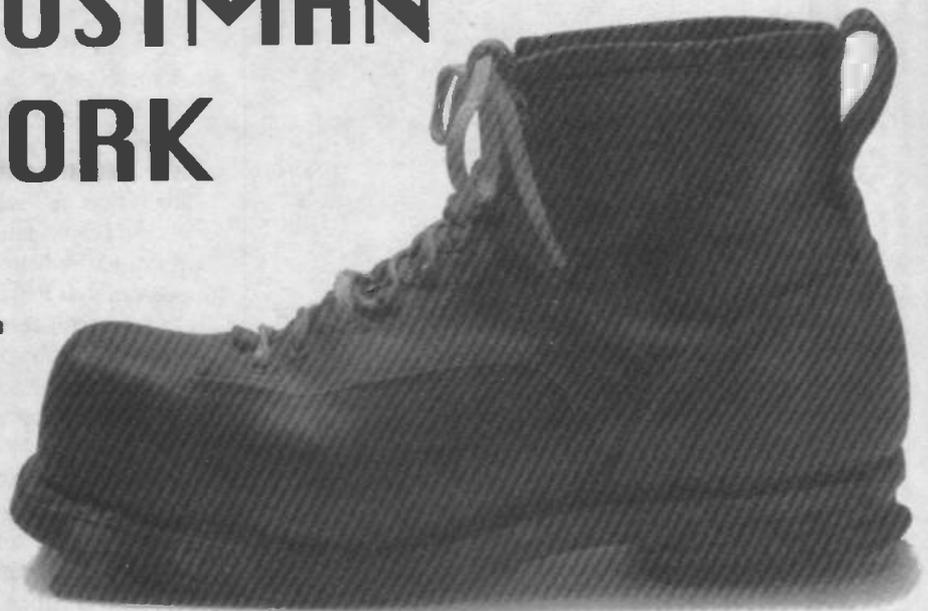
field of view. Self-adhesive pads are readily available from car accessory shops. In the prototype unit, the sensor was placed behind a grill so that it could not be seen. This automatically restricts the light reaching it and makes for easy adjustment to RV2. Do not place it where shadows will be cast on the sensitive surface by items inside the car since this will cause false operation.

Test the unit under actual conditions. Re-adjust the timing if necessary. Over a period of use, set RV2 to provide a warning at the required brightness level and set the operating time as required.



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# "Eprommer"

## Eprom Programmer

### with Personality Modules

*Keith Wardill's versatile Eprom Programmer uses a PC serial port and a set of six "Personality Modules" to connect a wide range of eproms up to 64KB.*

**T**his Eprom Programmer connects to a computer serial port, and will program and read most eproms up to 64 kilobytes now available. To avoid complex switching to cope with different eprom configurations in use, the Programmer uses a plug-in 'Personality Module' for each type of eprom. This module makes the correct pin connections, sets the programming voltage (Vpp), and determines the program pulse duration.

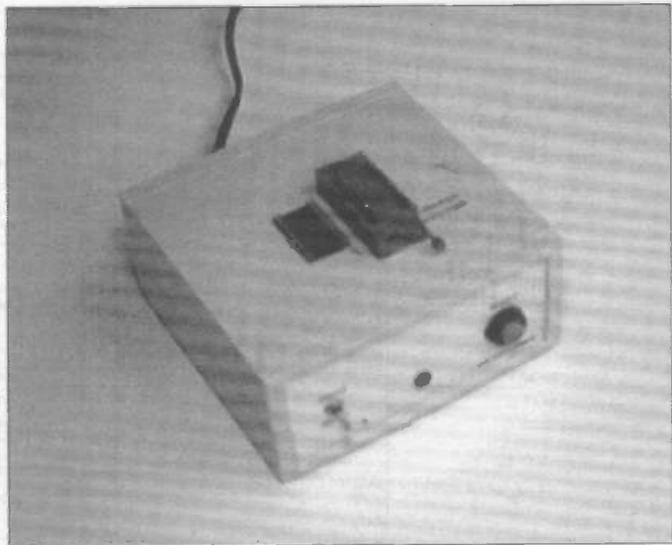
#### Operating protocol

The Programmer is designed to answer with a data byte when it receives a data byte. In PROGRAM mode, the data to be programmed to a particular address is sent to the Programmer, written to the eprom, and then the Programmer will read the programmed data back and send it to the host computer. By doing this, the software on the host can check if the eprom is correctly programmed before carrying on. In READ mode, a dummy byte is sent to the Programmer, which prompts the Programmer to read a byte of eprom data and transmit it to the host computer. Note that random access to program or read specific addresses is not possible, but as it is usual to program or read the entire eprom in one go, this is not really a handicap.

After PROGRAMming or READing, the eprom Address Counter is incremented. If the reset switch is pressed to zero the Address Counter, then by repeatedly sending data the complete eprom can be stepped through and programmed or read. It is the responsibility of the host computer to send the correct number of programming bytes, or dummy bytes to allow reading.

#### The serial interface

The serial computer interface (figure 1) uses a MAX232 RS232 converter, IC1, to convert serial RS232 levels to and from the computer into standard TTL for the Programmer.



The Transmit and Receive Data signals are connected to a 6402 UART, IC2, which carries out serial\parallel conversion. This is an industry standard device, and is easy to use because it can be configured by connections to its pins, rather than writing control data to it from a CPU. In this application, it is set up to operate with 8 bits, no parity, and 2 stop bits. This ic also has a number of status and handshake signals, which are used by the Programmer as described later.

The Transmit and Receive clocks are generated by the 2.4576MHz crystal oscillator IC3C/D. Dual binary divider IC4 divides the output to produce four clock outputs. The UART has an internal divider, and requires a clock which is 16 times the required Baud speed. Thus, if it is to operate at 9600 Baud, the clock frequency will be:

$$16 * 9600 = 153600 \text{ Hz.}$$

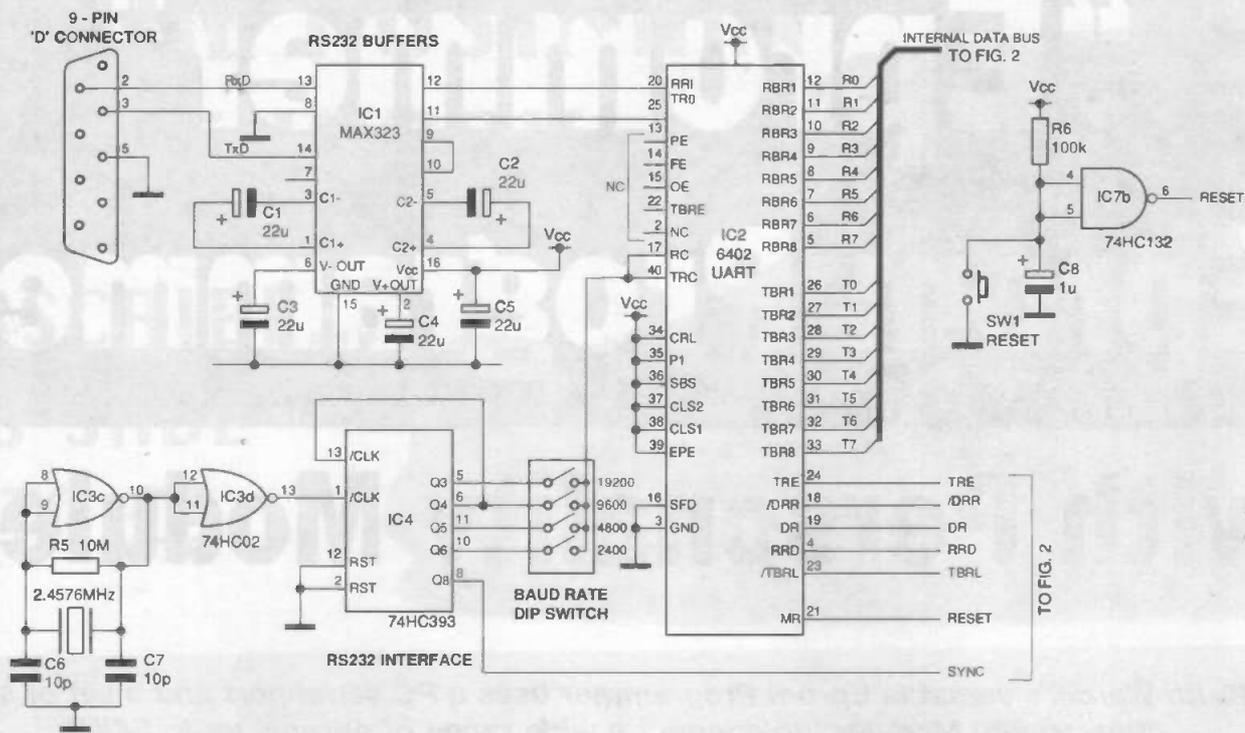


Figure 1: the Eprom Programmer serial interface circuit

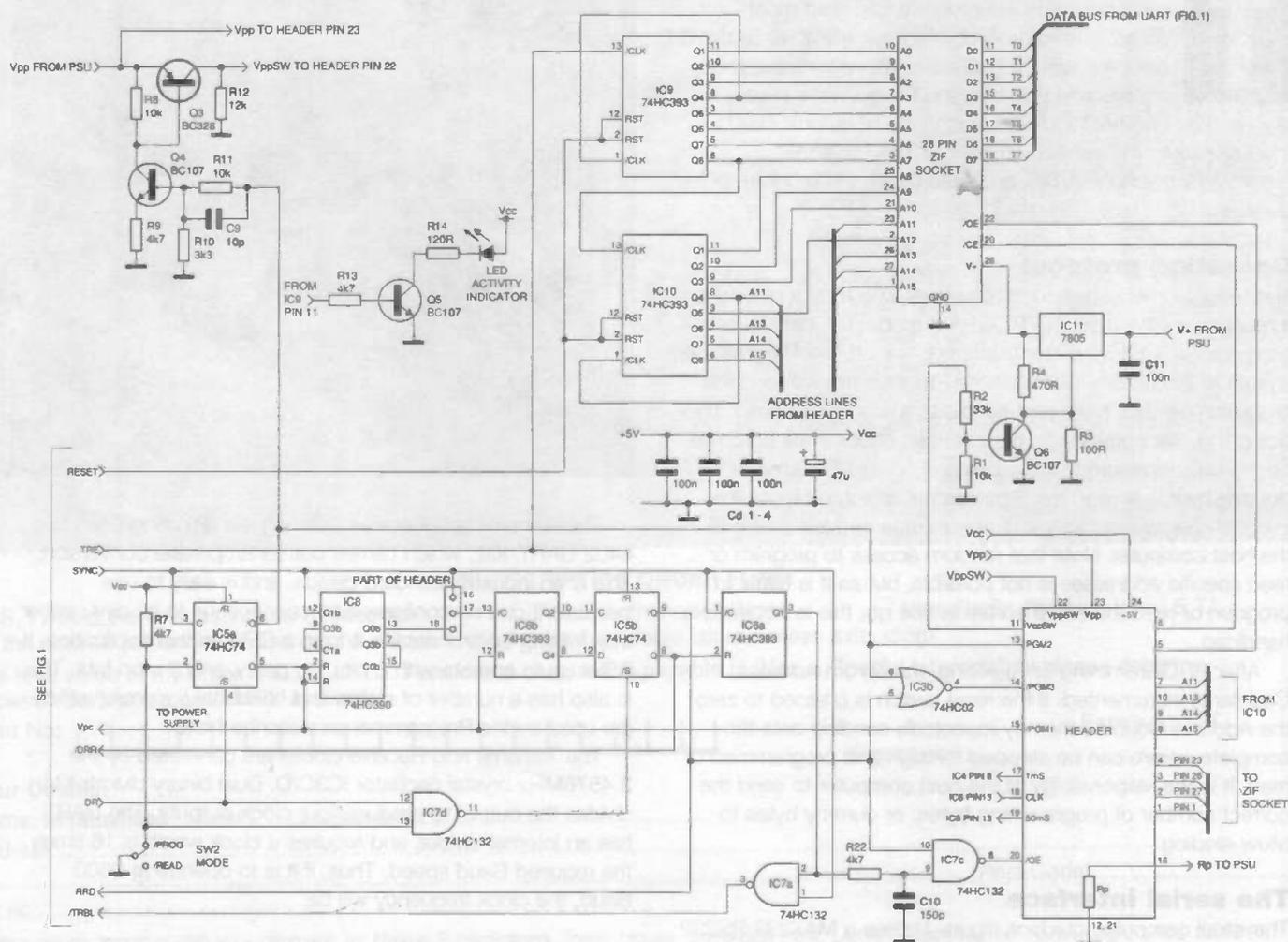


Figure 2: the Eprom Programmer control circuit

The divided outputs available correspond to Baud rates of 2400, 4800, 9600 and 19200, selected by a dip switch. Normally, the highest speed can be used, unless a long cable is used to connect the Programmer to the computer. Note that no RS232 handshakes, software or hardware are used, so the cable for the computer connection need only be a simple three core type, carrying Tx and Rx Data, and Ground.

The divider IC4 also provides a sync signal of 9600 Hz to the Programmer Control.

The Reset Circuit around NAND gate IC7B provides a switch-on reset, by means of C8, and a manual reset, with SW1. This resets the eprom address counter, as well as the UART.

### Programmer control

In the Programmer Control section (figure 2), many of the pins, such as the Data pins D0-D7, and Address pins A0 to A10 and A12, are the same on most eproms. Unfortunately as eproms have increased in size over the years, manufacturers have been forced to use other pins on the eproms for different purposes. Table 1 shows the pin functions of the 27XX series, which differ from eprom to eprom: these are pins 1, 20, 22, 23, 26 and 27. The appropriate connections to these pins are made via the Personality Module.

### PROGRAM mode

(In the following, note that /Q means 'not Q'.) To program most eproms, a programming voltage (Vpp) is applied to the appropriate pin, then a program pulse, usually negative going of 1 millisecond duration is applied. The exceptions are the 2716, which requires a positive going 50ms pulse, and the 2732, which requires a negative going 50ms pulse.

IC5A, IC5B and IC6B generate the program pulse. Until data is received by the UART, its DR (Data Ready) output on pin 19 is low, keeping D-Type flip-flop IC5A in its 'set' state, so its Q output is high, and the /Q is low. The high Q output keeps IC6B in its 'reset' state. It also maintains TR1 in the Power Supply in the 'on' state. If TR1 is on, then R17A and B are effectively short-circuited, and the Vpp output of the PSU is held at +5V by R15/R16. (It also keeps IC8 reset, but more about this later.) Let us assume that a connection exists between pins 16 and 17 of the Personality Module, so the 9600 Hz sync signal is applied to pin 13 of IC6B.

Before any WRITE operation, the reset button must be pressed to set the address counters to zero. The 'D' input of IC5A (pin 2) is pulled low when switched to 'PROG'. When a data byte is received, DR goes high, and the next positive edge of the sync signal at IC5A clock input will set the Q output of IC5A low, releasing the 'reset' on IC6B (and IC8). Q1 in the PSU is switched off, so Vpp rises to a level determined by R17A and B, and Rp on the Personality Module.

Counter IC6B will now count negative going edges of the sync signal. After 8 edges, the Q4 output of IC6B goes high, and on the 10th edge, Q2 also goes high (reference to the Timing Diagram, figure 3, will make this clear). This causes the /Q output on pin 8 of IC5B to go low, and the Q output on pin 9 to go high. The /Q output is applied to the DRR (Data Ready Reset) input on pin 18 of the UART. This resets DR, allowing the UART to accept a new data byte. Since DR has gone low, IC5A will be forced into its 'set' state, and the circuit will now be in its original state, with Vpp switched back to +5V.

Table 1: Eprom pin functions

EPROM	1		20		22		23		26		27	
	R	W	R	W	R	W	R	W	R	W	R	W
2716	X		0 - [1]		0 - 5V		5 - 25Vpp		5 - 5			X
2732A	X		0 - [2]		0 - 21Vpp		A11		5 - 5			X
2764		5 - 21Vpp	0 - 0		0 - 5V		A11		X			+5 - [3]
2764A		5 - 12.5Vpp	0 - 0		0 - 5V		A11		X			+5 - [3]
27128		5 - 21Vpp	0 - 0		0 - 5V		A11		A13			+5 - [3]
27128A		5 - 12.5Vpp	0 - 0		0 - 5V		A11		A13			+5 - [3]
27256		5 - 12.5Vpp	0 - [3]		0 - 5V		A11		A13			A14
27256A		5 - 12.5Vpp	0 - [3]		0 - 5V		A11		A13			A14
27512		A15	0 - [3]		0 - 12.5Vpp		A11		A13			A14

[1] POSITIVE GOING TTL 50ms +/-10%

[2] NEGATIVE GOING TTL 50ms +/-10%

[3] NEGATIVE GOING TTL 1ms +/-5%

X DONT CARE - NOT CONNECTED

NOTE THAT 2716 AND 2732 ARE 24 PIN DEVICES. THE PIN NUMBERS GIVEN ASSUME THAT A 28 PIN SOCKET IS IN USE, AND THE EPROM IS FITTED SO THAT DEVICE PIN 1 IS IN SOCKET PIN 3. HENCE PINS 1,2,27, AND 28 ARE NOT USED

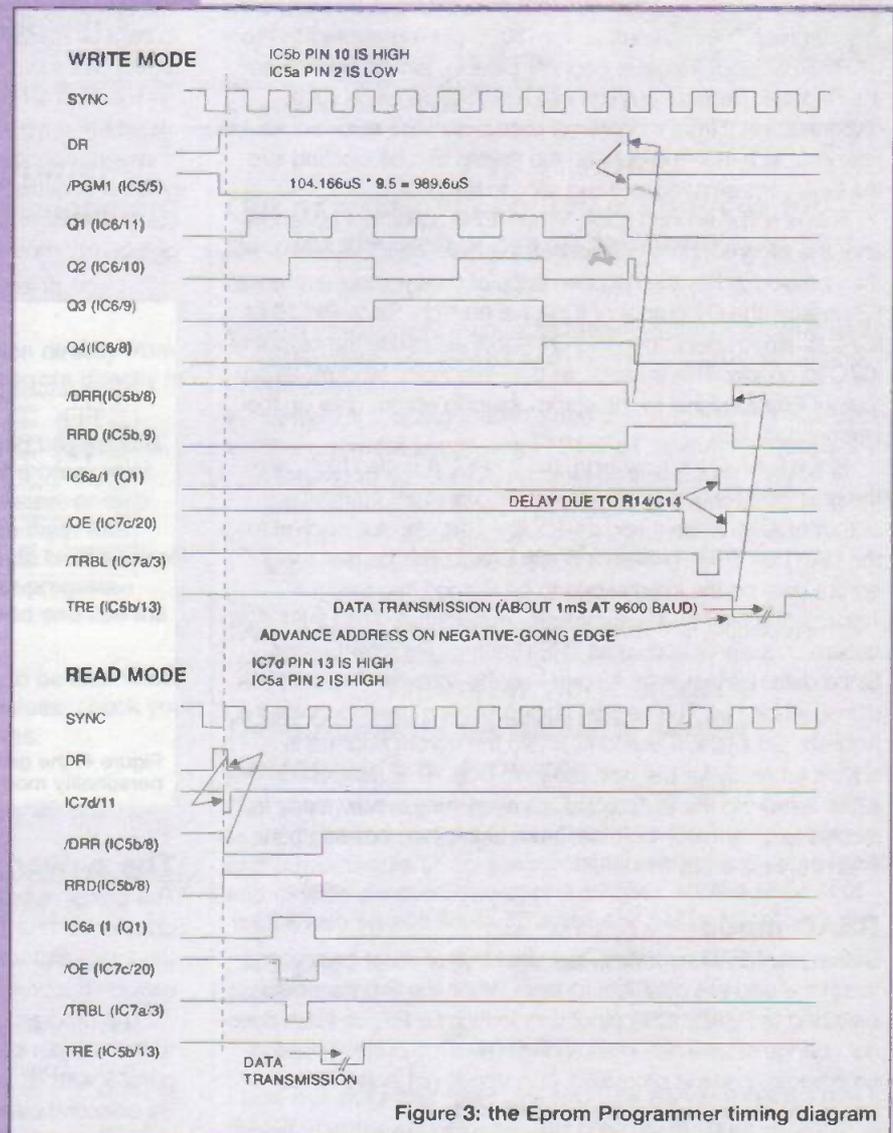


Figure 3: the Eprom Programmer timing diagram

The timing diagram in figure 3 will show that after IC5A changed state, IC6B effectively counted  $9\frac{1}{2}$  periods of the sync signal, then IC5A reverted to its original state. Since the period of the sync signal is  $1/9600$  seconds, or slightly more than 104us, then dividing this by 9.5 gives a time interval of slightly more than 989 us. We need 1 ms for the program pulse, but the eeprom tolerance is 5 percent, so 989 us is sufficient. Hence a negative going program pulse is produced at pin 5 of IC5A, connected to the Personality Module as PGM1. A positive going pulse appears also at IC5A pin 6, and is connected as PGM2. This is also inverted and gated by IC3B, to produce a negative going PGM3. This is used for 2732 and 27512 eeproms.

To produce the 50 ms pulse required for the 2716/2732 eeproms, the sync signal is divided by 50 by IC8, connected to divide by 25 and 2, producing a sync/50 clock with a period just over 5.2ms. Now the program pulse period will be  $9.5 \times 5.2 = 49.48$ ms. To select the appropriate period for the programming pulse, both the sync signal and sync/50 are fed to pins 16 and 18 respectively of the Personality Module. Depending on which eeprom is to be programmed, the appropriate connection to Personality Module pin 17 is made, thence to IC6B pin 13. If you intend never to program 2716 or 2732 eeproms, then IC8 can be omitted, and a permanent link made between pins 16 and 17 of the Personality Module.

Several other signals are also derived from the control circuit: After the programming pulse has finished, the Programmer must transmit the programmed data back to the host computer as a check, and to inform it that the next data byte can be sent to the Programmer. The high output from IC5B pin 9 is applied to the UART RRD input (Receive Register Disable, pin 4). This forces the Receiver Register outputs into a high impedance state, disconnecting it from the internal data bus. If the eeprom output is now enabled, then the data on the eeprom can be clocked into the UART transmit register and sent to the host.

This is achieved with IC6A. When IC5B /Q output goes low after the program pulse, it releases the reset on IC6A, which will then be clocked by the negative edge of the sync pulse. Hence 52 us later, the Q1 output of IC6A will go high. Since Pin 10 of IC7C is already high, then this Q1 signal will cause the output of IC7C to go low. This is used, via the Personality Module, as an Output Enable signal to the eeprom, placing eeprom data on the internal data bus.

Since IC6A Q1 is now high, pin 2 of IC7A is also high, and the next positive half-cycle of the sync signal will cause the output of IC7A to be a negative pulse. This signal is applied to the UART as TRBL (Transmit Buffer Load). This causes the eeprom data on the internal bus to be latched into the UART Transmit Register and automatically transmitted. The UART also generates a signal known as TRE (Transmit Register Empty). Since data has just been loaded into the Transmit Register, this signal will be low. The negative going edge is used to clock the Address Counters, IC9 and IC10, so the eeprom address is advanced ready for the next program byte. TRE also resets IC5B, removing the RRD signal, so everything is now ready to receive another byte. TRE will return high when the data has been transmitted by the UART.

## READ mode

Before any READ operation, the reset button must be pressed to set the address counters to zero. When the Programmer is switched to READ, IC5A pin 2 is held high by R7, so IC5A does not change state when clocked with the sync pulse. Therefore no program pulse is produced, and Vpp is not switched on, hence the eeprom will not be programmed.

IC7D pin 13 is also pulled high by R7. When a data byte is received the DR signal from the UART is applied to IC7 pin 12. It is inverted and 'sets' IC5B, producing the DRR, RRD and OE signals as described for programming. This will place the eeprom data on the internal bus, and transmit it back to the computer as described above.

To provide some indication of what is happening, an LED is driven by one of the outputs of the address counter, to indicate that the counter is incrementing during programming and reading.

## The personality modules

The 'personality modules' (figure 4) consist of small double-sided PCBs which plug into a 24-pin DIL socket, and make the appropriate connections for a particular eeprom. It also carries Rp, the programming resistor for the programming voltage. The PCB layouts for the standard 27XX range are given: obviously if you don't need all of them, just make the one(s) you require. Table 2 shows the connections to be made on the Personality Modules for most of the standard eeproms. Note that some eeproms are available with different programming voltages, notably the 2764, which is available with 21V or 12.5V programming voltages. Usually those types with an 'A' suffix are 12.5V types. Make sure that you fit the correct values for Rp to provide the appropriate programming voltage.

It is not essential to use the personality module PCBs as given: I originally used IC sockets, soldering small wire links on the top side of the socket to make the connections. This turned out to be a bit messy and fragile, so I opted for the PCBs. Maybe the reader has a better idea.

The pins for the PCBs were made from standard office staples (the type used to staple paper together). Any PCB 'through' connections are made with short wire links. The general construction is shown in Figure 4. After making all the connections, and ensuring that Rp was the correct value, the personality modules were given a coat of protective varnish.

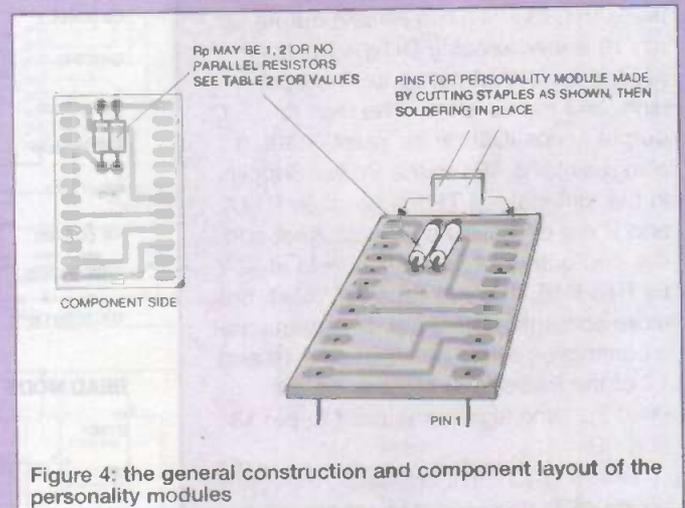


Figure 4: the general construction and component layout of the personality modules

## The power supply

The power supply (figure 5) uses a simple voltage doubler/rectifier to produce a stabilised +5V supply via IC12 for the logic, and a higher voltage, which is regulated by IC13 to provide the programming voltage, Vpp.

The programming voltage is determined by the ratio of R15 to the resistor chain R16 and the value of R17A and B in parallel with Rp, which is mounted on the personality module. By selecting different values for Rp, then Vpp can be set as

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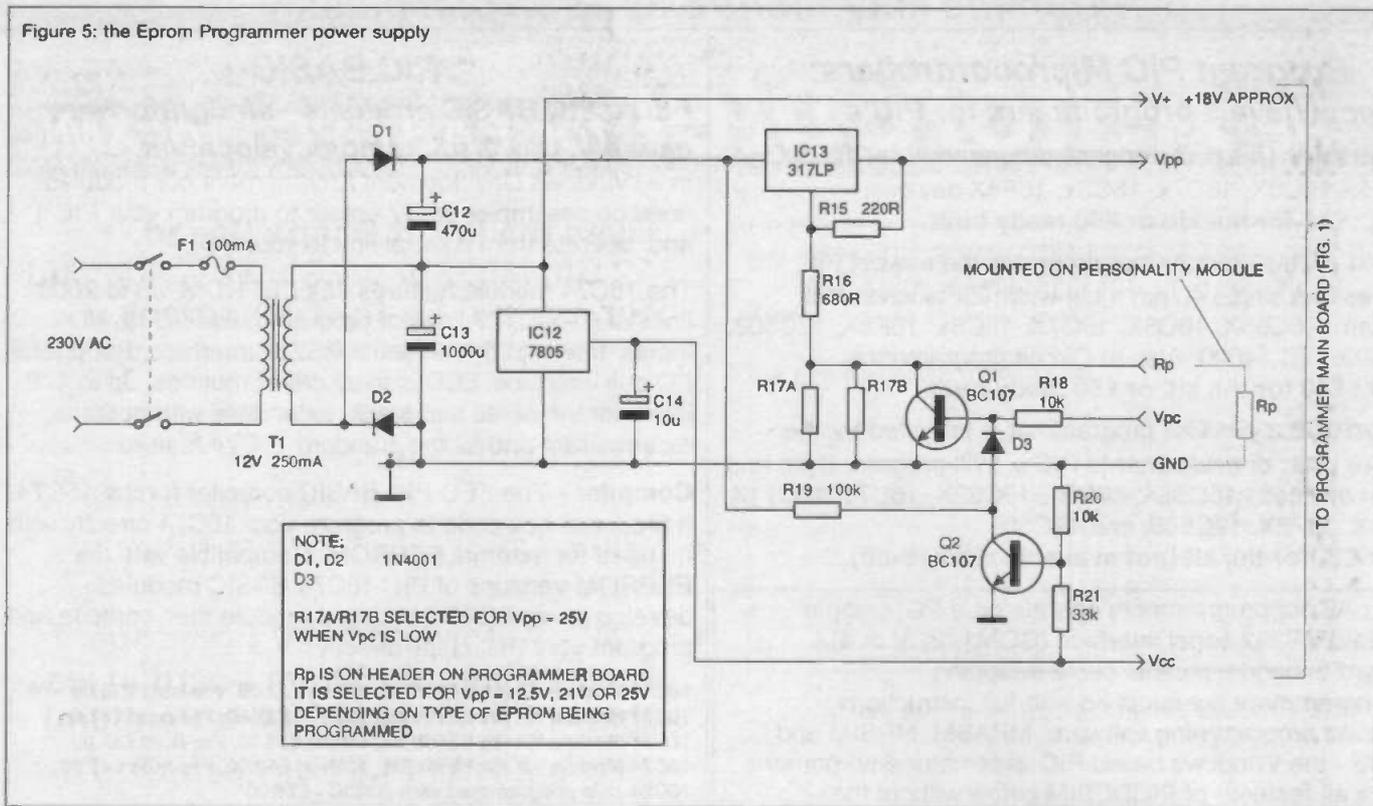
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Figure 5: the Eprom Programmer power supply



required for a particular eprom. Resistor values for the common programming voltages of +12.5, +21V and +25V are given in Table 2. Use 1 percent or 2 percent resistors.

Unfortunately, because of the different functions of various pins as mentioned earlier, the programming voltage must be switched between Vpp when programming, to +5V when reading. (Just to make life difficult, the 2732 and 27512 are different) This is done with IC13 and Q1. If Q1 is on, then R17A and B and Rp are effectively short-circuited and the output of IC13 is determined by the ratio of R15 to R16. (+5V). The Programming Pulse is applied to the base of Q1. When high (in the Read Mode), Q1 is on, and the output of IC13 is +5V. When the Program Pulse goes low (Program Mode), Q1 is off, so the output voltage of IC2 is determined by Rp.

A further complication is caused by the differing requirements of the Output Enable (pin 22 of the eprom) during reading and programming. Due to lack of pins, the programming voltage is applied to the OE pin of the 27512, so it must be high during programming, and 0V to read. The other eproms only require logic 0 or 1. To do this, Vpp is switched by Q3 and Q4. When the level from IC5A pin 6 is high (programming), then Q4 is switched on, pulling the base of TR3 low, and switching it on to apply Vpp to the Personality Module, and thence to the appropriate eprom pin.

A final power problem is that Vcc to 27256 and 27512 eproms, normally +5V, must be increased to +6.2V during programming. This is done with IC11 and Q6. The negative going 1 ms programming pulse on pin 15 of the Personality Modules for these eproms is connected to pin 11, so that Q4 is switched off during programming, and the output from IC11 will rise to 6.2 volts, determined by R3/R4. When Q6 is switched on, during Read Mode, then R3 is effectively short-circuited, and the output of IC11 is +5V. For those eproms which normally only require +5V during both Read and Program modes, then the connection to Q6, via the Personality Module pin 11, is taken to +5V, ensuring IC11 output is always at +5V.

Table 2: pin connections for Personality Modules. Note: "3K3//5K6" means 3k3 resistor in parallel with 5k6 resistor. Some Eproms are available with different program voltages. The value of Rp connected between pins 12 and 16 should be selected accordingly where both values are given. The values of R17A and B on the PSU are 10K//5K6

EPROM 2716		
PIN	PIN	FUNCTION
3	→ 24	+5V Vcc
4	→ 23	Vpp (+25V)
5	→ 20	OE
6	→ 13	CE & PGM PULSE
18	→ 19	50mS PULSE
Rp	—	Vpp SET BY R17A & B

EPROM 2732		
PIN	PIN	FUNCTION
3	→ 24	+5V Vcc
4	→ 7	A11
5	→ 22	Vpp (+21V)
6	→ 14	CE & PGM PULSE
18	→ 19	50mS PULSE
12	→ 16	39k // 22k

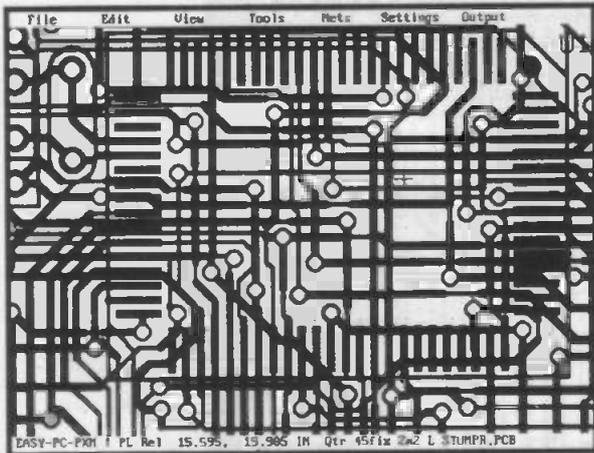
EPROM 2764		
PIN	PIN	FUNCTION
1	→ 23	Vpp
2	→ 15	PGM PULSE
4	→ 7	A11
5	→ 20	OE
6	→ 12	CE
11	→ 24	+5V Vcc
17	→ 18	1mS PULSE
12	Rp 16	39k // 22k (21V TYPE)
12	Rp 16	3k3 // 5k6 (12.5V TYPE)

EPROM 27128		
PIN	PIN	FUNCTION
1	→ 23	Vpp (+12.5V)
2	→ 15	PGM PULSE
3	→ 10	A13
4	→ 7	A11
5	→ 20	OE
6	→ 12	CE
11	→ 24	+5V Vcc
17	→ 18	1mS PULSE
12	Rp 16	3k3 // 5k6

EPROM 27256		
PIN	PIN	FUNCTION
1	→ 23	Vpp (+12.5V)
2	→ 9	A14
3	→ 10	A13
4	→ 7	A11
5	→ 20	OE
6	→ 14	PGM PULSE
11	→ 15	+6.2V/+5V Vcc
17	→ 18	1mS PULSE
12	Rp 16	3k3 // 5k6

EPROM 27512		
PIN	PIN	FUNCTION
1	→ 8	A15
2	→ 9	A14
3	→ 10	A13
4	→ 7	A11
5	→ 22	OE/ Vpp 9+12.5V)
6	→ 14	PGM PULSE
11	→ 15	+6.2V/+5V Vcc
17	→ 18	1mS PULSE
12	Rp 16	3k3 // 5k6

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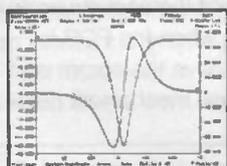
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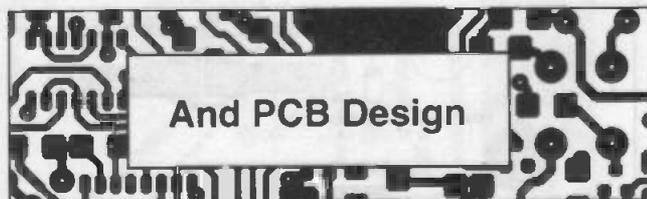
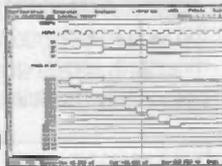
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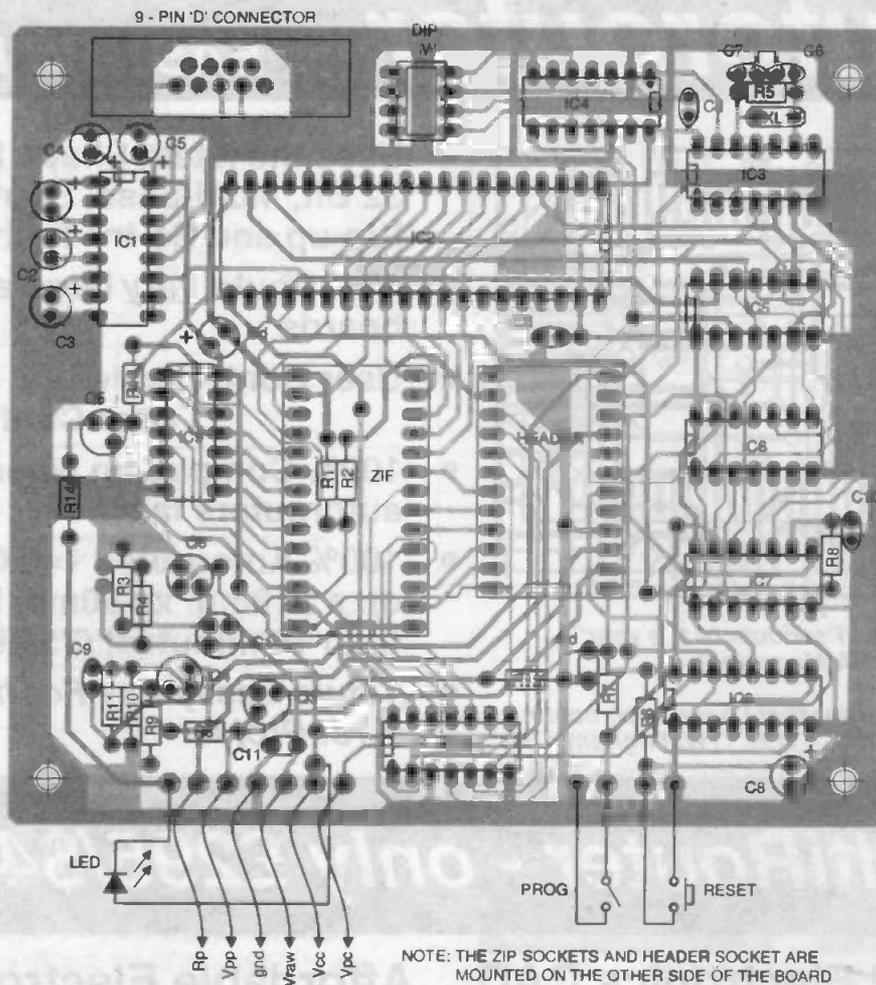


Figure 6: the component layout of the main board

### Mechanical construction

The main PCB layout is double-sided (figure 6): this normally requires through-hole plating to connect one side of the board to the other. This is virtually impossible to do if you make the board yourself (as I do), and the board expensive to have made. To get round this, some of the 'through' connections are made by soldering the IC sockets on both sides of the PCB. I strongly recommend that good quality turned-pin IC sockets are used. These stand slightly clear of the PCB when fitted, and it is possible, with a steady hand, to solder to pads on the component side of the board under the sockets, and on the 'copper' side of the board, creating a through connection. Figure 7 shows how this may be done. Be warned: a steady hand, good eye, and small, hot soldering iron is essential for this. It is possible to do without the IC sockets, and solder directly to the pins of the ICs, but this is not recommended because testing and replacement is difficult (or impossible).

There are also a number of through connections made by wire links soldered on both sides of the PCB, and also a number of the resistors and capacitors are soldered on both sides. The through links are shown on figure 8, the component layout, by an 'X'. Fit all these links first, because some are under IC sockets, followed by the other components, the IC sockets, and the crystal. A small blob of glue gives added security for the crystal. Check carefully to see which components require soldering on both sides of the PCB.

The EPROM socket should ideally be a zero insertion force (ZIF) type, although a normal good quality IC socket can be used if you don't anticipate a lot of use. This socket, and also the 24 pin socket for the personality module, are fitted on the 'copper' side of the board. This is so that the sockets protrude through matching holes cut in the case when the PCB is mounted, allowing you to insert and remove the EPROM and personality module. Figure 8 shows these mechanical details.

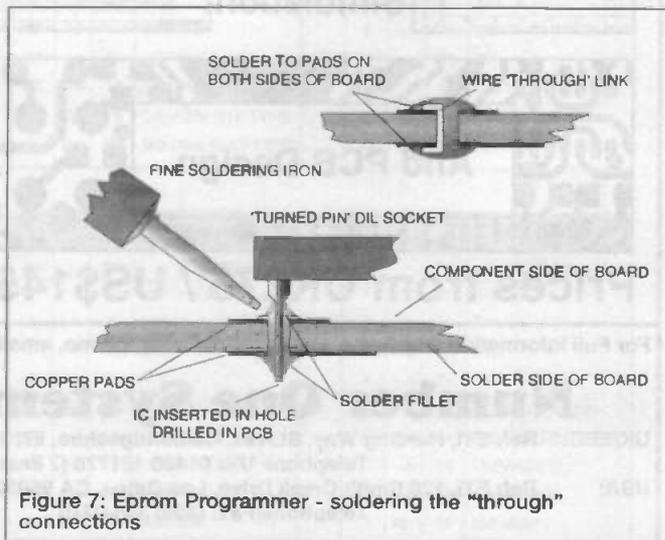


Figure 7: Eprom Programmer - soldering the "through" connections

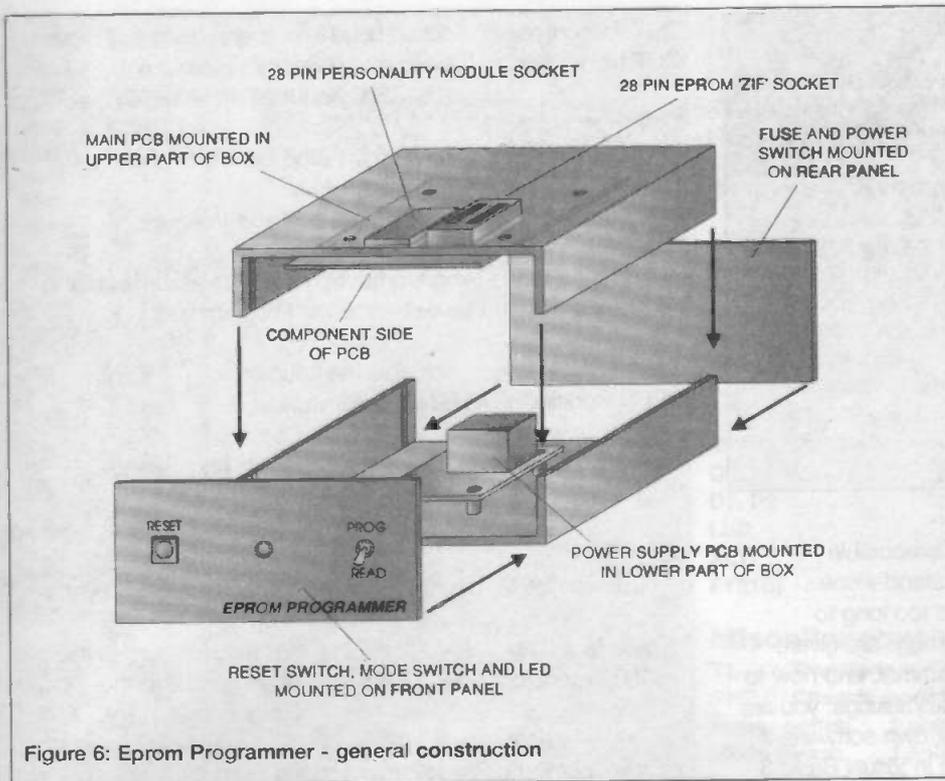


Figure 6: Eprom Programmer - general construction

Once again, some care is necessary to fit these sockets because they require soldering on both sides.

The PSU presents no special difficulties: it is a single-sided board (figure 9), and no through connections are required. Observe the polarities of the diodes and electrolytic capacitors when fitting them. Check also that no 'whiskers' of copper remain around the connections to cause short circuits. It is wise to check operation of the PSU before connecting to the main board. See under 'Testing'

It is difficult to give mechanical design instructions for the box, since not everyone can lay hands on the same box, or has the same skills. However, Figure 8 shows how the original was mounted in its box. Holes were cut in the appropriate place in the top of the box, and the PCB mounted with plastic spacers so the ZIF socket protruded through the hole, and the 9 pole D-type RS232 connector protruded through a hole cut in the rear panel. If you have problems finding plastic spacers, short pieces cut from a plastic ballpoint pen barrel are ideal.

The Reset Switch, Mode Switch and Mode LED are mounted on the front panel, connected to the PCB with short

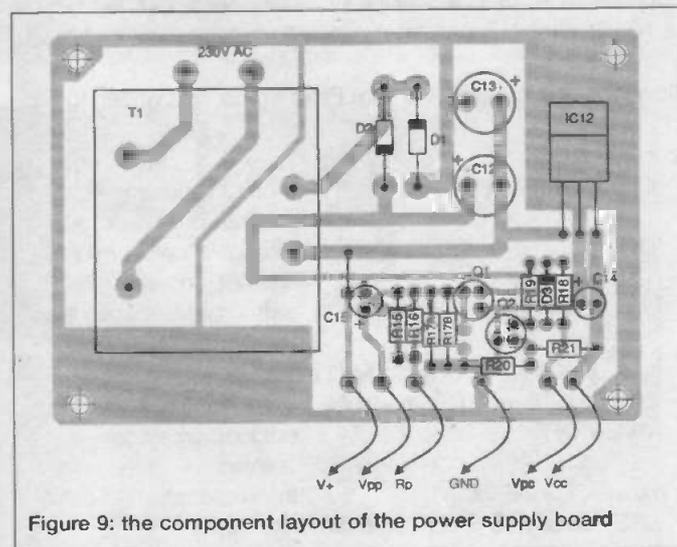


Figure 9: the component layout of the power supply board

lengths of multicore wire. I used a piece of flat cable as this keeps things tidy. The power switch and fuse are mounted on the rear panel, and the power cable is routed through a notch cut in the edge of the rear panel.

The PSU was bolted into the base of the box, again using short M3 bolts and plastic spacers.

### Testing

Connect 230V AC to the PSU, and switch on, taking great care not to touch the high voltage points - **230V AC is lethal! This is a mains construction - if you do not have mains construction experience, seek the assistance of someone who does.**

With a voltmeter, check the +5V supply is available. If so, connect the voltmeter between ground and the output of IC13 (Vpp). If +5v is applied to the Vpc input (R18) using a short piece of wire,

then Q1 should switch on, and the Vpp output should be +5V. If the Vpc input is now taken to ground, Q1 should be off, and Vpp should be +25V. This is determined by the values of R17A and B in parallel. If it differs by more than 0.2 volt, switch off and disconnect the 230V AC and adjust the values of R17A and B. When all these voltages appear OK, switch off the power, connect the main PCB without inserting the ics, and switch back on. Check that +5V is available on the appropriate pin of any IC socket. If it is not, switch off and **disconnect** the power and look for the probable short-circuit on the main PCB.

When all is well, insert the ics. If you have access to an oscilloscope, then the oscillator can be checked, as can the Baud rate divisor. If the UART is not fitted, then a link between pins 24 and 17 of the UART socket connects a clock signal to the address counters for checking at the eprom socket. Don't forget A11, A13, A14 and A15 are connected via the personality module.

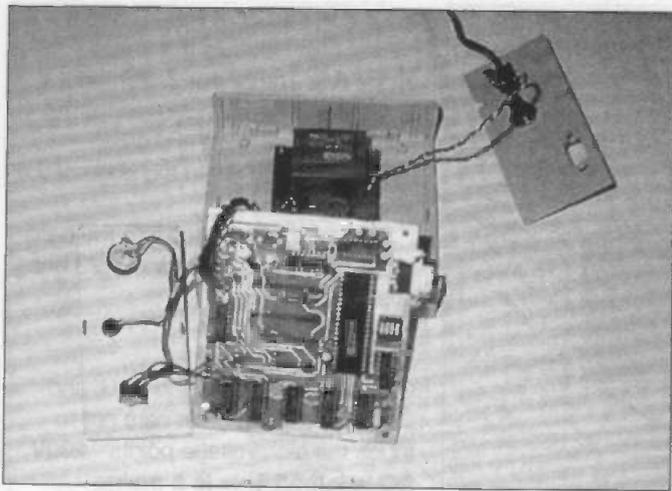
Operation of the Vcc Switch, Q6/IC11 is checked by monitoring the eprom Vcc on pin 28 of the ZIF socket. Connecting pin 11 of the Personality Module to +5V (pin 24), should switch on Q6, so pin 28 of the eprom should be +5v. Remove the connection, and it should rise to +6.2 volts.

### Conclusion

The Programmer is not restricted to use on an IBM compatible PC. There is no reason why it should not be used on any machine with a serial port, providing the user can produce the appropriate software. I have tested the prototype with an Atari ST machine, as well as 386/486/Pentium based PCs running at various speeds.

### Software

Because the Programmer responds to a byte, and returns a byte, it is not possible just to use a simple terminal program and transmit a data file to the Programmer. The software must be able to check that each byte has been received and acted on by getting a response. The original software was written in QBasic supplied with the later versions of DOS. The problem



here is that it is not possible to compile an executable program, although it can be done with the stand-alone versions of QBasic. The full QBasic listing is too long to reproduce here. However, two annotated listings are given: these show how to write data to the Programmer, and how to Read data. These are very much 'bare-bones' listings: you are encouraged to expand these and write your own software: it really is not difficult. An executable program in Visual Basic 3 for use with Windows 3.1 or Windows 95 has been developed, plus a small application which sends a selected number of bytes to check the operation of the Programmer on a 'one-shot' basis, and to confirm the operation of the address counter.

### Programming the Programmer

The following addresses apply to standard PCs, and can be declared as constants.

Constant Name	Port COM2	Port COM1	Function
txport	&H2F8	&H3F8	Tx Buffer
rxport	&H2F8	&H3F8	Rx Buffer
intenreg	&H2F9	&H3F9	Interrupt Enable Reg.
intident	&H2FA	&H3FA	Interrupt Identification
linecont	&H2FB	&H3FB	Line Control

In the following description the actual program instructions are shown in bold type. &HFF is the QBasic convention for showing hexadecimal data, in this case hex FF. The following variables must be initialised in your program:

workfile % integer containing the active filename.  
 Byte % integer containing the databyte.  
 workfile\$ string variable containing the name of a file.  
 counter& long integer used as an index.  
 programmersize& size of the eeprom in use in bytes  
 available = 4 interrupt bit mask for RxData

### Port Setup

Before using the Programmer, your program should set up the PC port to be used to match the UART settings. This requires a divisor to be loaded to set the baud rate (in this case, 9600 baud), and the appropriate stop bits, parity and handshakes must be set:

```
OUT linecont, &H80 set up baud rate: enable baud rate divisors
OUT txport, &HC    baud rate division
                  (1843200/baudrate*16 = &H0C)

OUT intenreg, &H0
OUT linecont, 3   no parity, 1 stop bit, 8 bits/char,
                  no handshakes
OUT intenreg, 1   enable data available interrupt
```

Writing data to the Programmer from file (this assumes that a file of data exists to be written to the Programmer):

```
counter& = 1           'initialize the counter
OPEN workfile$ FOR BINARY AS workfile %

'Now loop until all the data is read from file and sent to
Programmer.

do
  GET workfile %, counter, byte % 'QBASIC reads 2 bytes
                                  from file
  byte % = byte % AND &HFF        'so reduce it to 1 byte
  OUT txport, byte %             'then send it to the
                                  Programmer

'now wait until the Programmer echoes the data

do
  'wait for 'data available'
  interrupt
  TIMER ON                       'switch timeout watchdog
                                  on
  ready = INP(intident) AND available
  LOOP UNTIL (ready = available)
  TIMER OFF                       'exit when data received

  counter& = counter& + 1        'increment the counter
  loop until counter& > Programmersize& 'and loop if not
                                  completed
close workfile %                 'close the file when
                                  finished

Reading data from Programmer to file: if data is received by
the PC, it sets a bit in the Interrupt Identification Register. The
Read process enters a loop until this bit is set, then reads the
waiting byte of data and writes it to file.

counter& = 1           'initialize the counter
OPEN workfile$ FOR RANDOM AS workfile % LEN = 1
FIELD #workfile %, 1 AS databyte$

'Now loop until all data read from Programmer and written to file.

do
  OUT txport, &HFF        'prompt programmer to
                          return data
  DO                      'loop until 'data available'
                          interrupt occurs.
  TIMER ON                'switch timeout watchdog
                          on
  ready = INP(intident) AND available
  LOOP UNTIL (ready = available)
  TIMER OFF                'exit loop when data
                          received
  rxbyte % = INP(rxport)   'then read returned data
  LSET databyte$ = LEFT$(MKI$(rxbyte %), 1)
```

## Resistors

All 0.25 Watt 5%	
R1, R8, R11, R17A, R18, R20	10k
R2, R21	33k
R3	100R
R4	470R
R5	10M
R6, R19	100k
R7, R9, R13	4k7
R10	3k3
R12	12k
R14	120R
R15	220R
R16	680R
R17B	5k6
Rp	See Text

## Capacitors

C1, C2, C3, C4, C5	22uF 35V radial electro (Maplin AT56L)
C6, C7, C9	10pF ceramic (Maplin WX44X)
C8	1uF 63V radial electro (Maplin AT73Q)
C10	150pF ceramic (Maplin WX58N)
C11, Cd 1- 3	100nF polyester (Maplin DT98G)
C12	470uF 25V radial electro (Maplin AT51F)
C13	1000uF 25V radial electro (Maplin AT52G)
Cd 4	47uF 16V radial electro (Maplin AT39N)

## Semiconductors

IC1	Max 232CPE (Maplin FD92A)
IC2	6402 uart (Maplin QQ04E)
IC3	74HC02 quad 2 I/P NOR (Maplin UB01B)
IC4, IC6, IC9, IC10	74HC393 dual binary divider (Maplin AE60Q)
IC5	74HC74 dual 'D'-type flip flop (Maplin UB19V)
IC7	74HC132 quad 2 I/P NAND Schmitt (If difficult to obtain, use 74HC00) (Maplin UB00A)
IC8	74HC390 dual divide 2/5 (Maplin UB84F)
IC11	74L05 voltage regulator (Maplin AV02C)
IC12	78M05CV (Maplin QL28F)
IC13	317LP voltage regulator (Maplin AV26D)
Q1, Q2, Q4, Q5, Q6	BC107 (Maplin QB31J)
Q3	BC328 (Maplin QB69A)
D1, D2	1N4001 (Maplin QL73Q)
LED	Red LED (Maplin UK48C)
XL1	2.4576 MHz crystal (Maplin FY81C)

## Miscellaneous

T1	Transformer 230Vac/ 12Vac - 250mA (Maplin YN14Q)
F1	Chassis mount fuse holder 100mA (Maplin RX96E)
Power switch	2 pole on-off (Maplin FH04E)
Mode switch	1 pole 2-position (Maplin FH04E)
Reset switch	Momentary push to make (Maplin FF98G)
Serial connector	9 pin 'D' type plug PCB mount (Maplin FG66W)
Baud rate switch	4 pole dip switch (Maplin JH08J)
14-pin I/C sockets (7 off)	use 'turned pin' type (Maplin FJ64U)
16-pin I/C sockets (2 off)	(Maplin FJ65U)
24-pin I/C socket for target cable	(Maplin FJ67X)
40-pin I/C socket	(Maplin FJ69A)
28-pin ZIF socket for eproms	(Maplin FT14Q)
Box	(HxWxD) 6.5 x 13 x 13 cms

```

PUT #workfile %
counter& = counter& + 1
loop until counter& > Programmersize&
close workfile %

```

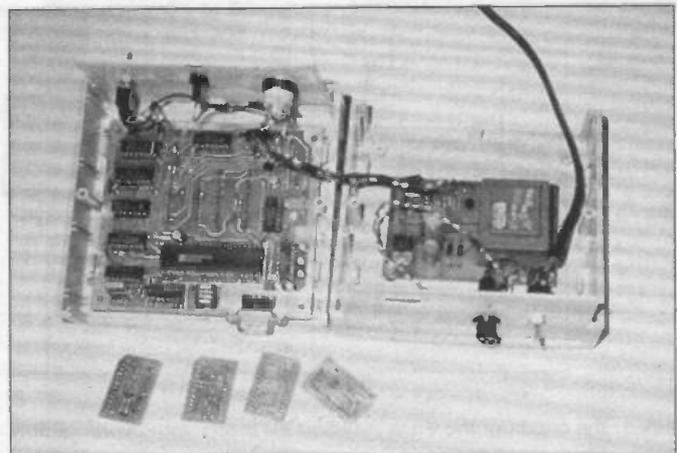
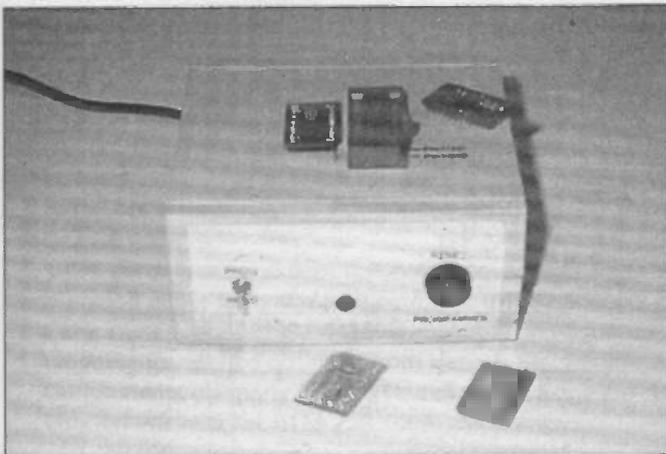
'convert number to a 1-  
char string  
'and put it in the file  
'increment the counter  
'and loop if not  
completed  
'close the file when  
finished

Any data may be sent to the Programmer as a prompt in Read mode (I used FF). It may also be worth noting that if the Programmer fails to return a byte for any reason, this routine

will stay in the loop forever waiting for data. You should include a check on the Timer value, and exit after a short period, noting this is a Timeout Error. The remainder of the program is left up to you.

## Software

A disk containing the Eprommer software is available from Forest Electronic Developments, 10 Holmehurst Ave., Christchurch, Dorset BH23 5PQ, priced £5 inclusive of post and packing. A kit may be available in the future - please enquire at FED at the address above.



# Peak Reading VU Meter

**Tony Sercombe's peak-reading VU meter combats momentary overloads in your audio system.**

**VU** or volume unit meters, were first used in the early days of broadcasting, principally in the United States. These early versions suffered many drawbacks. They consisted of a basic moving coil meter, with a diode, and perhaps resistor in series, simply connected across the signal path. They thus did little more than indicate the average value of the signal. The main problems with such a simple system were an inability to follow the signal levels accurately, poor frequency response, and a tendency to indicate a different level for differing programme contents of the same level. Another point is that other than the 0 dBm or 100 percent scale marking, levels could not easily be read.

At that time these meters were used in the UK also, but in the early 1930s attempts were made to improve matters, and the PPM or peak programme meter, was developed. This represented a great step forward. These meters had active electronics driving them. To begin with, they used variable slope [gain] pentode valves, and a right-hand mechanical zero system, so that an increase in signal was indicated as the valve gain reduced, being pulled up by the hair-spring. Modern PPMs use the conventional left-hand zero system, and solid state electronics.

The PPM has many advantages over the VU meter. Perhaps the most significant is that the circuit is logarithmic in nature and thus imitates the response of the human ear. This allows the scale to be marked in a linear fashion. The scale covers a range of 28

dB, or 25 times, and can be read over its entire length quite easily. The meter itself is specially designed to have a very fast response, or attack, and the electronics provide a slow decay or fallback of about 1 second. This has been the standard in this country and the majority of Europe for many years. Lately the LED column has become popular, however, many technicians find they suffer a degree of eye strain with this system, and still prefer the PPM.

A professional PPM attempts to indicate .5 Hz at 20 kHz, and to do this they have become very expensive, particularly as a stereo pair, and so are probably well beyond the pocket of most enthusiasts.

## Between the extremes

This present circuit is intended to fall somewhere between these two extremes. It uses a VU meter movement, but has a fast response time and good frequency response beyond the audio bandwidth of 20 Hz to 20 kHz. The circuit is linear, in theory making the scale accurate. However, the point of real interest is the 0 dBm or 100 percent mark on the scale, which will be the reference point adjusted to in use.

The circuit uses two single ics and one dual ic per channel. The first stage is a TL071 connected as an inverting amplifier with a gain of approximately 6 dB, or two times. The 22k preset enables the basic sensitivity to be set. If somewhat more gain is required, the value of the feedback resistor from pin 6 to pin 2 may be increased. The input resistor should not be reduced much in value, otherwise the circuit being monitored may be loaded unduly by the meter. The resistor from pin 3 to ground goes some way to reducing the offset voltage, and should have the same value as the input resistor. The output of the amplifier is connected to the input of a full wave rectifier. Full wave rectification is needed because the level of the signal in one half of the waveform may exceed that in the other, and therefore could be missed using a half wave system. With all the resistors of equal value, the rectifier operates broadly by inverting a positive signal twice, with D1 off and D2 on, and with negative inputs it inverts it once, with D1 on and D2 off, to provide a positive output.

The output of the rectifier is fed, via a further diode, to a buffer amplifier. The purpose of this is to provide a very high input impedance, and to supply out put current from a very low impedance source. Because the input is at high impedance, a capacitor may be connected to ground. This has the effect of delaying the signal voltage decay after large peaks. Since the impedance is very high and the input current tiny, a large value resistor is needed to shunt the capacity. By this means, the decay time is controlled. The diode is used to prevent the decay voltage from dissipating through the previous circuit. The resistor at pin 6 of the buffer is simply to give the meter some protection, and should be

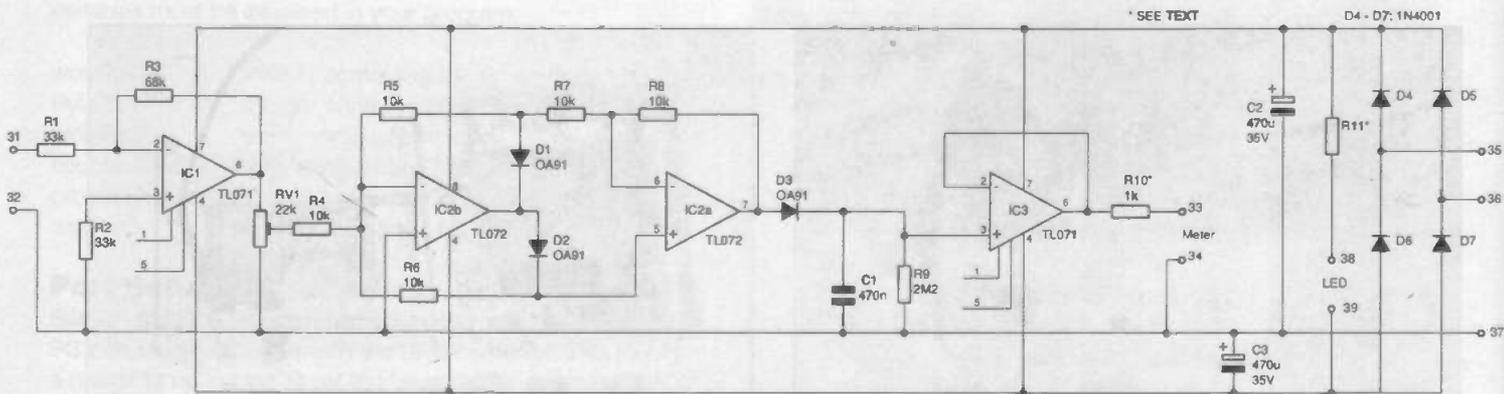


Figure 1: the circuit of the Peak Reading VU meter with power supply

adjusted to suit the movement, as should the values of the decay components. If the meter has a tendency to fall back too quickly, the capacitor may be increased to about 1 uF. It is recommended that if a stereo pair are to be constructed, that polyester type capacitors are used, since the tolerance of the electrolytic variety is such that they might have to be selected to give equal decay times. If it falls too slowly, the resistor value may be reduced by a small amount to adjust for this. No standard reference voltage is given, since it is assumed that this will differ, depending on the equipment to be monitored; however, a reference level of about .75 of the maximum to be used should be chosen and set by VR1 to 0 dBm or 100 percent on the scale.

### Linearity

Although the drive circuitry is essentially linear, there exists a degree of non-linearity at the diode output. Since VU meters first came into being, they have been very simple instruments, simple a diode and sometimes a resistor as already mentioned, in series with the meter. The scale calibration has historically taken this into account. Of course, this is only a half-wave rectifier, and thus ignores the other half of the signal, adding another limitation to the system. The reason why it is included in the present circuit is to bring the calibration to equality, and isolate the decay components, as already stated.

The next, and perhaps most practical point to note, is that the main importance of the scale is for setting steady tone. The main point of interest is the 0 dBm or 100 percent marking, and a slight overswing of the pointer on peaks here should be what is aimed for. The calibration cannot be used in the same way as a PPM - the diode specified will introduce a non-linear characteristic at the start of conduction, but in this case it is only around the -18/20 dB point on the scale, which is 10 times below peak, and of little importance in practical terms. Once the reference level of 0 dBm has been established, any signal below that must be within the dynamic range of the circuit being monitored anyway. The voltage drop across a germanium diode is in the order of 2/300 millivolts, which is absorbed by the previous circuitry

It is perhaps worth mentioning here that many VU meter movements are damped in much the same way as the traditional moving coil panel meter. This causes a VU meter to rise, or attach, a little more slowly. It is better to obtain an undamped movement if possible. A simple check for this is to gently rock the meter from side to side. The pointer should move by a few mil each time. However, an undamped meter will exhibit a degree of overshoot. That is to say, if a steady tone is applied, the pointer will rise quickly, and then settle back a small amount to a steady value. This is probably the best in use, since any overshoot will be taken up in controlling the levels anyway. The prototype instrument has been built into an audio mixer as an "aux send" level monitor, however, as described here it is intended as a stand-alone unit, and to this end a suitable power supply circuit is shown. If this is constructed, the proper care should be taken with mains supply connections, as mains voltages can be very dangerous, and all work and any adjustments, apart from the level setting preset, must be carried out without the power supply connected.

The choice of enclosure is left to the constructor; it could, for example, be used as a master monitor in a home studio, in which case it could be built into a separate free-standing case and so be transportable.

All components, except the meter movements, may be obtained either from Maplin Electronics (01702 554000) or Circuit Distribution (01992 448899). Suitable analogue movements may be obtained from Henry's Electronic Components (formerly Henry's Radio) at 404 Edgware Road London W2 1ED Tel. 0171 723 1008.

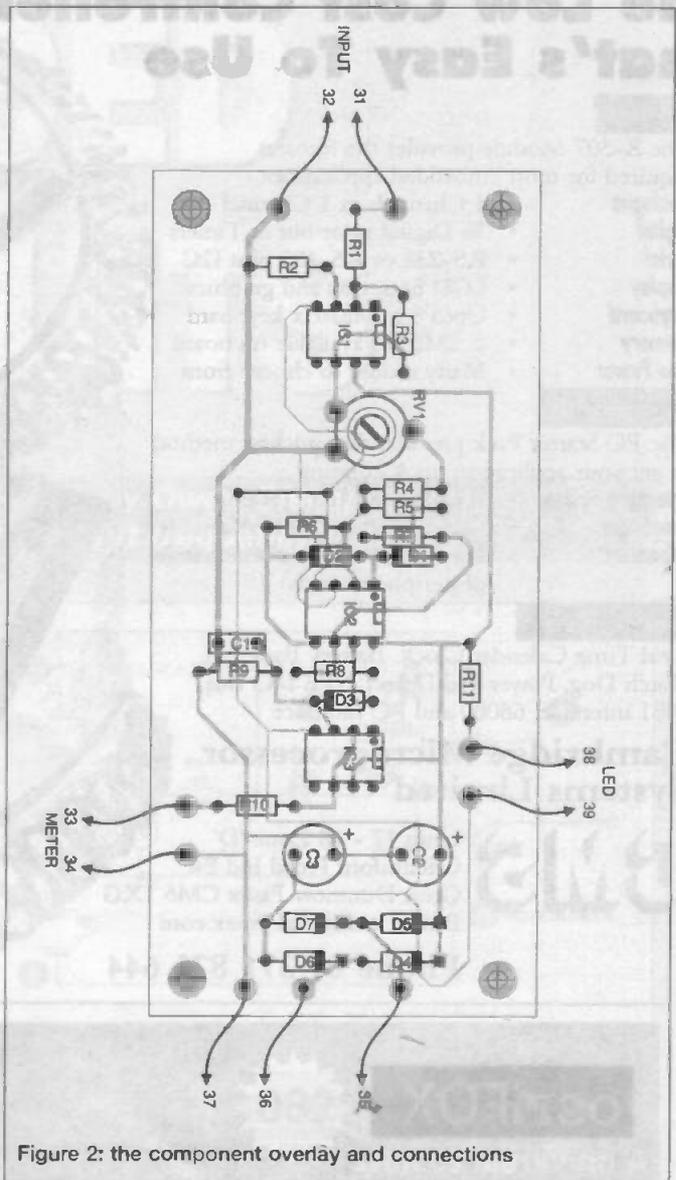


Figure 2: the component overlay and connections

## PARTS LIST for the VU Meter

### Resistors

R1,R2	33k
R3	68k
R4 to R8	10k
R9	2M2*
R10	1k0*
RV1	22k

### Capacitors

C1	470n*
----	-------

### Semiconductors

D1 to D3	0A90/91
IC1, IC3	TL071
IC2	TL072

### Miscellaneous

VU meter movement/s as required

\*Adjust on test

### Power supply section

R11	to suit LED and supply
C2, C3	470 uF
D4 to D7	1N4001
LED1	Red LED
TR1	Mains transformer pri: 6-0-6V to 10-V-10V
Switch, fuse as required.	

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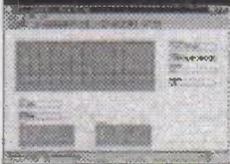


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# SPICED CIRCUITS

*Circuit simulation with software, by Owen Bishop.  
This month, part 3 - More about frequency.*

**W**e begin by analysing the circuit in part 2 (ETI Issue 5, 1997), which some readers may have recognised as a band-pass filter of the multiple feedback type (figure 1). The netlist is entered either by typing it in, or by using Geseca, as described in part 2. When specifying the voltage generator, set its amplitude to 100mV (a greater amplitude might saturate the op amp) and its frequency to 1kHz (see later). The problem was to investigate the frequency-dependent behaviour of the circuit.

Using a signal generator and an oscilloscope with a breadboarded version of the circuit, you can sweep the generator frequency over a given range, using a sine-wave signal, and watch the changing signal amplitude on the oscilloscope. A simulator program such as SpiceAge plots output amplitude against frequency to obtain the frequency response. To make interpretation easier, we can elect to have the results plotted on logarithmic scales. For the first run of this analysis, where we have no idea of the possible result, we select a wide range, say from 1Hz to 1MHz (figure 2). The plot has a sharply peaked pass-band, centre frequency 531Hz, rolling off symmetrically on either side. Note that it does not matter to what value we originally set the frequency of the generator, as the frequency response test sweeps it over the specified range, irrespective of its nominal value.

The operation of the circuit is based on feedback. We have two feedback loops, each containing a filter. The C1/R1 combination is a high-pass filter and the C2/R2 combination is a low-pass filter. High-frequency and low-frequency signals passing through the op amp are fed back to the inverting (-) input where they act to cancel out the signal coming from the generator. Only signals of intermediate frequency can pass. Taken as a simple high-pass filter, the C1/C2 combination has a cut-off frequency of  $0.5\pi RC = 4813\text{Hz}$ . This is the frequency at which the power of the output signal has been reduced to half the input power. This happens when the output voltage amplitude is 3dB below the input amplitude, so this is sometimes known as the -3dB point. For the low-pass filter C2/R2, the -3dB point is  $0.5\pi RC = 60\text{Hz}$ . The responses of the two filters overlap, as in figure 3 and we can see why the pass-band is so narrow, for the frequency at the -3dB point of the high-pass filter is higher than that of the low-pass filter. The location of the peak would be

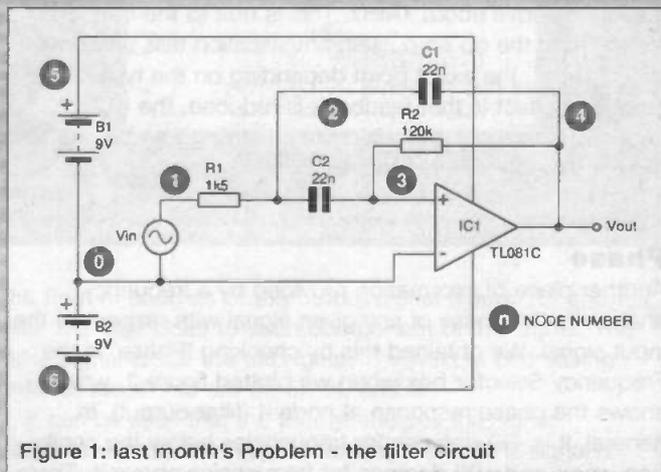


Figure 1: last month's Problem - the filter circuit

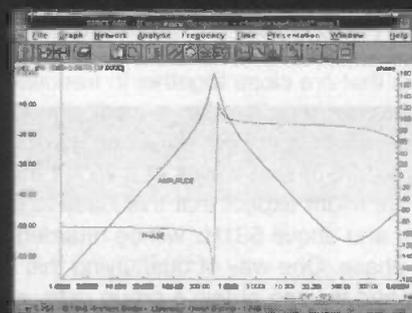


Figure 2: a Bode plot of the frequency response of figure 1

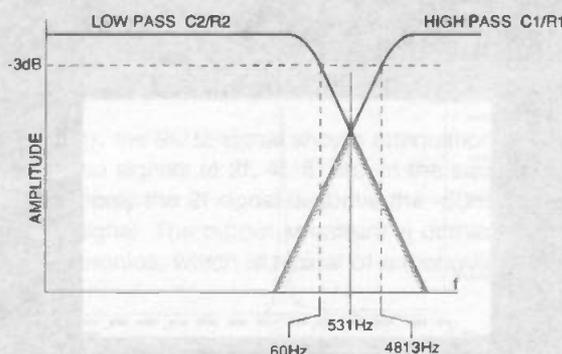


Figure 3: overlapping responses of the low-pass and high-pass sections of figure 1 produce a sharply peaked band-pass response

expected to come half-way between the two -3dB points, but this is a mean on a logarithmic scale, the geometric mean. Instead of adding them and dividing by 2, we multiply them and take the square root:

$$\text{Centre frequency} = \text{Square root of } (4823 \times 60) = 538\text{Hz}$$

This agrees well with the curve plotted in figure 2. We can use the cross-hairs to measure roll-off. For example, place the cross-hairs on the curve with the vertical cross-hair exactly on 10Hz. Read the corresponding decibel figure, which is -47.3. Now move the cross-hairs further up the curve to 80Hz. This is 10Hz doubled three times, a span of 3 octaves. The dB value at this point on the curve is -29.57. So the amplitude has increased by 17.73dB per 3 octaves, or about 6dB per octave. This is a typical figure for the roll-off of a single stage RC filter.

Similarly, the portion of the curve above the pass-band shows a roll off of -6dB/octave. There is a slight increase of response above about 1MHz. This is due to the frequency response of the op amp itself; amplification falls off above about 1MHz, the exact point depending on the type of op amp. The effect is that feedback is reduced, the high frequencies are not cancelled out as strongly as before, so there is an upturn in response.

## Phase

Another piece of information provided by a frequency analysis is the phase of any given signal with respect to the input signal. We obtained this by checking 'Phase' in the Frequency Selector box when we plotted figure 2, which shows the phase response at node 4 (filter output). In general, it is -90 degrees for frequencies below the centre frequency, and +90 degrees for frequencies above it. There is a sharp 180 degree change in frequency at centre frequency. Sudden changes in frequency are bound to mean abrupt changes in the timing of signals. Signals around 531Hz that are close together in frequency may show big difference of phase. Since most signals we produce in everyday life are not pure sine waves, but consist of a mixture of sine waves of a wide range of frequencies, we might expect that this leads to distortion. Signals below and above 531Hz will be retarded or advanced in phase. One way of quantifying this is known as Group Delay, and we can obtain a group delay analysis by checking Group Delay in the Frequency Selector box. The curve (figure 4) shows both positive and negative delay.

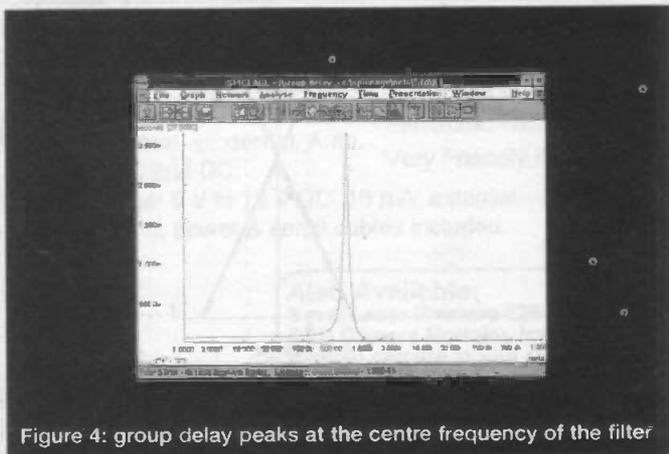


Figure 4: group delay peaks at the centre frequency of the filter

While delay may have its uses in other circuits, it plays havoc with a signal of mixed frequencies, and is a disadvantage of this kind of filter. But perhaps this is being unfair to the filter. Usually we set the cut-off point of the low-pass section to be higher than that of the high-pass section. This results in a wider pass-band (figure 5). Phase lag decreases gradually and fairly evenly from -90 degrees to about -180 degrees. The phase curve falls below the scale at the centre frequency, but is plotted 180 degrees higher up the scale above the cut-off point. This is a continuous fall, not an abrupt reversal, and a plot of group delay shows only a gradual variation in delay throughout the frequency range.

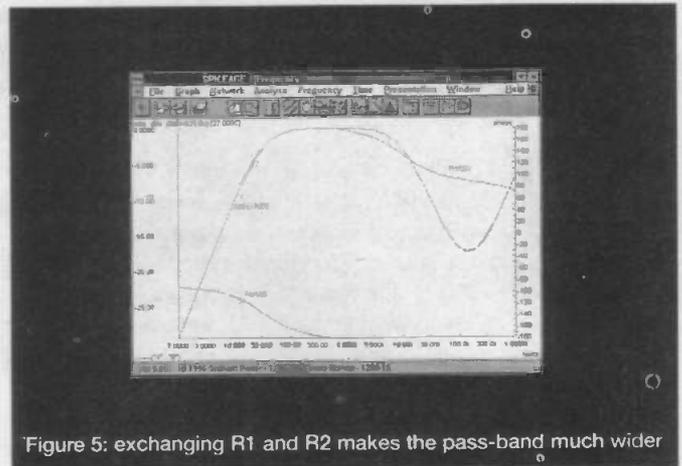


Figure 5: exchanging R1 and R2 makes the pass-band much wider

## Fourier

We continue by using last month's problem circuit to illustrate another important aspect of frequency response. The basis of the Fourier analysis is the fact that any periodic signal, or whatever shape, can be analysed into a number of sine waves of different frequencies and amplitudes. If  $f$  is the frequency of the original signal, the fundamental, the frequencies of its other constituent sine waves, the harmonics, are  $f$ ,  $2f$ ,  $3f$ ,  $4f$ , and so on. Sometimes one or more of the harmonics may have zero amplitude. We may, for example, find that only the odd-numbered harmonics are present. After you have processed a signal in SpiceAge, you may carry out a Fourier analysis to find out what sine-wave components it has and what are their amplitudes.

To obtain the signal, we use another major kind of Spice analysis, the Transient Analysis. In this analysis the voltage generator (one or more), and current generators (if any) are put to work feeding their outputs into the circuit and the resultant voltage at all the nodes and currents through all the branches are calculated at specified time-intervals. For example, since our generator has a frequency of 1kHz, and we would like to follow the action of the circuit for 10 cycles of the generator, we click on Time, then on Sweep Times and set the Start time to 0, Stop time to 10m (milliseconds) and the Step time to 10u (microseconds). This gives us 1000 sampling times during the run. We can then plot the voltage at one or more nodes. Figure 6 shows output voltage from time 0. The 1kHz signal has an initial bias superimposed on it as the capacitors gradually acquire their charges. To eliminate this stage, return to Sweep Times and amend Start and Stop times to 10ms and 20ms

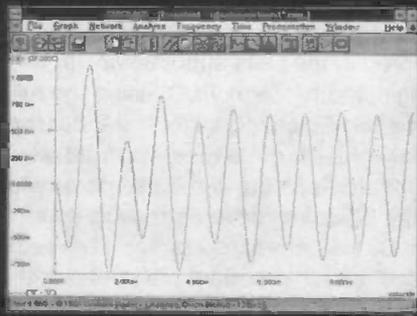


Figure 6: a Transient analysis shows how filter output varies in time

respectively. Re-run the Transient Analysis to obtain 10 reasonably stable oscillations. Having done this, clicking on Fourier produces a plot of the spectral composition of the signal. It is similar to what we could produce by using a Spectrum Analyser.

The analysis (figure 7) shows its strongest component at 1kHz, as expected. But distortion has introduced signals at other frequencies. The next strongest signal is at 500Hz, but this is -45dB down on the 1kHz signal. This means that its amplitude is only about 0.000032 of the main output signal, so we can ignore it. This particular component is probably due to the initial oscillations that have not completely died away. The other signals shown by a multitude of vertical lines really represent a continuous spectrum of 'signals' and probably all of them are due to approximations in the calculations. In short, they are mathematical noise. Usually we put a -60dB Hold on the minimum of the decibel scale to eliminate this noise from the display.

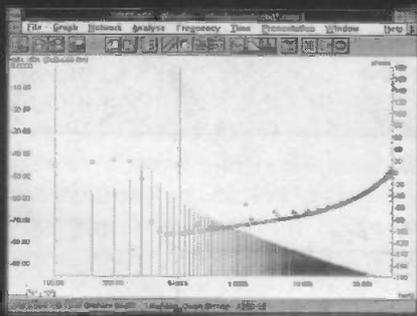


Figure 7: a Fourier analysis of the signal of figure 6 shows a peak at 531Hz, a hint of early oscillation, and mathematical noise

The Fourier analysis also displays the phase of the signals, using small square symbols. In figure 7 the phase of the 1kHz output signal is +9.81 degrees. Owing to the way the analysis is calculated, this is with respect to the input plus 90 degrees, so at 1kHz the output is 99.81 degrees ahead of the input, as already shown in figure 2.

### Square waves

Pure sine waves are instructive but now for something more exciting! Amend the netlist to Ex=square on the

generator line, and re-run the Transient analysis (figure 8) to plot both input and output signals. The square wave input is clearly seen and we can see an output signal of larger amplitude, but what has become of the 'squareness'? The Fourier analysis of the input (figure 9) demonstrates what we mean when we say that a square wave is rich in harmonics. We have cut out the noise by holding the minimum dB level to -50dB and are not plotting phase. The 1kHz signal dominates and the next strongest signals are the odd harmonics at 3f, 5f, 7f, 9f and so on. There are also even harmonics, but at considerably lower amplitudes.

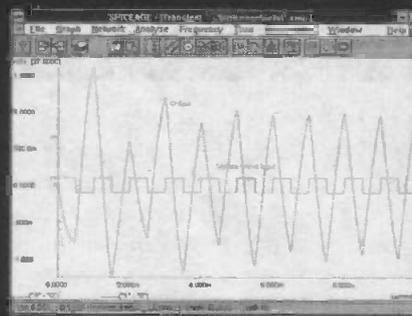


Figure 8: transient analyses of input and output demonstrate what the filter does to a square-wave signal

The Fourier analysis of the output signal (figure 10) shows what the filter does to each component of the signal. The 1kHz signal is still the strongest. The next in decreasing order of amplitude are 3f, 5f, 7f, and 9f.

It can be seen that the filter attenuates the higher frequencies more strongly. For example the 1kHz signal is amplified from 127mV to 850m, a gain of over 6 times. By contrast the higher harmonics are reduced more and more as frequency increases. The 5kHz signal, originally at -14.2dB has been reduced to 31.0dB, an attenuation of -16.8dB.

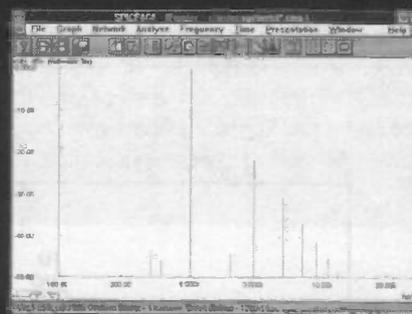


Figure 9: Fourier analysis of the square-wave input

Further along, the 9kHz signal shows attenuation of -22dB. There are also signals at 2f, 4f, 6f etc. in the square wave, but of these only the 2f signal is above the -50dB level in the filtered signal. The output spectrum is dominated by the odd harmonics, which is typical of a triangular wave.

### Exploding netlists

If you re-load the netlist used in Part 1 (ETI issue 3 1997), which included a BC548BP transistor, you can explode it

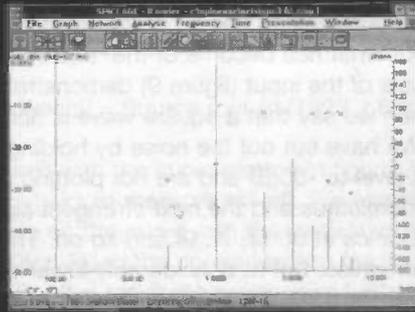


Figure 10: Fourier analysis of the output, for comparison with figure 9. The constituent signals have a complicated phase relationship

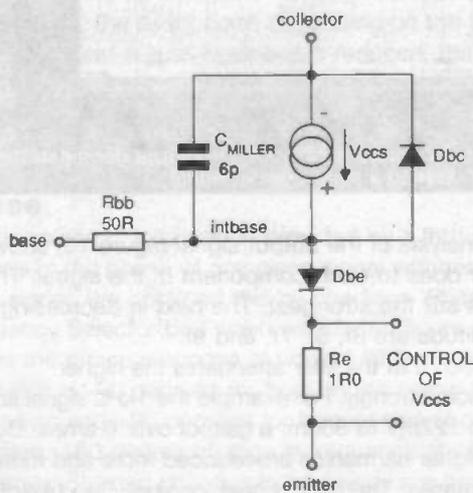


Figure 11: the 'nnp' transistor model is basic but quick-running

by clicking on Netlist and then Explode. The transistor is normally specified in the netlist solely by its model name and its netlist connections. Exploding calls up more detail from a file stored in the Nets subdirectory. In this case the file is one originated by Zetex PLC, using the full Spice format. As in other Spice model files, the description consists simply of a list of parameters and their values. What the computer is to do with these parameters is decided by the Spice software. Parameters include such quantities as  $I_S$ , the saturation current, here with the value  $1.3 \times 10^{-14}A$ . Another important parameter is the forward current gain,  $BF$ , which equals 400 in this transistor. Altogether Spice has 40 parameters for specifying a bipolar junction transistor. The BC548BP model specifies only 22 of these, the remainder of the parameters being given default values when a simulation is run. Thus the transistor is represented by a set of equations with 40 parameters to put into them. A model such as this gives a highly accurate representation of the transistor, but at the cost of taking an appreciable time to evaluate. If the netlist includes several transistors, it may take too long to run.

An alternative way of modelling a complex component is to represent it by a netlist of simpler components. Models of this kind are stored as .lib files and their netlist is incorporated into the main netlist wherever they are named. One such is the cheap-and-cheerful 'nnp' model (figure 11), which is perfectly good enough for non-critical modelling. We leave the reader to discover how it operates. At least it runs very fast!

### Problems

Set up the circuits of figure 12 on a breadboard or simulator: (a) is an amplifier which needs improving; find out what is wrong with it and do what you can to correct it. (b) is a precision voltage regulator; how precise is it?

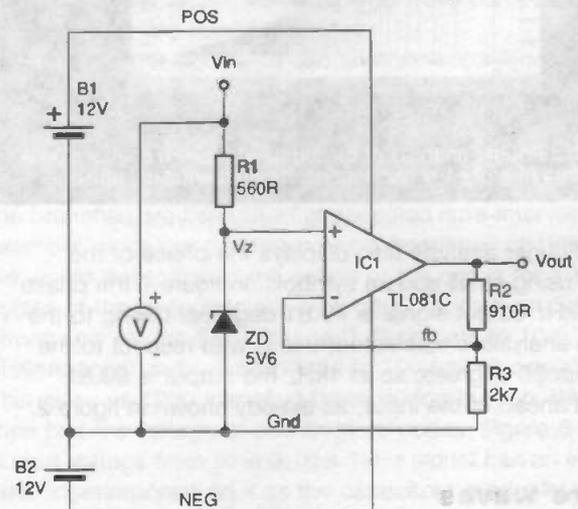
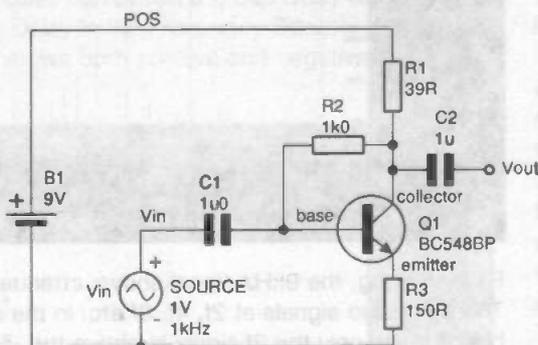


Figure 12: two more problem circuits

# REVIEWS

## **The Electronic Pathway in Advanced GNVQ Engineering**

**Barry Lewis and Tim Strickland**

**Price £16.99 ISBN 0 3334 9914 4 P/b**

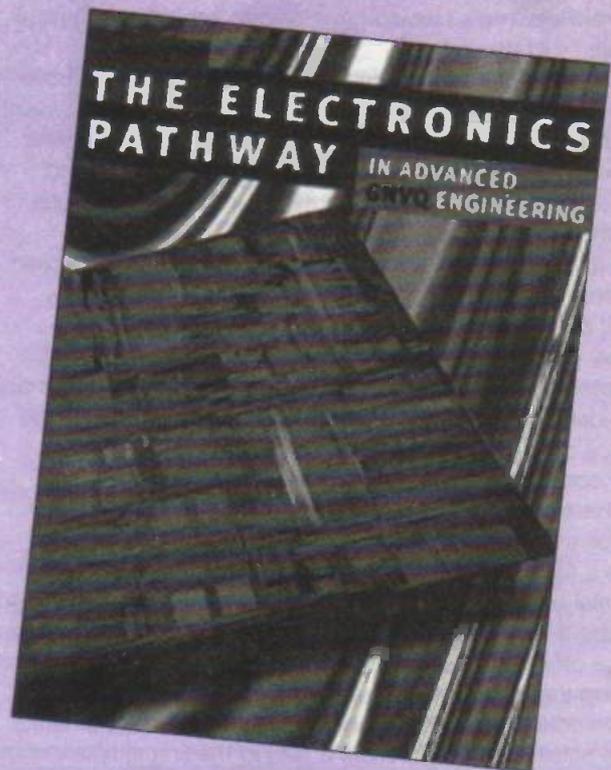
**Macmillan Press Basingstoke 1996**

This practical textbook covers the Advanced GNVQ Optional Units 11 (Electrical Principles) 13 (Electronics) 19 (Microelectronic Systems) and 9 (Communications Engineering), which between them contain many of the most important topics in basic electronics - to illustrate, the chapter headings run through DC circuits, capacitance, electro-magnetism, AC circuits, semiconductors diodes and transistors, power supplies, amplifiers, digital electronics, information transmissions (comms), microelectronics and test meters (including oscilloscopes).

The book starts with DC circuits, covering Ohms Law, series and parallel circuits, power dissipation, and so on. It has a good explanation of resistor colour codes, including five-band resistors, and goes on to describe other resistor components such as pots and light-dependent resistors. The text does not avoid advanced concepts - by page 23 we have Kirchoff's Laws - but there are practical experiments to demonstrate, and detailed circuit calculations to help make things clear. By page 31, you are learning Thevinin's Theorem and Norton's Theorem, and when you have completed this, you have a better than average understanding of DC circuits. Matched attenuators, bias networks, and the like are treated matter-of-factly. Chapter 2 covers capacitors equally well. There are examples, with detailed illustrations of individual types of capacitors showing the structure and the dielectric, with the relevant formulae.

Each section also has "guided examples", showing how calculations are applied to practical circuits. At the end of the sections there are sample questions to test your knowledge.

Subsequent sections include Semiconductors, Power Supplies, and Amplifiers (with a selection of op-amp building blocks). The important aspects of combinational and sequential logic circuits are covered, and there is a substantial section on information transmission, which includes radio modulation systems and fibre optics. Section 10 covers machine code programming, and section 11 covers test equipment.



The basics of electronics are presented clearly in this book, with understandable explanations throughout, even for topics which are easily made confusing. The authors have aimed for clarity and a down-to-earth approach without sacrificing detail. I would have appreciated this book in my first year at university, and yet it would also be useful to a complete novice.

If you want to learn about electronics at this level, I recommend this book - it is good enough to read for interest, not only to help with gaining GNVQs. The text makes nothing more complicated than it has to be, and will appeal to practical engineering people who want to make things work.

## **Satellite Projects Handbook**

**Price £14.99 ISBN 0 7506 2406 X P/b**

**ButterworthHeinemann/Newnes Oxford 1996**

This book has been written by Lawrence Harris, who has worked in a satellite ground station, sending commands and receiving data from British scientific satellites. Clearly he has maintained a personal interest in the subject in his spare time as well, and he relates his experiences of satellite monitoring with various types of equipment.

The first five chapters talk about satellites in general, experiences of launching and operating a particular satellite, and various current satellites in detail: what they are for, the frequencies on which they transmit, and other information

such as amateur radio retransmissions of shuttle communications. There are pictures received from weather and survey satellites to whet your appetite.

Chapter 6 begins the practical project side of this book, describing the basic hardware and terminology, and the basics of setting up a personal ground station. The author suggests starting with a simple station to receive UOSAT2, for which in his experience a simple and inexpensive dipole will suffice, though he suggests a crossed dipole for better reception of the circularly polarised signal. A simple scanner will receive UOSAT2 telemetry and data as an introduction to satellite receiving.

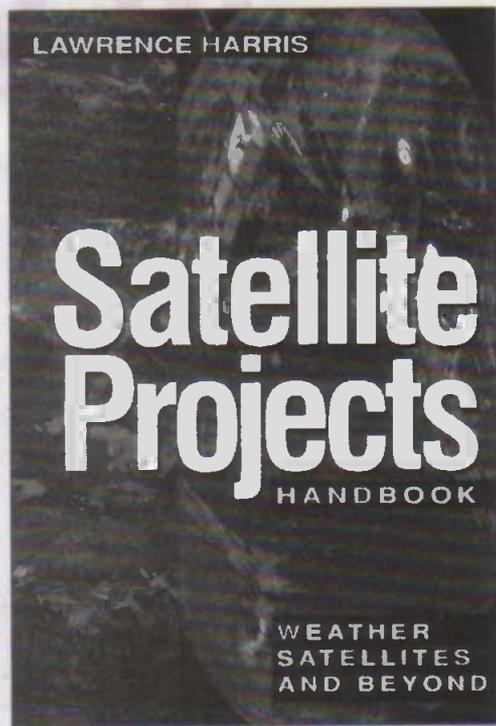
Chapter 7 covers the reception of weather satellite data. The necessary specification for the various parts is covered.

Kits are mentioned as a good way to build up equipment, but circuits and designs are not given. It must be said that the title may be a little misleading to project constructors: this is not a "project" book - it is a handbook for the electronics and radio enthusiast who already understands the principles but has not dealt with satellite reception before. Personally, I would have liked to see some practical circuits.

Subsequent chapters deal with the decoding of weather satellite images, including a fair amount about the necessary computer equipment, although much of this will be familiar to those readers of ETI who are more computer-oriented. There are pointers on how you might understand and use weather satellite data once it has been converted to a picture: monitoring hurricanes and looking at thermal ocean currents are among the suggestions.

Of course, there is more to satellite monitoring than simply receiving weather pictures from known spacecraft. Chapter 10 talks about listening for new or unfamiliar signals and identifying the source - with a warning: sometimes the source of an apparent satellite signal can be the clock of your computer.

This book is not a reference source for information on microwave and UHF reception. Assuming that you already have a fair knowledge of general electronics and at least a minimal acquaintance with rf, then this book tells you how to go about receiving satellite signals and how to start making sense of what you receive. There are useful appendices, and I found the one on Kepler elements of particular interest. I have noticed Kepler orbital elements specified for amateur radio satellites, but Appendix 3 here is the first simple



explanation I have seen of how to use these figures to calculate where the satellite will be at a given time.

Although developments move faster than any book publication can, much of the information here will probably be relevant for years to come. If you want to monitor satellite transmissions but do not know much about how to make a start, this is a good handbook.

## Construction Authors

**If** you have designed and built any kind of electronics gadget for yourself, your home, your friends or your business, it may be suitable for publication in ETI.

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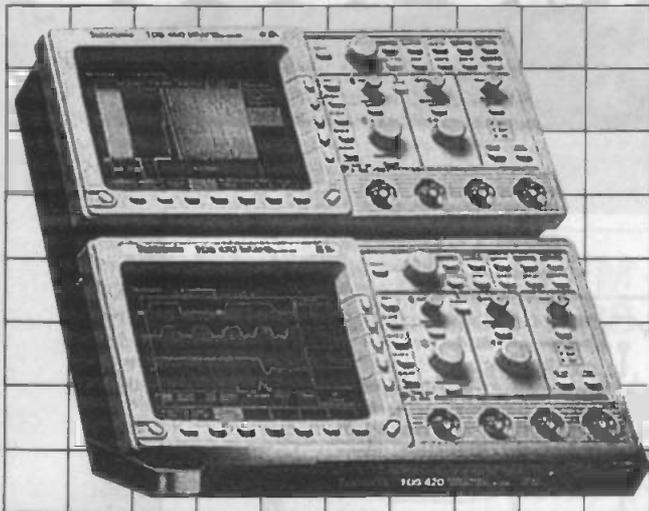
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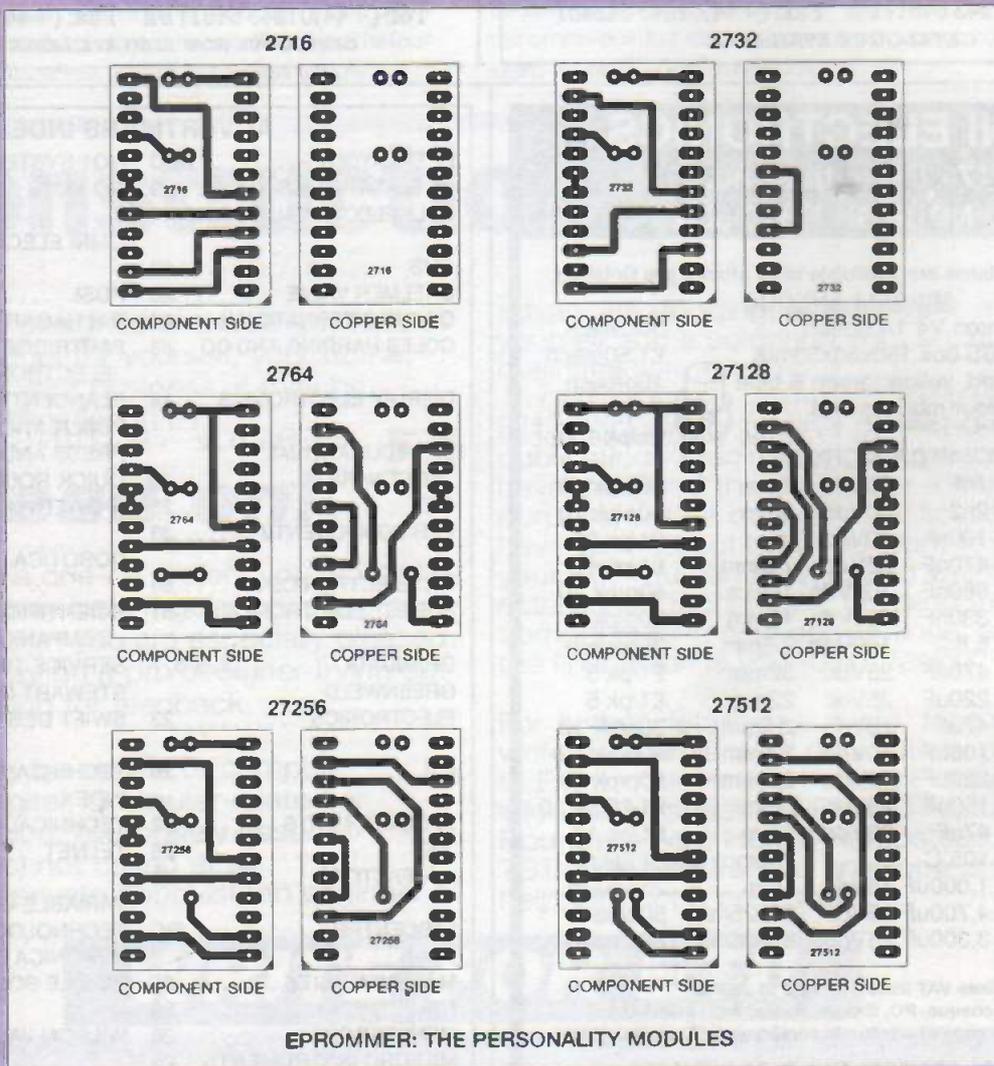
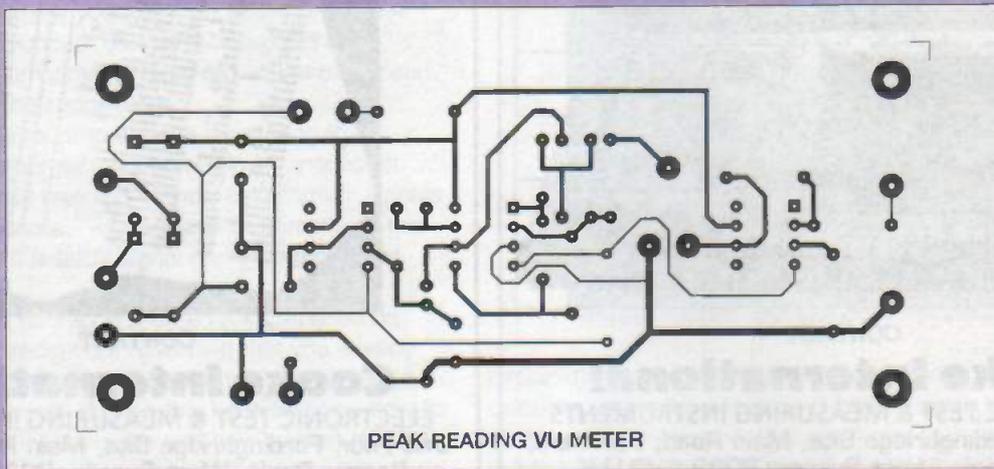
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BK ELECTRONICS	.....39	NO-NUTS	.....60
BULL ELECTRICAL	.....10, 17	OMNI ELECTRONICS	.....67
CMS	.....60	PDSL	.....69
CHELMER VALVE	.....26	P H HAGAR	.....69
COOKE INTERNATIONAL	.....67	PARTRIDGE	.....60
COLES HARDING AND CO	.....69	ELECTRONICS	.....60
DISPLAY ELECTRONICS	.....19	PLANCENTRE	.....69
EPT EDUCATIONAL	.....40	PUBLICATIONS	.....25
SOFTWARE	.....25	PRESS AND PEEL	.....25
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GREENWELD	.....23	SERVICE TRADING CO	.....69
ELECTRONICS	.....23	STEWART OF READING	.....37
ICS	.....26	SWIFT DESIGNS	.....38, 39
JJ COMPONENTS	.....32	TECHNICAL EDUCATION	.....26
JPG	.....25	INDEX	.....26
J+N FACTORS	.....69	TECHNICAL INFORMATION	.....23
LABCENTRE	.....IBC	TELNET	.....21
M M UNIVERSITY	.....25	VARIABLE VOLTAGE	.....69
MAURITRON	.....69	TECHNOLOGY	.....69
MENDESCOPE	.....26	VERONICA FM	.....69
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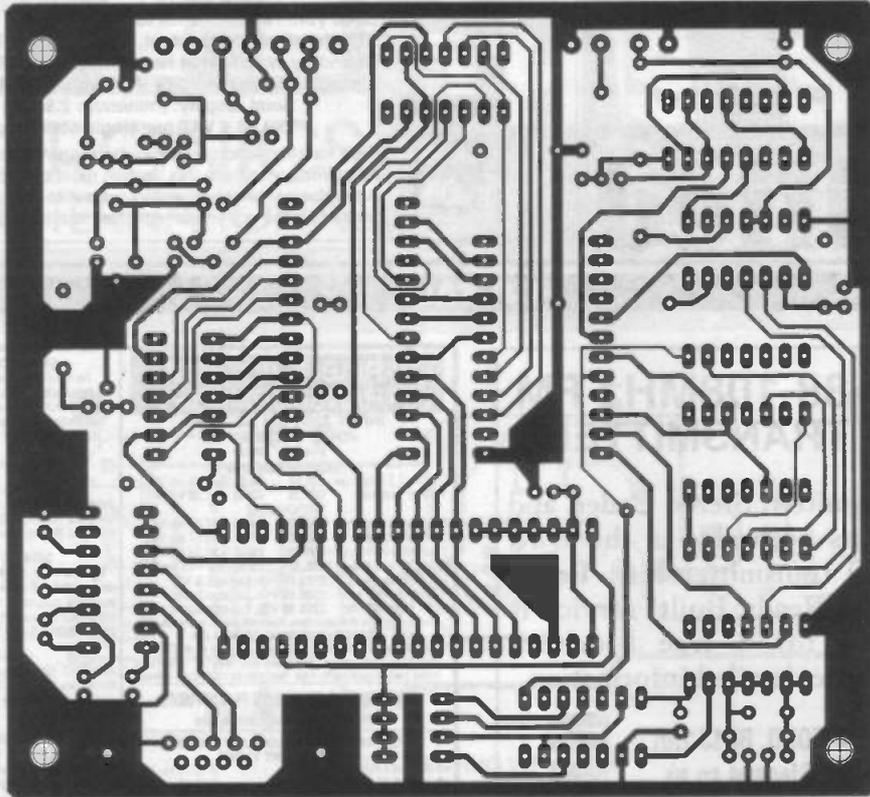
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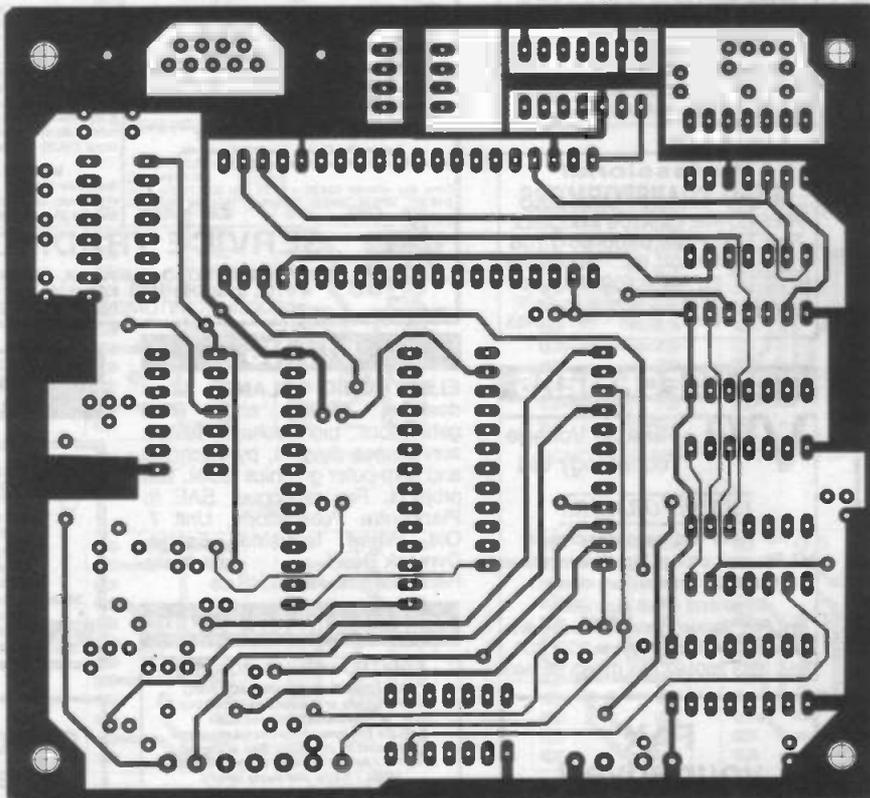
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# Practically Speaking

BY TERRY BALBIRNIE

*Over the next few months we shall be looking at some calculations used in electronics. Don't be put off - the maths is quite easy! We shall start by looking at units.*

**B**eginners - and not only beginners - in electronics are often confused by the number of abbreviations - usually standard scientific symbols - in use. For instance, on any multi-tester there are milliamp and microamp scales, and there may be a millivolt scale as well as volt scales, and so on.

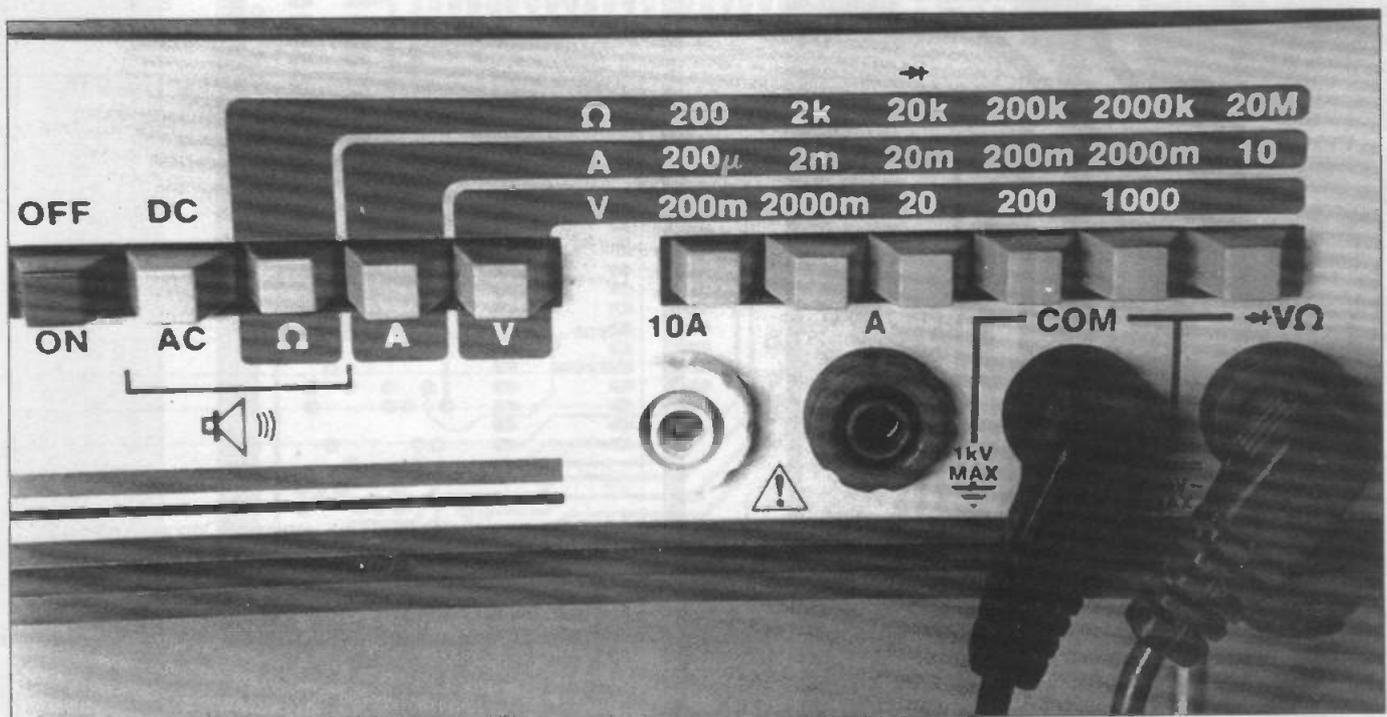
The reason for this complication is that the measurements need to span a very wide range of values, and the use of prefixes like milli (one thousandth) and micro (one millionth) makes life much easier and avoids a lot of zeros. Some examples will make this clear. Suppose, in making a check on a circuit, we measure a current of four amps (written 4A). This would be measured on the 0-10A scale (many amateur multi-testers do not have one of these, but many professional models do). Suppose, at some other time we measured a current of five thousandths of an amp. This would be measured on a milliamp scale - 1 milliamp being one-thousandth of an amp. This is written "5mA" because it is much quicker and more straightforward than writing 0.005A - and there is not the risk of "losing" zeros in reading or writing it. And please note! These "value signs" are case-sensitive - "m" is not the same thing as "M" - but we will come to that below.

In a similar way, one millivolt (mV) is one-thousandth of a volt and one milliwatt (mW) one thousandth of a watt.

Suppose we then need to measure six millionths of an amp. This would be measured on a microamp scale - 1 microamp being one millionth of an amp. Six microamps would be written 6( $\mu$ )A. The prefix micro  $\mu$  is the Greek letter called "mu" and is used - like many, many Greek letters in scientific notation - because otherwise we simply run out of letters to designate the different values. The letter "m" could not be used for both thousandths and millionths. The "longhand" way of writing 6 $\mu$ A is 0.000006A. Again, the micro prefix may be used in other situations such as microvolt ( $\mu$ V) and microwatt ( $\mu$ W).

But, as you will have noticed, there is a catch here - modern typesetting is faster and more reliable than the old "retying" process - but still shares some limitations with old-fashioned typewriters!. "Copy" (the publishers' name for what you are reading here) may "hop" several different computers before it reaches the printer - and Greek notation is not part of the basic ascii character set, just as it was not featured on a normal typewriter keyboard. Ascii is still the most reliable way of "moving" copy without inviting unwanted transformations - so authors and publishers still use substitute letters for Greek

Ohm, amp and volt scales on a popular type of multi-meter



notation. The most well-known ones are "u" for "μ" and "R" for "Ω" (ohms).

We sometimes use prefixes to divide a unit into even smaller parts. This is best illustrated by capacitor values. The standard unit of capacitance, the farad, is generally too large for everyday use. A 1F capacitor is not needed very often. It is much more usual to use millionths, thousand-millionths and millionth-millionths of a farad. The prefix for a thousand-millionth is nano (n) and for a millionth-millionth, pico (p). Thus, a capacitor having a value of 100 millionths of a farad would be written 100(μ)F or 100uF (or simply 100u). One having a value of 100 thousand-millionths of a farad would be written 100nF (or 100n) and one of 100 millionths of a millionth of a farad, 100pF, or 100p.

### Bigger and bigger

Other prefixes are used to scale units up. In electronics, this is usually by factors of one thousand and one million. One thousand is given the prefix "kilo-" and one million the prefix "mega-". This is familiar in everyday life as the kilogram (1000 grams), the kilometre (1000 metres) and even someone earning megabucks! It is particularly convenient when talking about resistance values. Remembering that the symbol for the ohm is "Ω" or (informally) "R", a 10-ohm resistor would be written 10R. However, it is common to use resistor values of thousands or millions of ohms. Ten thousand ohms ("ten kilohms" - not "ten kilo-ohms") is written 10kR or just 10k, and ten million ohms, 10MR or just 10M ("ten megohms", not "ten mega-ohms"). To carry this further, a resistor having a value of four thousand and seven hundred (4,700) ohms would be written 4.7k, and one of six million eight hundred thousand (6,800,00) ohms would be written 6.8M.

There is a clever way of avoiding the decimal point and that is to use the prefix in place of it. For example, in a parts list you may find 4k7 or 6M8 specified for the values of the resistors above. A resistor of value 4.7 ohms may be written 4R7 or 4Ω7, and so on.

### No mixing!

There are hundreds of alphabetic, Greek and other letters in use as abbreviations and symbols, although you may meet only a few of them, and you can see that there is room for confusion. For instance, if you write "Ma", instead of "mA", it could be read by the unwary as "mega-metres-per-second" (a unit of acceleration), instead of "milliamps" as required. Whereas "S" means "siemens" (a unit of conductance), "s" means "seconds" (the most commonly used unit of time in electronics) - an "ms" is a "millisecond", but an "mS" is a "milliseimens". "k" means "kilo" (one-thousandth) but to confuse matters further, a computing "digital thousandth" is 1024, written "K" and is still called a "kilo-" (as in a "kilobyte", 1024 bytes). The whole field of computing is still ambiguous as to abbreviations, but "B" is most consistent for "bytes", and "bit" for "bits". But in a different field "K" also stands for "Kelvin", a specialised measure of temperature. And "m", as everyone knows, also means "metre", as in "mm" for "millimetre". So it is wise to be wary.

Fortunately, not many of these notations clash in meaning in a disastrous way, but there is one where wariness is really important. The high voltage needed for a TV picture tube is several thousand volts (kV) and in scientific research voltages of several million may be used (MV). An LED may consume 20 milliwatts (20mW) but a power station may generate 2000 megawatts (2000MW). So you will see that it is vital not to mix up your "m" with your "M"!

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All contents are currently planned, but may alter.

# Around the Corner

**A**s you will see from our Contents page, the ETI PCB Service is back this month after a period of absence. This gives us great pleasure, because we know that although a lot of constructors make their own boards, there are always some of you who are unable to for one reason or another - lack of basic equipment, lack of somewhere suitable to set it up of just lack of time in the busy world of today - and would like the opportunity to buy ready-made boards for projects.

This gives us even more pleasure because, during that dark few years known by everyone in the UK as "the Recession" - which our governors, past and present, tell us is behind us now - there were many kinds of shake-out in businesses, including the entire electronics industry, and one of them was the need for suppliers to hold less stock. This affected the ability of our suppliers to make large batches of board at an economic price, and our ability to keep stock.

Now we have a new arrangement and a new supplier and we hope that our service will be helpful to many readers. Of course, as these things happen, we start with one rather unusual package this month - including the Eprommer personality modules, tiny double-sided optional PCBS - which we decided in the end to offer on a pick-and-mix basis. But no, we won't be doing liquorice allsorts next month.

I would like to thank our new suppliers, and I would also like to thank our old suppliers, who provided us with a fine service and still make boards for

us when we need long runs. The "recession" only affected the cash flow, not the quality.

## Bear essentials

Swindon-based part-time adventurer and explorer David Hempleman-Adams and fellow adventurer Rune Gjeldnes from Norway were presented with two custom-programmed "Minder" pagers by Blick Telefusion Communications Ltd. of Swindon to accompany them on their unsupported walk to the North Pole.

The four preprogrammed pager messages were: "Stop", "Lunch", "Emergency" and "Bear".

The two travellers are travelling some distance apart for safety reasons. The pagers pass essential information between them. On the face of it, these messages contain 75 percent of all the information needed for survival in the modern world - never mind the Arctic. But what is the fourth essential? Our consultant says that the answer is obvious - but is he right? The Editor welcomes suggestions, of course.

## Last Month's Competition Winners

The lucky winners of last month's Swift Designs competition (skilful as well as lucky, of course) are Mr. K Mitchell of Loughton and Mr. L Hadden of Ballygawley. Mr. Mitchell wins a copy of EDWin NC Deluxe 3 along with Thermal Analysis and EDSpice simulation. Mr. Hadden wins our runner-up prize of EDWin NC Basic. Congratulations and we hope that both readers enjoy many happy hours in CAD country.

## Next Month...

Volume 26 no. 8 of Electronics Today International will be in your newsagent on 18th July 1997 ... Douglas Clarkeson will be chasing intelligent robots (or maybe they will be chasing him) ... as valve hifi, radio and amplifier equipment becomes evermore popular, Peter Kenyon has designed a portable Value Characteristic Tester for identifying and matching valves ... If you can never find the tiles, the cards and the dice for the family word games, try Electronic A-Z for your letters of the alphabet ... Terry Balbirnie's Brake Light Checker is a safety feature for your car ... If you are thinking of doing Electronics at college, we look at some of the country's leading departments ... all the regulars, and more.

*Contents are in preparation but are subject to space and availability.*



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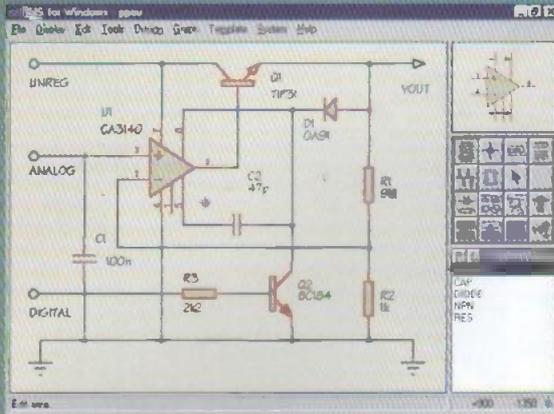
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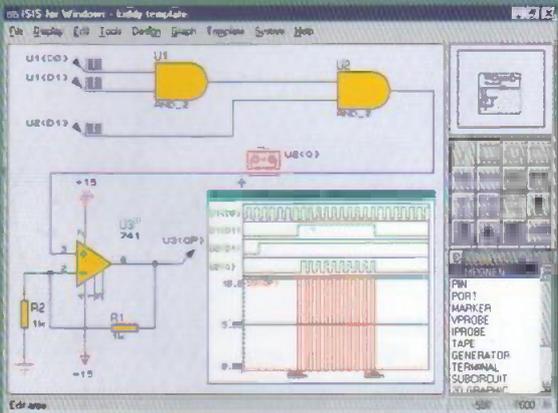
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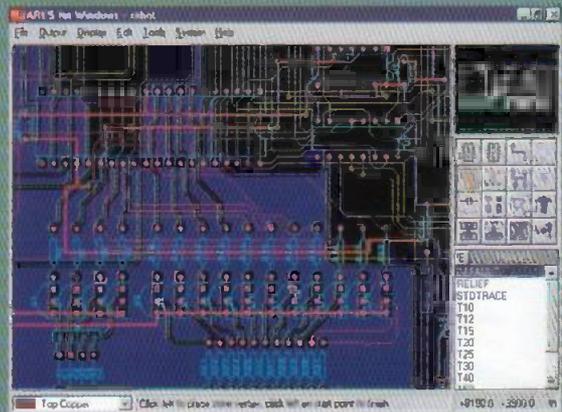
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