

EE EQUALISER-Ioniser Project

WASHER WARNING AUDIO LOGIC TRACER Exploring Electronics...

EOUALISER.

Special Feature... OP. AMP. LIMITS

The Magazine for Electronic & Computer Projects







The Magazine for Electronic & Computer Projects

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INCORPORATING ELECTRONICS MONTHLY

The Magazine for Electronic & Computer Projects

VOL 16 N95

MAY '87

HAVE YOU GOT A JOB YET?

OR many people the problem of finding a job when leaving school is now a very real one. Ten or twenty years ago you knew that there would be something to do when you finished studying but nowadays things are different. Of course electronics is one of the career areas that has excellent potential and plenty of jobs available at nearly all levels. Our electronics and associated industries are crying out for qualified engineers and look as though they will be for some time.

All this should mean plenty of school leavers aiming themselves at this potential area of employment. However, in many places this is not the case and I feel that the problem lies in the schools themselves-or more accurately, in teacher training. This magazine used to run a school scheme subscription service but it has now been discontinued through lack of interest from the schools.

In some cases subscriptions going to teachers are not renewed and at least one teacher has told us this is due to lack of pupil interest. Of course many schools do still subscribe and report on how useful the magazine is to both teachers and students. This is perhaps the crux of the matter; the teachers need the Teach In courses as much as the pupils! Most schools have no-one who has been trained to teach electronics and the maths or science teacher gets roped in

How can we try to stimulate students when we have no-one who can teach them? Electronics has been around in its present popular (semiconductor) form for about 20 years and only recently have our educational authorities woken up to it.

EXCELLENT WORK

To be fair, a great deal of excellent work is now being done and we sell many magazines to colleges, ITECs and various other training or retraining establishments. Indeed, our circulation is steadily increasing and EE is the most popular hobby electronics magazine in the UK. But it seems to me that we still have a long way to go with teaching the under 16s. I would like to hear from teachers, students and anyone else interested in this area. Let us know what you feel on this subject. You might all write and say I'm wrong; I hope you do, I would be delighted to put the record straight if I am.

EE is aiming to publish a very important series for students this autumn, a series that will lead to real benefits for those wishing to find a career in electronics. We would like more people to be able to move into this rewarding area of employment. Let's hear your views on what can be done or what is being done in this important field.

Nike Kenerente



BACK ISSUES & BINDERS

Certain back issues of EVERYDAY ELECTRONICS and ELECTRONICS MONTHLY are available price £1.50 (£2.00 overseas surface mail) inclusive of postage and packing per copy. Enquiries with remittance, made payable to Every-day Electronics, should be sent to Post day Electronics, should be sent to Post Sales Department, Everyday Electronics, 6 Church Street, Wimborne, Dorset BH21 1JH. In the event of non-availability remit-tances will be returned. *Please allow 28 days for delivery*. (We have sold out of Oct. and Nov. 85, April and May 86.) Binders to hold one volume (12 issues) are available from the above address for C4 95 (59 00 overseas surface mail inclu-

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You can have a fresh start if you call in the Equaliser

N ioniser is a device for injecting a A stream of negative ions into the surrounding air. The reason for doing so is based on the discovery that rural areas, where the air is supposedly "Fresh", have far higher concentrations of natural negative ions in the air than urban places; ergo, if the ion levels in our living rooms are artificially increased, we should become healthier, or at least feel better! There is still much debate regarding this, but commercially produced ionisers are now freely available and many users swear by them.

The author's personal experience is that ionised air does indeed seem "fresher" odours are reduced to some extent, and a room with an ioniser is generally a more pleasant place to inhabit. Whilst prices of commercial models remain at their present level however, home construction makes good sense.

Readers of long standing may recall an earlier design by the present author. This

COMPONENTS

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See Semiconductors D1 to D30 IN4007

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Miscellaneous

page 255

ABS Box 150 x 80 x 50mm, 20mm fuseholder and 100mA quickblow fuse, pins (3 off), 5A twin mains lead and plug, printed circuit board available from the EE PCB Service, order code EE566.

It is essential that the correct working voltage resistors and capacitors are used.



was produced at a time when there was still some mystique surrounding the subject and incorporated one or two features that have since proved unnecessary, resulting in extra expense and complexity.

A negative ion is simply an atom to which an extra electron has been added, giving it a net negative charge. To produce them in air all that is necessary is a high negative voltage, in excess of 4kV, applied to some sharp points.

Once ionised, the air will be repelled from the points (since like charges repel, remember those pith balls from the school lab?) so an actual "wind" of negative ions will be created. The aim of this design is to raise the disadvantages. These are greater complexity for a start, then low efficiency, since the 1N4007 diodes normally used have very poor recovery characteristics above a couple of kHz. Finally, such circuits tend to generate lots of nasty r.f. harmonics, rather difficult to keep from the house wiring if they're mains operated.

A multiplier circuit run directly from the 240V 50Hz mains has much to recommend it, so long as some basic safety precautions are properly observed. Cheap, simple and noise free, it also has greatly improved reliability, an important consideration in an item intended to be operated continuously for-literally years.



Fig. 1. Complete circuit diagram for the EE Equaliser air ioniser.

required voltage as simply and cheaply as possible.

DESIGN

A number of ioniser designs have been published in recent years using various methods of raising the voltage, though all of them end with a circuit known as the "Cockroft-Walton Multiplier", a ladderlike string of diodes and capacitors used for pumping the output voltage up to the final level required. Commercially made units usually start with a transformer to raise the mains voltage to 1kV or so, then continue with around five multiplier stages built with 5kV rated components. This is impractical for home constructors since both the transformer and the 5kV rated parts would be difficult to obtain.

Another approach employs a 12V power supply feeding an oscillator running at several kHz. This in turn drives a ferritecored transformer followed by the multi-plier. The claimed benefit is that the higher frequency permits the use of smaller capacitors, but unless battery operation is required this is heavily outweighed by the

CIRCUIT

The basis of the EE Equaliser circuit, Fig. 1, is a thirty-stage multiplier, using 1000V diodes and 1000V disc ceramic capacitors. A 100mA fuse is provided in the supply, as a safety precaution against the unlikely event of catastrophic failure of several chain components. R1 has little effect during normal operation, but when the unit is disconnected it ensures the discharge of any stored voltage that might cause a shock to be received from the plug. In theory the multiplier produces over 20kV, but it's regulation is fairly poor and the impedance so high that even the tiny ionising current drawn from it pulls the output down to around 5kV; ideal for ion generation.

Safety is a major consideration with a circuit of this type, of course, and this is ensured by the chain of resistors between the output and the ion emitters. These are special high-voltage rated types; on no account should they be omitted or replaced by a single resistor of a higher value, which may not have an adequate voltage rating. The "ion emitters" are ordinary needlework pins positioned so that they project just above the surface of the unit's case.

CLEANLINESS

Constructors should ensure both their hands and the board are clean before starting to build the board, as losses through natural skin oils, etc. on the surface can significantly reduce the ion output.

Note that the edge of the board is undercut where the emitters are positioned; if this hasn't been provided it can be done with a small file. The edge should be smoothed here too, with some fine emery paper.

CONSTRUCTION

Construction could hardly be simpler, though the compact layout calls for careful use of a soldering iron with a small tip. The component layout and p.c.b. master pattern is shown in Fig. 2 and Fig. 3. This board is available from the EE PCB Service: code EE566.

Since all the resistors, capacitors and diodes are of the same type it would be difficult to get it wrong! The diodes are



Fig. 4. Preforming the diodes, cathode (band) at top of the bend, prior to inserting on the board.



Fig. 2. Printed circuit board component layout.



Fig. 3. Full size printed circuit board master pattern. This board is available from the EE PCB Service: code EE566.

The finished board showing the preformed diodes.

The completed unit showing the tips of the "ion" emitter pins.

EQUALISER





Fig. 5. Low voltage set-up for testing the board. Check voltage drop across each diode.

polarity-conscious of course; start by bending them all to the shape shown in Fig. 4 with the cathode (band) at the top, then they will all be correct when fitted to the board. Solder these in first, then fit the capacitors, then the resistors.

Because ions are emitted from sharp points, the solder joints should be smooth and rounded, especially towards the output end of the board. The easiest way to achieve this is to solder the components in place tion as shown in Fig. 5. Silicon diodes exhibit a voltage drop of about 0.6V in the forward direction, so the drop across the chain will be of the order of 16V to 18V. The remainder will appear across the resistor, which thus limits the current to one or two milliamps. It's now a simple matter to check that the appropriate voltage is appearing across each diode; faults in either components or construction will be immediately apparent. emitter points end up nearly central. Try it in position and mark the position of the points, so that holes, about 4mm dia., can be accurately drilled for them. A suitable cord-grip should be fitted to the mains lead.

Cable entry and fuse are on the copper side of the board, as there's more room here. The Neutral is connected directly to the board, the Live goes via the fuse. Ensure the connections are the right way round, as a peak-to-peak mains ripple superimposed



Fig. 6. Interwiring and layout of components inside the case.

with the leads projecting vertically from the board, crop them, remove all the sharp bits with a large flat file, then run over them all again with the iron and some extra solder to achieve the smooth rounded joints required.

The emitter points are made from ordinary pins. Cut these to length so that, when the points are positioned to project 1mm or so above the top of the case, the bottoms are within the area of the mounting pad. The easiest method of fitting is to stick them into something, cardboard perhaps, at the correct spacing, and prop them in place whilst soldering is carried out. Once again the joints should be made smooth and rounded.

TESTING

The board should be tested next. Live testing should never be attempted owing to the obvious dangers involved, but an excellent low-voltage check is quite easy to carry out. A d.c. supply of around thirty volts (three 9V batteries will suffice) should be connected across the diode chain with a series resistor of 10k or so, in the forward direc-

ASSEMBLY

Final assembly is just as simple. The board is designed to fit into the moulded slots in the specified case as shown in Fig. 6, slightly to one side of centre, so that the on the output voltage is not to be recommended, even though it wouldn't prevent it from working. A small piece of foam plastic suffices to hold the board firmly in place when the base is screwed home; on the prototype pieces of draught excluder strip were used.



The printed circuit boad slotted into the case of the ioniser.



OPERATION

Fifteen seconds or so after the ioniser is plugged in, it should be possible to hear a very faint "rushing" noise from the emitters. The impedance of the multiplier is such that it takes this long to build up to full voltage. It may be possible to feel a slight coldness against a hand held some 5-10cm above it, due to the "ion wind" of ionised air being repelled from the emitters.

A body or object placed too close seems to raise the potential difference and may cause the production of ozone, which has a distinct smell like arcing motor brushes: these tests may cause this. In normal use though, this unit will not produce ozone. If your body is well insulated, holding your hand over it for a few seconds will cause a charge to accumulate. Subsequent touching of an earthed object will produce a faint discharge, similar to static.

TEST WAND

The most effective test can be carried out if desired with the indicator shown in Fig. 7, made from a piece of scrap p.c.b. This is a simple neon relaxation oscillator; ions picked up from the air charge the capacitor until the striking voltage of the neon is reached, when it will flash. Simply hold the device between thumb and forefinger with the other end close to the emitters. The neon should begin to flash some fifteen centimetres away and the rate will increase rapidly as the distance is reduced. The ioniser will probably cause your air to seem fresher and cleaner, especially if used in a small "stuffy" room. It may help to clear odours such as tobacco smoke; it certainly does this in the author's home. Whether it confers any health benefit is open to conjecture, though the cost of this design is certainly low enough to encourage trial!

One unwanted effect that should be noted is that airborne dust and dirt particles become charged and promptly stick to the nearest neutral object. Over a period of months this can amount to a surprising amount of dirt, which seems to ingrain itself right into the surface and becomes difficult to clean off. Ionisers, therefore, should not be operated for long periods in close proximity to expensive decorations.

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WINDSCREEN WASHER WARNING

T. R. de VAUX-BALBIRNIE

Stay within the law with this simple add-on unit

The LAW requires car windscreen washers to be kept in good working order. The water bottle is often badly located, however, and the average motorist does not check the contents before every journey. Driving under poor weather conditions, the water can be used up very quickly and a dangerous situation develops.

This unit delivers a bleep each time the washers are used if the water has fallen below some predetermined level. There is then time to refill at the next service station. This circuit is suitable for all cars having electrically-operated windscreen washers and will operate with plain water, windscreen cleaners or antifreeze mixtures in the bottle.

CIRCUIT DESCRIPTION

The circuit diagram for the Windscreen Washer Warning is shown in Fig. 1. IC1 is an integrated circuit timer connected as a monostable. This means that once triggered by making pin two low (supply negative voltage), the output (pin three) goes high (positive supply voltage) for a certain time then reverts to low. Pin two is normally kept high since the potential divider R5 and R6 applies almost 12V to it. The time during



Fig. 1. Complete circuit diagram for the Windscreen Washer Warning.

which the output remains high depends on the values of C3 and R7—with those used this will be approximately one second.

The circuit receives current from the existing wires feeding the washer pump motor so that when this is operated, ICl pin two receives a momentary trigger pulse as Cl charges through R5. Note that Fig. 1 shows the washer motor switch in the *positive* battery wire but there will be no difference in operation if it is in the *negative* one.

The trigger pulse will have no effect if IC1 is disabled by keeping the reset input (pin four) low and this is the case when there is sufficient water in the reservoir. Then, probes A and B situated in the bottle, are effectively short-circuited by the water



which is a reasonable conductor of electricity. Base current then flows to TR1 turning it on. The collector—hence IC1 pin four—is thus kept low and IC1 disabled.

With a low water level, there is no conducting path between A and B so TR1 remains off. The collector is then high and IC1 allowed to operate. Diode D1 prevents possible damage if the circuit is connected with the wrong polarity. Also, in conjunction with C4, it smooths the supply from the charging system and allows for correct operation in cases where the washer switch contacts do not "make" cleanly.

CONSTRUCTION

Construction is based on the layout shown in Fig. 2. This uses a piece of 0-1 inch matrix stripboard size seven strips by 20 holes. Begin by making the breaks in the copper tracks and soldering the link wires as indicated. Follow with the on-board components including the i.c. socket. Do not insert the i.c. itself until construction is complete, however. Take care to observe the polarity of D1 and C4. After a careful check for errors—particularly for accidental "bridging" of adjacent copper tracks —solder 10cm pieces of light-duty stranded connecting wire to strips A, B, C, F (2 off) and G as shown.

Prepare the case by drilling a small hole for the wires passing through to the terminal block, TB1. Mount TB1 on the side using two small fixings (see photograph). Attach the circuit panel to the side and the buzzer to the base of the box with adhesive fixing pads. Refer to Fig. 3 and complete all wiring.



Fig. 2. Circuit board component layout and details of breaks to be made in the underside copper tracks.

PROBES

For the prototype unit, the probes consisted of a piece of "figure 8" twin stranded wire (see Fig. 4). This has the advantage of being easily replaced should the need arise. The wire is passed through a small hole drilled in the top of the water bottle. Fit this with a rubber grommet to grip the wire and keep it at the required level. Decide on a suitable place for the unit behind the car dashboard. Connect the probes to TB1/1 and TB1/2 then connect TB1/3 and TB1/4 to the positive and negative terminals respectively of the windscreen washer pump motor-use auto-type stranded wire of 3A rating for these connections. If the polarity of the motor is not known, connect either way and test the unit for correct operation-it will not work if it is the wrong way round.

If the motor is fitted with spade terminals, the additional wires may be added using "piggy-back" converters. Many cars, however, use a special block connector. In this case attach the new wires, if possible, using connectors which do not involve breaking the wires. These are available from car accessory shops. Wherever wires pass through a hole in metal, a rubber grommet must be used.



Most small soild-state buzzers may be used except for the driven type which is not suitable. A high-intensity buzzer should be used if the unit is intended for a vehicle where the washer system is situated near the driving position and therefore noisy. The lid of the case may be drilled with a matrix of holes, if necessary, for the sound to pass through.

The completed circuit board showing the 555 chip mounted in an i.c. holder.

It now only remains to test the system under real conditions. Note that R3 determines the sensitivity of the circuit. If the buzzer sounds with the probes in water, R3 should be increased in value. If, on the other hand, it fails to sound when the probes are removed and are just slightly damp, its value should be reduced. Do not alter the value of resistor R3 unless a check reveals





TO T81/1 & TB1/2

Fig. 4. Suggested arrangement for using "twin-flex" wire as the level sensor probes.

that there are no circuit faults, however. When testing, allow a pause of at least 10 seconds between operations for capacitor C4 to discharge.



Everyday Electronics, May 1987



THIS month, as promised, we shall be turning our attention to the construction of a Versatile Sound Synthesiser for use with the Spectrum. Before we get started, however, a number of readers have asked whether a Spectrum can produce specialised electrical symbols. The answer, of course, is "yes"; all that is required is a knowledge of the Spectrum's User Defined Graphics which can be gained from the User Guide.

As an example (see listing 1), we show how it is possible to print the Greek letters Ω , μ , and λ whenever the O, U, and L keys respectively are depressed when in graphics mode (G cursor). Those familiar with the software package which formed part of our 'Teach-In '86' series will have already seen these UDG's in action!

10 REM Set User Defined Graphics 12 DATA 0,60,66,66,66,36,102,0 14 FOR j=0 TO 7 16 READ k 18 POKE USR "O"+j,k **20 NEXT j** 22 DATA 0,0,68,68,68,124,64,64 24 FOR j=0 TO 7 26 READ k 28 POKE USR "U"+j,k 30 NEXT j 32 DATA 0,0,32,80,8,20,34,0 34 FOR j=0 TO 7 36 READ k 38 POKE USR "L"+j,k 40 NEXT j

SOUND SYNTHESISER

One of the principal shortcomings of the early versions of the Spectrum (16K, 48K, and Plus) is associated with the inability to produce any sound other than a strangled "beep"! Whilst some programmers have managed to provide some surprisingly good results from this, the 128K and Plus Two machines with their built-in sound synthesiser chips must be heralded as a major step forward. Those not lucky enough to own the latest breed of Spectrum machines may like to know that it is not too difficult to add a sound synthesiser to an existing Spectrum. Such an addition will NOT make your

48K Spectrum compatible with any soft-

ware which runs exclusively on the newer machines, however, it will allow you to add some really exciting sound effects and music to your own programs. Indeed, there is a great deal of fun to be had simply in experimenting with the sound synthesiser—it is eminently possible to produce some astounding sound effects using very simple BASIC, assembly language, or FORTH routines.

The Sound Synthesiser is based on a General Instrument device, the AY-3-8913. As this device is quite complex, it is worth spending a little time describing its principal features and architecture before discussing the detailed circuitry of the Sound Synthesiser.

The AY-3-8913 has a well proven track record (close relatives are used in Z80 based MSX machines as well as in the powerful 68000 based Atari ST series of "top-of-the range" home computers) and is one of the latest of a family of sound synthesiser chips from General Instruments. Several of these devices include parallel I/O capability (see table below), however, all have virtually identical circuitry for sound synthesis.

Resistors See **R1** 1k3k9 **R**2 **R3** 3k9 3k9 **R**4 R5 22k 470 R6 page 255 R7 470 **R**8 270 All 0.25W 5% carbon Potentiometer VR1 10k miniature skeleton preset Capacitors

COMPONENTS

C1 1	00µ p.c. elec. 16V
C2 1	Oµp.c. elec. 16V
C3 1	Oµp.c. elec. 16V
C4 1	00n polyester

Device	Number of analogue output channels	Number of I/O ports	Package
AY-3-8910	3	2 × 8-bit	40-pin DIL
AY-3-8912	1 3	1 × 8-bit	28-pin DIL
AY-3-8913	3	nıl	24-pin DIL

The AY-3-8913 has three independently programmable tone generators, a programmable noise generator, a programmable mixer (for combining tones with noise) and is capable of producing fifteen logarithmically raised volume levels, with programmable attack, delay, sustain, and release (ADSR). The deivce is TTL compatible and designed for direct connection to a microprocessor bus.

The simplified internal architecture of the AY-3-8913 is shown in Fig. 1. The heart of the device is the array of sixteen registers which define the sounds produced. Data can be transferred to/from the control register array by means of eight bi-directional buffers. An eight-bit latch is also driven from the system data bus and this is used to select a particular control register depending upon the state of the bus control (BC1) and bus direction (BDIR) lines.

Individual bits stored in the control register array are responsible for determining, amongst other things, the frequency and amplitude of the outputs of the three tone generators. In addition, bits are used to determine the envelope of the waveform (more on this topic next month!) and the amount of noise mixed into the output.

The complete circuit of the Spectrum Sound Synthesiser is shown in Fig. 2. The AY-3-8913 is enabled whenever the Spectrum's IORQ and A7 lines are simultaneously taken low. Thereafter, the action of IC1 is determined by the state of the BC1 and BDIR control inputs, as shown below:

	C5	100n polyester
	C6	100µ p.c. elec. 16V
	C7	10µ p.c. elec. 16∨
	C8	100µ p.c. elec. 16V
	C9	220µ p.c. elec. 16V
	C10	10n polyester
S	emic	onductors
	D1	Red I.e.d.
	IC1	AY-3-8913 sound
		generator
	IC2	74LS08 Quad 2-input
		AND gate

IC3	74LS27 Triple 3-input
	NOR gate
10.4	74100411

- IC4 74LS04 Hex inverting buffers
- IC5 LM380N 2W audio amp

Miscellaneous

LS1 8ohm loudspeaker. X1 2 MHz crystal

X1 2 MHz crystal. Low-profile d.i.l. sockets, 1 × 24pin, and 4 × 14-pin; 0-1in matrix stripboard, measuring approx. 80mm × 100mm; 28-way open end double-sided 2-54mm (0-1in) pitch edge connector, (e.g. Vero part number 838-24826A); terminal pins (2 required).

Approx. cost Guidance only £16.50

CPU operation	AY-3-891 inp	13 control uts	IC1 action			
Cr O operation	BC1	BDIR	ACT action			
Read, A7 taken low Read, A7 taken high Write, A7 taken low Write, A7 taken high	0 1 0 1	0 0 1 . 1	inactive read data from registers write data to registers latch data, write register numbers			



When assigning port addresses to the sound synthesiser, it is important to avoid using any address for which the five least significant address lines go low. This precaution is necessary to avoid conflicts which may arise from the simplified method of address decoding employed in the Spectrum's uncommitted logic array (more of this in a future instalment of On Spec).

We must thus ensure that address lines A0 to A4 are all high when the Sound Synthesiser is being addressed. Assuming that A5 is also high (e.g. for controlling some other external hardware) we are left with just two possible addresses, as shown:

Operation		A6	A5	AÀ	A3	A2	Al	AO	Address		
									dec.	hex.	
Red/write data from/to last				-					-		
register selected	0	0	1	1	1	1	1	1	63	3F	
Write register number	1	0	1	1	1	1	1	1	191	BF	

The AY-3-8913 requires a stable 2MHz clock. This is generated by crystal X1, inverters IC4a, IC4b, and IC4c and associated circuitry. The three analogue outputs, pins 15, 17 and 18, of IC1 are summed and applied to a simple 1W audio amplifier stage configured around the ubiquitous LM380N (IC5).

Construction

The Sound Synthesiser may be assembled on a piece of Veroboard measuring approximately $80mm \times 100mm$. The precise dimensions of the board are unimportant provided that it has a minimum of 28 tracks aligned in the vertical plane sufficient to allow the mounting of a 28-way double-



Everyday Electronics, May 1987

sided edge connector. This connector should be fitted to the lower edge of the board and will require five holes across the full width of the stripboard so that the board stands vertically when the connector is mated with the Spectrum.

Before soldering any of the components (including the four i.c. sockets) it is important to allow some clearance for the rear overhang of the case. For the Spectrum this gap should correspond to 8 rows of holes (20mm approx.) whilst for the Spectrum Plus and 128, the gap should be increased to 12 rows of holes (30mm approx.).

Component layout is generally uncritical though, as with most of our On Spec projects, considerable economies can be made by carefully planning the layout in advance of mounting the components and i.c. sockets. Readers are advised to carry out this exercise on paper first (using, if desired, the layout sheet provided with our On Spec Update).

After mounting the five i.c. sockets, great care must be taken to ensure that all unwanted tracks are cut (including, in particular, those which link the upper and lower sides of the 28-way connector). A purpose designed "spot-face" cutter is ideal for this purpose or, if such a tool is not obtainable, a small sharp drill bit may be used.

The remaining components (resistors, capacitors, variable pre-set resistor, crystal, and l.e.d.) should then be fitted to the board. As usual, the decoupling capacitors (C1 to C6 and C8) should be distributed around the board with the smaller value capacitors (C4, C5 and C8) being placed as close as possible to the supply inputs of IC1, IC4, and IC5 respectively. Care should also be exercised when soldering the crystal X1 to the board as excessive heat from the soldering iron can cause permanent damage to the quartz element.

Links on the underside of the board should make use of appropriate lengths of miniature insulated wire (of the type normally used for wire wrapping). Readers requiring further information on the connector should refer to March 1985 On Spec.

When the stripboard wiring has been completed, the integrated circuits should be inserted into their respective sockets (taking care to ensure correct orientation of each device), preset VR1 should be set to midposition, and an 8 ohm loudspeaker should be connected. Finally, the entire board and wiring should be very carefully checked *before* attempting to connect it to the Spectrum. (Note that the Spectrum should ALWAYS be disconnected from its supply before either connecting or disconnecting any interface module).

If all is well, when power is re-applied, the normal copyright message should appear. If not, disconnect the power, remove the interface and carefully check again!

Simple Test

For those anxious to test the interface in advance of next month's instalment, the following simple BASIC program can be used to load the internal registers of the AY-3-8913:

10 INPUT "Register number"; rn 20 INPUT "Data value"; val 30 OUT 191,rn 40 OUT 63,val 50 GO TO 10 Enter the test program and reply to the prompts which the following values:

Register Number	Data Value
0	28
1	1
7	62
8	8

The data entered should produce a continuous tone of moderate volume having a frequency of approximately 440Hz (for the musical, this should be reasonably close to middle-A). Once enabled, the Sound Synthesiser will continue producing its output until the power is disconnected (or the reset button is pressed). To switch the tone off, it is merely necessary to load a data value of 8 into register 8.

If you have any comments or suggestions or would just like a copy of our On Spec Update, please drop me a line at the following address and enclose a large (A4 size) stamped addressed envelope:

Mike Tooley,

Department of Technology, Brooklands Technical College, Heath Road, Weybridge, Surrey, KT13 8TT.

Next month: We shall take a detailed look at programming the Versatile Sound Synthesiser. We shall also include some routines for rapidly manipulating the Spectrum's video memory to provide a simple "screen swapping" facility.

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4000 5	ERIES	741 8 SER	ES	MICRO & LSI		IR REMOTE	IC'S	ZENER DIO	DES	TIP146	1.63	CAPACITORS		POLYESTER	l l	RESISTORS	
400111B	0.16	741 500	0.20	MC344BAP	3.90	SL486DP	2.20	BZY88C2V7	0.05	2N2646	0.55	ELECTROLYTI	C	0.01#F250V	0.05	CARBON FILM	
4011B	0.16	741 504	0.20	MC68488P	8.04	SL490DP	1.92	BZY88C4V3	0.06	2N3055	0.50	A=AXIAL		0.022#F250V	0.05	0.25W 5%	
4011UB	0.16	741 513	0.28	ZB0A-CPU	1.80	ML926DP	2.73	BZY88C5V1	0.06	2N3704	0.10	104F35V	0.05	0.033#F250V	0.06	1R - 10M	0.02
4012B	9.21	741 514	0.42	Z80A-DART	4.28	ML 928DP	2.73	BZY88C7V5	0.06	2N3819	0.40	100#F25V	0.07	0.047#F250V	0.06	Ø. 5W 5%	
40138	0 30	741 520	0.20	780A-P10	1.68	VOI TAGE RE	6'6	877880901	0.06	215307	0.20	100 #E50V	0.17	0.068#E250V	0.06	108 - 10M	0.03
40178	0. 2B	741 532	0.20	780A-510/0	4.96	LM317L	0.72	8ZY88C10	0.06	2N5308	0.20	1000#F10V	0.15	0.14F250V	0.08	METAL FILM	
40198	0 50	741 837	0.20	UPD41256-15	2.65	7805	0.40	BZYBBC11	0.06	IC SOCI	ETS	10000 #F40V	3.12	0.47#E250V	0.14	0.4W 1%	
40208	0 66	741 542	0.47	TC55164PL-2	3.00	7808	0.45	B7Y88C15	0.06	TURNED	PIN	2200 #F16V	0.30	POL YPROPOL NE		10R - 1M	0.04
4023B	0.25	741 885	0.62	ICM7217IPI	4.21	7812	0.40	BRIDGE REG	r's	8 PIN	0.21	33#F16V	0.05	2200pF1KV	0.13	ENAMEL	
4024B	0.40	74LS123	0.67	AD & DA CONVE	RTERS	7815	0.45	KBPCBØB	1,42	14 PIN	0.26	330#F16V	0.12	4700pF1KV	0.13	WIRE WOUND	
4025B	0.20	74LS132	0.54	AD7525LN	19.25	7824	0.45	SKB202L5A	0.43	16 PIN	0129	4.74F63V-A	0.08	POLYSTYRENE		2.5W 5%	
4028B	0.26	74LS139	0.46	DACBON-CBI-V	19.50	78L05	0.24	KBU4D	0.95	18 PIN	0.33	4704F10V-A	0.30	33pF160V	0.07	1R2 - 1K5	0.27
4030B	0.29	74L5193	0.98	ADC1210HCD	45.55	78LØ8	0.25	W005	0.26	20 PIN	0.37	470#F50V	0.30	47pF160V	0.07	6W 5%	
4040B	0.50	74L5240	0.67	ADC1211HCD	39.96	78L12	0.25	TRANSISTOR	RS	24 PIN	0.46	4700 #F25V	1.58	100pF160V	0.07	1R2 - 2K4	0.46
4042B	0.41	74LS244	0.58	DACOBOOLCN	2.45	DIODES		BC107	0.09	28 PIN	0.54	DISC CERAMI	C	330pF630V	0.11	CERAMIC	
4050B	0.29	74LS245	0.75	DAC1200HCD	18.84	1N4001	0.03	BC108	0.08	40 PIN	0.75	0.047#F50V	0.04	470pF160V	0.08	17W 10%	
4053B	0.50	74LS365	0.42	DAC1201HCD	15.15	1N414B	0.02	BC182	0.08	IC SOCI	KETS	0.1#F25V	0.06	1000pF250V	0.11	1R0 - 10K	0.28
4063B	0.70	74L5373	0.58	ICL7109CPL	8.40	1N4933	0.25	BC212	0.09	PLAIN		0.1#F63V	0.14	1800pF160V	0.08	SIL NETWORK	5
4066B	0.20	74HC SER	ES	AD7542KN	18.94	1N3891	1.89	BC327B	0.08	6 PIN	0.07	47pF63V	0.03	SIEMENS		Ø.125W %5	1
40688	0.21	74HC00	0.33	LINEAR		1N5339B	0.36	BC546B	0.07	8 PIN	0.08	120pF63V	0.05	0.1#F100V	0.11	BCOM (9PIN)	
4069B	0.20	74HC02	0.33	LF398N	3.95	1N5401	0.12	BC556A	0.08	14 PIN	0.09	MONOLITHIC		0.47µF100	0.19	100R - 100K	0.31
4070B	0.20	74HCØ4	0.33	LM311N	0.44	310003	0.64	BD131	0.40	16 PIN	0.10	MULT I-LAYER		1.0µF100V	0.22	THERMISTOR	-
4071B	0.20	74HC11	0.33	LM324N	0.41	BAT85	0.10	BD233	0.33	18 PIN	0.15	100pF50V	0.13	MONOLITHIC		BEAD (NTC)	
40788	0.21	74HC85	0.83	LM308N	0.65	BYV32-100	1.24	BF259	0.26	20 PIN	0.17	220pF50V	0.12	SINGLE-LAYER	2	4K7 GM472W	1.95
40818	0.16	74HC139	0.58	LM741CN	0.32	BYV95B	0.18	BSR50	0.44	22 PIN	0.19	470pF50V	0.10	100pF50V	0.03	POTENTIOMET	ERS
4510B	0.46	74HC200	1.01	MC1458CP1	0.41	BYV95C	0.20	BUS48P	2.65	24 PIN	0.23	1000pF50V	0.10	1000pF63V	0.02	CERMET 3/8"	SQ
4511B	0.46	74HC240	0.58	MC3340P	1.30	BYX71-600	1.10	BUS98	5.70	28 PIN	0.25	2200pF50V	0.10	2200pF63V	0.03	PCB TOP ADJ	UST
4514B	0.91	74HC244	0.95	ICL7660CPA	1.76	BY206	0.20	IRF520	1.75	40 PIN	0.30	4700pF50V	0.12	4700pF63V	0.04	100R - 200K	0.30
45188	0.40	74HC245	0.92	SG3526N	3.69	40HF20	1.16	J112	0.30	IC SOCI	KET	6800pF50V	0.14	TANTALUM		PCB SIDE AD	JUST
4543B	0.58	74HC251	0.43	SG3526J	4.92	40HFR20	1.16	IRF840	7.59	TRANSIS	STOR	0.014F50V	0.08	1.0µ16V	0.09	500R - 200K	0.30
4547B	1.23	7.4HC273	0.83	TL074CN	0.66	M16-100	0.93	MTP8N10	1.85	3 PIN	0.17	0.022#F50V	0.08	6.8#F10V	0.12	MULTITURN	
40174B	0:48	74HC354	0.51	TLØ72CP	0.65	M16-100R	0.93	MJ3001	1.46	8 PIN	0.38	0,033#F50V	0.15	10µF10V	0.10	PCB TOP ADJ	UST
40192B	0.56	74HC373	0.79	TL071CP	0.39	M25-100	1.27	MJ2501	1.52	10 PIN	0.42	0.047#F50V	0.14	10#F16V	0.13	100R - 200K	0.85
40193B	0.56	74HC374	0.79	UA714HC	4.48	M25-100R	1.27	TIP110	0.36	SIL SO	CKET	0.068#F50V	0.10	22#F16V	0.21	PCB SIDE AD	JUST
40194B	0.65	74HC4002	0.71	OPØ7DP	1.43	I.R OPTO		TIP115	0.39	STRIP		0.1#F50V	0.08	33#F16V	0.32	200R - 200K	0.85
40195B	0.83	74HC4022	0.54	UA759U1C	2.72	TPS703A	1.25	TIP121	0.39	6 WAY	0.12	0.22 #F50V	0.25	47#F6.3V	0.23	PLASTIC TRA	CK
40374B	1.10	74HC4040	0.54	MC1436CG	5.70	TLN105A	0.44	TIP126	0.39	12 WAY	0.22	0.47 # 50V	0.49	100µF6.3V	0.57	SINE/COSINE	
40374	1.10	74HC4060	0.56	UGN3020T	2.58	TLN105	0.40	TIP141	1.59	20 WAY	0.56	1.0µ50V	0.84	150µF6.3V	0.94	2 x 5K 5%	18.25
RM FEI	RROXC	JBE		L.E.D'S .2" I	AIC				-				-				
RM6+BO	BBIN+	ADJ+CLIP	1.07	RED TLR113A	0.10	0.43"		ORDER	RS TO:			PLEASE ADD	£1.0	2 pkp & 15% \	AT.		
RM7+BO	BBIN+	ADJ+CLIP	1.17	GRN TLG113A	0.13	CA TLR342	0.89	Xen-l	Electr	onics		Orders Over	£20	Post and Pac	:king	Free	
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Extra motors may be bought at the same time for £13.45, this is again a 20% reduction. The Interface Kit includes etched, drilled and tinned p.c.b., ribbon cable (1 metre), 20 way IDC connector, full instructions, circuit diagram, software demonstration cassette and listing for the BBC ''B'' Micro.

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RRD REPORT

The report on the activities of the Radio Regulatory Division of the DTI for 1985/86 makes interesting reading from the radio amateur's point of view. Published in December, it is introduced by Geoffrey Pattie, Minister of State for Industry and Information Technology, as an attempt to improve openness and consultation with users of the radio spectrum, and provides a lot of factual information.

The RRD, renamed the Radiocommunications Division since the period covered by the report, is concerned with the use and management of the radio frequency spectrum, excluding broadcasting policy and sponsorship of the radio communications industry.

Amateur radio is just one of 13 different types of licence category, and like those other categories it depends on the Division for the allocation and protection of frequency bands, both nationally and internationally. The Division is concerned with preventing and overcoming radio pollution, eg, illegal or unsatisfactory transmissions: and is dedicated to a policy of deregulation wherever possible.

FEWER CONTROLS

Traditionally, radio has been regulated in immense detail but, says the report, it is now becoming apparent that the spectrum can be safeguarded with fewer controls, provided there is rigorous enforcement of those which remain. The aim is to make radio more readily and widely available, eliminating licensing altogether where this can be done without damage to licensed use—for example it is proposed to exempt a wide range of low power devices.

A potentially worrying development is the DTI Spectrum Pricing Study which was due for completion by the end of 1986. Phrases like "pricing (as a resource rationing mechanism)", and "promoting competition and consumer choice in the communications market", make one wonder what is to come. It is understood that the Study report proposes detailed control of blocks of radio frequencies by Spectrum Management Licensees (SMLs), who would "sell" them to users. It is also understood that, at this time, amateur radio has been excluded from the new proposals.

For radio amateurs, the RRD report records three major events in the year ending 31st March, 1986. Firstly, the granting of an allocation at 50MHz from 1st February, 1986; secondly, the DTI's acceptance of the Radio Society of Great Britain's tender for the running of the amateur Morse test, with the new arrangements commencing on 1st April, 1986; and, lastly, following a successful experimental period, the decision to allow class B licensees to use Morse permanently on the v.h.f. bands.

Computerisation of the licence records by the Post Office, acting as agents for the DTI, was completed in 1984, and the issue of licences is now normally completed in five days. The report also records that during 1985 agreements were concluded with the USA, Canada, and the Falkland Islands, enabling international greetings messages to be passed from special event stations, in time for the Scouts' Jamboreeon-the-Air that year.

CONSULTATION

It is important for radio amateurs that their national society, the RSGB, is involved in ongoing discussions with the DTI on both national and international matters. The report records that regular meetings are held with the Society at which a wide range of amateur topics are discussed. During the year in question, topics coverèd included a strategy for dealing with interference to television and radio reception, crossband working, packet radio, licence revision and research permits.

Some interesting statistics emerge. Prosecutions under the Wireless Telegraphy Act for illegal transmitting activities show five convictions for unlicensed use of the amateur bands as opposed to 896 convictions for illegal CB operation and 124 on the broadcast bands. As at 31st March, 1986, there were 56,346 amateur licences on issue. The total income from these was £700,000 for the year, and since 1970 increases in the cost of an amateur licence have more or less kept in step with increases in the retail price index.

LOW PRIORITY

It should be a sobering thought for hobby users of the radio spectrum to realise that in 1985/86 the RRD issued 290,173 licences which permitted a further (approximate) 900,000 mobile stations to operate. The management of the entire spectrum is subject to national and international legislation, resulting in a great deal of work for the DTI. Realistically, the interests of hobbyists can't attract a very high priority within the Department unless there is a great deal of responsible and authoritative input from the users themselves, via a representative body, to protect their own interests. There has been a certain amount of internal unrest in the RSGB in recent years, but this first annual report of the RRD must surely demonstrate to all amateurs the continuing need to have, and to support, a national society.

The DTI has three radio amateur information sheets available, free of charge: Explanatory leaflet on the licensing work of the RRD (RALIS 1); Amateur Service allocation in the 50MHz band (BR42); and Morse (BR41). For copies write to: The Library, Room 605, Waterloo Bridge House, Waterloo Road, London SE1 8UA.

CALL THE DOCTOR

I have been reading, in Australia's Amateur Radio magazine*, about the early radios used by the Royal Flying Doctor Service, which were developed by radio amateur Alf Traeger. The Aerial Medical Service, as it was originally called, the brain-child of the Rev. John Flynn, first flew an aircraft on 15th May, 1928, without radio equipment. It was soon realised that cheap but reliable communication would be necessary, and Flynn employed Traeger to devise suitable equipment.

Within twelve months, the first set was on the air. The operator cranked a handle, to provide power, with one hand, and sent Morse code with the other. The first successful experiment was conducted from a nursing home in Alice Springs to Hermannsburg Mission, 150km to the west. To overcome difficulties with the hand-cranking, Traeger substituted the now well-known foot-pedal system, and pedal sets were installed at remote outstations to provide vital links with the flying doctor bases.

The next improvement was the development of the keyboard transmitter, first used in 1931, to send Morse signals without the need to learn Morse code. Around 1937 came the beginning of voice transmission, while more reliable batteries, with vibrators, began to replace the pedal generators. Traeger's first transmitter, at the AMS original base in Conclurry, Western Queensland, remained in use until the 1940s, and in 1947 was used at a new base in Charleville until improved equipment became available.

ROUND THE CLOCK

Until the mid-50s all equipment was either a.m. or d.s.b. (double sideband) but since January 1978 all communications have used s.s.b. (single sideband). Nowadays, there is a round-the-clock service with a medical officer always on call. When receivers are unattended an incoming signal actuates an alarm which can only be silenced by operating the transmitter.

The RFDS radio system provides the only form of communication for many people living in complete isolation in the outback. When the channels are not occupied for official traffic, telegrams, etc, time is set aside each day for a 'galah' session, a general chat session for all-comers.

Alfred Hermann Traeger, OBE, ex VK5AX/VK8XT, died on the eve of his 85th birthday in 1980. His ingenuity brought immediate medical assistance to innumerable homesteads across the vast spaces of Australia. His invention was the forerunner of the School of the Air, and other communication facilities of today. It is thought there were 30 stations in 1934. Today there are thousands. Traeger has an honoured place in the history of the RFDS. He also stands among the ranks of members of the "amateur fraternity" who have made significant contributions to the development of communications.

"'The Flying Doctor, Pedal Radio and Alf Traeger", by K. McLachlan, VK3AH, October '85.

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Do not exceed the limitation of your Op-Amp circuit

THIS article actually started life in response to a letter in Everyday Electronics, in which it was asked why more articles are not written using the 741C operational amplifier rather than the modern f.e.t. types as many constructors will have a small supply of 741's. In this article it is hoped to present information on the limiting and guaranteed specifications for a number of devices and hope to show why so many of the f.e.t. devices are now in use and will continue to be used to the exclusion of the 741C.

LIMITATIONS

A list of the limiting conditions of a small selection of op-amps, from the old 709C (the grandad of op-amps) to the more recent 741C and the latest j.f.e.t. and MOSFET types is contained in Table 1. The op-amps used in the list were chosen for no better reason than that they are types used by the author in different applications.

In addition many devices are available in dual or quad amplifier packages i.e. the TL071 single, TL072 dual and TL074 quad and also devices without internal frequency compensation such as the TL070. All of these types have identical limiting characteristics.

The ideal op-amp should have the following characteristics:

- 1) Infinite input impedance.
- 2) Zero output impedance.
- 3) Infinite voltage gain.

4) Common mode gain of zerozero output with inputs tied together.

5) Inputs and outputs usable over the full power supply range.

6) Instantaneous output changes —infinite slew rate.

7) All of the above independent of temperature and supply voltage.

No amplifier is able to meet this ideal and it is Table 1 that shows either the maximum or minimum that a particular op-amp can be expected to achieve and still be within specification. In many cases these values are very different from the typical values.

Whilst for a one-off design a constructor can select an op-amp from a batch which is close to the typical characteristics to obtain a working circuit, where the designer is building for mass manufacture or publication then if the designer does not allow for these maxima or minima there is no guarantee that the circuit will work. Failure to design to these limits can result in the output being controlled by the amplifier itself not by the amplifier acting on its input signals. the impedance that the circuit can achieve.

INPUT BIAS CURRENT

The input terminations sink or source a small current, the direction of current flow depending on the input device. This current is the base or gate current of the input transistor.

The significance of the input bias current is that it causes a voltage drop in the feedback network (resistors R1 and R2 in Fig. 1b) and across the bias resistor (Fig. 1a, R3). For a 741C the



Fig. 1. Input biasing and feedback arrangement for (a) non-inverting and (b) inverting inputs.

SUPPLY VOLTAGE

Although not covered by our list of ideals the supply voltage limits have been shown in Table 1. With the exception of the CA3160 the limits are +/-18V or 36V for a single supply rail, all the devices having bipolar (normal transistor) output stages.

It is common to operate op-amps from +/-15V or 30V to give a safety margin as beyond +/-18V damage to the device may result. The CA3160 differs in having a CMOS output stage identical to CMOS logic and this limits the maximum supply to 16V for single rail, but the design of its output stage provides some interesting output differences which we shall discuss later.

INPUT RESISTANCE

Input resistance is the apparent resistance between the two input terminals. In practice when using the operational amplifier circuits in Fig. 1a and Fig. 1b the feedback network attempts to retain both inputs at the same voltage.

This bootstrap effect virtually increases the input resistance to infinity, even for the low input resistance of the 709C device. In normal circumstances it is the input bias current that limits typical bias current is 80nA and with a 47kilohm resistor for R3 the noninverting input will be offset from zero by 4mV and with a 100 times gain amplifier the output, assuming this is not offset by high resistance in the feedback loop, will be 400mV.

In a d.c. amplifier we would need to offset this voltage by a change in design of which the simplest would be the addition of a 47kilohm resistor between the inverting input and the junction of the feedback resistors R1 and R2. There is in fact a small difference in current to the two inputs due to manufacturing tolerances but this is substantially smaller than the input current itself. This difference is called the *input offset current*.

In the limiting conditions we must remember that the input bias current can rise to 500nA rather than the typical 80nA and to guarantee that any 741C will work, the designer must allow for this maximum input current. Consequently a circuit design with the 741C will often call for a low value for resistor R3 and it is substantially easier to choose a f.e.t. input device to overcome the input bias current difficulty, as the f.e.t. types have maximum input currents of 50 to 200pA (0.05nA to 0.2nA).

OUTPUT IMPEDANCE AND VOLTAGE SWING

In our ideal op-amp the output impedance could be zero ohms and the output voltage range would extend to both the positive and negative supply rails. In practice the op-amp's intrinsic output impedance is about 50 to 75 ohms, that is without feedback, but feedback lowers this value to insignificance for a voltage amplifier or raises it to very high values for a current source. Thus output impedance becomes a parameter that is controlled by circuit design.

The output swing remains controlled by the op-amps characteristics and as shown in Table 1 a 741C with a 2 kilohm load can only be guaranteed to give outputs of +/-10V with a dual supply system i.e. outputs of +5 to +25V with a single 30V supply referenced to the lowest potential. Typical values will again be better than this but we must still design for the worst case.

The interesting point about the CA3160 is that with high load impedances say above 10 kilohms the output can reach the supply voltage to within very small limits. With an open circuit output this output range can be from +0.01V to 14.99V with a 15V supply. In some applications this output range may be essential and overcome the choice of a device with a normal output range.

SLEW RATE

The output voltage of an op-amp is not only controlled by the output voltage limits but also by the rate at which the output transistors are able to change the output voltage. This is known as the *slew rate* and is measured in V/ μ s (volts per micro-second).

The implication is that for high frequencies even with a $\times 1$ amplifier which would give an unaltered gain up to 1MHz, due to its compensation

capacitors, the output voltage is limited by the slew rate. Hence whilst a 741C may give a peak to peak output of 26V (typical) on a 1kHz signal this reduces to 20V at 10kHz and to 8V at 20kHz. It is obvious that some care is needed if the device is to form the driver stage of an a.f. amplifier, and it would be safer to ensure that the design only calls for an op-amp output of say 4V over the full audio range remembering that the values given are typical.

INPUT VOLTAGE RANGE

The input voltage range in fact consists of two parts, which in Table 1 are called the *operational input voltage* range and the safe input range. The safe input range is the value of common mode (i.e. both inputs tied together) voltage that can be fed to the inputs of the op-amp without causing damage.

In all cases except the old 709C the input may go up and down over the full power supply range, but to exceed this range will damage the op-amp. We can therefore apply voltages between +15 and -15V to all our op-amps without damage but to have an input at + or -10V with a +/-9V supply from batteries will result in irreparable damage to the op-amp. This is however the safe input range, it says nothing about whether the op-amp will act as an opamp with very high or low input voltages. If the inputs go beyond the input voltage range the gain of the opamp may change drastically or even reverse sign.

All operational amplifiers except the 709C will accept as safe voltages up to +/-15V or the supply limits whichever is the smaller, the 709C being limited to 10V. As to the input voltage range over which the op-amp is usable the values vary considerably depending on the input device design, but for the bipolar and j.f.e.t. devices the limits

are symmetrical about zero volts but substantially less than the supply voltage of +/-15V if correct operation is to be assured.

In the MOSFET input devices CA3140 and CA3160 the common mode voltage may exceed the power supply by +8V or -0.5V without damage and these devices will operate correctly with input down to the negative supply rail. The positive operational limit is similar to the other op-amps.

SAFE DIFFERENTIAL INPUTS

The safe differential input parameter shows the maximum voltage that can be applied to the inputs of an op-amp without damage. The voltage is measured with respect to the noninverting input.

The value of +/-30V for the 741C shows that one input can be shorted to one power supply rail whilst the other is connected to the opposite supply. The 741C will be safe and undamaged no matter what input voltages are placed on the input terminals providing we do not exceed the power supply voltages.

Not so the 709C or the CA devices. The old design of the 709C will suffer damage if the difference in input exceeds 5V in either direction. Any designer using one of these must seriously consider this limitation, and it is a primary reason for the immediate take over of the 741C when it was released.

Again we should note that the CA devices also suffer from a low differential input range but this is due to the metal oxide gate protection diodes rather than the limits of the input transistors themselves. In fact this only serves to show that whilst the MOS-FET inputs of the CA devices excel in common mode input range their design also suffers a limitation in differential input range that needs to be considered.

Table 1.

All values based on split nower supp	ly of +15V except CA3160 which is 0 to 15V single su	vlag

									-		_							
		7090	2		7410		T	L071	С	L	.F35	1	C	A314	10	C	A316	50
Characteristic	min.	typ	max.	min.	typ	max.	min.	typ	max.	min:	typ	max.	min.	typ	max.	min.	typ	max.
Supply voltage range (V)	-	-	±18		-	±18	-	-	±18	_		± 18	±2	—	±18	±5	_	±16
Input resistance (ohms)	50k	250k	—	300k	2M	-	-	10 ¹²	_	-	10 ¹²	-		1.5 ×10¹²	_	-	1-5 ×10 ¹²	
Input bias current (Amps)		0·3µ	1.5µ	-	80n	500n	-	30 p	200p		5 0 p	200 p	-	1 0 p	50p	-	5p	50p
Input offset current (Amps)		100n	500n	-	20n	200n		5p	50 p	-	25p	1 00 p	—	0·5p	30 p		0 · 5p	30p
Output resistance ohms		150	-	_	-		-	_	_		—			60		-	-	-
Output current mA			-			-		-	—	—		_	-	-	_	12	22	45
Output voltage swing (2k load)	± 10	±13	-	±10	±13	-	± 10	±12	_	±12	± 13•5	—	+12 -14	+13 -14·4	- <u>-</u>	12	13·3 0·002	0.01
Safe common mode input voltage range			±10			±15			±15		—	±15	—		±15	-0·5		23
Safe differential input voltage range	-	_	±5	—		± 30			± 30		—	± 30	_		±8	-	-	±8
											+15			-15-5			-0.5	10
Operational input voltage range	±8	±10	-	±12	±13	-	± 10	±11		±11	-12	-	-15	to +12-5	+11	0	to 12	10
Slew rate V/µS		—		—	0.5	-		13	-	_	13		—	9	-		10	_

min = minimum

typ = typical

ICal .

max = maximum

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CIRCUIT EXAMINATION

Before leaving the reader to look at a number of his or her own circuits let us examine a few simple projects and see what limits are placed on us by the opamp in use.

In 1971 the author constructed a regulated power supply unit using a 709C amplifier using the circuit shown in Fig. 2. The frequency compensating capacitors and resistors needed to stabilise the 709C have been left out for simplicity. This design was carried out before the introduction of dedicated power supply chips notably the 723C. The power supply unit is still in use after 15 years.



Fig. 2. Circuit diagram for a regulated power supply.

Looking carefully at Fig. 2 what can we decide as the limits for this power supply. If we assume that the input voltage is 30V, the guaranteed maximum and minimum output voltages from the op-amp will be +5V to +25Vreferred to the negative rail. With a single transistor drop of 0.6V this will limit our output range to +4.4V to +24.4V for a 741C device.

With this output limit what limits are imposed by the input conditions? For the 741C it is impossible to guarantee satisfactory operation if the Zener diode D1 has a voltage less than 3.0V a better choice will be 3.3V. This will, however, limit the minimum output in this circuit to a voltage which will allow resistor R1 to provide a voltage drop from the output voltage to allow stable Zener operation. A suitable output minimum may therefore be 5.0V.

Resistors R2 and R3 in Fig. 2 provide a potential divider such that in normal operation the voltage at the junction of the resistors R2 and R3 will be equal to the Zener voltage. The input bias current maximum of 500nA will not cause us any difficulty as in this case the values of R1 and hence the Zener current and the current through R2 and R3 can be made to swamp any variation in the op-amp input current.

We must, however, look at the minimum gain of the transistor (this again varies with collector current) and the maximum output current of the opamp, to ensure that at the maximum



Fig. 3. Circuit diagram for a variable regulated power supply.

current output of the PSU, the op-amp can supply sufficient drive to the transistor. To overcome this current difficulty the transistor could be made a Darlington type but the transistor voltage drop then increases to 1.2V.

Thus with a 741C it is impossible to guarantee satisfactory operation except where the output exceeds 5V. The jf.e.t. op-amps are only marginally better being limited in output and input as is the bipolar 741C, and we are unable to take any advantage from their low input requirements.

The MOSFET input devices however offer some possibilities. With either CMOS's output of below -14Vto +12V this equates to a power supply output voltage of 0.4V to 26.4V. The input can be allowed down to zero volts without trouble, hence with these devices we could, with a small modification, build a 0.5V to 25V power supply, and with a Darlington transistor this output becomes 0V to 25V (Fig. 3).

In Fig. 2 we derived the Zener current from the stable PSU output voltage, but in Fig. 3 we need to provide a stable reference voltage to the non-inverting input of the op-amp. Potentiometer VR1 allows a proportion of this stable reference voltage, down to zero volts to be fed to the noninverting input of the op-amp and allows the output voltage to be varied linearly over the full range.

Our greatest problem is the heat output of the Darlington transistor. In the worst case where the output is at a very low voltage say 0.1V this leaves the balance of 29.9V across the transistor. With a 1A output the transistor dissipates nearly 30 watts (29.9V \times 1A) which must be dissipated.

In normal power supply designs a transformer feeds a capacitor via a diode bridge and if a value of capacitor of 2000μ F per amp of output current is selected an additional drop of 3.5V at 100Hz needs to be accommodated. Thus for a peak input of 30V the minimum input voltage to the op-amp will be 26.5V and the op-amp's maximum output will be 21.5V (741C)

limiting the practical PSU output to 20.9V.

PEAK VALUE DETECTOR

Let us now consider the circuit in Fig. 4. This is the circuit of a peak value detector. Op-amp A1 pumps a voltage via the diode D1 into the capacitor C1 until the voltage at the output equals the input voltage.

With an input below the output the capacitor discharges via the resistor R1, the diode reverse current and through the input of A2 (its input bias current). If the value of bias current of A2 is not to affect the discharge time then it is certain that A2 wil be a j.f.e.t. or MOSFET type.



Fig. 4. Peak value detector circuit.

The reverse leakage current of the diode D1 can be ignored being similar to a j.f.e.t. input bias current. There are however no limits set on A1 at present. The input to the inverting input of A1 from A2 will be very low impedance and can be ignored.

The input to the non-inverting input will decide the choice of device for A1. With a low input impedance to be matched A1 could be a 741C, but the packages containing more than one opamp will be more useful. Therefore if A2 is a TL071 type it is as easy to use a TL072 with two amplifiers in the one 8-pin package and make A1 and A2 identical devices. In this case device selection has been made through considerations of p.c.b. space and cost.

RIAA INPUT AMPLIFIER

For our final circuit we return to the non-inverting amplifier Fig. 1a and take a look at the use of an op-amp as the input and matching stage of an audio power amplifier. The practical circuit of an RIAA input amplifier is shown in Fig. 5.

Remembering the circuit of Fig. 1a we could at d.c. achieve an output of 400mV with a typical 741C (actually it is better to use the 748C uncompensated op-amp with an external 10pF capacitor rather than the 30pF internal capacitor of the 741C), but at a maximum the output voltage could rise to 100× max. input bias current × 47k ohms) i.e. 2.35V which is unacceptable.

However, if we alter the circuit slightly we can overcome this difficulty. Since with an audio amplifier we



Fig. 5. RIAA input amplifier circuit.

do not need gain below say 10Hz we can arrange the circuit so that at d.c. the gain is $\times 1$. Looking at Fig. 5,



Super Alpha

For those constructors living in the Sussex area, we have received news of yet another new mail order component supplier starting up trading. (There must be a "high-tech component corridor" in the Sussex/Hampshire area, or is it that people are again waking up to the excitement and opportunities of electronics?)

Offering a comprehensive range of components, we have been informed by the proprietor, Roy McKenzie, that the aims and intentions of Super Alpha Electronics is to run a fast mail order service tailored to meet the needs of the constructor. All the popular components will be available off-the-shelf and no minimum order restrictions will be levied.

It is also hoped that complete kits, including printed circuit boards, will eventually be produced. For further information Super Alpha Electronics can be contacted on 0243 607108.

Spring Sale

We have just had the "Spring Fashiôn Shows", now we have the "Spring Component Sales". Although sales are usually the result of annual stocktaking, the items offered are nearly always excellent value for money and a trip to your local supplier or a browse through forthcoming advertisements can usually unearth real bargains.

Leading the spring "star buys" must be the Elan Enterprises 64 home computer available from Greenweld. Launched on the market just too late to establish a strong public following, the Enterprise 64 is a Z80 based machine which boasts 64K of RAM, built-in wordprocessor, colour, graphics, built-in joystick, stereo sound, printer port and cassette interface.

Originally priced at about £199, Greenweld are offering the last few hundred, in their original boxes, at only £39.95.—Yes £39.95, including VAT and postage!

If you want one, we suggest you ring them on 0703 772501/783740 before they disappear forever. Whilst you are phoning, ask about their special "Sinclair Microvision TV" bargain offer.

EE Equaliser

Because of the very high voltages present in the *EE Equaliser* air ioniser, we strongly recommend that constructors only use top quality components rated as indicated in the "comp list".

The high voltage disc ceramic capacitors should not be too difficult to locate. These are currently stocked by Electromail, Omega, Marco and Cirkit.

The high voltage resistors appear to be only available from Maplin, code HV Res V2M2. The case used to house the components for this project *must* be a plastic type. The printed circuit board is available through the EE PCB Service, code EE-566—see page 284.

Spectrum Sound Synthesiser

In this month's On Spec pages, details are given for a *Sound Synthesiser*.

This circuit is built around the AY-3-8913 sound generator chip. If readers experience any difficulties in locating this device, it is available through Maplin; order code RA91Y.

The audio amplifier chip, 1MHz crystal and the LS series i.c. devices are now fairly common items and most component suppliers should carry stocks. capacitor C1 blocks the d.c. path from the output to the ground in the feedback loop and so at d.c., if we look back and compare with Fig. 1a, the value of resistor R2 becomes infinity and the d.c. gain becomes 1. But at a frequency of 10Hz, the capacitor C1 represents about 160 ohms which is small compared to the 1 kilohms of R3 and decreases with increasing frequency. I have included this to show that all is not too black for the 741C.

Hopefully, the reader will now be able to look at a circuit and decide why an op-amp other than the 741C has been chosen but as now the price differences of the 741C and the alternatives is so small the author cannot see any other effect other than the reduction in use of 741C's.

In the long term it will surely be "so long 741 goodbye but not forgotten".

We understand, from a reader's phone call and subsequent checks, that stocks of the 28-way edge connector are in very short supply and, in some cases up to three weeks delivery times are being quoted to customers.

EE Apex Hi Fi Amplifier

If readers are to obtain the very high performance expected from the *EE Apex Hi Fi Amplifier* then we strongly advise constructors to adhere to the components specified.

A complete kit of parts, including printed circuit boards, for the ''standard'' or ''enhanced'' version may be purchased from Audiokits Precision Components. Also, separate individual circuit stage kits, to enable the constructor to spread the costs over several months, are available. The p.c.b.s. are also available separately.

For a complete listing and prices write to Audiokits Precision Components, Dept EE, 6 Mill Close, Borrowash, Derby DE7 3GU.

Fridge Alarm

The glass bead thermistor type G16/GL16 used in the *Fridge Alarm* should not prove too difficult to source. However, we have only been able to find one source which lists the ICL7611 CMOS op amp i.c. This device is currently listed by Omega Electronics.

Also, the only stockist we have be able to find for the 4-pole 3-position slide switch is Maplin and should be ordered as: FH38R (4-pole slide). You can, of course, replace the slide switch with the more common 2-pole 3-way slide or rotary switch.

Audio Logic Tracer

If readers wish to use the same probe case as shown in the photographs of the *Audio Logic Tracer*—this month's *Digital Troubleshooting Test Gear* project—this was purchased from Electromail: stock no. 508 217.

The earpiece used in this project is an 8 ohm magnetic type and should be available from most of our advertisers. The rest of the components are all readily available and should not cause buying problems.

We do not envisage any component purchasing problems for the Windscreen Washer Warning or the Simple Timer (Exploring Electronics) projects.



Business Sense

Two old stories with a modern twist. Forty years ago Joel Tall invented the Editall tape editing block which most professional studios use. An American, Tall had volunteered for the Navy in 1942 but was turned down because there was something wrong with his mouth, "I am not going to eat the Japs", he argued but Uncle Sam was adamant.

The CBS radio network offered Joel Tall a job and he stayed 21 years. He started off as a technical trouble shooter. His first job was solving the mystery of a transmitter that went unstable as soon as a maintenance engineer had checked it, pronounced it perfect and gone home. Tall realised that closing the transmitter cabinet's door produced a resonant cavity which sent it into oscillation.

At that time all recordings were made on wire or disc. Tall developed a technique for making wire recording edits. After cutting and tying the wire in a knot he would aneal the joint with the hot end of a cigarette. This erased all magnetism at the knot and produced the equivalent of a "blooped" optical film edit—black ink masks the cemented joint in the film sound track.

At the end of the war Jack Mullin, who was subsequently Bing Crosby's tape engineer, brought a German Magnetofon tape recorder back to America. CBS first used it in 1947 to put together a radio documentary called "The British Crisis" (Nothing changes!) They had to use very fragile paper tape because there were no plastics available. The tape was cut with scissors, held flat with a steady hand and stuck together.

One night in 1949 Tall quite literally dreamed up the answer—a curved and grooved channel, precision machined so that it held the tape firm for a cut and splice. In Tall's own words, the Editall made him ''scads of money''.

The Depression

Cut to before the war. Tall, like millions of other young Americans, was trapped by the Depression. He had a fascination for radio and during the 20s survived by going from door to door offering to "fix anything for one dollar".

In the 30s, radio firms employed him to troubleshoot on a professional basis. One had replaced rubber-wound components with cotton-wound replicas because they were cheaper. Once sold, the sets flickered on and off as the humidity changed. Tall pinpointed the fault just ahead of the firm going bust. It was work like that that prepared him for the years with CBS, the Editall block and those "scads of money".

Homework

Cut to Boston, Massachussetts. The Bose Corporation is by far the most successful loudspeaker company in America. It employs 1,500 people and has a 10 per cent share of the enormous US market—in which literally hundreds of loudspeaker firms battle for sales. The Bose Corporation was started by Dr. Amar Bose who was Professor of Electrical Engineering at the Massachussetts Institute of Technology. Bose began teaching at MIT in 1956 and founded the Bose Corporation in 1964.

During the war, Bose's father had no work and the young Amar supported his family by repairing valve radios. He was so successful at it that, with other kids on the block, he organised a primitive home production line. Faulty radios were booked in at one end and repaired radios came out at the other.

Often early radios would have several completely redundant valves; they were built in simply to glow in the dark and look like value for money. Bose soon learned how to spot what was wrong with the parts that mattered and then just told one of the other kids how to fix it. He then tracked down the fault on the next set in line.

Call For Jobs

Cut to today. I have often thought that anyone with electrical and electronic skills could earn a very good living today by knocking on doors and offering to repair things. But there are two snags in this idea. Most modern electronics is unrepairable—it relies on replacement modules or i.c.s which may well be unobtainable for equipment more than few years old.

me by Joel Tall. "I was lucky, I was blessed with a troubleshooter's sixth sense. It's something that engineers either have or have not got. If I can hear something wrong I know where to look, and if I can't hear anything then I also know where to look."

The other problem was summed up to

Not many people have that sixth sense. Like a musical ear, you can't acquire it.

But a casual remark by a self-made millionaire on television recently triggered an alternative thought. I'll pass it on for the benefit of anyone out there who is without a job, but with initiative.

Every home in Britain now has electronic equipment which is too complicated for most or all of the family to use. For instance, people with video recorders never learn to set the clock timer or they are baffled by the electronic self-seeking circuitry which is supposed to make tuning easier on modern TV sets.

Also, we have the people who have a word processor but can't make head nor tail of the manual; or they have bought a compact disc player which can be programmed to "play selected tracks of a record in any order, but only if they can work out how to program the programmer. Then there are those who have a gramophone which needs baffling adjustments of the pickup arm and cartridge, or can't wire in a new phone with a nonstandard socket.

Good Money

There is good money to be made from doing the modern equivalent of what Joel Tall and Amar Bose did all those years ago. All you have to do is walk from door to door over a weekend, knocking on doors and asking "Do you have anything in your house which you don't know how to use—if you do, I'll teach you."

- On Cue-

If you are in a disco and see the disc jockey doing impossible things with a compact disc record, he is probably using the new professional player from Technics.

The laser head in the SL-P1200 tracks across the disc on a linear motor. The optics are attached to a coil which moves along a guide rod between two bar magnets. In this way the laser can search out any part of a one hour recording within 0.6 seconds. Other players move the pick-up with a train of gears and have much slower access time.

By using the new system, the DJ can now pre-set a start or cue point with an accuracy better than 0-1 seconds. So he can cut between musical tracks almost as quickly with a single pro player as with two conventional players.

The player chassis is die cast from heavy metal. Even the most clumsy DJ cannot knock the laser off its cue point and make the music start in the wrong place.

Playing speed and musical pitch can be varied by 8 per cent up or down with a slider control. This lets a DJ alter the playing time of a piece of record to fit an available slot, for instance at the end of a broadcast or before a commerical break. The pitch variation lets a DJ with a good musical ear match the last note of one piece of music with the first note of the next.

The oddest feature of all is the facility for "scratch play". On a conventional gramophone, "scratching" is done by moving the record by hand, backwards and forwards under the pickup. It creates the stutter effect now heard on some pop records. The same technique cannot normally work with CD; the decoder which converts the digital pulses into analogue music relies on a stream of digital bits arriving at a constant speed of just over four million a second.

The Technics player "scratches" with a modified pause control. Normally when the pause control of a CD player is pressed, the laser stops tracking across the disc and the sound mutes. In the new player the laser stops tracking but the player does not mute. It repeats the same musical phrase over and over again, like an old-fashioned gramophone record stuck in the groove. The repetition time varies depending on where on the disc the laser is reading, because the rotational speed for a compact disc varies as the laser moves from the short inside spiral tracks to the longer ones at the outside,



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		DC VULTAGE	
Range	Resolution	Accuracy (3210)	Accuracy (3225)
200 mV	100uV		
2001111	roopre		
100001	414	$\pm (0.1\% rdo + 1 d)$	$\pm (0.25\% rdo + 1 d)$
10000	1V	- (
(five rang	es)		
Maximum	nermissible innu	t-voltage: 1100V d.c. or neak a.c.	Input impedance: 10M O + 0.1%
in oxinition	permissione inpu	DC CUDDENT	input impossible. Form at an office
-		DCCONNENT	(2005)
Hange	Resolution	Accuracy (3210)	Accuracy (3225)
200µA	100nA		
2mA	1uA		
20mA	1000	± (0.15% rdg + 1 d)	± (U.3% rdg + 1 d)
200-4	100		
ZUUMA	ΤυυμΑ		10 5 11 4 4 11
2000mA	1mA	± (0.3% rdg + 1 d)	± (U.5% rdg + 1 d)
10A	10mA	$\pm (2\% rdg + 3 d)$	\pm (2% rdg + 3 d)
Overload	protection: 2A (2	50V) fuse excent 10A ranne Max	on 10A range is 20A for 10 sec.
0 for load	AC VOLTACE	THE DALE (2210) AND AVED	ACE READING (2226)
	AG VOLIMOE	THUE HING SZTUJ MILD AVEN	AGE NEADING (3223)
Hange	Kesolution	Accurac	Y
		45Hz-1kHz 1kH	Iz-TUKHZ TUKHZ-ZUKHZ
200mV	100µV	(±(1.5%	rdq + 2d ± (3% $rdq + 5d$)
21	1mV	+(0.5% rdo + 2 d)	
201/	10m\/	+12%	$d_0 \pm 2 d_1$ $\} \pm (5\% rdg + 5 d)$
200	TORIV }	4511- 40011- 400	
		43HZ-400HZ 400	
200V	100miV (+ (0 5% rda + 2 d) ± (3% r	$dg + 5 d) \pm (5\% r dg + 5 d)$
750V	1V (± (0.5 % lug + 2 u)	
Accuracy	specifications a	only for inputs between 20% at	nd 100% of full scale. Maximum
narmiccih	la input voltant	200mV range 250V rms 400V d.e	All other ranges 750Vrms 1100V
hermisen	te input voitage.	200111 Tallye 200 1113, 400 0.0	All other ranges / Joannis, 11004
peak a.c.	TUOVXHZ, Input	mpedance: TUM S2.	
		AC CURRENT	
Range	Resolu	tion Accuracy 3210 1	True RMS and 3225 Average
			Reading
200	100~ 4		1547 1647
ZUUUA	TUUIA		4002-1802
2000uA	J 1µA		
20mA	10µA		t (1% rdg + 2 d)
200mA	100uA	1	
2000mA	1		
2000000	10-0		12 EV ada + 2 d
IUA	TUMA	/ <u>_</u>	(2.5% rog + 2 d)
Overload	protection: 2A (2	(50V) fuse except 10A range. Max	. on 10A range is 20A for 10 sec.
		RESISTANCE	
Ranne	Recolution	Accuracy (3210)	Accuracy (3225)
2000	100-0	10001009 (0210)	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
20052	100102		
ZkQ	102	+ (0 15% rda + 1 d)	+10.3% rdn $+1.d)$
20kΩ	10Ω	10.15 % lug + 1 u/	- 10.0 % 109 + 1 0)
200kO	1000		
200040	110	+ (0.2% rdo + 1.d)	+ (0.4% rda + 1 d)
2000002	101.0	+ (111/ ada + 2 d)	± (10) rda + 2 d)
ZUMSZ	IUK22	± (1% rag + 2 0)	± (170 lug + 2 u)
Maximum	n permissible inpu	t voltage: 250V d.c. or a.c. r.m.s.	
		Plus diode test and continu	uity test.

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MIKE TOOLEY BA

Our nine part series on Digital Troubleshooting aims to provide readers with a practically biased introduction to the diagnosis of faults within digital equipment. The series should also be of interest to anyone wishing to update their knowledge of modern digital devices and circuitry.

N LAST month's instalment of *Digital Troubleshooting* we dealt with semiconductor memories. This month we examine techniques for input to, and output from, microprocessor systems. We shall be explaining some fundamental concepts associated with parallel and serial data transfer, describing methods used for controlling input and output, and introducing several of the more popular programmable input/output (I/O) devices.

Our companion Digital Test Gear project involves the construction of an *Audible Logic Tracer*. This unusual device provides a means of investigating the nature of signals present in a microprocessor based system without recourse to an oscilloscope.

I/O REQUIREMENTS

Without a means of input and output (I/O), a microprocessor system would be of little use. The amount of I/O provided in any particular microprocessor system is, of course, largely determined by the range of applications envisaged. A simple home computer, for example, would, as an absolute minimum, be expected to accept inputs from a keyboard and send its output to a conventional TV receiver—the output being modulated on an r.f. carrier.

In addition, the system might provide input and output facilities for use in conjunction with a conventional domestic tape recorder as well as a further output for driving a printer. The minimum I/O



requirement to be expected from a simple home computer is shown in the block diagram Fig. 7.1. On the other hand, a microprocessor based system for controlling

an industrial process might have no need for a keyboard or display since it would almost certainly be remotely programmed and monitored by a "ghost" microcomputer (using a conventional serial RS-232C link). The controller might provide for as many as twentyfour separate input and output lines for controlling such external hardware as relays, motors, and lamps (see Fig. 7-2).

MEMORY MAPPED I/O VERSUS PORT I/O

A basic distinction exists between systems which treat I/O devices as one, or more, memory addresses and those which assign specific port addresses to each I/O device. In either case, data is output by simply writing data to the appropriate memory or port address. Conversely, data is input by simply reading it from the appropriate memory or port address.

Using memory mapped I/O, the CPU treats I/O operations in exactly the same way as operations performed on memory. A proportion of the memory space is then reserved for I/O (it cannot, at the same time, be assigned to RAM or ROM!). Port I/O, on the other hand, reserves a set of port addresses which are quite independent of the conventional memory space. Port addresses are







distinguished from memory addresses by means of signals present on the control bus.

Taking the Z80 microprocessor as an example, the following (active low) control signals are used to distinguish memory and port I/O operations:

MREQ—the memory request line goes low whenever the CPU is performing a memory read or write operation

IORQ—the input/output request line goes low whenever the CPU is performing an I/O operation

To aid the process of port I/O, instructions are available in the Z80 instruction set which deal specifically with port input and output. As examples:

OUT (FFH), A-writes the contents of the accumulator (an eight bit value) to hexadecimal port address of FF

IN A,(FFH)—reads the contents of hexadecimal port address FF and places the result in the accumulator

PARALLEL VERSUS SERIAL I/O

Another important distinction has to be made between parallel and serial I/O. In the former case, a byte of data is transferred at a time (thus necessitating an 8-bit buffer or latch) whereas, in the latter case, data must be transferred a bit at a time.

Parallel I/O is thus reasonably straightforward as witnessed by the simple single-byte parallel I/O arrangement shown in Fig. 7.3. Here a standard 8-bit latch is used for output and an octal tri-state buffer is used for input. This arrangement is simple but not very flexible and a better solution would be that of making use of a programmable parallel I/O device.

Since the data present on a microprocessor bus exists primarily in parallel form (it is "byte wide"), serial I/O is a little more complex. Serial input requires a means of conversion of the serial input data to parallel data in a form which can be presented to the bus. Serial output, on the other hand, requires a means of conversion of the parallel data present on the bus into serial output data.

In the first case, conversion can be performed with a serial input parallel output (SIPO) shift register whilst in the second case a parallel input serial output (PISO) shift register is required. These arrangements are depicted in Fig. 7.4 and, whilst both may be





Fig. 7.4. (a) Serial input using a SIPO shift register. (b) Serial output using a PISO shift register.







Fig. 7.5. Basic I/O control methods. (a) Programmed (polled), (b) Interrupt, (c) Direct memory access (DMA). implemented using conventional logic devices, the most effective solution to implementing such an interface is again with the aid of a dedicated programmable device.

I/O CONTROL METHODS

There are three basic methods of controlling I/O operations. These vary in complexity and flexibility and each has its advantages and disadvantages. The simplest method, and that which is perhaps the most obvious, is that of allowing the CPU to control *all* I/O operations using its control program. This method, known as "programmed I/O" or "polled I/O", ensures that the CPU has full control of the situation but is particularly inflexible and rather slow.

Essentially, the CPU periodically asks (polls) each peripheral device (via its respective I/O device) whether it requires service. If a service request is detected the CPU will then execute the necessary service routine. Once a service request has been honoured, any requests for service generated by other peripheral devices will be ignored; they will simply have to wait until the CPU becomes available to process their request for service!

An arguably better method, but slightly more complex, is that of allowing peripheral devices to "interrupt" the normal CPU operation. In such a case, and depending upon the state of its "*interrupt flag*", the CPU will have to suspend its current operation (preserving any important parameters as well as the return address on the stack) before executing the required service routine. Interrupts may be "prioritised" in hardware such that the most

Interrupts may be "prioritised" in hardware such that the most important peripheral device is attended to first. A particular example might be a vehicle braking system; we would probably want the CPU to respond to brake failure and generate a warning regardless of what else was happening at the time!

A third, and much more complex alternative, is that of giving external devices full access to the memory space of the system without involving the CPU in any of the data transfers. This is known as "direct memory access" (DMA) and is a very powerful technique. It has the advantage that data can be transferred at an exceptionally fast rate (since CPU intervention is not required) and is the means by which, for example, data is transferred to and from a hard disc drive.

DMA is not really appropriate in simple control applications but it is worth mentioning in order to make the story complete! The three I/O control methods are illustrated, in simplified block schematic form, in Fig. 7.5.

PARALLEL I/O DEVICES

Having attended to the basic concepts of microprocessor system I/O, it is about time we turned our attention to some common programmable I/O devices. The internal registers of a typical programmable parallel I/O device is shown in Fig. 7.6

Parallel I/O devices enjoy a variety of names depending upon their manufacturer. Despite this, parallel I/O devices are remarkably similar in internal architecture and operation with only a few subtle differences distinguishing one device from the next. The pinning details for some common programmable parallel I/O devices is shown in Fig. 7.7.

The following types are commonly encountered:

6520 Peripheral Interface Adaptor (PIA)

6521 Peripheral Interface Adaptor (PIA)-similar to the 6520

6522 Versatile Interface Adaptor (VIA)

- 6820 Peripheral Interface Adaptor (PIA)
- -equivalent to the 6520
- 6821 Peripheral Interface Adaptor (PIA) —equivalent to the 6521
- 8255 Programmable Parallel Interface (PPI)

Z8O-PI0.Programmable Input/Output (PIO)

As their names imply, programmable parallel I/O devices can normally be configured (under software control) in one of several modes:

- (a) all eight lines configured as inputs
- (b) all eight lines configured as outputs
- or (c) lines individually configured as inputs or outputs

In addition, extra lines are usually provided for "handshaking". This is the aptly named process by which control signals are exchanged between the microcomputer and peripheral hardware.

The nomenclature used to describe port lines and their function tends to vary from chip to chip but here again there is a reasonable degree of commonality. The following applies to most of the devices listed previously:

PA0 to PA7 Port A I/O lines; 0 corresponds to the least significant bit (LSB) whilst 7 corresponds to the most significant bit (MSB).

CA1 to CA2 Handshaking lines for Port A; CA1 is an interrupt input whilst CA2 can be used as both an interrupt input and peripheral control output.

Ves 1		40 CA1	Vss 1	1.	40 CA1	
PA0 2		39 CA2	PAD	2	39 CA2	
PA1 3		38 TROA	PA1	3	38 R50	
PA2 6		37 IRG8	PA?		37 RS1	
PA3 5		36 850	PA3		36 RS2	
PA4 6		35 RS1	PA4	5	35 853	
PAS 7		34 RES	P45	7	34 RES	
PA6 8	6520	33 06	PAS E	6522	33 00	
PA7 9		32 D1	PA7	9	32 01	
PB0 10		31 02	PB0	0	31 D2	
PB1 11		30 63	PB1 1		30 D3	
PB2 12		29 04	P82 1	2	29 D4	
PB3 13		28 DS	P83 [3	28 05	
PB4 14		27 D6	P84 1	<u> -</u>	27 D6	
PB5 15		26 07	PBS 1	5	26 07	
PB6 16		25 EN	P86 1	6	25 92	
P87 17		24 CS2	PB7 1	17	24 CS1	
CB1 18			23 CS3	CB1 [18	23 CS2
CB2 19		22 051	CB2	19	22 R/W	
Vcc 20		21 R/W	Vcc Z	20	21 180	

PB0 to PB7 Port B I/O lines; 0 corresponds to the least significant bit (LSB) whilst 7 corresponds to the most significant bit (MSB).

CB1 to CB2 Handshaking lines for Port B; CB1 is an interrupt input whilst CB2 can be used as both as interrupt input and peripheral control output.

The electrical characteristics of an I/O port tend to vary from chip to chip however signals are invariably TTL compatible. Several programmable parallel I/O devices have port output lines (usually the B group) which are able to source sufficient current to permit the direct connection of the base of a conventional; or Darlington type transistor. This device can then be used as a relay or lamp driver. Alternatively, high-voltage open-collector octal drivers may be connected directly to the port output lines.

SERIAL I/O DEVICES

Serial data may be transferred in either synchronous or asynchronous mode. In the former case, all transfers are carried out in accordance with a common clock signal (the clock must be available at both ends of the transmission path). The pin connections for some common programmable serial I/O devices is shown in Fig. 7.8.

Asynchronous operation involves transmission of data in "packets"; each packet containing the necessary information required to

Fig. 7.6. Internal registers of a typical programmable parallel I/O device.



-		ε.				ġ.,			_	
V98 1		40	CA1	PA3		40	PAL	D2 1	• •	40 03
PAG 2		39	CA2	PA2	2	39	PAS	07 2		39 D¢
PAT 3		38	IRDA	PAI	3	38	PAG	D 6 3		38 DS
PAZ 4	12	37	TROB	PAO	<u>.</u>	37	PA7	CE 4		37 MT
PA3 5	N 19	36	RSD	RD	5	36	WR	C/0 5	=	36 IDRQ
PA4 6		35	R\$1	ĉŝ	6	35	RÈSET	B/A 6		35 RD
PA5 7		34	RESET	GNO	7	34	D0	PA7 7		34 PB7
PA6 8	6820	33	00 .	A1	8255	33	01	PA6 8	Z80-P10	33 P86
PA2 9	1 1 1 3	32	01	A.O	9	32	D2	PA5 9		32 P85
PB0 10		31	02	PC7	10	31	03	PAL 10		31 PB4
P81 11		30	03	PC6	11	30	D4	GN0 11		30 PB3
P62 12		29	04	PCS	12	29	05	PA3 12		79 PB2
PB3 13		28	05	PC4	13	28	D6	PA2 13		28 PB1
PB4 14		27	06	PCO	14	27	87	PA1 14		27 PB0
P85 15		26	D7	PC1	15	26	Vec	PA0 15		26 +5V
PB6 16		25	EN	PC2	16	25	P87	ASTE 16		25 CLK
P87 17		26	CS1	PC3	17	24	P86	BST8 17		24 IE1
CB1 18		23	CS2	P80	18	23	PB5	ARDY 18		23 INT
C82 19		22	CSO	PBI	19	22	PB4	DC 19		22 160
Vcc 20	100	21	R/₩	P82 4	20	21	P63	Ð1 20		21 BRDY

Fig. 7.7. Pin-outs for some common programmable parallel I/O devices.

decode the data which it contains. Clearly this technique is more complex but it has the considerable advantage that a separate clock signal is not required. We shall be considering this topic in much greater detail in Part Eight.

As with programmable parallel I/O devices, a variety of different names are used to describe programmable serial I/O devices. The most commonly encountered devices are listed below:

6850 Asynchronous communications interface adaptor (ACIA)

6852 Synchronous serial data adaptor (SSDA)

8251 Universal synchronous/asynchronous receiver/transmit-

Common signals of note are listed below:

D0 to D7-data input/output lines connected directly to the microprocessor bus

RXD-received data (incoming serial data)

TXD-transmitted data (outgoing serial data)

CTS-clear to send. This (invariably active low) signal is taken low by the peripheral when it is ready to accept data from the microprocessor system

RTS-request to send. This (invariably active low) signal is taken low by the microprocessor system when it is about to send data to the peripheral.

ter (USART) 8256 Universal asynchror Z80-DART Dual asynchro As with programmable para commonality in the internal	nous receiver/tra onous receiver/t illel I/O devices, architecture of	ansmitter (UART) transmitter (DART) , there is a degree of f serial I/O devices.	Fig. 7.8. Pin-outs for some common programmable serial I/O devices.	D1 1 • 03 2 05 3 07 4	60 00 39 02 38 04 37 05
				161 6	35 CE
Vss 1	24 CTS		27 00	IEO 7	34] B/Ā
RXD 2	23 OCD	RXD 3	26 Vcc	MT B	33 C/D
RICLK 3	22 D0	GND 4 9251	25 RXC	Vcc 9	32 RD
TXCLK 4 6850	21 01	04 5	24 DTR	WIRDY 10	31 GND
RTS 5	20 02	DS 6	23 RTS	SYNC A 11	30 W/RDY B
TXD 6	19 D3	06 7	22 OSR	RXDA 12	29 RXDB
180 7	18 D4	. 07 8	21 RESET	RXCA 13	26 PXCB
		THC 9.	20 CLK	TXCA 14	27 TXCB
CSU 8		WR 10	19 TXD	TXDA 15	26 17.08
CS2 9	16 D6	CS 11	18 TXEMPTY	DTRA 16	25 DTRB
CS1 10	15 D7	C/0 12	17 575	RTSA 17	22 8158
RS 11	14 EN	RD 13	18 SYNDET		22 0000
Vdd 12	13 RIW	RXRDY 14	15 TXRDY		22 0000
and the second sec					Li nese i

The signals generated and used by programmable serial I/O devices are invariably TTL compatible. It should be noted that, in general, such signals are unsuitable for anything other than the shortest of transmission paths (e.g. between a keyboard and a computer system enclosure). Serial data transmission over any appreciable distance invariably requires additional buffers and level shifters between the serial I/O device and its associated transmission path.

Fault finding on I/O devices

Since programmable parallel I/O devices are reasonably predictable in operation, it is possible to detect faults by simply measuring the conditions present on the various input and output lines. Fault finding on parallel I/O devices is thus a fairly straightforward task—the same cannot be said for their serial counterparts!

One should first ensure that a suspect programmable parallel I/O device is actually being selected by the CPU. This can be achieved by monitoring the state of the various chip control lines using nothing more than a logic probe.

Having established that a particular device is being selected, the next task involves determining what, if anything, is happening on the peripheral side of the chip. If possible, a short routine should be written to "exercise" the port (i.e. read or write data as appropriate) and the resulting logic conditions should be checked.

Failure of an external driver transistor will often result in damage to an associated latching buffer/driver within a programmable I/O device (note, however, that damage may not necessarily extend to all eight lines in a particular port group). It is, therefore, well worth checking external devices and hardware before replacing a failed parallel I/O device! Where devices are socketed, it may be expedient to substitute a device which is known to be operational for one which is suspect. Do ensure that you switch off and disconnect any external hardware before attempting to do this! As with semiconductor memories, it is advisable to fit a socket before replacing a device which is considered suspect.

Faults on programmable serial I/O devices are, unfortunately, less easy to pinpoint. Here again it is important to investigate the conditions which are prevalent on the CPU side of a device (notably the various control bus and chip select signals) before making any other tests.

Having ascertained that the device is being selected, it is then worth checking the state of the RTS and CTS lines (for output) and IRQ or INT lines (for input). It is also worth checking that the clock signals are present and correct (a typical transmit/receive clock would be at 500kHz). Furthermore, due to variations in circuitry it is highly desirable to have access to the original manufacturer's service information.

Finally, as with parallel I/O devices, a short routine to "exercise" the port may be useful (e.g. a loop which continuously outputs a particular byte value). Note, however, that an RS-232C serial interface requires the use of handshake signals and it is thus important to check associated line-drivers, line-receivers, connectors and cables as well as the peripheral itself before blaming the serial I/O device!

Next month: We shall be delving into the circuitry and operation of a standard RS-232C serial interface. We shall also be taking a look at the popular IEEE-488 Instrument Bus. Our Digital Test Gear Project will feature a Versatile RS-232C Break-out Box.

MIKE TOOLEY B.A.

AUDIO LOGIC

TRACER

This month's Digital Test Gear Project deals with the construction of an Audible Logic Tracer. This somewhat unusual device allows the user to listen to the signals present in a microprocessor-based system. It thus provides an alternative to the conventional logic probe which is somewhat limited in that it only provides visual indications of logic states and thus it is not usually possible to make a meaningful as-

sessment of the activity on a line that is pulsing.

By listening to the signals in a microprocessor-based system, it is possible to gain a meaningful impression of what is going on. The Audible Logic Tracer will not only indicate that a particular line is active but it will also provide an indication of the frequency at which the line is pulsed and whether or not the pattern is repetitive. It is





thus possible to distinguish between the signals present on the various bus, clock and chip enable lines.

Anyone who still has doubts about the potential of this simple instrument and is regularly involved in fault finding in microprocessor systems is well advised to check the audible logic tracer out!

CIRCUIT DESCRIPTION

The operating principle of the Audio Logic Tracer is delightfully simple. Signals that are pulsing at a fast rate (e.g. those present in a microprocessor bus) may be converted to signals which pulse at a fairly slow rate (within the audio frequency band) simply by applying them to a multi-stage binary divider. These frequency scaled signals can then be shaped and fed to a conventional audio amplifier stage.

The complete circuit of the Audio Logic Tracer is shown in Fig. 1. A CMOS divider, IC1, provides frequency division of the input by a factor of 1024 (2¹⁰). Resistor R1 together with diodes D1 and D2 provide protection for IC1 in the event of the probe tip being applied to a voltage source which is outside the normally acceptable input range (maximum \pm 50V).

The second stage, IC2, is a simple fixedgain audio amplifier stage based on the popular TBA820M. The frequency response of this stage extends from a few Hz to beyond 20kHz.

CONSTRUCTION

All components for the Audio Logic Tracer are mounted on a 0-1in matrix board comprising 10 strips of 37 holes. This can easily be cut from the standard size stripboard used in this series (24 strips of 37 holes).

The stripboard layout of the Audio Logic Tracer is shown in Fig. 2. Constructors should note that a total of 21 track breaks are required and these should be made using a spot face cutter. If such a tool is unavailable, a sharp drill bit of appropriate size may be substituted.

The following sequence of component assembly is recommended; i.c. sockets, terminal pins, links, resistors, diodes, and capacitors. The earpiece is mounted in the upper section of the probe case. A round hole (approximately 14mm in diameter) should be cut and the earpiece glued into place using an epoxy resin-based adhesive. The supply leads should then be connected, taking care to ensure the correct polarity (red crocodile clip/striped lead to positive).

Before inserting the two integrated sockets into their holders and mounting the stripboard in its final position, constructors should very carefully check the components, links, and track breaks. Furthermore, it is also worth checking that all of the polarised components have been correctly oriented. Constructors should also carefully oriented. Constructors should also carefully joints, solder splashes, and bridges between adjacent tracks.

COMPONENTS

Resistors		See
R1	3k9	Sho
R2	22k	
R3	3k9	
R4	220	0200

55

All 0.25W 5% carbon

Capacitors

C1	10µ tant 25∨
C2	100n polyester
C3	10µ p.c. elec. 16V
C4	10µ axial elec. 25∨
C5	100µ p.c. elec. 16∨
C6	220µ p.c. elec. 16V

Semiconductors

D'1	1N4148
D2	1N4148
D3	1N4001
IC1	4020B CMOS 14-
	stage binary counter
IC2	TBA820M 2W audio
	power amp.

Miscellaneous

LS1 8ohm earpiece.

8-pin low profile i.c. socket; 16pin low profile i.c. socket; probe case, measuring 140mm x 30mm x 20mm approx; single-sided 1mm terminal pins (5 off); 0·1in. matrix stripboard (10 strips, 37 holes) measuring 95mm x 63mm approx; connecting wire.

Approx. cost	SE	case
Guidance only	LD	extra /



Fig. 2. Circuit board component layout and details of breaks to be made in the underside copper tracks. The completed board wired inside the probe case can be seen in the photograph below.



When the board has been thoroughly checked, the two integrated circuits should be inserted into their holders—taking care to ensure correct orientation. The circuit board should then be mounted in the base of the probe case, no mounting hardware is required as the board should be held snugly in place when the two probe case halves are mated together. The probe tip mounting boss should now be connected to the probe input using a short length of insulated wire and the tip screwed in place.

TESTING

The Audio Logic Tracer should be tested using a known operational microprocessorbased system, for example a home computer. The 5V supply should be derived from a convenient point on the main TTL supply rail (taking care to observe correct polarity).

With the probe tip left unconnected, no output should be detected. Now take the probe tip to the output of the clock generator (ideally this should be in the range IMHz to 4MHz); a "clean" sounding tone should be produced (of between 1kHz and 4kHz respectively). Now transfer the logic probe to one of the data bus lines; a "rasping" note (of typically between 100Hz and 1kHz) should result.

If the Audio Logic Tracer should fail to produce these indications, the circuit board should be removed from the probe case and carefully checked with particular emphasis on the orientation of polarised components (diodes, electrolytic capacitors, and integrated circuits) and on the placement of links and breaks.

Finally, it is well worth familiarising oneself with the indications produced by the Audio Logic Tracer when presented with various common types of microprocessor signal. As a first step constructors should transfer the logic probe to the following lines in turn and note the sounds produced:

-remaining data bus lines					
- I chiaiming data bus tilles		0.00.0.00.77	doto	D	11 20 00
I WAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA	-1 CI112	111112		THES	DHES
	T CATTOR	AAAAAA	C	C the D	Perve D

 add	ress	bus	line	s (ca	an y	ou d	letect
any	diff	eren	ces	betv	veen	the	high
and	low	ord	er ad	idre	ss lir	ies?)	
		1	12	1.		ar (1

Next month: RS-232C Breakout Box

GROWTH AWARD

Optoelectronic scientist wins medal for his pioneering work

A SCIENTIST whose pioneering work has helped to put Britain in the world forefront of optoelectronic technology has been awarded the Martlesham Medal.

Dr Marc Faktor, who spent more than two decades researching the optical and electrical properties of materials, was presented with the medal at a ceremony in the London Telecom Tower recently.

Sir George Jefferson, Chairman of British Telecom, described Dr Faktor's achievements as "quite outstanding in their versatility and significance.

"He has demonstrated an immense grasp of materials science both in principle and detail—and a high degree of originality."

Dr Faktor's work ranged widely over the field of optoelectronics and other semiconductors. During the early 1970s he headed a team which used organic materials to build reliable "directly modulatable" semiconductor lasers.

He also devised the electrochemical technique at the heart of the British Telecom profile plotter, which produces an accurate profile of the electriccurrent carriers in semi-conductors. This is now manufactured under licence and the thirdgeneration is sold worldwide.

Growth

Much of his work with British Telecom concerned growing crystals from vapour—building

THE MARTLESHAM MEDAL

The Martlesham Medal gives recognition to members of British Telecom, past or present, who have made an outstanding personal contribution to science or technology with a particular relevance to telecommunications. It takes its name from British Telecom's research laboratories at Martlesham Heath, near Ipswich.

A particular discovery, contribution to knowledge or an innovation, is sought by the award panel.

The first recipient of the Martlesham Medal in 1980 was Dr Tommy Flowers, the man who invented Britain's and possibly the world's first computer and the acknowledged father of electronic switching. During the war, Dr Flowers played a leading part with his invention Colossus, which performed a major role in breaking German High Command codes.

up layers of semiconductor ma-

terial no more than a millionth

of an inch thick-on which the

Dr Faktor with the core of his contribution to the development of thin-film technology —building up layers of semiconductor material no more than a millionth of an inch thick.

> as metallo organic vapour phase epitaxy (MOVPE) are now sufficiently well defined as to be capable of commerical exploitation. The joint venture company set up in 1986 between British Telecom and du Pont is to develop, manufacture and market optoelectronic components and devices which stem directly from Dr Faktor's work.

> The worldwide market for these devices is worth more than £350 million now and is expected to grow by 30 per cent a year to more than £4 billion by the mid 1990s.

DO YOU BELIEVE IT?

Most people have heard of solar-powered calculators — but a water-powered one? It sound incredible, but the latest news from the Hong Kong Trade Development Board is that a Hong Kong based firm has developed a revolutionary "hydropowered" LCD calculator.

The water-resistant calculator was developed by engineers of Swank International Electronic early last year and a decision to manufacture taken last August. Patents have already been secured in the UK and USA, which Swank sees as its main markets.

"The actual operation is quite simple", says Swank's general manager, Mr. T. M. Lee.

"The calculator is immersed in water, which enters the calculator body through special holes, even though the general body is water-resistant.

"This water is then used to power the calculator through a specially-designed water generator which keeps the calculator going for three months, after which it simply has to be immersed in water to renew the power source," Mr Lee explained.

JOB LINE

fabrication of today's optical

devices are based. The techni-

ques which he pioneered, such

The trade newspaper *Electronics Weekly* has just introduced a new daily phone-in service for those wanting industry news updates and the latest info' on the jobs front.

Every day their "Jobs Line" will give details of two different jobs with a contact number. The new service number to ring is: 0898 200103.

British manufacturers of dry cell batteries have joined together to form the British Battery Manufacturers Association. The new trade association will look after the common interest of the members in relation to central and local government, dealers, users and other trade associations. A new range of industry standard soldering tips for their cordless 3 in 1 tool, the Ultratorch 3, has been announced by Master Heat Tools.

Intelligence Fund

An investment of over £600,000 to develop artificial intelligence-based manufacturing systems has just been announced by IBM. They are sponsoring a research project at the Heriot-Watt University to apply the latest AI techniques to manufacturing requirements.

The aim of the two year project is to develop a robot manufacturing cell which can cope with unexpected production changes. Even the most advanced automation systems are unable to do so, since they can only cope with situations they are programmed to deal with.



...from the wo

d of electronics

ANGLO-JAPANESE SPACE VENTURE

The successful launch, on 5 February, of a satellite carrying the largest-ever X-ray sensor marks the first Anglo-Japanese collaboration in space, and will enable the study of some of the most exotic and powerful objects in the Universe.

The sensor, known as a Large Area Counter (LAC) weighs over 100kg and has a sensitive area of 0-5m². The LAC was launched on a Japanese ASTRO-C satellite from the Kagoshima Space Centre, Japan.

The project is the result of collaboration between research groups at the British National Space Centre's (BNSC) Rutherford Appleton Laboratory (Space Department); the University of Leicester; the Tokyo Institute of Space and Astronautical Science (ISAS) and the University of Nagoya.

The eight flight detectors which form the full LAC were delivered to Japan at roughly monthly intervals during 1986 to be installed on the satellite in the clean rooms at ISAS. The Tokyo group then tested the completed satellite with support for critical tests from British specialists The satellite was mounted on the Japanese Mu IIIS rocket.

When in orbit, ASTRO-C will be the only active observatory for Xray astronomy available to astronomers worldwide.

More than 2,500 customers in the City of London are to be offered more details on their telephone bills in a British Telecom trial scheme beginning this month.

Bills will show details of all dialled calls of 10 units and more (i.e., which cost more than about 50p, including VAT). Details to be shown on the bill will be: date and time that the call is made; number called; duration of the call and charge for the call.

Customers opting for this new service will be charged £1 per quarter for each exchange line on which calls are itemised. The sixmonth trial will cover about 10,000 lines on three exchanges in the City of London.

MIDDLE EAST AWARD

Over 200 students at the Dhahran Technical Training Institute Oare using construction kits supplied by Cirkit, UK, as part of their official 2-year practical training programme.

The British company has supplied the DTTI with a broad range of electronic construction kits covering robotics to transmission lines and recently presented the Cirkit Award to the top student at the Institute's graduation ceremony.

Saleh Hashbool Al-Ghamdi receives his certificate from Sheikh Nasser Assar, President of Civil Aviation, Dhahram. Also pictured (left) is Saleem S. Al-Quarni, Director of the Technical Training Institute, Dhahran.



ON THE AIR

With the full approval of the Irish Minister of Communications, Radio Tele Luxembourg (RTL) in association with Radio Telefis Eireann (RTE) are to commence transmitting from the Irish mainland on 254kHz (1181 metres) long wave.

The radio station will operate on the long wave band and will broadcast from a transmitter on the East Coast of Ireland, permitting daytime coverage of the majority of Britain as well as Ireland itself.

Mr Gust Graas, Director General of RTL said: "The joint venture is a natural extension of our company's European broadcasting philosophy which has become so well established for over 50 years. In this case, the three countries of Ireland, Britain and Luxembourg will pool their considerable resources to produce a high-quality mixture of entertainment and information that will be popular with the public."

A US electronics company is to set up a new manufacturing plant in the Vale of Leven this year, creating 34 new jobs.

JMK Inc., based in Amherst, New Hampshire, USA, makes radio-frequency filters and other products for computers and medical equipment.

ON THE RAILS

to be a second as a second second

An order from the State Rail Authority of New South Wales, Australia, for electric equipment for 450 railcars, which feature such state-of-thé-art technologies as electric propulsion equipment with gate turnoff (GTO) thyristors, has been won by Mitsubishi Electric Corp.

They will supply the entire electric equipment incorporating high technologies such as power electronics for a fourquadrant chopper-control system with GTO thyristors for energy savings, microelectronics for destination display and speed control using 16-bit micro computers, and opto-electronics in train management systems.

It is hoped that the motor technology and peripheral equipment will be transferred to several Australian local manufacturers so that they can be produced locally.

ADVANCED, TESTING

In what is thought to be the largest order of VLSI test equiment for two years, Advanced Micro Devices (AMD) has agreed to purchase over \$7 million worth of VLSI test systems from Teradyne. The centrepiece of the order is a pair of J953 Test Systems which offer unrestricted 50MHz operation at up to 256 pins.

AMD has also ordered a J967 Test System for use in its new Test and Design Centre near Tokyo, Japan, and a second J983 VLSI Test System as part of the multiple-system purchase.

After a career spanning 46 years with A.F. Bulgin & Co. plc, Tom Smith is to retire from his position as Divisional Director of Design. He will, however, continue his association with the company as a consultant.

Yellow Pages Goes Live

British Telecom's Electronic Yellow Pages (EYP) is now up and running, allowing most online communicating terminals in the UK and abroad free access to a database of Yellow Pages advertisers.

Information on EYP will initially cover the whole of London, Reading, Guildford, Watford and St. Albans. The information will be broken down by classification headings as in the printed books. EYP is available to users free of subscription and computer time based charge. Connection to EYP can be either via a Gateway on Prestel or by direct dialling over the telephone network (PSTN) at the normal tariff.

Full instructions on how to access EYP will be published in all Yellow Pages books where EYP is available. The first book to carry such information is London South West covering the Richmond, Egham and Leatherhead areas.

All Yellow Pages display advertisers will be entitled to "space" on the system to publicise fast-changing information such as availability, pricing, and special offers about their products and services, as well as a listing of names, addresses and telephone numbers. Businesses taking out advertising at semidisplay level will automatically be listed.



COLOUR coding of components is a subject that has been covered in previous articles in this series, but no apologies are made for returning to this subject. It is potentially a very confusing aspect of electronics for the beginner, and one which has to be mastered before even the most simple of projects can be tackled.

Colour codes are probably used somewhat less these days than in the past, and they are now relatively rare as a means of value marking on capacitors. However, on resistors they are still by far the most common method of value indication, and things are complicated by the use of several slightly different systems of coding. A recent development seems to be the use of colour coding on small inductors of the r.f. choke type, and they are still used to some extent on capacitors. You may also encounter components that are not of recent manufacture which use colour coding, where their modern equivalents do not.

FOUR BAND CODING

By far the most common type of colour coding is the four band type used for most resistors. Fig. 1 helps to explain the way in which this type of coding operates. The first task when deciphering a colour code is to determine the correct order of the coloured bands (i.e. which end of the component has the first band in the sequence). This is quite easy as the first band at one end of the resistor will be practically right at the end of the component, while the first band at the opposite end will be positioned somewhat further onto the body of the component. The sequence begins with the band that is nearest to one end of the component. In most cases the wrong" band will be gold in colour, and obviously not the first colour in the sequence as this can never be gold.

Bands one and two indicate the first two digits of the value, and Table 1 shows which numbers correspond to each colour. As a couple of examples, if bands one and two are red and violet respectively, this indicates that the first two digits of the value are two and seven, or if they are orange and orange this indicates that the first two digits are three and three.

The third band gives the rest of the value, but it operates in a different way to the first two bands. It is the "multiplier", and it indicates the figure by which the first two digits must be multiplied in order to give the full value. This is more simple in practice than one might expect though, since the multiplier is always a straightforward 1, 10, 100, etc., and never an awkward value. It is really just indicating the number of zeros to be added to the first two digits, or in the case of a very low value component it shows the position of the decimal point. Table 1 shows the multiplier value for each of the applicable colours.

If we again take a couple of examples this should help to clarify the way in which this system operates. If the first three bands are respectively yellow, violet, and red, the first two digits are four and seven, and the multiplier value is 100. This gives 47 x 100 which is 4,700 ohms. A convenient way of looking at things is to take the multiplier value as being the same as when it appears in the first two bands, but with it signifying the number of zeros to be added to the value rather than a digit. Thus in the example given above, the red multiplier would indicate two zeros to be added to the first two digits, which again gives us a value of 4,700 ohms (47 plus 00-4,700).



Fig. 1. Standard four band colour coding.

Table 1						
Colour	1st	2nd	3rd	4th		
Black	_	0	x1	_		
Brown	1	1	x10	1%		
Red	2	2	x100	2%		
Orange	3	3	x1000	-		
Yellow	4	4	x10000	-		
Green	5	5	x100000	0.5%		
Blue	6	6	x1000000	0.25%		
Violet	7	7	_	0.1%		
Grey	8	8	-	-		
White	9	9	-	-		
Gold	-	-	×0.1	5%		
Silver	-	-	x0.01	10%		
None	-	-	-	20%		

This system is not usable with very low value resistors as the multiplier is then 0.1 or 0.01, but you will not often encounter resistors so low in value that they have these multiplier values. As an example of a very low value coding, green, blue, gold would mean that the first two digits of the value are five and six, and that the multiplier is 0.1. This gives 56×0.1 which equals 5.6 ohms.

k AND M

It is quite common for electronic circuits to utilize resistors having values of many thousands of ohms, or even a few million ohms. This gives very large numbers to deal with, and matters are generally rendered more convenient by the use of 'kilohm'' or just ''k'' to indicate a value in thousands of ohms, or ''megohm'' or just ''M'' where a value is expressed in millions of ohms. Thus a value of 560,000 ohms would more normally be given as 560k, and a value of 3,300,000 ohms would be given as 3.3M. It is now quite common for the "k" or "M" to be used to show the position of the decimal point where appropriate. Therefore, a value such as 2,700 ohms could be given as 2.7k or 2k7. Where the value is in ohms the letter "R" is often used in much the same way as the "k" and "M" in higher values, and a component with a value of 6.8 ohms would often be referred to as a 6.8R component, or a 6R8 type.

The point of using a letter to indicate both the units in which the value is expressed and the position of the decimal point is that it enables values to be given on circuit diagrams using as few characters as possible. This might seem like a very minor point, but the complexity of many modern circuits is such that it can be difficult to compress everything into the available space, and this system does help to make diagrams as clear as possible.

The fourth band shows the "tolerance" of the component. This simply means the maximum error between the marked value and the actual value of the component. As with any component, electronic or mechanical, a resistor will not have precisely its specified characteristics, and its actual value will not be exactly the same as its marked value. The value is guaranteed to be within a certain percentage of the marked value, and the most common tolerance is ±5 per cent (a gold band). Thus, if a component has a value of 1k (1,000 ohms) and a tolerance of ±5 per cent, its actual value could be anywhere between 950 and 1,050 ohms.

Although five per cent carbon resistors are the most common type, some suppliers now supply one per cent metal film types as standard, and do not offer five per cent carbon types at all. Obviously a one per cent component can be used where a higher tolerance is specified in the components list, but a five per cent tolerance component can not be guaranteed to have adequate accuracy for an application where a one or two per cent component is called for. You should therefore never use a component having a tolerance rating that is inferior to the one specified in the components list.

POWER RATING

Components lists normally specify power ratings for the resistors, and nine times out of ten all the resistors are low power types having a rating of $\frac{1}{4}$ watt or thereabouts. In most cases the actual power fed to the resistor is a matter of a few milliwatts or even less, and something like $\frac{1}{3}$ or $\frac{1}{4}$ watt resistors are specified merely because they are the smallest type that are generally available.

You should not use small resistors where higher power types are called for as inadequately rated resistors will soon overheat and be destroyed. From the electrical point of view there is no reason why (say) a one watt resistor should not be used where a $\frac{1}{4}$ watt or other small type is called for. In practice higher power resistors will normally be unusable as they are physically much bigger than lower power types, and will simply not fit into the available space on the printed circuit board.

The power rating is not something that is indicated in the colour coding, but you should find that before too long you can accurately judge the power rating of a component from its physical size and appearance.



the second s

Value	E12	E24
1.0	YES	YES
1.1	NO	YES
1.2	YES	YES
1.3	NO	YES
1.5	YES	YES
1.6	NO	YES
1.8	YES	YES
2.0	NO	YES
2.2	YES	YES
2.4	NO .	YES
2.7	YES	YES
3.0	NO	YES
3.3	YES	YES
3.6	NO	YES
3.9	YES	YES
4.3	·NO	YES
4.7	YES	YES
5.1	NO	YES
5.6	YES	YES
6.2	NO	YES
6.8	YES	YES
7.5	NO	YES
8.2	YES	YES
9.1	NO	YES
10	YES	YES



Fig. 3. An alternative system of five band coding.



Fig. 4. An old system of coding used on some high power resistors.



Fig. 5. A more common (but obsolete) method of colour coding used on some high power resistors.

Terms such as "carbon film" and "metal film" refer to the composition of the resistor, and metal types generally have better stability and lower noise than do carbon types. It is consequently quite in order to use a metal type where a carbon resistor is specified, but probably not advisable to use a carbon resistor where a metal type is called for.

With most circuits the type of composition is not important and carbon types are stipulated in the components list, but a few critical applications, particularly some hi fi and test gear applications, do require high quality resistors in order to obtain and maintain a high level of performance.

OTHER CODES

There are variations on the basic four band resistor coding, and you are quite likely to encounter resistors having these alternative codings. The most common alternative is the five band type shown in Fig. 2. This is very similar to the four band type, but it differs in that there is an extra band which indicates a third digit ahead of the multiplier. This coding is designed for use with close tolerance resistors where values other than the standard resistor ranges are available, and values like 29k4 can be accommodated whereas the ordinary method of coding could get no nearer than 29 or 30k.

This is all rather academic to the amateur user as normal retail outlets only handle values in the E12 and E24 ranges (see Table 2). The third band will always be black (0), making it quite easy to decipher the value. In fact with this type of resistor I generally ignore the third band, work out the value in the normal way using the other three bands, and then multiply the answer by ten to compensate for the ignored band. This is likely to be the easiest way of doing things if you are already used to the four band codes, or predominantly deal with resistors of the four band type.

The other system of five band coding is shown in Fig. 3, and this is somewhat easier to deal with. The first four colours show the value and tolerance in the normal way, and the fifth band indicates the temperature coefficient of the resistor. The temperature coefficient is simply the amount by which the value of the component changes with variations in temperature, and is generally given in parts per million per degree Centigrade. This is a parameter which is very unlikely to be specified in a components list, and you can simply ignore the last band and treat the component as an ordinary four colour type

There are no other resistor colour codes in common use, but there are a couple of codes that were once commonly used for high power resistors. I must admit that all the high wattage resistors I have encountered over the last few years have had the value etc. written on them, and this is probably a more practical way of doing things on these larger components. It might seem like a more sensible way of doing things with small resistors, but modern. $\frac{1}{4}$ watt and other low power resistors are so small that the lettering would be illegible to all but those with the keenest of eyesight.

For the sake of completeness, and because there may well be resistors using these codes still in circulation, Fig. 4 and Fig. 5 give details of these methods of coding. They are actually just standard four colour codes which indicate the value and tolerance in the normal way, but the method of arranging the colours on the body of the component is different, and with the method of Fig. 5 the tolerance is not indicated.

If you are familiar with the methods of colour coding described here you should be having little difficulty in determining the values of the vast majority of resistors. Problems can arise though, with codes becoming partially obliterated, colours being indistinct, or with the five band codes it might not be apparent which of these codes has been used. A multimeter, even an inexpensive type, is invaluable in these situations as it should enable the value to be read accurately enough to eradicate any doubt as to the marked value of the component.

INDUCTORS

Some inductors use colour coding, but these just seem to use standard four band resistor fashion coding. However, the value is given in micro henries and not ohms.

Robert Penfold



This series is designed to explain the workings of electronic components and circuits by involving the reader in experimenting with them. There will not be masses of theory or formulae but straightforward explanations and circuits to build and experiment with.

Part 11 Another versatile timer circuit

LAST month we saw how the versatile 555 Timer i.c. can be used to build an astable multivibrator circuit. This month's project uses the 555 as the basis of a monostable multivibrator.

SIMPLE TIMER

The circuit diagram for a Simple Versatile Timer is shown in Fig. 11.1. This circuit lights a light emitting diode (l.e.d.) for a given period of time and comes on when the timer circuit is first triggered and extinguishes at the end of the predetermined time interval.

The timer can be adapted to run for periods of different length, so it has many uses. It can be used for timing moves in Chess or Scrabble. Use it for timing the various stages in developing a film. Use it to warn yourself when it is almost time to switch on your favourite TV programme or, even, use it to boil an egg!



Fig. 11.1. Circuit diagram for the Simple Timer using the versatile 555 timer i.c.

HOW IT WORKS

The method of operation is similar to that described for the Metronome



The completed stripboard version of the Simple Timer. The "ske-leton" preset preset VR1 may be substituted for an ordinary rotary type, with spindle. Make sure that the copper tracks have been cut at points L9, M9, N9, O9 and /18.

last month. One difference is that the trigger input (pin 2) is connected so that it can be brought low by "activating" the push-button switch, S2. Thus, the i.c. is *not* automatically re-triggered when it completes its operation.

Another difference is that pins 6 and 7 are joined directly; there is no resistor between them. The result is that the capacitor Cl is discharged instantly at the end of the charging phase.

The output from ICI pin 3 goes to an l.e.d., which lights when the i.c. has been triggered and stays on until the end of the charging phase. Since we need to run the circuit for periods of several minutes, we need a large value for capacitor C1.

Fig. 11.2. Demonstration layout for the Simple Timer.





CONSTRUCTION

The demonstration breadboard component layout for the Simple Timer is shown in Fig. 11.2. A stripboard version is shown in Fig. 11.3. There are five breaks to be made in the copper strips and these can be made by using a medium sized twist drill. It is important that the small link wire, inserted at points M12 and N12, is not overlooked.

If the value of capacitor C1 is 1000μ F, the values for resistor R1 and preset potentiometer VR1 depend on the time periods you require:

Period (minutes)	VRI	RÍ
1	22k	33k
2	22k	68k
3	22k	100k
4	47k	150k
5	47k	180k

The values given above allow you to set the timer fairly accurately to the chosen single value.

If you would prefer to be able to set the timer to several different delay periods, select the value of resistor R1 from the table above for the shortest time period you need. Then, by replacing the skeleton preset VR1 with a carbon track variable potentiometer, having a value between 100k and 1M, you can alter the timing period by simply rotating the potentiometerspindle to another setting.

For different positions of the variable resistor, find how long the timer takes and from this information you can mark out a time scale against which a pointer knob can be set. You can then run the timer for any set period up to about 18 minutes.

To obtain longer periods, increase the resistance or replace C1 with a capacitor of even greater value. With very high values, the small charging current will mostly leak away through the capacitor and the timer may never complete its operation. For this reason the longest practicable period is about one hour.

VARIATIONS

Instead of, or as well as, an l.e.d. that goes out when the period is over, you can arrange for an audible alarm to be triggered at the end of the period.

Fig. 11.3. Component layout for the stripboard version.



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COMPONENTS SR C

Resistors R1 180k R2 10k R3 180k All 0.25W 5% carbon



Potentiometer VR1 47k min. horizontal skeleton preset, lin. (single-period timer), or carbon track spindle type pot. (variable timer)

Capacitor C1 1000µ electrolytic

Semiconductors

D1 TIL 209 light-emitting diode

IC1 555 timer

Miscellaneous

S1 S.P.S.T. switch S2,S3 Push-to-make push-button (S3 optional)

Breadboard; 0-1in. matrix stripboard, measuring 95mm × 63mm; audible warning device (optional); 8-pin d.i.l. i.c. socket (optional); connecting wire; 6V battery.

f5

Approx. cost Guidance only



Fig. 11.4. Adding a "reset" button to the timer.

Simply connect the buzzer WD1 as shown by the dashed lines in the circuit diagram Fig, 11.1. It sounds as soon as the circuit is switched on, is silent when the circuit is triggered, and sounds again when the time is up.

If you trigger the circuit by mistake, it is a nuisance to have to wait about 5 minutes or more for it to complete its "cycle" of operation before you can use it again. It is useful to be able to reset the circuit at any time during its operation. This can be accomplished by adding a resistor and a "Reset" push-button switch, S3, connected to the reset input (pin 4), as shown in Fig. 11.4. Remove the connection between pin 4 and the positive 6V line, shown in Fig. 11.1.

Next Month: Introducing the operational amplifier.

b...Beeb...Beeb...Beeb...Bee

... timer and frequency meter ...

NLAST month's article the use of the two Timer/Counters in frequency counting and event timing were outlined, and in this article we will look at some simple software and hardware for these applications.

With the aid of a computer such as the BBC model B it is surprising how little hardware is needed to give good results in these applications. The software does not need to be particularly complex either.

Eventing

The timer set up that is featured here is very much along the lines of the system described last month, with Timer 1 generating a clock signal at the desired frequency, and Timer 2 counting the clock pulses fed to it via a gate circuit. In practical applications there is a minor problem with this basic arrangement in that some means of generating a suitable gate pulse is needed.

Just how this is done depends on the particular applications, but in some cases the gate pulse generator will need to be based on a simple set/reset flip/flop circuit. The output of a circuit of this type goes high when the "set" input is pulsed low, and high again when the "reset" input is pulsed low.

In this case the idea is to have a sensor to provide the "set" pulse at the beginning of the timing period, and another to give the "reset" pulse at the end of the timing period. This system does not lend itself well to all applications, and in some cases a single sensor might be sufficient to directly provide the gate pulse. However, this flip/flop method is probably the one which is most generally applicable.

A practical gate pulse generator of this type is shown in Fig. 1. This circuit also includes the gate itself (ICIc).

The circuit uses two photocells to provide the "set" and "reset" pulses, and these are TRI and TR2. These are silicon *npn* phototransistors, and virtually any devices of this general type will function well in this circuit (the TIL81 for example). Types which have a built-in lens are generally preferably in an application of this type though.

The photocells should normally be under fairly bright conditions, with suitable light sources being provided if the ambient light



level is unlikely to be sufficient. The timing period is started by a shadow being cast on TR1, and ended by a shadow being cast onto TR2.

A system of this type can, for example, be used in physics experiments in schools where acceleration of a ball-bearing running down an inclined plane has to be measured. The photo-transistors could be illuminated by separate ultra-bright light emitting diodes, with things arranged so that the ballbearing passes between each photo-transistor and its light source as it moves down the inclined plane.

A system of this type can also be used as a sports timer, although in this application each light source would need to be a considerable distance from its photo-transistor, and a simple optical system would probably be needed in order to obtain usable results.

The circuit, Fig. 1, is very straightforward in operation. Phototransistors TR1 and TR2 are both operated as what are effectively light dependent resistances. When subjected to low light levels they are effectively ordinary silicon npn transistors with very low collector to emitter leakage levels. If the light level to which they are subjected is steadily increased, the leakage level increases, producing a reduction in the collector to emitter resistance. Normally both devices are subjected to quite high light levels, and this produces a high enough leakage level to take their collector voltages down to little more than 0V. Note that no connection is made to the base terminal of either device.

A conventional CMOS bistable circuit is formed by IC1a and IC1b, and under stand-



by conditions the output at pin 4 of IC1 is high. IC1c is the signal gate, and as it is a NOR type, the high signal from the flip/flop blocks the flow of clock pulses from PB7, through the gate, and into PB6. Switch S1 is a manual reset control which can be used to make sure that the flip/flop commences in the correct state, or to reset it if the photocells fail to trigger it properly for some reason.

When a shadow is cast onto TR2 it sets the flip/flop, taking the control input of IC1c low and enabling the clock pulses to flow through to PB6. When a shadow is cast onto TR1, it resets the flip/flop, and cuts off the clock signal again. This gives the required gating effect, with a certain number of clock pulses from PB7 being fed through the Timer 2 via PB6.

Other forms of sensor can be used to activate the unit, and something as basic as a couple of microswitches are often adequate. Note though, that the flip/flop is a type which requires *positive* set and reset pulses, and *not* the negative pulses required by most types. The sensors must therefore be wired accordingly

The flip/flop is a simple type which will only operate properly if it is never taken to the state where both inputs are simultaneously fed with trigger pulses. If it is likely that the set and reset pulses will overlap, this must be avoided by adding a pulse shaper circuit to shorten the two pulses.

This can be achieved using the circuit of Fig. 2, which is just two positive edge triggered, non-retriggerable monostables, which are formed from the four NOR gates of another 4001BE device. The output pulse duration is only a few microseconds, which should be short enough to ensure proper operation of the system.

Software

The simple program below provides a basic Event Timer action: 5 REM event timer prog

10 CLS 20 7&FE6B = 224 30 7&FE64 = 80 40 7&FE65 = 195 50 7&FE68 = 255 60 ?&FE69 = 255 70 A = ?&FE68 80 B = ?&FE69*256 90 C = 65535 - (A+B) 100 PRINTTAB(10,10)" 110 PRINTTAB(10,10)C/10 120 IF INKEY(-99) THEN GOTO 50 130 FOR D = 1 TO 300:NEXT 140 GOTO 70

This simple program is very much the same as the Running Timer program provided in the previous article, but in this case, of course, the count only runs during the gate period. The figure initially displayed will therefore be zero. The display will increment during the gate period, and will "freeze" with the event time (to the nearest tenth of a second) when the gate pulse ends.

For timing in one hundredths of a second the values written to Timer 1 at lines 30 and 40 should be altered to 136 and 19 respectively, and the division rate at line 110 should be increased to 100. For timing in milliseconds the values written to timer 1 are 244 and 1, and the division rate at line 110 should be 1000.

Increasing the resolution reduces the maximum time available before Timer 2 cycles through zero and back to 65535, with this taking just 65.535 seconds with 1 millisecond resolution. Line 120 enables the timer to be reset to zero by depressing the spacebar.

This routine should enable the timer to be set up and tested satisfactorily, but there is obvious room for improvement. However, this is something that must be tailored to suit the particular application involved.

Frequency Meter

The obvious method of using the timers for frequency measurement is to have Timer 1 in the single-shot mode to generate a gate pulse, and then count input pulses using Timer 2. In practice this method does not work very well because Timer 1 cannot produce gate times of more than about a fifteenth of a second in duration. Short gate times give relatively poor resolution and prevent low frequency measurement.

Although the maximum possible count would suggest that quite high frequencies can be accommodated, due to the maxi-



mum input frequency restrictions of the 6522 VIA this is not actually the case. This gives the worst of both worlds, with poor resolution and limited maximum input frequency.

A much better method is to use Timer 1 in the free-running mode to feed into a divider circuit, and to use one set of output half cycles as the gate pulses. In this way accurate gate periods of a second or more can be produced. This is the general scheme of things in the basic frequency meter circuit of Fig. 3.

Timer 1 generates an output at a frequency of 10Hz on PB7, and this is divided by IC1 to give a 1Hz output. The output of IC1 is divided by two in IC2 to give a 0.5Hz output, and a high output period of one second. It is this high output period that constitutes the gate pulse.

IC3 is the signal gate, and is a NAND type. Therefore, it enables input pulses to pass through to PB6 during the period when the output of IC2 is high. The other four gates of IC3 are left unused incidentally.

Note that in this basic form the circuit will only respond to inputs at normal logic levels. The frequency range is 0 to 65.535kHz with 1Hz resolution.

The simple program below can be used to test the Frequency Meter. 5 REM DFM PROG

10 CLS 20 ?&FE6B = 224 30 ?&FE64 = 80 40 ?&FE65 = 195 50 ?&FE68 = 255 60 ?&FE69 = 255 70 IF (?&FE60 AND 32) = 0 THEN 70 80 IF (?&FE60 AND 32) = 32 THEN 80 90 A = ?&FE68 100 B = ?&FE69*256 110 C = A + B 120 PRINTTAB(10,10)""" 130 PRINTTAB(10,10)65535 - C 140 GOTO 50

The above program sets up the timers in exactly the same way as in the Event Timer program. A slight problem with this general set up (Fig. 3) is that of determining just when a gate pulse has been completed and a new reading should be taken and displayed.

There is more than one way of tackling this, but the method used here is to use PB5 as an input line which monitors the gate signal, with lines 70 and 80 of the program then periodically inspecting PB5 and holding off reading of Timer 2 until the end of a new gate pulse. Although the gate period is one second, there is a one second delay between readings, and the display is therefore updated once every two seconds.

The frequency range of the unit is a useful one for audio frequency measurement, and other low frequency applications such as r.p.m. measurement. The frequency range could be boosted by a factor of ten by eliminating IC1 from the external divider chain, or simply altering the values fed to Timer 1 so as to give a 100Hz output signal.

The maximum input frequency parameter of the 65622 VIA does not permit higher frequencies to be handled, although this can be overcome by adding a prescaler at the input of the circuit. However, raising the full scale value of the circuit is achieved at the expense of reduced resolution.

Next month we will conclude our look at timing and frequency measurement using the BBC Micro with some more versatile and sophisticated hardware and software.



ONE FOR THE RECORD

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HE APEX power amplifier has been designed to compliment the Apex preamp both in styling and in performance. Considerable attention has been paid to those factors which affect the sound quality and these will be explained in this article. The heart of the power amplifier is a massive 300VA mains transformer. This is far in excess of the power requirements of the amplifier, but its justification lies in the extra improvement in sound quality that a larger transformer gives in relation to its cost. For high current rectification a 25amp bridge rectifier is used and this is bolted directly to the heat sink to minimise any possible effects of temperature generated distortion. I myself have yet to investigate the effects on sound quality of different rectifiers, but I have heard claims from various sources that differences in sound quality do exist for different rectifiers.

Power supply reservoir capacitors are known to have a large effect on sound quality. For this reason, the capacitors I have specified for the main high current reservoirs are a special kind developed for audio use. The internal construction of the capacitor is based upon the ideas of Denis Morecroft of DNM Supplies who is known for his preamps of outstanding sound quality. These capacitors are manufactured under the brand name Filmcap DNM and are used in a number of popular brands of hifi amplifiers. Although these capacitors are quite expensive, it is not prudent to save on such basic electronic components.

The power supplies for the low current voltage amplifiers of each power amplifier have been taken via separate rectifiers for each channel and then fed into a high

performance i.c. regulator. This gives considerable isolation from the effects of the current drawn from the output stages when driving a loudspeaker at high levels. Under such circumstances the voltage on the main reservoir capacitors fluctuates considerably. In most amplifiers, this voltage fluctuation is fed back into the sensitive input circuitry. Even with high quality input circuitry its effect is easily audible and results in distortion of the sound rather like the visual distortion of a telescope out of focus. As very few hifi amplifiers use separate supplies for high and low current parts of a power amplifier, the amplifier you are about to build should represent a worthwhile improvement in sound over comparable (in power rating but costing at least twice as much) manufactured hifi power amplifiers.

POWER AMPLIFIER

Unless you have perfected the perfect power supply, you still need a good power amplifier circuit with good power supply ripple rejection. In simple terms, if an unwanted signal voltage is present on a power supply line, the power supply ripple rejection is the ability of the circuits to prevent such a signal from reaching the output. When you get down to the nitty gritty of defining ripple rejection for measurement purposes things get a little complicated and confusing. However, measurement of ripple rejection of an audio circuit is quite easy to set up and circuits can easily be evaluated in a way in which measured performance relates quite closely to sound quality. In the Apex power ampli-



fier, excellent ripple rejection performance is achieved on the positive side by cascode circuitry (see first article on preamp for a discription of cascode circuitry and its effects on power supply ripple rejections) and on the negative side by constant current sources of high dynamic impedance.

A simple definition of high dynamic impedance is that changes in the amount of current supplied by the circuit are very small in comparison with the change in voltage across the constant current circuit. The two transistor constant current sources used in the Apex power amplifier give a higher impedance (about four times) than the more popular two diode plus transistor constant current source used in many power amplifiers, and it will sound better. Even better results would be achieved by replacing the resistor from the constant current circuit to 0V with a J500 series current diode (eg. J507 at 1-8mA).

TRANSISTOR SOUND

For many years people have talked about the transistor sound and described it as hard and brittle and not as natural as valves. Many explanations have been put forward over the years, but the best explanation I have found is that such differences are caused by temperature generated distortion in transistors. The amplifying parameters of a transistor are its gain and its base-emitter junction voltage. These parameters are not constant, but vary with different temperatures at the transistor semiconductor junction. When an audio signal is being amplified by a transistor, its current and voltage will vary in relation to the instantaneous magnitude of signal.

The power dissipated will vary in relation to the instantaneous product of current times voltage. If the transistor has a high thermal resistance, the temperature at the semiconductor junction will rise and fall as the power level varies above and below its mean value. Although the heat generated in relation to the power is instantaneous, its dissipation takes time. As a result of this time-smearing effect the sound signal is distorted by the temperature generated gain changes. By changing to transistors of low thermal resistance, the temperature variations of the junction are minimised and the sound quality is audibly improved. In the Apex power amplifier the driver transistors TR8 and TR10 are 65 watt TO-220 types and the extra cost over TO-92 types (e.g. MPSA06 and 56), is more than justified by the improvement in sound quality. Further improvements in sound quality can be achieved by attaching an aluminium heatsink to both transistors (remember to insulate at least one transistor from the heat-



Fig. 23. Class AB amplifier.

sink). Another example of the way in which the Apex is designed for low temperature generated distortion in the use of Darlington transistors for TR12 and TR13. Put quite simply, Darlington transistors sound better because the driver transistor enjoys a much lower thermal resistance. Temperature generated distortion of the output transistors is kept very low by the use of a generously large heatsink.

CLASS "A"

A great deal of emotional hot air is talked about class A operation in power amplifiers. Whilst class A operation is a good idea, because it avoids any effects of the switching characteristics of the output devices, it is not really the most important feature in the design of an amplifier. In fact the most significant benefits in sound quality of a class A amplifier are a direct result of: (a) using a much bigger power supply and (b) much lower temperature generated distortion caused by large heat sinks. If you build a class B amplifier with the same power supply and heat sink its sound quality will

almost approach that of the class A. The class A amplifier will have more "depth" to the sound. Now we can take the class B amplifier and "upgrade" it by the following measures:

- (1) Replace the driver transistors with high power (i.e. low thermal resistance) transistors fixed to the heatsink (or replace these and the output transistors with Darlingtons).
- (2) Replace the bias transistor with a low thermal resistance type. Mount it on the heatsink with all the output devices very close.

For a typical class B amplifier in which these measures have not been provided, the sound quality will be improved quite considerably. It may even sound quite a bit better than the class A amplifier. Because of the high dissipation in a class A amplifier, it might not prove possible to mount the output transistors close together or attach the bias transistor close to each one. The class A might not be capable of further improvement without a complete change of design.

Most commercial class A amplifiers are class AB designs run at a high bias current. They still require the emitter load resisitors R1 and R2 (Fig. 23). From my own test, I can confirm these components contribute considerable distortion to the sound in two ways:

- (a) Non linearity caused by VI drop across the resistors. This can be tested by replacing another resistor in parallel and resetting the bias.
- (b) Losses caused by the component quality.

Change the resistors from wirewound types or $\frac{1}{2}$ watt metal film types in parallel (low power amps mainly) to a batch of 1W IRO Holco H2 resistors in parallel to the same value and significant improvement in sound quality will be obtained. I have yet to try bulk foil resistors in TO3 cans, but these should yield further sonic benefits at a price.

Eliminating these resistors would result in considerable sonic benefits and the class A circuit of Fig. 24 almost achieves that. Although R1 and R2 have been eliminated, Resistor R3 needs to be of the highest sonic quality as it determines the constant current

supply. This is a better output stage, but it limits the output current to the current supplied by the current source and power dissipation in the output stage is very high in relation to the power available to drive the speakers. With the use of bipolar transistors there is a disadvantage that emitter resisitors are required to share the output current if more than one output device is required. Back to square one. However, these resistors are not required if mosfet output devices are used (Fig. 25). But here we have the disadvantage of a larger voltage drop between gate and source of mosfets.

APEX OUTPUT STAGE

The output stage of the Apex power amplifier combines a number of the advantages of both class A and class B design (Fig. 26). The driver transistors are Darlington types which are run at a current of around 200mA. The output transistors are TO3 types operating in class B. By mounting the power transistors very close to the bias transistors, the resistors R6 and R7 can be made very small (0.15 ohms) without thermal runaway and the driver transistors operate in class A via R4 and R5 up to quite high output currents.

Another advantage of the close thermal tracking between the bias and output transistors is that a variable resistor is not required in the bias circuit.

Substantial sonic improvements would be obtained by replacing R6 and R7 with a direct wire, but this would increase the possibility of thermal runaway. Other sonic improvements which may be obtained at the expense (!) of reduced safety margin against thermal runaway are:

(a) Reduce the value of R4 and R5 to increase the current in TR1 and TR2. (b) Increase bias voltage marginally.

Such measures are only recommended to readers with considerable experience of amplifiers and good supplies of the output transistors.

CIRCUIT DESCRIPTION

The power amplifier is based on the circuit shown in Fig. 27 but with a number of refinements which have been added to

Fig. 24. Genuine class A amplifier.



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Fig. 25. Class A output stage with bipolar constant current source.





improve the performance. The best way to understand the full circuit is to describe the operation of the circuit of Fig. 27 and then cover each refinement to it separately. To assist in relating the circuit of Fig. 27 to the full circuit, particular components have been given identical component numbers. The first stage consists of a long tail pair



Fig. 27 (above). Power amplifier basic circuit.

Fig. 26 (left): Class A plus class B output stage.

TR1, TR2 fed by a constant current source (TR5, TR6 in final circuit). A positive going input at the base of TR1 raises its base voltage above the base of TR2. As the feed to the emitters of TR1 and TR2 is constant, the shift in base voltage of TR1 causes the collector current of TR1 to increase (and TR2 to decrease). The increase in TR1 collector current causes an increase in the voltage across R4, and also across TR9 base-emitter junction. This results in more current flowing from the collector of TR9. As its load is a constant current source, the increased current feeds the base of TR12. This causes more current in TR12 and TR13 and this current difference is the current available to drive the load. The voltage at the output rises to feed the load. At the same time a part of the output is feedback into the base of TR2 via R10. The ratio of the voltage at TR2 base to the output voltage is R7/R7+R10. As the open loop gain of the ampifier is quite high, the difference between the bases of TR1 and TR2 is very small compared to the input voltage. So if the voltage at TR1 base is approximately equal to the voltage at TR2 base, the output voltage is approximately equal to

$\frac{R7+R10}{R7} \times \text{ input voltage}$

The overall gain of the amplifier is determined by resistors R7 and R10.

The full circuit is shown in Fig. 28. A description of all the additional circuitry now follows. Capacitor C1 is the d.c. blocking capacitor, this prevents d.c. voltage reaching the input transistors. It also has the important function of keeping the offset voltage at TR1 base constant when connected to preamps of different d.c. output resistance. Resistor R1 holds the input point at zero volts. d.c. If R1 were omitted the free end of C1 would be charged to a small d.c. voltage, depending on the signal at its other end. If an input would short



Everyday Electronics, May 1987



the d.c. to ground and cause a thump from any speakers connected to the amplifier output. Resistor R3 holds the base of TR1 at a small negative voltage = $RI \times I_b$ (of TR1). Resistor R3 can be varied to adjust the output d.c. offset. If transistors TR1 and TR2 are closely matched the lowest output offset will be obtained with R3 = R10. As the low frequency response of the input is determined by the values of Cl and R3 (R2 being small by comparison to R3), there is a practical lower limit on the value of R3. The final part of the input circuit is the high frequency filter made of R2 and C2. As the input circuit will have a finite impedance at audio frequencies, the actual filter frequency is determined by the combined impedance with R2 in relation to C2. C2 will have an additional advantage as its low impedance at very high frequencies improves high frequency stability of the amplifier.

CONSTANT CURRENT

The first stage constant current source is formed from TR5 and TR6. The current is

defined by the value of resistor R5 and the base emitter voltage of TR6. As the collector of TR6 is held constant at close to 1.2Vabove the emitter, its base/emitter voltage variations are kept very low in relation to ripple in the power supply or changes in voltages of the emitters of the long tail pair due to the input signal voltage. Hence the current through R5 is constant and the circuit offers a high dynamic impedance at the collector of TR5.

Transistors TR1 and TR2 form the input long tail pair. TR3 and TR4 are cascode transistors. Resistor R9 and diodes D1 and D2 act as a filter to ripple in the power supply and hold the bases of TR3 and TR4 at approximately 1.2V above earth. Their emitters are thus at approximately 0.6V above earth. The collectors of TR1 and TR2 are held at the same voltage. As ripple voltages on the power supplies have been filtered, modulation of the gain of TR1 and TR2 due to power supply ripple is greatly reduced and hence the sound quality much better with the addition of TR3 and TR4. TR9 acts as a second stage amplifier with its collector voltage held constant relative to power supply ripple by TR10. TR10 is a TO-220 high power device BD243C chosen for low temperature generated distortion and high minimum gain specification.

Capacitor C4 provides local feedback to reduce high frequency open loop gain and improve the stability of the circuit. The constant current source formed by TR7, TR8, R11 and R12 provides a load for the second stage.

OUTPUT STAGE CIRCUIT

The output consists of a class A stage (TR12 and TR13) and a class B stage (TR14 and TR15). The quiescent current is controlled by a bias transistor TR11 and resistors R14 and R15. This is set to give just under 5Vbe voltage drops, four of which are used for TR12 and TR13 Darlingtons with about 0.3V to 0.4V across the output transistors TR14 and TR15.

The values of R20 and R21 are kept very low so that when a large current flows through either TR14 or TR15, the com-

COMPONENTS

Amen	dments to the compo	onents list
Resistors R5 R14 R15	Standard kit 220 metał film 1k4 Holco H8 332 Holco H8	Improved version 221 Holco H8
R32 R33,34 R35 R30,31,130,131 R36	68 metal film 330 metal film see text 47 5W w/w 4k7 carbon	68Ω1 Holco H8 332 Holco H8
Capacitors C7 C8,9 C19,20	not used 10µF radial elect. 63∨ 470nF polyester	470n polypropylene
Semiconductors TR3,4 TR12 TR13 D3 D4	BC547C TIP121 TIP126 red I.e.d. not used	
D11	red I.e.d.	

bined value of its V_{be} drop and voltage across the resistor prevents TR12 or TR13 from cutting off. With the values as published, TR12 and TR13 will not cut off until a current of at least two to three amps is reached. Safe operation of the circuit depends on TR11 being mounted as close as possible to TR14 and TR15. Any increase in temperature of TR14 and TR15 will result in increased current. Close thermal coupling to TR11 will ensure that TR11 heats up and its base/emitter voltage falls. As a result the bias voltage to the output stages falls reducing the quiescent current and maintaining equilibrium. Capacitor C10 and R17 are provided to ensure low output impedance at high frequencies and diodes D5 and D6 are used to prevent damage to the output stage as a result of the high voltages that might occur when loudspeakers are disconnected. Capacitor C3 ensures that the gain at d.c. is reduced to

one so that any d.c. offsets caused by changes of temperature in the input stage are not multiplied by the gain of the amplifier (X48) at the output stage. A large value of C3 is required to ensure that full amplification is maintained down to the lowest audio frequency. With the values used the frequency at which the power is halved (-3db) is around 6Hz. Loudspeaker protection is provided by FS4 which is contained within the feedback loop for better sound.

SUPPLY

The output stage is powered from a single unregulated supply of +36V using BR1 and two large reservoir capacitors C17 and C18. Resistors R27 and R28 are mounted on the capacitors themselves. These are particularly useful when testing power supplies as they will discharge the capacitors slowly within five minutes.

Separate power supplies including rectifiers and reserviors are provided for the low current side of each power amplifier. These are mounted on a separate board. The earth returns from the reservior have their own direct connection to the earth point. The resistance of the earth return wire will cause ripple voltages at the earth point due to the high ripple currents used in charging the reservior capacitors. If not taken directly to earth, these ripple voltages will be present at the output of the regulators. The output voltages are set to $\pm 30V$ by i.c. regulators IC1, IC2, IC101, IC102. The voltage is adjusted by the resistors R23 to R26, R123 to R126 and is set close to \pm 30V. The regulators require a minimum of 3V from input to output for correct operation and the output voltage is set to allow for variations in mains supply as well. If your mains supply varies by more than +10 per cent, you may wish to reduce the value of the 4k75 resisitors to reduce the output voltage. High frequency stability of the regulators is provided by C15, C16, C115, C116. Fuses FS2 and FS3 will blow in the event of failure of one of the output transistors. Resistor R29 is a voltage dependent resistor which removes high voltage spikes from the mains.

Resistor R35 is included to maintain stability at high frequencies when long (inductive) leads are used for signal and supply earths. Typical values might be $2\Omega 2$ to 10Ω , but this resistor is not strictly necessary if the Apex is built into its case. **Next month:** final construction and testing.

PLEASE NOTE. Some components have been changed from the original components lists to ensure amplifier stability on different power supplies (see list above).

We appologise for a mistake in part 2-components IC4 to IC7 and IC104 to IC107 are shown the wrong way around (Fig. 19). The metal tabs should be on the right.





This circuit was designed musical instrument tuner as a multi-purpose that could be pre-set to cover a wide range of crystal controlled reference frequencies. Tuning the instrument is achieved by a novel and very effective display using a ring of l.e.d.s.

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NOTE

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FLAT SHARP

VISUAL

It is possible to emulate the ding dong chime effect electronically and it is a simple matter to generate

eight-pin integrated circuit.

It is possible to emulate the ding dong chime effect electronically and it is a simple matter to generate uncomplicated tones over a loudspeaker: the tones however is to modify this by modulating character to introduce the reverberation and decay character istics of a "Friedland". The Door Chime to be described utilises a single

tics of a Friedland . The Door Chime to be described utilises a single inht-nin integrated circuit

GUITAR

TUNER

Of all the disco effects available, lights which flash to the beat of the nash to the year of the music continue to enjoy greatest popularity. This circuit fills the gap between simple lowvoltage circuits and highpower multi-channel systems.

REGENERAT

The regenerative receiver, a radio set that's just about the oldest of all the useful amateur designs, is still effective today and is cheap to build. In a series of two articles we explore the theory and practice of regenerative and practice of regeneration, radio sets, their operation, some practical ideas, and the results you can expect from the circuits described.

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JUNE ISSUE ON SALE FRIDAY MAY 15, 1987

FRIDGE ALARM T.R. de VAUX-BALBIRNIE

Designed for caravanning but equally suitable for household use

Most caravans are fitted with a refrigerator. This uses a gas flame or electric element to heat the refrigerant contained in a small boiler to the rear of the food cabinet. Interruption of the supply, for whatever reason, will soon result in ruined food especially in warm weather.

This project is a self-contained alarm of very small physical size and near-zero power consumption. Placed inside the refrigerator, it emits a loud tone if the internal temperature rises above a preset level.

When used in a trailer caravan the sound cannot, of course, be heard while driving so the device is limited to on-site operation. In a motor caravan, however, it may be heard from the driving seat—although not, perhaps, with the engine running. The alarm is equally suitable for household use and is especially useful where children leave the door open.

The prototype unit has been shown to be loud enough for all normal purposes but there could be times when it is insufficient —under noisy conditions, for example. In case of doubt, obtain the buzzer only (make sure it is the *high intensity* type as specified) and by connecting this *direct* to a 9V battery inside the fridge, the value or loudness of the device may be assessed.

Standby current consumption of the

alarm is less than $5\mu A$ so the internal battery will give excellent service. Consumption rises to approximately 35mAwith the alarm sounding. The on-off switch provides a test facility whereby the battery may be checked periodically. The small size of the unit ensures that very little food space is lost—this is especially important with one cubic foot fridges.

CIRCUIT

The circuit for the Fridge Alarm is shown in Fig. 1. IC1 is an operational amplifier of a type chosen for its extremely small current requirement. R2 is a negative temperature coefficient thermistor which, together with R1, forms a potential divider connected across the supply. Thus, a certain voltage exists at the op-amp inverting input (pin two). This voltage falls as the temperature of R2 rises.

A second potential divider consisting of R3 in the upper section in conjunction with R4 and VR1 in the lower one is connected to the op-amp non-inverting input (pin three). With VR1 correctly adjusted and with R2 below the required operating temperature, the voltage at the inverting input will exceed that at the non-inverting one. The op-amp is then off with pin six low (battery negative voltage) and having no further effect. With R2 sensing a higher temperature, the voltage at the inverting input falls below that at the non-inverting one and the op-amp switches on with pin six high (battery positive voltage). This oper-ates TR1 hence the buzzer, WD1, in the emitter circuit.

Switch SI is a three-position four-pole



type. In this application only two poles are used. Pole one directs current to the circuit when the switch is set to either of the extreme positions (Test and On)—the centre position is Off. Battery checking is provided by Pole 2. When SI is in the Test position, R5 makes ICl pin three go high so operating WDl irrespective of the temperature of R2. Note that very high value resistors are used in the potential dividers to reduce the continuous current drain whenever the circuit is switched on.

CONSTRUCTION

Warning: since IC1 is a C-MOS device and therefore vulnerable to static damage, do not unpack it until it is required. When inserting it into its holder, do not touch the pins.

The prototype was constructed in an aluminium case but a plastic one of similar size would serve just as well. The circuit is based on a printed circuit panel size 40×27 mm. The master pattern to be etched is shown full size in Fig. 2. The component layout is also shown in Fig. 2. This small printed circuit board is available from the EE PCB Service—Code EE 565.

Mount the on-board components. Using light-duty stranded connecting wire, extend R2 connecting wires by 5cm and sleeve them using pieces of insulation removed from connecting wire (see photo). Solder 5cm pieces of stranded connecting wire to the points indicated on the p.c.b. Drill countersunk holes in the base of the case for circuit panel mounting and make the holes for WD1 and S1.

Note the manner in which WD1 is mounted—this sis neater than direct surface mounting. Measure the position of VR1 and drill a hole in the case so that this component may be adjusted with the lid in position using a small screwdriver. Drill a small hole near R2 position and fit it with a small hole near R2 position and fit it with a rubber grommet. Mount the buzzer and switch and complete all wiring (see Fig. 3). WD1 is polarity-sensitive and will not work if connected the wrong way round.

Attach the circuit panel to the base of the box using small countersunk fixings through the holes drilled for the purpose. If a metal case is used, prevent short-circuits occurring between the copper tracks on the underside of the p.c.b. and the metalwork by using small stand-off insulators. Alternatively, place a thick piece of cardboard between the base of the case and the circuit panel.

TESTING

Switch S1 off (centre position) and rotate VR1 sliding contact fully clockwise. Connect the battery and place in position—this is self-wedging if the specified case is used. Switch S1 to Test and check that the buzzer sounds. Switch to On—the buzzer should sound—and adjust VR1 to silence it. Warm R2 between the fingers whereupon the buzzer should sound again. If this basic operational test reveals that all is well, offer the lid into position and gently coax R2 so that the body protrudes through the rubber grommet (see photo). Secure the lid and check that VR1 can be adjusted through the hole in the case using a small screwdriver.

Often the best place for the unit is attached to the inside of the fridge door. Heavy-duty double-sided adhesive tape may be used to secure it. The door will then act as a sounding board and give loudest results. It also leads to a fast response time if the door is left open.

USING THE ALARM

With the unit in position, allow the fridge to reach operating temperature before switching on. If the fridge is already at operating temperature, allow half an hour at least before making adjustments. VR1 should be adjusted quickly (before the temperature rises unduly) so that the buzzer just remains off. This adjustment is critical and should be made over a trial period. Check that the buzzer sounds when the door is left open. The unit may now be forgotten except to check the battery every few months. If ever the alarm sounds for a long time, it is advisable to replace the battery and this should be done, as a matter of routine, before each season's use.



Fig. 2. Printed circuit board component layout and full size p.c.b. master pattern.

(above) The completed unit with top re-

moved showing the compact component layout. (below) The completed circuit

EE 541

board with thermistor attached.

Fig. 3. Interwiring to the thermistor (R2), switch and buzzer. Note the use of plastic sleeving when extending WD1 and R2 leads.



Completed alarm with thermistor protruding through rubber grommet in side of the case

COMPONENTS

Resistors R1,R3 8M2 (2 off) R2 G16/GL16 bead thermistor R4,R5 100k (2 off)

10k



0.25W ± 5% except where stated

Potentiometer

VR1 2M2 standard preset —vertical mounting

Capacitor

R6

C1 10nF

Semiconductors

IC1 ICL7611 CMOS op. amp. TR1 ZTX300 npn silicon.

Miscellaneous

S1	miniature 4-pole 3-posi-
	tion slide switch
WD1	6V high intensity solid-
	state buzzer; approx. op-
	erating frequency 450 Hz,
	current 35 mA.
B1	PP3 battery and
	connector

AB12 aluminium box size 76 x 51 x 25 mm; stranded connecting wire; fixings; rubber grommet; printed circuit board available from the EE PCB Service, order code EE 565; eight pin d.i.l. i.c. socket.

Approx. cost Guidance only

Everyday Electronics, May 1987



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DIODE RESISTANCE

F YOU measure the resistance of a diode with an ohmmeter you get different values on different ranges of the meter. I'm talking about the conducting or ''forward'' resistance, of course. For a silicon diode, the reverse resistance is so high that it doesn't register on an ordinary multimeter.

The reason for this variability is just that a diode is not a resistor. Its resistance is not constant but changes when the measuring conditions change. Before delving into the reasons for this, let's just do a brief recap on how proper resistances behave.

The effect of voltage on three different resistances is shown in. Fig. 1. Take 20 ohms, apply 1V and 50mA flows. If this voltage is halved the current is halved. Current is strictly proportional to applied voltage, just as Ohm's Law says.

If the 20 ohms is halved to 10 ohms then for the same voltage the current is doubled; and if the resistance is doubled to 40 ohms then all the original currents are halved. The lower the resistance the steeper the slope of the graph, but it's always a straight line.



Fig. 1. Voltage-current graphs for three resistances.

GRADIENTS

This idea of the steepness of the slope—the gradient, if you like—comes in handy when looking at diode behaviour. The resistance is quite simply the gradient of the graph. For a straight line the gradient is the same at all points, but for a curve it isn't. Diodes give curved voltage-current graphs. In Fig. 2, a graph for a silicon junction diode is superimposed on the graphs for our three resistances. The voltage range has been restricted because if you apply more than about 1V to a junction diode you are liable to destroy it. This is because the current doesn't rise in a straight line but in an upward curve of ever-increasing steepness.

One way of looking at the diode resistance is to note where the diode curve intersects a resistance line. At this point the diode's resistance and the real resistance are the same. Thus at point 1 the diode resistance is 40 ohms, at point 2, 20 ohms, and at point 3, 10 ohms. The curve doesn't attempt to represent a particular diode but it gives a good idea of the general behaviour of a diode.

The resistance values at these points of intersection are the "large-signal resistance" of the diode. The "signal", in this case, is just the applied voltage.



Fig. 2. Diode graph on resistance graphs.

SLOPE RESISTANCE

We saw, however, that the slope of the graph is also an indicator of resistance. For the diode, the slope at any point of intersection is much steeper than for the corresponding resistance. Steeper slope means lower resistance.

So, in one sense, the diode resistance is different from the real resistance at the intersections. We have TWO values for the diode resistance at each point: the "large-signal" value and the slope value.

The slope value is what would be seen by a very small extra voltage superimposed on the test voltage. For this reason it's called the "small-signal resistance". For a typical silicon junction diode the small-signal resistance is only a few per cent of the large signal value.

For a perfect semiconductor junction diode the small-signal resistance depends on the current rather than the voltage, and it's easy to estimate. At 1mA the resistance is about 25 ohms. If the current is doubled the resistance is halved and vice versa. The rule is, divide 25 by the number of milliamps of current and the answer is the small-signal resistance in ohms.

The rule works for both silicon and germanium diodes and for transistor junctions, and at low currents, say 10mA or less, it's fairly accurate. At high currents it gives an underestimate because in real diodes the junction isn't the only resistance. There's the "bulk resistance" of the piece of semiconductor material and this, though low, becomes significant as the current is increased.

The dotted line in Fig. 2 shows the slope of the diode graph at high current. The line intercepts the voltage axis at about 0.7V.

This is the voltage at which a silicon diode would begin to conduct if the electrons inside it had no thermal energy but were moved only by the applied voltage. Current would then start to flow when the voltage was large enough to overcome the internal potential barrier which develops across the junction.

This turn-on voltage is sometimes called the "forward Zener voltage". It's about 0.7V for ordinary silicon diodes and about 0.25V for germanium junctions. For the materials used to make l.e.ds it's greater and varies with the l.e.d. colour because this is a function of the composition. The voltage is temperature dependent and reduces as the temperature is raised.

METER READINGS

It's a reasonable rule-of-thumb approximation to take the turn-on voltage as 0-7V for a silicon diode. When an ohmmeter is applied to the diode in the conducting direction about 0-7V of the meter's internal battery is used up in turning on the diode, leaving what's left to drive current through the internal resistor which sets the meter's ohms range.

For a 1-5V cell inside the meter this leaves 0-8V to drive current through the internal range resistor. The meter pointer then moves a certain distance. If the meter is switched to a higher range also operated by the same cell the current is reduced but the diode drop is still not very much less than its former value. So the pointer ends up in roughly the same position on both ranges. The result is that the indicated resistance increases from range to range by a bit less than the range factor.

The meter, in fact, indicates the largesignal resistance of the diode at the current set by the test voltage and the resistance standard. If the test voltage is well below 0.7V then the diode is not turned on and the meter reads "infinity".

Some electronic multimeters (and even a few non-electronic ones) are deliberately designed not to turn on diodes. This enables in-situ resistance measurements to be made on transistor circuits without the confusion caused by the meter driving junctions into conduction.



Fig. 3. Diode rectifier circuit.

RECTIFIER CIRCUITS

When you put a diode into a rectifier circuit, Fig. 3, you may be interested in the resistance seen by the a.c. source which drives it. In fact the current drawn from the source is time-varying. For a rectifier feeding a load R shunted by a reservoir capacity C the current flows in pulses near the positive peaks of the voltage.

The rest of the time the diode is nonconducting. So the resistance seen by the a.c. source is infinite for most of the time but quite low briefly when C recharges. However, this varying resistance can be averaged out over a period of time to give an equivalent steady value and this is what



Fig. 4. Biased diode detector.

you want to know if you are interested in how much power the a.c. source must supply.

With a perfect diode with no resistance when conducting and zero turn-on voltage, a large reservoir capacity C would hold the d.c. output voltage steady at the peak a.c.

voltage. This peak is the r.m.s. voltage multiplied by $\sqrt{2}$.

Suppose R is 100 ohms and the peak voltage (i.e. the d.c. output voltage) is 100V. Then the current is 1A and the power 100W. The a.c. source must deliver 100W. But the r.m.s. voltage is not 100V but $100/\sqrt{2} = 70.7V$.

For 70.7V to deliver 100W the current must be 1.414A and the resistance must be 70.7V/1.414A = 50 ohms. This is just half the value of R. So when a diode is used in this sort of rectifier circuit the resistance. seen by the a.c. source is not R but R/2.

DETECTORS

If the a.c. voltage (Vin) is small, as it may be when the diode is used as the detector in an a.m. radio, then it needs help to turn on the diode. This help takes the form of

applying enough forward bias voltage, Fig. 4, to make the diode pass a few microamps d.c. The a.c. input then makes the diode conduct more, when positive, or less when negative.

For very small a.c. inputs the diode conducts about as well on both half cycles and Vin sees the small-signal resistance at the applied d.c. bias current. This, as we saw, is 25 ohms divided by the number of milliamps of current. For a typical bias of 10µA (1/100mA) the resistance is 2500 ohms.

If V_{in} is now made very large the effect of the bias is swamped and the ciruit behaves like Fig. 3, the diode seeing R/2. It may be desirable to keep the resistance seen by Vin fairly constant. To do this R is made twice the small-signal resistance (or 5k with our bias).

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