# D **JUNE 1989**

**INCORPORATING ELECTRONICS MONTHLY** 

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SIGNAL INJECTOR



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The Magazine for Electronic & Computer Projects

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	No All enti	I. 2 LIST BAKERS DOZEN PACKS packs are £1 each, if you order 12 then you are tied to another free. Please state which one you	
Ť	war ref in th	n. Note the figure on the extreme left of the pack number and the next figure is the quantity of items he pack, finally a short description.	computers. We can
6D4	5	Surface mounting 5amp switches. Intended for	ULTRA SONIC
BD5	3	White flush switches. Will replace any standard	detect movement i makes it independe its on off switch, un
BDS	2	30 volt 80 watt brass cased elements. Ideal for	you to switch it on which is very pene
6D14	4	Photo transistor Mullard reference OCP70.	door alarm if requi
BD16	4	Reel to reel tape heads. 2 record, 2 erase.	der and using the i
6D17	1p	r Ultrasonic trandsducers. 1 to transmit the other	arrives without tha
6D20	4	to receive. Different mains micro switches, each average	brand new, guara type). Price is £20 p
BD23	15	1.000,000 operations. et Loud speaker cross-overs up to 40 watt for	110 DECIBEL I
0010		woofer, mid-range and tweeter.	should you have a
BD26	2	Counters, belt driven as in tape decks.	and comes comple Incidentally, this co
BD36	2	Air spaced tuning condensers. 14in spindle with trippners	3½in FLOPPY
BD37	2	Ditto, but solid di-electic,	BLE DENSITY
BD41	6	Standard size rocker switches. 13 amp at 230 volts.	famous Japanese
BD46	1	Time-on switch. Stays on up to 6 hours,	1. Thates
8019	2	depending on control setting. Read relays: 4.8 yolt coil operated	
BD51	1	12 volt relays. Miniature enclosed with 2 change-	
		over contacts. 700ohm coil.	
BD57	5	Miniature switches for battery circuits, dolls houses atc	
BD60	4	Ferrite rods. 4in long, 14in diameter. Suitable	65XE COMPEN
		for aerials, inverter transformers, etc.	der XC12 and its ju delivery.
BD64	10	Assorted control knobs, 14in spindles, push-on and grub screw type	TIME AND TE
BD65	4	Different thermostatic switches. Control mains	hour clock, a Celcin hot alarm and final
		or battery devices.	with 12.7mm digit Requires only a 1.5
BD69	4	Wire wound pots with built-in knob. One each	circuit diagram and
		18ohm, 35ohm, 50ohm and 100ohm.	1/8th HORSEP
BD78	5	Samp stud rectifiers, 50 volt.	spindle 5/16th of an
BUð2	<b>4</b> p	vour own fuse board).	which revs at 3,000
BD84	10	.1mfd 250 volt mains supressor condenser (cut	have any projects i two years, price £6
0.005		out the clicks).	
BD85	2	Sin aluminium fan blades. Will fit Van shaft	This is helium-
BD93	4	11 pin bases for relays, etc.	eye damage co
BD94	5	B7G valve bases.	gives 8kv strikin
BD95	4	B9A valve bases with metal skirt.	case £15. Batter
BD101	1	Delayed-on switch. Can be set at any time up to	HAND-HELD VIDE
		2 hours. 115 volt so supplied with dropper.	pany, this uses a 1
BD104	1	Cased mains unit. 4.5 volt regulated output.	door assembly an
80108	2	iz voit alarms. Round, approximately Zin diameter. Sounds like a car horn	£60, we offer these
BD109	10	Assorted ¼in spindle volume controls.	HIGH RESOLU
BD110	10	Slider type volume controls.	sides. Made for us
80114	1	2 watt amplifier made by Mullard, their reference 1172	Brand new. £16 plu 12 VOLT BRI
8D115	1	Wall mounting thermostat, 24 volt.	square shape (41/
BD126	4	Batten holders, MES, will take torch bulbs, etc.	as the brush type r
BD133	1	Case for micro-radio in zipped pouch.	caravan. £8 each. (
00135	19	speed hook-ups.	a mono data reco
BD137	1	61/2in. 10 watts PM speaker, 4 ohm, fits our	with the Acorn E
BD138	1	cabinet BD11. 9in.×3in. 8 ohm 5 watts per speaker.	back of music and a mains adaptor.
			Our ret 10P44. MUSIC FROM YOU
disc. one	HAS 35mi	<ul> <li>Three cameras, all by famous makers, Kodak, etc. One m and one instamatic. All in first class condition, believed</li> </ul>	octave keyboard,
to be in three for	perfec £10 i	ct working order, but sold as untested. You can have the including VAT, which must be a bargain—if only for the	own music. Price

lenses, flash gear, etc. Our ref 10P58 VERY POWERFUL 12 VOLT MOTORS--- 1/3rd HORSEPOWER. Made to drive the Sinclair C5 electric car but adaptable to power a go-kart, a mower, a rail car, model railway, etc. Brand new. Price £15.00 plus £2.00 postage. Our ref 15P8.

WHITE CEILING SWITCH 5 amp 2 way surface mounting with cord and tassle. Made by the famous Crabtree Company. Price £1 each. Our ref RD528

13A SWITCH SOCKETS Top quality made by Crabtree, fitted in metal box with cutouts so ideal for garage, workshop, cellar, etc. Price £2 each. Our ref 2P37.

MAINS TRANSFORMER Upright mounting Normal mains input, gives 28V at 3.5A so should be ideal for big amplifier, etc. Price only £4. Our ref 4P24, please add £1 postage.

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į.

ORGAN MASTER Is a three octave musical keyboard. It is beauti-fully made, has full size (piano size) 

tully made, has full size loss and size keys, has gold plated contacts and is complete with ribbon cable and edge connector. Can be used with many ssupply information sheet. Brand new, only £15 plus 115P15.

INTRUDER ALARM Small, nicely cased, will n a room up to 10m \ 10m. Ingenious construction int of the mains; cannot be switched off, even with til you know the secret; has delayed action enabling til you know the secret; has delayed action enabling and leave the room; it has an inbuilt pieces sounder trating and high pitched and would frighten away sinternal switching and could be coupled to an out-red. It is the basis of a very efficient burglar alarm, or r instance: you could disconnect the internal soun-internal switches you would know when somebody t person being aware that you know. Similarly, the d to operate other equipment ultra-sonically. It is aved OK explored has battery (PPP) alkaline nteed OK, complete but less battery (PP3 alkaline plus £3 insured delivery. Our ref 20P11.

OBN For use with the ultra sonic intruder detec al positioning to attract the attention of neighbours in truder. This unit has its own mounting bracket te with good length of lead. Price £7. Our ref 7P9 uld also be used as a loud speaker.

80 TRACK Shugart compatible, has 34 way IDC interface with almost any computer. Made by the NEC Company. Price £59.50 plus £3 insured post.



ATARI 65XE COMPUTER At 64K this is most powerful and suit-able for home and business. Complete with PSU, TV lead, owner's manual and six games. Can be yours for only £45 plus £3 insured delivery

**DIUM** Contains: 65XE Computer, its data recor-pystick, with ten games for £62.50 plus £4 insured

MPERATURE LCD MODULE This is a 12 us thermometer, a Fahrenheit thermometer, a too y a too cold alarm. Nice size, approx 50mm×20mm, s which clearly display the time or temperature. / battery and a few switches. Comes complete with explanation. Price £6. Our ref 6P12.

OWER 12 VOLT MOTOR. Made by Smiths, this is approximately 3in, the diameter 3in and the inch diameter. It has a centre flange for fixing or can nds by means of 2 nuts. A very powerful little motor 0 rpm. We have a large quantity of them so if you in mind then you could rely on supplies for at least . Our ref 6P1, discount for quantities of 10 or more.

#### PHILIPS LASER

rinciro CADEM teon and has a power rating of 1.6mW. Com-ng as you do not look directly into the beat when uid result. Brand new, full spec, £30 plus £3 Mains operated power supply for this tube g and 1.25kv at 5mA running. Complete kit with r operated P.S.U. now available at £15. NAAPD

LAMP. Mains operated and will enable you to take D LAMF, Mains Operated and will enable you to take and videos. Made by the famous Ferguson Com-000w halogen lamp in a fan cooled, hand-held and tal housing. Comes complete with optional barn-d camera bar. Obviously intended to retail at over at £30 each plus £3 insured delivery. Our ref 30P3.

JTION MONITOR. 9in black and white, used 06W. Made up in a lacquered frame and has open e with OPD computer but suitable for most others. s £5 post. Our ref 16P1.

JSt Jest: Our ref for 1. JSHLESS FAN, Japanese made. The popular zinx41/zinx13/xin). The electronically run fans not little current but also they do not cause interference motors do. Ideal for cooling computers, etc., or for a Jur ref 8P26

PUTER DATA RECORDER (ref ALFO3) this is der with switchable motor control intended for use lectron or BBC computer but also functions with proputer and can be used for normal record and play speech. Can be battery operated but is supplied with Brand new in manufacturer's wrapping. Price £10.

UR SPECTRUM 128 We offer the Organ Master three complete with leads and the interface which pluos u can then compose, play, record, store, etc., your £19 plus £3 special packing and postage. Order ref.

#### FDD BARGAIN

31/zin made by Chinon of Japan. Single sided, 80 track, Shugart compatible interface, interchangeable with most other 31/zin and 51/zin drives. Completely cased with 4 pin power lead and 34 pin computer lead **f40**. Our ref 40P1.

OUR ALADDIN'S CAVE. You may be a new reader and now What have a browse around at our assortment of 'goodies'. Unfortu-nately, because of staff shortages, we cannot be open on Saturdays yet, so the hours are 9.30am to 5pm, Monday to Friday. We of course still serve callers at 250 but request that you bring a completed order form as 250 is really the mail order depot.

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POPIII AR ITEMS Some of the many items described in our current list

which you will receive if you request it EHT TRANSFORMER 4kv 2mA Ex-unused equipment. £5. Our ref

FOIL CAPACITORS Axial ended .33uf 1,000v. 4 for £1. our ref BD672. Many other sizes in stock, send for May newsletter. 4 CORE TINSEL COPPER LEAD As fitted to telephones, terminating with flat B1 pig. 2 for 11. Our ref BDB39. EHT TRANSFORMER 8kv 3mA. £10. Our ref 10P56.

DOUBLE MICRODRIVES. We are pleased to advise you that the Double Microdrives which we were offering at about this time last year as being suitable for the 'QL' 'OPD' and several other computers are again available, same price as before namely £5. Our ref 5P113.

SOFTWARE FOR REMAKING. Just arrived. Large quantity of mainly games. All are on normal tape spool in cassette holders and should be suitable for wiping out and re-making into games or programmes of your own design. We offer 5 different for £2 or 100 assorted for £20. your own design. We offer 5 different for £2 or 100 assorted for £20. Important note: We cannot say which titles you will get nor accept orders for specified titles or 'so many, all different', etc., so only order if you can take them as they come. Order ref 5 for £2 is 2P224, 100 assorted is 20P10

VERY USEFUL MAGNETS. Flat, about 1in long, 1/2in wide and 1/4in thick. These are polarised on their faces which makes them ideal to operate reed switches in doors and windows or to hold papers or labels, etc., to metal cabinets, or even to keep cupboard doors firmly closed. Very powerful. 6 for £1. Our ref BD274(a).

Closed, Very powerful, 6 for £1, Our ref b02/4(a). ACORN COMPUTER DATA RECORDER REF ALFO3 Made for the Elec-tron or BBC computers but suitable for most others. Complete with mains adaptor, leads and handbook. £10,00. Ref 10P44.

mains adaptor, leads and handbook, £10.00. Ref 10P44. **FREF DOWER**I Can be yours if you use our solar cetis – sturdily made modules with new system bubble magnifiers to concentrate the light and so eliminate the need for actual sunshine – they work just as well in bright light. Voltage input is .45–you join in series to get desired voltage – and in parallel for more amps. Module A gives 100mA, Price E1, Our ref. B0631. Module C gives 400mA, Price 12, Our ref. 2P199. Module D gives 700mA. Price 16. Our ref. 6P3

SOLAR POWERED NI-CAD CHARGER 4 Ni-Cad batteries AA (HP7) ed in eight hours or two in only 4 hours. It is to use unit. Price £6. Our ref. 6P3.

METAL PROJECT BOX Ideal size for battery charger, power supply, etc.; sprayed grey, size 8in x 4¼ in x 4in high, ends are louvred for ven-tilation other sides are flat and undrilled. Price £2. Order ref. 2P191.

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13A PLUGS Pins sleeved for extra safety, parcel of 5 for £2. Order ref.

2P185. **13A ADATERS** Takes 2 13A plugs, packet of 3 for £2. Order ref. 2P187. **2W-0-2BY** Mains transformers 2<sup>1</sup>/<sub>2</sub> amp 1100 wattl loading, tapped primary. 200-245 upright mountings £4. Order ref. 4P24. **BURGLAR ALARM BELL**—6<sup>2</sup> gong OK for outside use if protected from rain. 12V battery operated Price £8. Ref. 8P2. **VERY RELIABLE CAPACITOR** 4.7 $\mu$  400v not electrolytic so not polarised, potted in ali can, size 1<sup>3</sup>/<sub>4</sub>x <sup>3</sup>/<sub>4</sub>x 1<sup>1</sup>/<sub>2</sub>/<sub>1</sub> high. A top grade capacitor made for high class instrument work. Ideal for PCB mount-ing. 2 for £1. Our ref BD667.

CAPACITOR BARGAIN-axial ended, 4700µF at 25V. Jap made, nor-mally 50p each, you get 4 for £1. Our ref. 613.

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SPRING LOADED TEST PRODS—Heavy duty, made by the famous Bulgin company. very good quality. Price 4 for £1. Ref. BD597.

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3-CORE FLEX BARGAIN No. 2—Core size 1.25mm so suitable for long extension leads carrying up to 13 amps, or short leads up to 25A. 10m for £2. Ref. 2P190.

ALPHA-AUMERIC KEYBOARD—This keyboard has 73 keys giving trou-ble free life and no contact bounce. The keys are arranged in two groups, the main area is a QWERTY array and on the right is a 15 key number pad, board size is approx. 13° x 4°—brand new but offered at only a fraction of its cost, namely £3, plus £1 post. Ref. 3P27.

WIRE BARGAIN-500 metres 0.7mm solid copper tinned and p.v.c. covered. Only £3 plus £1 post. Ref. 3P31—that's well under 1p per metre, and this wire is ideal for push on connections.

1/8th HORSEPOWER 12 VOLT MOTOR Made by Smiths, the body length of this is approximately 3in, the diameter 3in and the spindle 5 length of this is approximately 3in, the diameter 3in and the spindle 5) f8th of an inch diameter. It has a centre flange for fixing or can be fixed from the end by means of 2 nuts. A very powerful little motor which revs at 3,000rpm. We have a large quantity of them so if you have any projects in mind then you could rely on supplies for at least two years. Price £6. Our ref 6P1, discount for quantities of 10 or more.

3 VOLT MOTOR Very low current so should be very suitable for working with solar cells. £1 each. Our ref BD681. MINI SPEAKERS to use instead of headphones with your personal stereo-simply plug in to earphone socket. Excellent sound quality, only £4 per pair. Our ref 4P34. SEALED LEAD ACID BATTERIES Japanese made re-chargeable and

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ot mess up your hair do! Come complete in a neat carrying case. STEREO HEADPHONE AMPLIFIER Very sensitive. A magnetic cartridge

STEREO HEADPHONE AMPLIFIER Very sensitive. A magnetic cartridge or tape head will drive it. Has volume control and socket for stereo headphones. 3V battery operated. £1 each. Our ref BD680. FET CAPACITOR MICROPHONE EAGLE CLOU Output equivalent to a high class dynamic microphone while retaining the characteristics of a capacitor microphone. Price £1. Our ref BD646. SUB-MIN TOGGLE SWITCH Body size 8mm×4mm×7mm SBDT with chrome dolly fixing nuts. 4 for £1. Our ref BD649. SUB-MIN PUSH SWITCH DPDT. Single hole fixing by hexagonal nut. 3 for £1. Our ref BD650.

for £1 Our ref BD650

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## **JUNE 1989**

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724	SUPER SOUND ADAPTOR Aug 87	£38.39	387	MAINS CABLE DETECTOR Oct 84	£5.53
718	3 BAND 1.6-300MHz RADIO Aug 87	£26.53	386	DRILL SPEED CONTROLLER Oct 84	£8.68
719	BUCCANEER I.B. METAL DETECTOR inc. coils and		362	VARICAP AM RADIO May 84	£13.15
	case, less handle and hardware July 87	£26.45	337	BIOLOGICAL AMPLIFIER Jan 84	£24.14
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	inc. case July 87	£67.07	242	2-WAY INTERCOM no case July 82	£5.69
722	FERMOSTAT July 87	£12.14	240	EGG TIMER June 82	£6.86
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707	EQUALIZER (IONISER) May 87	£15.53	106	WEIRD SOUND EFFECTS GEN Mar 78	£7.82
700	ACTIVE I/R BURGLAR ALARM Mar 87	£35.65	101	ELECTRONIC DICE Mar 77	£6.26

Everyday Electronics, June 1989

0283 65435





The first two articles in an on-going series of relatively inexpensive projects aimed at those who need easy to build designs. Whilst being well suited to inexperienced readers these designs will also provide plenty of circuits and ideas for everyone.



## PROGRAMMABLE POCKET TIMER

Human beings are notoriously bad at keeping track of time, especially when preoccupied. This can have embarrassing or downright dangerous consequences. Enter the Programmable Pocket Alarm! Small enough to carry in a pocket or handbag, simple to operate, and, carrying a penetrating alarm, the device can accurately time any period from 15 minutes to 3 hours 45 minutes in 15 minute steps.

# POWER SUPPLY SERIES

This short series will introduce a few practical stabilized power supply projects which are reasonably simple to build and have good specifications. It will also delve into the basic theory of p.s.u. design and look at potential problems.



Everyday Electronics, June 1989

# HIGH GRADE **COMPONENT PARCELS**

## COMPONENT PACKS

This month we have a delicious selection of top grade component packs for you. They all contain brand new components of the very highest quality - ideal for experiment, circuit design and development, or education. All the packs are £1 (+ VAT) each, but if you order five packs you can select another pack FREE. Order ten packs and you can have three extra packs FREE.

FOR THE EXPERTS - Just look at those ICs! They are all at the very top of their class, made for peak performance without compromise. The kind of ICs it's a delight to design with.

Of course, there's no point in buying an expensive IC unless you know exactly how to use it, so each comes with its own data sheet, specifications, design ideas and circuits. The nicest thing of all is that any one of them could be yours in a few days time if you order right now!

## **PASSIVE COMPONENTS**

PACK 1 - 200 RESISTORS. Mostly 1/4W carbon film. Lots of E12 values with some E96. PACK 2 - 100 CAPACITORS. Ceramics, metallised film, all types. A fine selection! PACK 3 - 30 ELECTROLYTICS. Values to 500 µF. PACK 4 - 15 LARGE ELECTROLYTICS. Values to  $5,000 \mu F$ PACK 5 - 10 TANTALUM CAPACITORS. Values to  $47\mu$ F. PACK 6 - 20 HIGH VALUE POLYESTER CAPS. Values to  $2\mu 2$ . PACK 7 - 15 DIL RESISTOR NETWORKS. PACK 8 - 20 CARBON AND CERMET TRACK PRESETS

## **OPTO ELECTRONICS & DISPLAYS**

PACK 11 - 10 5mm LEDs: 4 red, 2 yellow, 2 orange, 2 green. PACK 12 – 10 3mm LEDs: 4 red, 2 yellow,

2 orange, 2 green. PACK 13 - 2 CQY89A high power infra-red emitters.

#### HI-FI PRE-AMPLIFIER IC

£2.80! + VAT The HA12017 is a top grade Hi-Fi pre-amplifier, turning in a THD of less than 0.002% over the entire audio bandwidth! The low noise, wide dynamic range and excellent power supply ripple rejection make this IC the first choice for an audio pre-amplifier of formidable specifications. Eac 'C is supplied with its own data sheet giving

performance figures and graphs, the circuit for a top flight pre-amplifier and a PCB foil pattern and component layout. SPECIFICATIONS

THD = 0.002% typ. (f = 20Hz to 20kHz,  $V_{out}$  = 10V RMS, RIAA) Input noise  $V_n$  = 0.185 $\mu$ V typ. (IHF-A network,  $R_g$  = 43R, RIAA)

Supply rejection: SVR+ = 56dB typ.  $(f = 100 \text{Hz}, R_3 = 43 \text{R})$  SVR - = 45 dB typ.  $(f = 100 \text{Hz}, R_3 = 43 \text{R})$ 

#### POWER AMPLIFIER IC

As easy to use as an ordinary op-amp, the L165V's massive  $\pm 3A$ current handling make it the ideal choice for a minimum component Hi-Fi amplifier.

This IC's data sheet includes circuits for a basic amplifier, a motor controller and a power oscillator. A separate sheet gives circuits and construction details for two high quality audio amplifiers, one giving 20W and the other 50W output. All information comes free with the IC. PCBs for the amplifiers are available separately, if required.

#### SPECIFICATIONS

Frequency range: DC to 200kHz Output current: +3A Supply voltage: 12V to 35V Input noise: 2µV (10Hz to 10kHz) ACCESSORIES 20W-Hi-Fi-amplifier PCB £1.20 + VAT 50W Hi-Fi amplifier PCB £1.60 + VAT

PACK 14 - 2 HIGH POWER SENSORS. Matched to emitters in PACK 13. PACK 15 - 2 FND10 0.1" miniature 7-segment CC LED displays PACK 17 - 20 NEON BULBS (use 100k series resistor for mains) PACK 18-2 INFRA-RED COMPONENTS. Emitter and phototransistor. PACK 19 - 3 FLASHING LEDs. A built-in IC makes the LED flash. PACK 21 - 1 SLOTTED INFRA-RED OPTO SWITCH. PACK 23 - 10 RECTANGULAR GREEN LEDs. For bar graph, etc.

## SEMICONDUCTORS

PACK 26 - 3 TAG136D MAINS TRIACS (400V, 4A). PACK 27 - 30 IN4000 SERIES RECTIFIERS. PACK 28 - 30 MIXED SEMICONDUCTORS. Transistors, diodes, SCRs, ICs, FETs, etc. PACK 29 - 20 ASSORTED ICs. CMOS, TTL, linear, memory, all sorts. PACK 30 - 20 TRANSISTORS. High grade general purpose NPN.

#### UHF AMPLIFIER

£12.20! + VAT The OM335 is a high gain wideband amplifier (10MHz to 1.4GHz) for VHF and UHF signals. It can be used as a masthead amplifier for better TV reception, a booster for indoor aerials, a distribution amplifier, and so on. The only external component needed is a decoupling capacitor for the power supply

Each amplifier is supplied with a data sheet giving specifications design hints and performance figures. A separate eaflet, also supplied with the IC, gives a complete design for TV aerial booster, with layout and construction details. A PCB for the amplifier is available separately, if required. SPECIFICATIONS

Frequency range: 10MHz to 1.4GHz Mid-band gain: 26dB at  $V_s = 24V$ ACCESSORIES PCB for TV aerial booster £1.80 + VAT

1 3

#### BAR GRAPH DISPLAY

Screening piece

80p + VAT

For visual impact, there's nothing to beat a bar graph display you can see at a glance exactly what's going on. The LM3915 needs only ten LEDs and a few resistors to make a moving dot or expanding bar display. The logarithmic response means that the graph will automatically be scaled in dBs and will cover a wide

£3.60! + VAT

dynamic range - ideal for audio work. The data we supply with the IC gives circuits for a peak detector, VU meter, vibration meter, light meter, audio power meter, and a dozen more project ideas!

#### SPECIFICATIONS

Range: 30dB in 3dB steps Supply voltage: 3V to 25V Outputs: direct LED drive (no series resistors needed). PACK 31 - 1 CF 585 CALCULATOR IC. With data.

## MISCELLANEOUS

PACK 36 - 4 12V BUZZERS PACK 37 - 3 PANEL NEON LAMPS. PACK 39 - 5 'BEEHIVE' TRIM CAPS. PACK 40 - 3 VDRs. Mains transient suppressors just wire between L and N of plug PACK 42 - 12 PP3 BATTERY CONNECTORS. PACK 43 - 100 MYSTERY PACK. At least 100 top grade components PACK 44 - 1 MINI BIO-FEEDBACK KIT. With PCB, components and instructions. PACK 45 - 1 MINI DREAM MACHINE KIT. With PCB, components and instructions.

## **EXTRA PACKS**

PACK 50 - 12 BC212 TRANSISTORS. General purpose PNP. PACK 51 - 12 BC213 TRANSISTORS. General purpose PNP PACK 52 - 2 PIEZO BUZZERS. Use as microphone, speaker or buzzer.

#### A/D CONVERTER

#### £4.80! + VAT

Built-in clock generator, easy interface to microprocessors outputs suitable for MOS and TTL, differential inputs - the ADC0804's got the lot! As a stand alone converter, it needs only one external resistor and one small cap. What could be easier? The converter comes with its own data sheet, giving full

specifications, design hints and over 25 circuit ideas! Stocks limited on this one I'm afraid, and at this price they'll be gone in no time, so reserve yours now

#### SPECIFICATIONS

Resolution: 8 bits Access time: 135ns Supply voltage: 5V Outputs: MOS and TTL.

COMMUNICATION THROUGH THE MAINS £6.20! + VAT Messages through the mains is the function of the LM1893. Although intended for reliable, long distance data communication, it can just as easily become a powerful mainsintercom – the instructions tell you how. Each IC contains a transmitter which sends an FSK modulated signal along the mains wiring of your house or office. The IC also has a receiver to pick up and decode the signals, so two ICs will give you full two-way communication without any wires or cables!

The instruction leaflet gives detailed design procedures, circuits, and everything you need to know to build a speech or digital communications system.

## SPECIFICATIONS

Transmission rate: up to 4.8kBaud Carrier frequency: selectable 50kHz to 300kHz Power boost: optional x10 power boost with single transistor

UK Orders: Please add 80p postage & packing and 15% VAT to the total (including postage). Europe and Eire: Please add £2.50 carriage and insurance. No VAT.

Outside Europe: Please add £4.50 carriage and insurance. No VAT.

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Quantity prices available on request Write or phone for free Data Pack

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143 Reculver Road, Beltinge, Herne Bay, Kent CT6 6PL Telephone: (0227) 375254 Fax: 0227 365104

## NEW THIS MONTH

SWITCH BARGAINS 50,000 miniature switches by C&K. Top quality sub-min and min toggle, rocker, slide, lever and push switches from 5p each! Over 100 types in Bargain List 46, together with 15,000 thumbwheel switches from 20p and 5,000 DIL switches from 5p. Ask for your FREE copy now!! 2808 Mega Solar Cell — This 300 x300mm unit incorporates glass screen and backing panel, so is véry robust. Wires are attached. Output is 12V 200mA min on a sunny summer SWITCH BARGAINS

robust. Wires are attached. Output is 12V 200mA min on a sunny summer day. Can be series or parallel wired for greater output. £24 ed (difficult to pack-singly) or box of 7 for £99 LCD DISPLAYS Z4115 8 digit 12.7mm high, with hol-der, 14 seg allowing alpha-numeric dis-play. List £15+ £4.50 Z4148 6 digit as above £3.00 Z1637 31/2 digit direct drive, sim to DS588.572 12 7mm digits.

21637 31/2 digit as above 23.00 21637 31/2 digit direct drive, sim to RS588-572. 12.7mm digits. £2; 10/£17.50; 100/£100

#### TEST GEAR

TEST GEAR AG2603 AUDIO GENERATOR/ COUNTER. Combination unit with 6 digit display. Generator has a freq range from 10Hz-1MHz sine or sq wave. The counter operates from 10Hz-150MHz from internal or external source. Excellent value at £175 SG4162 RF GENERATOR/COUNTER. Similar to above, but generator has a range from 100Hz-150MHz £179 SE6100 SIGNAL TRACER £55 FC5250 FREQUENCY COUNTER, 10Hz-150MHz £65

150MHz CM3300 CAPACITANCE METER 0.1pF-£65 99.9mF Full details on all above on request.

#### COMPUTER ART-£19 95

Z811 Cumana touch pad for the BBC B computer. Enables you to draw on the screen using the stylus with the touch sensitive pad. Supplied with 2 stylii, power/connecting leads and demo tape with 4 progs. Originally sold at 579.95. Our price £19.95

## HALF-PRICE KITS Range of 'OK' Kits at half pricel 5 diff. top quality kits containing all parts, inc. PCB, plastic case and comprehensive

instructions Instructions! EK1 Quick Reaction EK2 Electronic Organ EK3 Digital Roulette EK4 Electronic Dice EK5 Morse Code Oscillator £2.90 £3.34 £4 29

£3.98 £1.99

## KEYBOARDS

COMPUTER KEYBOARD £4.00!! Yes, only £4 for this Cherry keyboard-67 full travel keys inc. func-tion keys. Size 340×130mm. Pale/dark brown £4.00

28848 Alphanumeric plus separate numeric keyboard. 104 keys plus 11 chips. 442 x 175mm. £12.00 24116 24 way (8 x 3) membrane keypad. Large (200 x 90mm) area-they were used in a teaching aid. Over-lay template and pinout supplied £3.00

Z8852 Keyboard: Superb brand new keyboard 392×181 with LCD display-ing 1 line of 10 characters & a further line with various symbols. 100 keys, inc separate numeric keypad. Chips on board are 2×74HC05, 80C48. £15.00

Z8863 KEYBOARD-High quality by Microswitch. 69 keys, 6 LED's, 15 vari-ous LS chips+socketed D8048 by Intel. Output via 7 way plug. Size 317×170mm £12.00

Z810 KEYBOARD. Really smart alpha 2810 KEYBUAND, healty sindf applied numeric standard qwerty keybard with separate numeric keypad, from ICL's 'One Per Desk'. Nicely laid out keys with good tactile feel. Not encoded—matrix output from PCB taken to 20 way ribbon cable. Made by Alps. Size 333×106mm. 73 keys £8.95

Back in stock **Z8933** Tatung cased keyboard VT4100. 85 keys inc. sep. numeric keypad. 450×225×65/25. £14.95



#### 1989 CATALOGUE 🛧 100 BIG pages of components equipment+32 Page and Spring Supplement

- Low, low prices
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## ★ STAR BUY ★

GREEN SCREEN HI-RES 12in. MONITOR CHASSIS

MONITOR CHASSIS Brand new and complete except for case, the super high definition (100 lines at centre) makes this monitor ideal for computer applications. Oper-ates from 12V d.c. at 1.1A. Supplied complete with circuit diagram and 2 pots for brilliance/contrast, plus con-necting instructions. Standard input from IBM machines, slight mod (details included) for other computers. Only £24.95+£3 carr.

#### MONITOR INTERFACE KIT

MONITOR INTERFACE KIT Enables our hi-res monitor (above) and most others to be used with virtually any computer, PCB <u>53.00</u> Complete set of on-board components plus regulator and heatsink <u>59.95</u> Suitable transformer for interface and above monitor <u>55.31</u>

#### CURRAH MICROSPEECH

We've bought up remaining stocks of this popular add-on to re-sell at a frac-tion of the original cost

Z4140 New complete set for ZX Spec-trum unboxed. (They were bulk packed) £7.95

Z4142 Speech 64 for the C64. No software needed! New and working, but no case. With full instructions. £6.00

**Z4138** Microslot. 'T' connector allow-ing peripherals to be connected to the Spectrum. New and boxed **£2.00** Also a quantity of returns' available. See Spring Supplement for details



ELECTRONICS

This exciting new series can lead to a worthwhile qualification—and we can supply all the components you need The first six parts: Everything as listed in the booklet given free with EE. Just £12.951

28858 Hitachi Video Battery Charger BC60U for DP60 batts. used in GP7 camera. Extremely high quality unit £17.00

Z8862 10 game video unit-2 hand held controllers with joysticks, beautifully made. Requires 7.5V DC input (suitable PSU £2.95). Composite video and sound outputs (modulator + wiring details for direct connexion to TV £6). £9.95

## FREE!!!

With every Vero Easiwire kit purchased for £15, we're giving away, absolutely FREE, a complete set of components for the SIREN featured in Jan. issue. Limited supplies, so order NOWI

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PARTS FOR PRO.	JECTS SO FAR	
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SIREN (inc. case)		£3.00
TILT ALARM (inc	case)	£5.30
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(h attached out	ching holes)	£1.69
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Z497 Complete	radio chassi	s with
push-button sel	ection for LW/M	NW/FM
and ON/OFF, Fe	errite rod for L	W and
MW selection.	co-ax socket	for FM
aerial. Supplier	d with mains	trans-
former and re	tifier/smoothin	a can.
and wiring detail	le PCB is 333 y	90mm
and wiring detail	13. 1 CD 13 000 A	F7 95
	Uni	y 27.35

All prices include VAT; just add £1.00 P&P; Min Credit Card £5. No CWO min. Official orders from schools welcome-Min invoice charge £10.00. Our shop has enormous stocks of components and is open 9-5.30 Mon-Sat. Come and see usl. HOW TO CONTACT US

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#### ART AUDIO KITS -YOUR VALUE FOR MONEY ROUTE TO ULTIMATE HI-FI

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Yes, you too can afford the very best in real Hi-fi equipment by building a HART kit. With a HART kit you can avoid the hilarious prices and magical claims of the 'oxygen free grain oriented copper' brigade and the flashy exterior and mundane interior of the mass market products. With every HART kit you get the benefit of circuit design by world leaders in their field, men of the calibre of John Linsley Hood for instance who has been in the forefront of audio design for many years. This circuit expertise is harnessed to realise its full potential by HART engineering standards which have been famous in the kit field since 1961. The HART approach is simply to give you the best value in Hi-fi by combining the best circuit concepts with the latest and best components within a unit carefully designed to bring out your hidden skills as an equipment builder.

Units in the HART audio range are carefully designed to form matched stacks of identically sized cases, in many cases even the control pitches are also lined up from unit to unit for a cohesive look to your customised ensemble

Flagship of our range, and the ideal powerhouse for your ultimate system is the new AUDIO DESIGN 80 WATT POWER AMPLIFIER, described in the May issue of 'Electronics Today International'. This complete stereo power amplifier has so many features that you really need our list to browse through them all. Glossing over its technical merits, which its pedigree guarantees anyway, it is a power amp with the extra versatility of a built-in passive input stage giving three switched inputs, volume and balance controls. Tape or CD players may, therefore, be directly connected along with a standard pre-amp

output, indeed your system may not need a preamp at all with the well balanced output of competent CD players. Send for our new FREE Spring '38 List. It has full information on this new amplifier as well as details of improvements to other kits in our

range. Our 300 SERIES amplifiers for instance now feature optional Phono input sockets and double size LCR power supply capacitors.

The 400 SERIES John Linsley Hood Audiophile Tuner range now incorporates the very latest updated stereo decoder circuit which can also be retro-fitted to existing tuners with our 'Tuner Enhancement

Package'. Also listed are many exciting new products for the serious audiophile such as our Gold plated phono and XLR plugs and sockets and ultimate ality connection leads for CD audio or digital signals.

CASSETTE MECHANISM

**VFL600 VERTICAL FRONT LOADING** 

High quality, reasonably priced front loading cassette deck, fitted with good quality reasonably price including usage of the state of the stat functions marked below. Cassette door/carrier has a hydraulically damped 'soft eject' feature. Motor is internally governed and only needs a 12V DC supply with an average current of 80mA. A change-over switch is fitted to energise the motor when required and provide a make contact in the stop position for replay mute. Overall size is 160mm wide including counter, 100mm high and 85mm deep including motor and keys. A robust and thoroughly useful deck for

VFL600 Vertical Front Loading Cassette Deck .....

#### SOLENOID CONTROLLED FRONT LOAD **CASSETTE DECK TN3600**



High quality (0.08%W&F) successor to our very popular SF925F. A very useful high quality cassette mechanism for domestic or industrial use. Offers all standard facilities plus cue and revue modes all under remote, logic or software control. The power and control requirements are very simple with 12V solenoids and 12V Motor with built in speed control. Deck is supplied as standard fitted with a very nice 10kHz R/P head and a 15mH erase head TN3600 Deck with stereo head

INF340 Full manufacturers data HIGH QUALITY REPLACEMENT

**CASSETTE HEADS** 

Do your tapes lack treble? A worn head could be the problem. Fitting one of our replacement heads could restore perform ance to better than new! Standard inductances and mountingsmake fitting easy on nearly



all machines and our TC1 Test Cassette helps you set the azimuth spot on. As we are the actual importers you get prime parts at lower prices, compare our prices with other suppliers and see! All our heads are suitable for use with any Dolby system and are normally available ex. stock. We also stock a wide range of special heads for home construction and industrial users.

HS16 Sendust Alloy Stereo Head, High quality head with excellent frequency response and hyberbolic face for good tape to head contact

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RP373 Downstream Monitor Stereo Combination Head	£44 39
Q551 4-Track Record & Play Permalloy Head for auto-reve	rse car
avers or quadraphonic recording	£16.79
524 Standard Erase Head	£2.59
M166 2/2 AC Erase Head, Standard Mount	£12.60
S9510 2/4 Stereo DC Frase Head	£8.70
Q751E 4/4 AC Erase Head, tracks com patible with HQ551 /e can supply card reader heads for OEMs at very keen prices	£57.06

#### REEL TO REEL HEADS

#### TAPE RECORDER CARE PRODUCTS

HART TC1 TEST CASSETTE Our famous triple purpose test cassette. Sets tape azimuth, VU level and tape speed £5.36 DEM1 Mains Powered Tape Head Demagnetizer, prevents noise on playback due to residual head magnetisation \_\_\_\_\_\_\_\_\_ £4.08 DEM115 Electronic, Cassette Type, demagnetizer £8.61

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#### ELECTRONIC GUARD DOG



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## DISCO LIGHTING KITS



in opto-isolated sound to light input. Only requires a box and control knob to com-£31.50 DL1000K 4-way chaser features bidirectional sequence and dimming 1kW ner channel £19.25 DLZ1000K Uni-directional version of the above. Zero switching to reduce interference ... £10.80 DLA/1 (for DL & DLZ1000K) Optional opto input allowing audio 'beat'/light response 77n OL3000K 3-channel sound to light kit. zero voltage switching, automatic level control and built-in mic. 1kW per channel £15.60

POWER STROBE KIT Produces an intense light pulse at a variable frequency of. 1 to 15Hz. includes high quality PCB, components, connectors, 5Ws strobe tube and assembly instructions. Supply: 240V ac. Size: 80 x50 x45. XK124 STROBOSCOPE KIT.... £13.75

ELECTRONICS

# VOICE RECORD/PLAYBACK KIT

**TS & COMPONEN** 

This simple to construct and even simpler to operate kit will record and playback short messages or tunes. It has many uses – seatbelt or lights reminder in the car, welcome messages to visitors at home or at work, warning messages in factories and public places, in fact anywhere where a spoken message is announced and which needs to be changed from time to time. Also suitable for toys – why not convert your daughter's £8 doll to an £80 talking doll!!

Size		
Message time	. 1-5 secs normal speed,	2-10 secs slow speed
XK129		£22.50

#### TEN EXCITING PROJECTS FOR BEGINNERS

This kit contains a solderless breadboard, components and a booklet with instructions to enable the absolute novice to build ten fascinating projects including a light operated switch, intercom, burglar alarm and electronic lock. Each project includes a circuit diagram, description of operation and an easy to follow layout diagram. A section on component identification and function is included, enabling the beginner to build the circuits with confidence. **XK118 F15 00** 

## MULTIMETER BARGAINS A high accuracy Autoranging meter with

 Display Hold, Memory features.

 AC volts.
 .0-2-200-750 1.2%

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 AC current0-2m-200mA 1.2% 0-10A 2%
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 Size
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 A 15 range Autoranging multimeter with
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 8x5x108mm.
 Complete with wallet.

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processor, PCB, displays and all electronics to produce a digital LEDreadout of weight in Kgs or Sts/Ibs. A PCB link selects the scale – bathroom / two types of kitchen scales. A low cost digital ruler could also be made. ES1. 26.50



MK12 IR Receiver
(inc transformer) £16.30
MK18 Transmitter£7.50
MK9 4-way Keyboard. £2.20
MK10 16-way Keyboard £6.55
601133 Box for Transmitter



ORDERS: 01 · 5678910

Everyday Electronics, June 1989

24 HOURS



## The Magazine for Electronic & Computer Projects VOL. 18 No. 6 **June '89**

## IT GREW IN THE TELLING

The SCS catalogue which is presented free inside this issue was billed last month as being 64 pages. By the time SCS had covered everything they sell the catalogue turned out to be 80 pages! So you have even more value, more reading, more kits and bits than we promised.

#### **CIRCUIT CARD**

The last in our line of Circuit Cards appears this month, these designs have all been very popular. Should you have missed any our back numbers service can still supply them. The previous cards covered Infra Red, Remote Control Transmitter and Receiver (December 1988), Tilt Alarm and Siren (January 1989) and Metal Detector and Radio (May 1989).

We hope to do another free gift based on Easiwire later in the year, it certainly is a popular method of construction especially for the less experienced hobbyists.

#### **FIRST AGAIN**

Once again Everyday Electronics has topped the circulation figures for U.K. monthly electronic hobbyists magazines. In fact our U.K. circulation lead over our nearest rival is now more than 5,000 copies per month; thank you for buying Everyday Electronics. These figures are not guesses. or wishful thinking-the circulation of Everyday Electronics is audited to the strict professional standards administered by the Audit Bureau of Circulations.

To keep us on the straight and narrow we always like to hear your views on our content. Please keep your letters flowing, whilst we cannot undertake to publish everything everyone wants, we can and do try to shape the magazine to meet your general needs. Everyday Electronics has now, with your help, been number one in the U.K. for the last four years and we intend to keep it that way!

Nike Kenner

#### SUBSCRIPTIONS

Annual subscriptions for delivery direct to any address in the UK: £15.70. Overseas: £19.00 (£36 airmail). Cheques or bank drafts (in f sterling only) payable to Everyday Electronics and sent to EE Subscriptions Dept., 6 Church Street, Wimborne, Dorset BH21 1JH.



Subscriptions can only start with the next available issue. For back numbers see below.

#### BACK ISSUES

Certain back issues of EVERYDAY ELEC-TRONICS are available price £1.50 (£2.00 overseas surface mail-£ sterling only please) inclusive of postage and packing per copy. Enquiries with remittance, made payable to Everyday Electronics, should be sent to Post Sales Department, Everyday Elec-tronics, 6 Church Street, Wimborne, Dorset BH21 1JH. In the event of non-availability remittance will be returned. Please allow 28 days for delivery. We have sold out of Sept. Oct. & Dec. 85, April, May, Oct. & Dec. 86, April, May & Nov. 87, Jan., March, April, June & Oct. 88.

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Everyday Electronics, June 1989

## Constructional Project

# BAT DETECTOR

## Explore the fascinating world of bats and unravel some of their mysteries with this low cost instrument

STEADY stream of readers' suggestions for projects is received in the offices of *EE*, and one idea which seems to crop up fairly regularly is for a "bat detector". In other words, a unit that will effectively extend human hearing so that the ultrasonic sounds produced by bats, or any other sounds at similarly high frequencies, can be heard.

R. A. PENFOLD

Apparently units of this type do have practical applications in such diverse fields) of interest as nature study and the detection of gas leaks. The unit described here may well be suitable for serious applications, but it was really only designed for its interest value.

It it surprising how much ultrasonic sound there is in the average house. Some quite loud sounds such as moving furniture around seem to have little or no ultrasonic content. Other sounds seem to contain little audio frequency content, but have quite significant ultrasonic levels. Dropping a pin or some other small object onto a hard surface produces little audio frequency sound, but seems to be readily detectable using this unit.

## **DETECTION METHODS**

Detecting ultrasonic soundwaves is not very difficult technically, and the *Breaking Glass Alarm* in the September 1988 issue of *Everyday Electronics* is a basic detector of this type. Processing the received signal to give a useful audio frequency output signal is a little more difficult.

Some means of reducing the frequency of the received soundwaves is clearly needed, but the method used must not affect the relative amplitudes of the signals or otherwise severely distort the signal. What is needed is a system that not only gives some form of audio output, but one which also gives the sort of sound that you would hear if your ears could detect the ultrasonic input frequencies unaided.

One obvious approach is to use some form of frequency division systems. Dividing input frequencies by (say) five, would effectively extend the 20Hz to 20kHz audio frequency range to a 100Hz to 100kHz range. Bats apparently operate at around 35kHz to 80kHz or so, and this should enable them to be detected.

Unfortunately, the only reasonably simple frequency divider circuits are digital types which only deal in pulse signals. Using one of these would give a very distorted and crude form of output signal.

## HETERODYNE APPROACH

The other obvious approach, and the one adopted in this design, is to utilize the heterodyne principle. This is the same effect that is used in most radio receivers and television sets.



In these applications it is used to convert a signal at a high radio frequency to one at a lower radio frequency. Here the frequencies involved are much lower, but the principle of operation is exactly the same. Heterodyning mixes two signals to generate an output signal that contains new frequencies, plus the input frequencies.

By using a balanced or double balanced mixer it is possible to remove one or both of the input signals from the output. In this case both the input frequencies will be at ultrasonic frequencies, and failing to remove them will not necessarily prevent the unit from functioning properly. However, there is some advantage in removing both input frequencies in that this avoids problems with these signals overloading or otherwise having adverse effects on the output stages of the unit.

## SUM AND DIFFERENCE

The new frequencies produced by the heterodyning technique are the sum and difference frequencies. As a simple example we will assume that a bat is using a frequency of 60kHz for its "RADAR". If we heterodyne this with the 55kHz output from an audio frequency oscillator, the sum frequency is 115kHz (60kHz + 55kHz = 115kHz), and the difference frequency is 5kHz (60kHz-55kHz=5kHz). The sum frequency is of no interest as it is at an even higher frequency that the input signal, but the difference frequency provides the desired effect with the input signal being brought down to an audio frequency output.

In effect, the heterodyning technique reduces all the input frequencies by an amount which is equal to the frequency of the signal with which they are mixed. By using a suitable oscillator frequency it is therefore possible to bring a signal at any frequency down into the audio range.

The relative strengths of the input frequency components is reflected in the relative strengths of the output components that they generate. The output signal consequently reflects the nature of the input signal. A single input tone will give a single output tone—a noise input signal will produce a noise signal from the output.

## FREQUENCY RANGE

This system does have its limitations, and one of these is that a single oscillator frequency will not provide coverage of the full ultrasonic range. This is simply because the ultrasonic range covers a much wider span of frequencies than the audio frequency range. With this method you can only cover 20kHz chunks of the ultrasonic spectrum.

Another problem is that signals at frequencies below the oscillator frequency can also produce an output at audio frequencies. In our previous example a signal at 60kHz was reduced to 5kHz using an oscillator frequency of 55kHz. An input signal at 50kHz would also give a 5kHz audio output signal (55kHz - 50kHz = 5kHz).

Having the oscillator above the signal frequencies inverts the audio output signal. The lower the input frequency, the higher the output frequency.

In practice these problems do not seriously detract from the effectiveness of the unit. For best results the oscillator should be set at something approaching the lowest frequency that gives a good audio output signal. The audio output signal should then give a good idea of the type of input signal that is being received.

## **BASIC SETUP**

The block diagram of Fig. 1 shows the basic setup used in the Bat Detector. A microphone detects the ultrasonic sound-waves and converts them into corresponding electrical signals.

There is a slight problem here in that ordinary microphones are only designed to operate efficiently over the audio frequency range. Most have responses that fall away rapidly above 20kHz, and some do not even operate well to the upper limit of the audio range.

We opted for a 40kHz ultrasonic transducer of the type used in remote control units, burglar alarms, etc. Although these have peak efficiency at a frequency of about 40kHz, they seem to work reasonably well from the upper part of the audio range to a frequency of around 80kHz.

Their frequency response is far from flat, but they provide reasonable sensitivity and quite good results overall. Of course, if you can find an ordinary miocrophone that works well at frequencies well into the ultrasonic range, then it should work perfectly well with this unit.

Whatever type of microphone is used, its output is unlikely to be anything other than very weak. A high gain preamplifier stage is therefore used to boost the input signal.

A gain control permits the gain of the unit to be backed off if a strong input signal causes overloading. This is followed by highpass and lowpass filters.

## **HIGHPASS FILTER**

The highpass filter is important as it severely attenuates any audio frequency output signals from the microphone. This helps to keep any audio frequency breakthrough to the output down to an insignificant level. When using a fairly low oscillator frequency it also avoids having audio frequency input signals reacting with the oscillator to produce further audio frequency signals at the ouptut.

The lowpass filtering might seem to be unnecessary, since the response of the microphone is unlikely to provide any significant output at frequencies beyond about 80kHz. It is not signals from the microphone that are likely to be troublesome, a more likely cause of difficulties is stray pick-up of radio frequency (r.f.) signals. These r.f. signals could cause break-

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through in demodulated form at the output of the unit, or they could react with harmonics of the oscillator signal to produce heterodyne "whistles" on the output signal. The lowpass filtering eliminates both these possibilities.

Even after being boosted by the preamplifier the signal is still quite weak, and a further high gain amplifier stage is used to raise it to a more useful level. The signal is then applied to one input of a balanced mixer. The other input is fed from a variable frequency oscillator. A dual balanced mixer is used, and both input signals are suppressed at its output. This avoids the need for any complex filtering at the output of the mixer, and a simple passive lowpass filter is all that is needed in order to prevent high frequencies on the output (the sum signal) signal from producing any problems. An output amplifier provides a certain amount of additional voltage gain, but its main purpose is to provide buffering so that a crystal earphone or medium impedance headphones can be driven from the output of the unit.

COM	PONENTS				
Resistors		C14	2µ2 radial el <mark>ec.</mark> 63V		
R1, R13 R2, R8, R12, F	1M (2 off) R14 2k2 (4 off)	C15	10μ radial elec. 25V		
R3, R26 R4, R15, R16,	390 (2 off)	C17	2n2 polyester d25V		
R22, R23 R5	4k7 (5 off) 2k7	C18	4µ7 radial elec. 63V		
R6, R7 R9, R10, R11	68k (2 off) 1k (3 off)	Semiconduc	tors		
R17	18k	TR1, TR2,	BC549 npn		
<b>R18, R24</b>	10k (2 off)	<b>TR3, TR4</b>	silicon (4 off)		
<b>B19</b> , <b>B25</b>	100k (2 off)	IC1	LE353 dual on		
B20	5k6	101	amn		
R21	270	102	SI 1640C double		
All 0 25 watt 59	% carbon		balanced mixer		
All 0.25 Wall 5					
Potontiomoto		103	μΑ/41C op. amp		
	See pa	ge 398			
VD1 4K	rotary, log	Manallanaa			
VR2 4/	k rotary, iin.	Ivilscenaneo	MIC1 40kHzultrasonietrans		
0		IVIIC1 40K	Iz ultrasonic trans-		
Capacitors		duce	er (see text)		
C1, C19,	$100\mu$ radial elec.	S1 s.p.s	s.t miniature toggle		
C20, C21	10V (4 off)	B1 9V (e	e.g. 6×HP7 size cells		
C2, C3, C10	10n polyester	in ho	older)		
C12, C13	(5 off)	JK1 3.5n	nm jack socket		
C4, C5,	1n polyester	Printed circ	cuit board, available		
C5, C11	(4 off)	from EE P	CB Service, code		
C7	3n3 polyester	EE647;	case, about		
C8, C16	4n7 polyester (2 off)	125×190mm	×45mm; 8 pin d.i.l.		
C9	330p ceramic	i.c. holder (3	off); battery connec-		
	plate	tor; control k	nob (2 off); connect-		
		ing wire; pins	s; solder, etc.		
	Appr Guid	ox. cost ance only <b>£3</b>	80 excl. batteries.		



## **CIRCUIT DESCRIPTION**

The full circuit diagram for the Bat Detector appears in Fig.2.

The output from Mic.1 is fed to the input of a high gain common emitter amplifier based on transistor TR1. This has its output fed to potentiometer VR1 which is connected as a volume control style variable attenuator.

The output of the amplifier is fed, via the wiper of VR1, to the input of the highpass filter which is a three stage (18dB per octave) active type which has transistor TR2 as the buffer amplifier. The cutoff frequency of this stage is approximately 30kHz.

Transistor TR3 is used as the basis of the lowpass filter, which is again a three stage type. This has a cutoff frequency at about 100kHz. The second high gain amplifier stage is another common emitter amplifier (TR4). The dual balanced mixer, IC2, is a device which is primarily intended for use as an s.s.b. (single side-band) modulator/ demodulator and other communications applications. However, it works equally well in its current role, where the only real difference is that it is operating with relatively low input frequencies.

The SL1640C used in the IC2 position is not a particularly cheap device, but it provides a high level of performance. It also has the advantage of achieving accurate balancing without the need for any setting up. It requires a six volt supply, and this is derived from the main 9V supply via dropper resistor R21 and decoupling capacitor C15.

The local oscillator signal is generated by a standard operational amplifier square/ triangular oscillator circuit. IC1a functions as the Miller integrator while IC1b provides the trigger function. In this case it is the triangular waveform (with its relatively low harmonic content) that is required.

The frequency response limitations of IC1 seem to round the waveform slightly, which gives an even lower harmonic content. There is still a significant harmonic content on the output signal, but is not large enough to prevent the unit from working properly.

Using a high quality sinewave generator as the local oscillator seems to give no noticeable improvement in performance. Potentiometer VR2 is the frequency control, and this gives a frequency coverage from just over 20kHz to a little under 100kHz.

The audio output of IC2 is processed by a two stage passive lowpass filter (R22— C16—R23—C17) which attenuates any high frequency components on the output signal (which will primarily be the sum signal). IC3 is a simple inverting mode amplifier, and this provides a voltage gain of about 20dB (ten times). It also provides a certain amount of current gain so that medium impedance headphones can be driven from the output.

Power is obtained from a 9V battery, and the current consumption is about 10 milliamps or so. For economic operation a fairly high capacity battery is required, such as six HP7 size cells in a plastic battery holder.

#### CONSTRUCTION

The component layout and full size copper foil master pattern for the Bat Detector is shown in Fig.3. This board is available from the *EE PCB Service*, code EE647 (see page 000).

None of the semiconductors are static sensitive devices, but the SL1640C used for



Fig. 2. Complete circuit diagram for the Bat Detector. The battery B1 is made up from six HP7 type cells.

IC2 is sufficiently expensive to warrant a socket anyway. In fact it is recommended that i.c. sockets be used for all three integrated circuits.

Do not overlook the single link wire between transistor TR4 and resistor R25. This can be made from 22 s.w.g. tinned copper wire, or a piece of wire trimmed from a resistor leadout should suffice.

Construction of the board is not particularly difficult, but it does demand that the proper miniature components are used. In particular, the electrolytic capacitors must be radial (vertical mounting) components, and the polyester capacitors must be printed circuit mounting types having a lead spacing of 7.5 millimetres. Other types could be very difficult to fit into the layout. At this stage only single-sided pins are fitted to the board at the positions where connections to off-board components will eventually be made.

## CASE

There are a number of plastic boxes that are suitable as the housing for this project. The prototype model used a low-profile case having removable front and rear panels. The headphone socket and controls being mounted on the front panel, with the microphone fitted on the rear panel.

A 3.5 millimetre jack socket was used for JK1, but this can be changed for any type of socket that will match the plug on the particular earpiece or headphones you will be using with the unit. For medium impedance headphones a 3.5 millimetre stereo jack socket will usually be needed.

Mounting ultrasonic transducers can be difficult as there is not usually any built-in mounting of any description. It is usually a matter of drilling two small holes to accommodate their terminals, and then gluing them in position on the front surface of the panel. A good quality gap-filling adhesive is required, and an epoxy resin type is ideal.

The printed circuit board is mounted on the base panel of the case using 6BA mounting bolts and spacers, or plastic stand-offs. Mount it well towards the front of the case so that there is sufficient space for the battery at the rear of the unit.

The board is wired to the off-board components using multi-strand p.v.c. insulated connecting wire. Details of this wiring is shown in Fig.3.

With many ultrasonic transducers one terminal connects to the component's case, and with a component of this type it is this terminal that should connect to the negative supply rail. Connection to the battery holder is via an ordinary PP3 style battery clip.

## IN USE

Either a crystal earphone or medium impedance headphones (the type sold as replacements for personal stereo units) can be driven from the output of the Bat Detector. If headphones are used they should be connected in series (ignore the chassis terminal of the socket and make the connections to the other two tags).

With the unit switched on and the "volume" control VR1 well advanced there should be a certain amount of background "hiss", but ordinary noises should not be picked up and reproduced through the headphones. Obviously some ultrasonic sounds are needed in order to test the unit, and bats are not the only source of these.







Completed unit showing wiring to ultrasonic "sensor" (Mic 1). Leave plenty of room for the batteries.

An ultrasonic remote control transmitter will provide a test signal, but there are much more simple methods that will suffice. Simply rubbing your fingers together about 300 millimetres in front of the transducer should produce a noise sound from the headphones.

Adjustment of the "frequency" control VR2 should have some effect on the pitch of this noise. Dropping a pin, small needle, or very short pieces of wire onto a table-top should also produce a signal that can be readily detected by the unit.

With suitable adjustment of VR2 the sound from the headphones may well sound very similar to the audio frequency sound of the pin (or whatever) dropping. A little experimentation should soon find some other sources of ultrasonic sound.

When using the detector bear in mind that ultrasonic sound waves tend to highly directional. The unit will probably be insensitive to sounds unless the transducer is aimed at the source reasonably accurately

Also bear in mind that the unit covers



Layout of front panel controls.

less than the full ultrasonic range at any one setting of VR2. Some adjustment of this control may be needed before an ultrasonic sound will give an audible output from the unit.

Avoid using the unit very near television sets. computer monitors, or other apparatus that generates electrical signals in the ultrasonic range. If the unit is used very close to equipment of this type it is almost inevitable that it will pick up a certain amount of interference from them.

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MAY 1989

meter, two new PSU designs and a 3.5MHz converter are among the innovative new kits this issue, while our construction project - a 2 Watt stereo amplifier - is bound to prove an absorbing activity for dedicated constructors. In the test equipment section there's a whole new range of multimeters, a bench DVM and a triple output PSU.

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## FEATURE PROJECT: 2W STEREO AMP

Our construction project this issue is for a straightforward but very effective 2 Watt stereo amplifier. Based on the LM1877, it is the perfect amplifier for a 'Walkman' cassette deck and equally suitable for AM/FM radios or mixer desks. Featuring 2W per channel and 75dB channel separation, it operates from a 10-26 volt supply, making it ideal for in-car applications. The catalogue includes full details of this economical kit.



## Constructional Project

# **CAPACITOR TESTER**

## T. R. de VAUX-BALBIRNIE

Check-out those spare capacitors with this low-cost instrument

APICTORS are very common components and appear in most electronic circuits. The value is usually marked on the body with either a type of colour code or expressed in alphanumeric form, for example, 223K—the value of this capacitor will be explained at the end of the article. The markings are not always clear, however, and to make matters worse different manufacturers appear to use their own variations in expressing the value.

Since a multitester cannot be used to check capacitors, there is a need for an amateur instrument which can perform this function. Such meters that are available are expensive—typically around £40. The Capacitor Tester described here is less accurate than these but is more than adequate for amateur electronics work and may be constructed for a fraction of the cost. The device operates from a small internal battery and, in occasional use, the life of this will be very long. The standby current requirement is 15mA approximately.

## **OPERATION**

In use, the capacitor under test is connected to a pair of terminals (TB1 and TB2) on the side of the instrument. The unit is switched on and the range selected by means of a rotary switch. One of a row of green l.e.d.'s lights to indicate the chosen range.

A "push-to-test" button is now operated and a red l.e.d. flashes at the rate of about three per second then goes off. This flash rate may be adjusted, within limits, to suit the user.

The number of flashes gives the value of the capacitor taking account of the range.



Thus, with the range switch set to "10n", five flashes will indicate a value of 50nF. The ranges provided by the prototype unit are: 10nF; 100nF; 1 $\mu$ F; 10 $\mu$ F; 10 $\mu$ F; 100 $\mu$ F; and 1000 $\mu$ F. When the value of a capacitor is completely unknown, the range is quickly found by trial and error.

## **CIRCUIT DESCRIPTION**

The complete circuit diagram for the Capacitor Tester is shown in Fig. 1. IC1 is a CMOS operational amplifier and, as such, has an exceptionally high input resistance—one million megohms approximately. The importance of this point will be explained later. The capacitor under test (C1) is connected to the input terminals TB1 and TB2. The purpose of capacitor C2 will also be explained later.

Assume that switch S2 (TEST) is in its relaxed (unpressed) state and that switch S3 (ON-OFF) is on. If capacitor C1 is charged for any reason, for example from a previous test, it will rapidly discharge through the normally-closed (n.c.) contacts of switch S2 and series resistor, R8. Resistor R8 is included to prevent large discharge currents which could damage S2 contacts especially where large-value capacitors were involved.

If switch S2 is now pressed (test position) capacitor C1 charges from the supply through one of the range resistors R2 to R7, as determined by the setting of rotary switch S1b (RANGÉ), and S2 normally-open (n.o.) contacts. The voltage across capacitor C1 therefore rises from zero towards the battery voltage. IC1 inverting input pin two, receives this voltage. Meanwhile, IC1 non-inverting input, pin three, receives a fixed voltage whose value is determined by the setting of preset potentiometer VR1 connected across the supply. While the voltage applied to the inverting input remains lower than applied to the non-inverting one, the op-amp will be on with its output, pin six, high (positive supply voltage). This will be the case when \$2 is first pressed.

## ASTABLE

As the voltage across capacitor C1, hence at IC1 pin two rises, a time will come when the voltage at the non-inverting input is exceeded and IC1 switches off with pin six going low (negative supply voltage). The high or low voltage state of IC1 pin six is applied direct to IC2 reset input, pin four. IC2 and associated components are connected as an astable multivibrator and,



Fig. 1. Circuit diagram of the Capacitance Tester.

while pin four is high this delivers squarewave pulses at its output (pin 3).

The rate at which pulses are produced is determined by R9, VR2 and C3. With the values specified this will be approximately three per second and adjustable within limits using preset VR2. Thus the l.e.d. connected to IC2 output, pin three, via current-limiting resistor, R10, will flash at this rate. When pin four is low, IC2 is inhibited and produces no pulses.

At the start of the test, l.e.d. D7 will flash but after a time dependent on the values of C1, R2 to R7 and VR1 the flashes will stop. With presets VR1 and VR2 correctly adjusted at the end of construction and with the values of R2 to R7 being accurately known (since they are close-tolerance components) the number of flashes is determined by C1 alone.

The capacitor C2 is a close-tolerance type which provides an accurately known reference value. This is connected to the terminals to enable accurate setting of potentiometer VR1 at the end of construction.

With switch S2 in its relaxed state, IC1 pin two is kept high through R2—R7 so the op.-amp will be off. This inhibits IC2 and D7 does not flash. The instant S2 is pressed, IC1 pin two goes low and flashes are delivered in the manner already described.

## **RANGE INDICATION**

Switch S1 is a two-pole six-position -rotary switch. S1b selects the range resistor as mentioned earlier. S1a operates one of six green l.e.d's, D1 to D6 confirming the selected range—these could be omitted and the range marked around S1 control knob if desired. However, in such a case it would be wise to use an l.e.d. indicator to remind the user that the unit was switched on.

For the small cost involved, the green l.e.d.'s seemed worthwhile and were included in the prototype unit. Note that these share a common current-limiting resistor, R1. This is possible since only one l.e.d. is illuminated at a time. For greatest accuracy, resistors R2 to R7 should be of the close-tolerance (one or two per cent) variety specified in the components list. Note that resistor R2 (100M) has an unusually high value and, as such, may not be readily available. Some highvalue components are sold as "high voltage" resistors having a tolerance of five per cent—it would be acceptable to use these.

However, in the prototype unit, resistor R2 was constructed by connecting 10 off 10M resistors in series (see Fig. 3). Accuracy is then limited chiefly by the fact that the value can only be expressed to the nearest flash below the voltage needed for IC2 to switch off. Accuracy is therefore less with small numbers of flashes. On the other hand, it improves with large numbers of pulses but counting them becomes tedious!

Accuracy is maintained even with an ageing battery since IC1 is used in *comparator* mode. The relative voltage levels at pins two and three do not depend on the battery voltage so will always occur at the same time. Eventually, however, the battery will reach such a poor state of charge that it will fail to operate the l.e.d's effectively and it will be obvious when battery replacement is due.

The high input resistance of IC1 is important since, otherwise, appreciable current would flow into the i.c. with less current available to charge the capacitor "under test". This would lead to considerable inaccuracy especially with small value capacitors.

## CONSTRUCTION

Construction is based on a main circuit panel made from 0.1in. matrix stripboard size 30 holes  $\times$ 9 strips. The component layout and underside details are shown in Fig. 2. Cut this to size, drill the two fixing holes and make all track breaks and inter-strip links as indicated.

Solder all on-board components into position but do not insert the i.c's in their holders until the end of construction. Note that C2 (calibration capacitor) is not soldered into the circuit—it is not used until the end of construction.

Resistors R1, R10 R2, R3 R4 P5	See page 398 330 (2 off) 10M (11 off—see text) 1M
R6 R7 R8 R9 All 0.25\ R1, R8 an ± 5%	10k 1k 47 2k2 W or 0.4W ± 1%, except d R10 may be 0.25W
Potention VR1 VR2	neters 1M sub-min. vertical preset 4M7 sub-min. horizontal preset
Capacitor C2, C3	<b>s</b> 100nF polyester, 5%
Semicond D1 to D6	luctors 5mm green I.e.d's (6 off)
D7 IC1	5mm red I.e.d. ICL7611 CMOS op-amp.
IC2	ICM7555 CMOS 555 timer
Miscellan S1	eous 2-pole 6-way rotary switch
S2	Push-button switch with single-pole change-over contacts
S3 Stripboa	Miniature s.p.s.t. toggle switch rd. 0.1in matrix size 30
holes×9 st (2 off); B1, nector; TB posts (blac size 115 internal; standed co sive fixing plastic feet	rips; 8-pin d.i.l. sockets PP3 battery and con- 1, TB2 4mm terminal k and red); plastic box, mm×95mm×43.5mm single strand wire; onnecting wire; adhe- g pad; self-adhesive (4 off); solder etc.

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Guidance only

14

After a careful check for errors—particularly for accidental "bridging" of adjacent copper tracks, solder 15cm pieces of light-duty stranded connecting wire to strips A, C, D, G, H and I along the lefthand side of the circuit panel. Use of "rainbow" ribbon cable here will keep the wiring neat and prevent errors. Set VR1 and VR2 sliding contacts to approximately midtrack position.

Prepare "resistor" R2 (100m) by connecting 10 off 10M resistors in series (see Fig. 3) unless a single component of this value is available. Solder R2 to R7 to rotary switch S1 contacts as indicated in Fig. 3.

Drill holes in the lid of the case for the switches and i.e.d's. Drill two holes in one of the case sides, for the terminals TB1 and TB2, and in the base for the circuit board. Attach all remaining components and com-



Fig. 3. Control interwiring.



plete the internal wiring paying attention to the polarities of all l.e.d's. The l.e.d's should be a tight push fit in the holes and secured, if necessary, with a dab of quicksetting epoxy resin adhesive.

Again, it is a good idea to use ribbon cable between switch S1 connections and the green l.e.d's. Note the link wire between S1a and S1b moving contacts and the common (k) connection at D1 to D6.

When wiring the terminals, note that TB1 is coloured RED and connected to strip D on the circuit board. TB2 is BLACK and connected to switch S2 moving contact.

Remove the i.c's from their special protective packing and, without touching the pins, insert them into their sockets with the correct orientation. Care must be taken in handling the i.c's since they are CMOS devices and can be damaged by static charge which may exist on the body.

Attach the circuit board to the base of the box using the holes drilled for the purpose, small fixings and short stand-off insulators. Connect the battery and secure it to the base of the unit using an adhesive fixing pad. Fit a control knob to switch S1 and self-adhesive plastic feet to the base of the case to prevent scratching of the work surface by the protruding bolt heads.

## TESTING AND CALIBRATION

For the initial test, bridge the terminals TB1 and TB2 with a short piece of connecting wire. Check operation of the green (RANGE) l.e.d's, D1 to D6, by switching on S3 and rotating switch S1 through all its positions. Each l.e.d. should light in the correct sequence with D7 remaining off.

Press S2 and, keeping it pressed, check that D7 flashes. Adjust preset VR2 to obtain three flashes per second (clockwise adjustment increases the number). It is possible to choose a slightly different rate to suit the user. Note that if any l.e.d. fails to light it is probably because it has been connected the wrong way round.

Now transfer attention to adjustment of the preset VR1. Set the range switch S1 to "10n". Remove the wire bridging the terminals (TB1 and TB2) and connect the "calibration capacitor" C2 in its place. Press S2 and, keeping it pressed, adjust VR1 until exactly 10 flashes are given clockwise adjustment decreases the number.

The procedure should be repeated until a consistent result is obtained. Note that there may be some slight eccentricity in the last flash—this is of no consequence. The instrument should now give accurate results for any capacitor connected to the "test" terminals.

An occasional calibration check may be made but the prototype unit was found to maintain its accuracy over a long period of time. It would be wise to tape the calibration capacitor inside the case so that it cannot become lost.

It may be necessary to make two short test leads each with a small crocodile clip at one end. These will be used to make connections to very small capacitors and those with rigid end wires.

It now only remains to label the switches and l.e.d's and to put the unit into service. Note that the terminals are polarised—red for positive, black for negative and it is important to observe this when measuring the value of an *electrolytic* capacitor. The body of such a capacitor is clearly marked with the polarity—the end connected to the metal body being the negative. Note that its voltage rating should not be *less* than 9V.

## CAPACITOR VALUE

The value of the capacitor mentioned at the beginning of the article is 22,000 pF (22nF or  $0.022\mu$ F) with a tolerance of ±10 per cent. This is arrived at in the following way.

The first two digits give the first two numbers of the value—in this case, 22. The third digit gives the number of zeros to express the value in picofarads. Suffix letter K means that the tolerance of this capacitor is  $\pm 10$  per cent.



#### **Air Entertainment**

If you are one of the lucky few who fly first class because your air tickets are paid for by someone else's tax deductions, you will notice some interesting changes. The rest of us must content ourselves with an interest in the technology.

First class passengers on four of British Airways Boeing 747s will spend the next six months helping BA choose between two rival inflight entertainment technologies. Both give passengers the opportunity to choose what films they watch, and when.

One of the systems on trial takes advantage of a failed venture by Kodak. Although this lowers the capital cost, airlines will still need to spend several thousand pounds to equip each seat. In the end passengers will pay for their freedom to choose in higher fares.

Currently airlines show films either on a large projection screen or individual liquid crystal displays (l.e.d.) on the seat backs. Passengers all have to watch the same film and at the same time, chosen by the airline. The two new systems give passengers the chance to choose their own films and watch them whenever they like.

Each seat has its own l.c.d. screen and video tape cassette player built into the arm rest. After take-off, the cabin crew offer passengers a choice of 50 films on video cassette. They then plug in, switch on and watch when they want. The 8mm video format is used instead of VHS because it offers at least two hours playing time from a video cassette no larger than an audio cassette.

Both systems rely on technology from Japan. One, designed by Avicom, a subsidiary of the Lockheed Corporation, uses an 11cm I.c.d. made by NEC and a video player from Sanyo.

The other, called Skyview, designed by British company Fieldtech and uses a 10cm l.c.d. made by Toshiba. Fieldtech hit on the clever idea of buying its 8mm video players from Kodak. Four years ago Kodak spent heavily on trying to break into the video market, by badging 8mm equipment made under contract by Matsushita of Japan. The venture flopped and Kodak was left with unsold stock, some of which is now used for Skyview. Fieldtech says it will ask Matsushita to start production again if it wins the BA contract. So the price is likely to be higher than for the unsold stock.

Both companies are cagey about price, because they are in competitive tender. But Skyview admits that even with bulk orders it will still need to charge the airlines around £2,500 per seat. So passengers will have to pay dearly for the privilege of making their own choice of film.

Both systems use equipment of Japanese/US NTSC TV format. So any passengers tempted to steal tapes will find they do not play on European PAL TV equipment.

#### Skyphone

Meanwhile, after three years of tests, passengers on some flights between London and New York can now make a telephone call to their home or office. The service, called Skyphone, is a joint venture between Racal, British Airways and British Telecom.

Racal developed the airborne credit card phones and the aerials which bounce signals off an *Inmarsat* satellite to BT's Goonhilly earth station in Cornwall. BT's research laboratory at Martlesham developed the equipment which converts analogue speech into digital code which streams slow enough for the small airborne aerials to handle.

Despite publicity for commercial launch of the service, British Airways is playing very safe. So far BA has equipped only one Boeing 747 aircraft with four phones in First and Club class cabins. The hardware, worth nearly half a million dollars, is on Joan from Racal. BA earns nearly \$3 a minute from every call made.

Currently pilots communicate with control centres on the ground by conventional h.f. and v.h.f. radio links which travel direct along line of sight from ground to air. Existing airline phone systems, as used in the US, work in a similar way and have limited range. Speech quality is often very poor.

#### **Technicalities**

There is no room on an aircraft for the large dish aerials normally needed to communicate with a low power satellite. Skyphone gets round the problem by using two different sets of frequencies for different legs of the journey and converting speech into low speed digital code.

## Revolutionary?

The plane transmits and receives signals to and from the satellite in the L-band, at around 1.5GHz, while Goonhilly transmits and receives signals to and from the same satellite in the C band at 4GHz and 6GHz. Although C band needs high power and a directional aerial dish many metres wide, L band can be made to work with a small blade aerial on top of the fuselage which is omnidirectional. The trick is to convert the speech into digital code which runs very slowly and thus occupies only a narrow frequency bandwidth with low risk of errors.

Skyphone speech is converted into a data stream running at only 9.6 kilobit/ second. By comparision, conventional terrestrial digital telephone systems rely on a data rate of 64 kbit/second.

The Skyphone data stream is compressed by analysing the speech before coding, ignoring gaps when there is no energy content, and re-structuring the speech at the other end of the link. Efficiency is improved by dividing the speech into several separate frequency bands and handling each band separately.

Initially the system will bounce signals only off the *Inmarsat* satellite over the Atlantic Ocean, but later satellites over the Indian and Pacific Oceans will be used to give worldwide coverage.

So far calls must be manually patched into the telephone network by an operator at Goonhilly. International trunk dialling is promised for later this year with fax transmission next year.

BA plans to fit four more phones in a second 747 in March '90. Capital cost, borne by Racal, is around \$500,000 per plane. BT and Inmarsat are charging BA a flat rate of \$6.70, from anywhere to anywhere. BA hikes this to \$9.50 from anywhere to anywhere. The cost to BA, other than fitting, is \$4,000 a year in fuel to carry the extra weight and compensate for aerodynamic drag from the aerial.

Passengers who regard a long hau flight as a way of escaping the 'phone will be relieved to hear that the system does not allow incoming calls. Yet!

Sir Clive Sinclair has always had a talent for creating publicity. Every product launch is preceded by tantalising information leaks, calculated to stir up interest. As often as not the product does not live up to the interest. The C5 electric "car", for instance, was a joke—a pedal trike with ordinary car battery back-up. But the lap top computer was a far more serious product, despite its odd keyboard. More recently we have been getting leaks about a "revolutionary" new satellite aerial for receiving programmes like Sky from Astra. It was finally unveiled at the third annual cable and satellite show which ran for four days at Olympia in March.

Sir Clive, of course, cannot use his own name-he sold it to Amstrad-so the new Sinclair flat aerial for Astra is called the Cambridge, after Sir Clive's computer company Cambridge Computers. The call it "radically different" as well as "revolutionary".

It turns out to be a big white plastic box that clamps to the wall or roof. Significantly all the models on display were sealed and opaque. What mysteries lie inside? Nothing more than a conventional 60cm dish and front-mounted LNB, all packaged in a flat white plastic box, inevitably larger than a conventional dish.

The only novelty is that the curve of the dish is made to focus incoming signals on a point quite close to the dish surface, near the bottom edge. So the microwave feed horn and LNB can be mounted low and close-otherwise the box would be enormous. Other than that, it's just packaging.

According to Sir Clive Sinclair it is "much more elegant" and weather-proof. Some will argue that it is simply more obtrusive than a bare dish, and question whether there is any real weather-proofing problem on existing dishes anyway. "Museum piece – master piece" is the Sinclair slogan which adorns a picture of conventional and covered dish.

There is talk of a smaller, flatter version. Just as there was talk of a more efficient C5!

# City and Guilds Certificate Course Introducing DIGITAL ELECTRONICS

## **Part 9 Transformers, Induction and Inductance**

## By Michael J. Cockcroft Training Manager, Peterborough ITeC

where a focus, this month, on the single objective in section 4.4 of the City and Guilds log book:

#### 4.4 Transformers

4.4.1 State, in very simple terms, the action of a transformer.

#### **Transformers**

A variety of transformers are shown in Fig. 9.1. A transformer is a device for converting a.c. voltage (from the mains, for example) into a higher or lower value. The mains transformer inside a bench power supply, for example, steps down the 240V mains to five or six volts for supplying low voltage electronic circuits. A transformer, like this, which reduces the amount of voltage in a circuit is called a stepdown transformer. One that is used to increase the voltage in a circuit is called a step-up transformer; an example of which is the high voltage transformer that produces several thousand volts for driving the tube inside a television set.

Transformers work on the principle of induction and so, before ex-



Fig. 9.1. Various transformers.

plaining transformer action, we need to expand on our previous treatment, in Part 6, of the principles of magnetism and electromagnetic induction.

## Magnetism

In ancient times the Greeks discovered that certain specimens of stone found in the ground (called natural magnets or lodestones) attracted small particles of iron ore. The Chinese, in later years, learned that the same stone when freely suspended always came to rest pointing in a North-South direction.

The end of the magnet that, when freely suspended (as shown in Fig. 9.2), points to the earth's north magnetic pole is termed the "northseeking pole" or the "north pole" of the magnet; the opposite end is the "south-seeking pole" or the "south pole" of the magnet.

Natural magnets (lodestones) are magneticlly weak but their properties can be imparted artificially to iron and steel to produce much stronger permanent magnets and electromagnets. You will recall from Part 6 that permanent magnets retain their magnetic properties for a long period of time under normal conditions, and electromagnets lose their magnetic properties once the magnetising influence (the current) is removed.

## The magnetic field

If, as shown in Fig. 9.3, the north pole of one suspended magnet is



Fig. 9.2. A suspended magnet seeks the earth's magnetic poles.



Fig. 9.3. Like poles repel, unlike poles attract.



Fig. 9.4. Lines of magnetic field.

brought near the north pole of another suspended magnet, the two poles repel each other; if, on the other hand, the opposite poles of two suspended magnets are brought near to each other, the two poles are attracted:

Like poles repel and unlike poles attract.



Fig. 9.5. Using iron filings to "see" the lines of force.

The induced energy in the space around a magnet is called the magnetic field. Magnetic fields are shown in diagrams as lines of force running parallel to each other out of one pole, through the magnet, and into the other pole; Fig. 9.4 illustrates this idea using a bar magnet and a horseshoe magnet as examples.

These lines are invisible but they can easily be detected. Magnetic lines of force can be seen by performing a simple experiment using a permanent magnet, a sheet of glass or acrylic (Perspex), and some iron filings. Lay the transparent sheet over the magnet, as shown in Fig. 9.5, and sprinkle a thin layer of iron filings onto its surface. You may need to raise the sheet a little above the magnet (or just tap it a few times) but the iron filings should form a definite pattern displaying the lines of force, as shown in the figure.

The direction of the magnetic field is shown by the arrows in the previous figure (Fig. 9.4). This is the direction that a compass needle would point if placed in the field: from north to south in the space outside the magnet and in the opposite direction (from south to north) inside the magnet.

## Magnetic Flux Density

The total number of lines of force per unit area surrounding the magnet determines the strength, or flux density, of the magnet. The practical unit of magnetic flux density is the *tesla* which corresponds to 10<sup>4</sup> lines per square centimeter.

## The Magnetic Circuit

The closed loop formed by the lines of flux is referred to as a magnetic circuit. It is interesting to compare electric and magnetic circuits in order to illustrate the similarities. We know from previous work that electromotive force causes current to flow in an electric circuit, and resistance acts to limit the flow of current. In a magnetic circuit the energy that creates the flux lines is **magnetomotive force** and the opposition that some materials offer to lines of magnetic flux is called **reluctance**.

Table 9.1 shows that flux lines in a magnetic circuit is equivalent to current in an electric curcuit, magnetomotive force (m.m.f.) is equivalent to electromotive force (e.m.f.), reluctance is equivalent to resistance, and magnetic materials (e.g. iron and steel) are equivalent to conductors of electricity.

A magnetic circuit is given in Fig. 9.6. The lines of flux are created by an electromagnet (an iron core placed inside a solenoid connected across a voltage source-see Part

How electric and magnetic circuits compare					
Electric circuit Magnetic circui					
e.m.f.	m.m.f.				
resistance	reluctance				
current	lines of flux				
conductors	iron and steel				

TABLE 9.1



Fig. 9.6. A magnetic circuit.



Fig. 9.7. Comparison of a magnetic circuit (a) and an electric circuit (b).

6.) and limited by the reluctance of the air in the gap between the two ends of the iron core. The magnetic circuit is therefore partly in the iron and partly in the air between the two ends of the iron.

A simple magnetic circuit and an expression for evaluating the amount of flux within it is shown in Fig. 9.7a; for comparision, Fig. 9.7b shows an electric circuit with an equivalent Ohm's law expression for evaluating the current.

## Electromegnetic Induction

Passing a current through a straight conductor creates a magnetic field around it, as shown in Fig. 9.8. The lines of flux are at right angles to the conductor and spaced uniformly a fixed distance away from each other. The magnetic field can be made stronger by coiling the conductor to form a solenoid so that the lines of flux are physically brought closer together and concentrated (this was illus-



Fig. 9.8. Passing current through a conductor creates a magnetic field around it.



Fig. 9.9. Inducing an e.m.f.



Fig. 9.10. The lines of flux must cut across the conductor to induce an e.m.f.

trated in Fig. 6.17 of the March issue-we will not reiterate here).

When a bar magnet is moved back and forth in a solenoid, as depicted in Fig. 9.9, an e.m.f. is inducted by the magnet; we call this **electromagnetic induction**. Experimentation with electromagnetic induction, during the last century, by an English physicist called Michael Faraday revealed the following points:

- (a) An e.m.f. is induced in the conductor only when there is relative motion between the magnet and the coil.
- (b) The greater relative motion, the greater is the magnitude of the induced e.m.f.
- (c) The stronger the magnetic field around the bar magnet, the greater will be the induced e.m.f. for the same relative motion between the magnet and the coil.
- (d) The conductor must cut across (the conductor or the field or both can be in motion) lines from flux before an e.m.f. is induced in the conductor. If the lines of flux are parallel with the turns in the coil, as shown in Fig. 9.10, the conductor is not "cutting across" the flux lines.



Fig. 9.11. The induced e.m.f. tends to oppose the movement generating it.

So, from the foregoing, we know that if a length of conductor is passed through a magnetic field, as shown in Fig. 9.11, an e.m.f. will be induced in the conductor (notice that the conductor is at right angles to the lines of flux and, therefore, cutting across them). The important point to grasp now about this action is that the lines of flux generated by the induced e.m.f. will tend to oppose the motion producing it; for example, if the conductor is moving from left to right, as in Fig. 9.11, the e.m.f. is directed towards the right in an attempt to move the conductor to the left. Since induced e.m.f. opposes its very cause, induced e.m.f. is often called back e.m.f.

## Inductance

An e.m.f. is induced in any conductor if the current flowing through it changes in any way and, at the same time, this induced e.m.f. will oppose the change in the value of current. Any circuit with varying (increasing or decreasing) current therefore possesses this property and is called **self-inductance** (symbol L).

#### Inductors

All conductors have a value of inductance; conductors formed into coils to increase the inductance to specific values are called **inductors**. Inductors are not really offthe-shelf components like most electronic components, they are usually formed by coil winding companies to order. A few common values are available from compo-



Fig. 9.12. A few types of inductor.

nent suppliers and often look like those in Fig. 9.12. The inductor circuit symbol is also shown in the digram.

The unit of inductance is the henry (symbol H). Typical inductors have values ranging from a few micro-henrys to many henrys. The unductance value of inductors determines the amount of back e.m.f. induced for the change of current with respect to time: Self-inductance=



An inductor (or circuit) has an inductance of one henry if a current in it, changing at the rate of one ampere per second, induces an e.m.f. of one volt across it.

### Inductance of Inductors

A number of factors influence the inductance value of an inductor. The inductance of a coil is determined by:

- (a) The number of turns in the coil.
- (b) The material around which the coil is formed (the core).
- (c) The diameter of the coil.

The inductance of a coil is proportional to the square of the number of turns in the coil: twice the turns gives four times the inductance, three times the turns gives nine times the inductance... and so on (see Fig. 9.13). The magnetic per-







Fig. 9.15. Inductors in parallel.

meability (the amount of magnetic flux it produces with a given current) of the core around which the coil is formed affects its inductance; as shown in Fig. 9.13c, different core materials inside the same coil change the inductance value. Finally, as shown in Fig. 9.13b, increasing the diameter of the coil increases the inductance.

#### Inductors in Series

Three series connected inductors are shown in Fig. 9.14; total inductance for inductors connected in series is the sum of the individual self-inductances:

 $L_t = L_1 + L_2 + L_3 ... + L_n$ 

## Inductors in Parallel

Three parallel connected inductors are shown in Fig. 9.15; total inductance for inductors connected in parallel is the reciprocal of the sum of the reciprocals of the individual self-inductances:

$$L_t = \frac{1}{L_1 + 1} + \frac{1}{L_2 + 1} + \frac{1}{L_3 + 1} + \frac{1}{L_n}$$



Fig. 9.16. Current change in an inductive circuit.

#### Inductive Circuits

Full treatment of inductive circuits is beyond the scope of this course but we include some interesting points which will allow you to compare the similarities between inductive circuits and capacitive circuits.

The time taken for current in an inductive circuit to either rise or fall through a resistance is measured in terms of inductance-resistance time constants (usually abbreviated to L/R time constants). The L/ R time constant is the time taken for current to rise through an inductor to a voltage equal to 63.2 per cent of its maximum value when connected to a supply, or fall by 63.2 per cent of its maximum value when disconnected from the supply. The time constant (T) of an L/R circuit is calculated as follows:

#### T = L/R

It can be seen from the graph of Fig. 9.16 that it takes about five L/R





Fig. 9.17. Graphs of the variation in voltage across the resistor and inductor in Fig. 9.18.







Fig. 9.19. Current will flow in a secondary circuit if the two coils are linked by the magnetic field.

time constants to rise to maximum current or fall to zero. Again, like the time taken to fully charge or discharge a capacitor (see Part 8), this is a rule of thumb: theoretically, the current in an inductor would never quite rise to its maximum value or fall completely to zero.

Fig. 9.17 is mainly for your interest and for you to compare it with Fig. 8.22, last month; it shows graphs of the variation in voltages across the inductor and resistor of the circuit in Fig. 9.18 when a square wave is applied to its input.

### **Transformer** Action

If two inductors are placed sufficiently near to each other, the changing current and associated magntic field around the coil in one circuit induces current in the other. Applying an a.c. voltage to one coil, and thus creating an alternating current in it, creates a magnetic field around it. Some of this flux, due to the close proximity of the second coil to the first, cuts the second coil and induces a voltage across it.

If the circuit of the secondary is closed, as shown in Fig 9.19a, a current will flow. These two circuits are linked such that energy is transferred from one circuit to the other by *transformer action*.

#### The Transformer

Any arrangement in which two or coils are magnetically more coupled to one another constitutes a transformer. The transformer just descirbed would be termed an aircored transformer, but iron-cored transformers are more common because they are more efficient (the permeability of iron is high and the reluctance of an iron magnetic circuit is very low compared to that of air, so nearly all the flux in an ironcored transformer is transferred from one coil to another); however, the principles of air-cored and ironcored transformers are the same.

A basic iron-cored transformer is made up of two coils wound on the same core, as depicted in Fig. 9.20; one coil of the transformer (the input) is called the primary winding, the other coil (the output) is called the secondary winding. The two windings are insulated from the core and from each other.

The primary winding, as shown in the figure, is connected to an a.c. voltage source, and the secondary winding is connected to the load. A transformer without a load acts like an inductor.



Fig. 9.20. Basic transformer.

The size of the voltage induced in the secondary coil (and whether it is increased or decreased) is determined by the difference in the number of turns in the two coils. If both windings of the transformer have the same number of turns, the voltage induced in the secondary will be equal to the voltage applied to the primary. If the secondary has half the windings of the primary, then the output voltage will be half of the input voltage; if the secondary has twice the windings of the primary, then the output voltage will be twice that of the input voltage.

#### The Transformation Ratio

The difference in the number of turns is known as the *transformation ratio* of the transformer. The relationship between the number of turns in the primary and secondary circuits and the voltages induced in them is expressed as follows:

Transformation ratio =  $\frac{V_s}{V_p} = \frac{N_s}{N_p}$ 

In which  $V_p$  is the primary voltage,  $V_s$  is the secondary voltage,  $N_p$  is the number of turns in the primary winding, and  $N_s$  is the number of turns in the secondary winding.

The transformation ratio can be set up to step up (if Ns is greater than N<sub>p</sub>) the primary voltage or, if Ns is less than Np, step it down. A step-down transformer is one where the result of Ns/Np is less than 1 and a step-up transformer is one where the result of N<sub>s</sub>/N<sub>p</sub> is greater than 1. A transformer with 10 times more turns on the primary side than on the secondary side has a step-down ratio of 10:1, and a transformer with 10 times fewer turns on the primary side than on the secondary side has a step-up ratio of 10:1.

By rearranging Equ. 1, above, any one of the four factors can be calculated if the other three are known; for example, if 240V is connected to the primary of the transformer with 1000 primary turns and 200 secondary turns, the voltage at the secondary may be calculated as follows:

$$\frac{V_s}{V_p} = \frac{N_s}{N_p}$$

Multiply both sides of the equation by  $V_{p}$ 

$$V_{s} = \frac{N_{s}}{N_{p}} \times V_{p} \rightarrow V_{s} = \frac{N_{s} \times V_{p}}{N_{p}} \quad (Equ. V_{s})$$
$$\therefore \frac{200 \times 240}{1000} = \frac{48,000}{1000} = 48V$$

To arrange this equation (Equ. V<sub>s</sub>) for calculating the primary voltage

 $(V_p)$ , first divide both sides by  $N_s$  to get

$$V_{s} = \frac{N_{s} \times V_{p}}{N_{p}} \rightarrow \frac{V_{s}}{N_{s}} = \frac{V_{p}}{N_{p}}$$

and then multiply both sides by Np

The arrangement for finding the number of turns in the secondary winding is derived by multiplying both sides of the original equation (Equ. 1) by N<sub>p</sub>

$$\frac{V_{s}}{V_{p}} = \frac{N_{s}}{N_{p}} \rightarrow \frac{V_{s}}{V_{p}} \times N_{p} = N_{s}$$
$$N_{s} = \frac{V_{s} \times N_{p}}{V_{p}} \qquad (Equ. N_{s})$$

Finally, the only arrangement left is the one for finding the number of turns in the primary winding. From the above equation (Equ.  $N_s$ ), first divide both sides by  $V_s$  to get

$$N_{s} = \frac{V_{s} \times N_{p}}{V_{p}} \rightarrow \frac{N_{s}}{V_{s}} \frac{N_{p}}{V_{p}}$$

then multiply both sides of this by  $V_p$ 

$$\begin{array}{l} \frac{N_{s}}{V_{s}} = & \frac{N_{p}}{V_{p}} \rightarrow \frac{N_{s}}{V_{s}} \times V_{p} = & N_{p} \\ N_{p} = & \frac{N_{s} \times V_{p}}{V_{s}} \qquad (Equ. N_{p}) \end{array}$$

These are all the possible forms of Equ. 1 for calculating transformer primary and secondary a.c. voltages and primary and secondary turns; the results are summarised in Table 9.2.

TABLE 9.2
$$V_s = \frac{V_p \times N_s}{N_p}$$
 $V_p = \frac{V_s \times N_p}{N_s}$  $N_s = \frac{V_s \times N_p}{V_p}$  $N_p = \frac{V_p \times N_s}{V_s}$ 

The current in the secondary of the transformer is related to the current in the primary and the transformation ratio in exactly the same way as voltage:

$$\frac{I_s}{I_p} = \frac{N_s}{N_p}$$

This equation can be transposed for calculating transformer primary and secondary curents and primary and secondary turns in the same way as shown above for voltages and turns; we will not repeat the examples but the results are summarised in Table 9.3.



Transformer Efficiency The above equations assume

#### that the efficiency of the transformer, given by

Output Power Input Power × 100,

is ideal; in practice, however, it is impossible to manufacture a perfect transformer. The efficiency of any transformer will always be less than 100 per cent because of factors such as resistance in the windings, flux losses (all the primary flux will not be transferred to the secondary), and a certain amount of power being dissipated as heat.

All this does make it sound as though there is more power lost than retained, but this is not so, transformers are very efficient—it is possible to achieve 99 per cent efficiency.

The power consumed in the secondary circuit must be supplied by the primary circuit. As the voltages in each circuit are fixed, the current in the primary circuit must vary to meet the power required by the secondary circuit. The power supplied by the primary is the primary current multiplied by the primary voltage (P=IV);

 $l_p \times V_p$ and the power delivered by the secondary is

 $l_s \times V_s$ Therefore, assuming no power losses, the primary power is equal to the secondary power.

## Illustrative Example

A perfect (100 per cent efficient) transformer steps up to a 100 volt supply to 650 volts. The primary has 1,000 turns.

(a) How many turns are there in the secondary winding?

$$N_{s} = \frac{N_{p} \times V_{s}}{V_{p}} \text{ (from Table 9.2)}$$
$$\frac{1,000 \times 650}{100} = \frac{650,000}{100}$$

6,500 turns

(b) A 65ohm resistor is connected across the secondary. What current will it draw?

V		650
	-	65

#### =10 amps

(c) What will be the power consumption in the secondary?

> P=IV =650×10 =6,500 watts

- (d) What power must be supplied to the primary winding? The same as the power at the secondary =6,500 watts
- (e) What is the primary current?

$$I = \frac{P}{V} = \frac{6500}{100} = 65 \text{ amps}$$

A study of this example shows that the current output was stepped down in exactly the same proportion that the output voltage was stepped up (the voltage was stepped up from 100V to 650V and the current was stepped down from 65A to 10A), and that the ratio of the primary voltages and currents to the secondary voltages and currents is the same as the ratio of the number of turns in the primary winding to the number of turns in the secondary winding:

Transformation ratio= $\frac{V_s}{V_p} = \frac{650}{100} = 6.5$ 

Also 
$$\frac{V_s}{V_p} = \frac{N_s}{N_p} = \frac{6500}{1000} \dots 6.5$$

Primary Current =65 Secondary Current=10

Transformation ratio= $\frac{l_p}{l_s} = \frac{65}{10} = 6.5$ 

A little more experimentation with these equations will reveal that if the voltage is stepped up by a transformer, current is stepped down; and if voltage is stepped down, current is stepped up.

#### National Grid

This very property is why transformers are so important to the national grid system which supplies the mains electricity to our homes and industry. Voltages generated at power stations are stepped up to very high voltages for transmission over long distances because power at high voltages carries less current than power at low voltages (since P=IV). This allows the transmission wires to be of a smaller cross sectional area.

A transformer can have one or more primary (or input) windings and one or more secondary (or output) windings; for example, the diagram of Fig. 9.21 shows a low voltage mains transformer and its symbol. Notice that the primary windings can be wired in series for 240 volt operation or can be wired in parallel for 120 volt operation (for use in the U.S.A., for example). The secondary side of this particular transformer has multiple taps or connections to give a choice of output voltage. Some commonly available types of transformer are depicted in Fig. 9.1.

Next month: Diodes.

#### Questions

 What curent would be found at the primary of the transformer below (assuming the transformer is ideal)?



- 2. What is the relationship between the power at the primary and the power at the secondary of a transformer?
- 3. What is the step-down ratio when the secondary side of a transformer has half the turns of the primary?
- 4. What is the step-up ratio when the secondary side of a transformer has twice the turns of the primary?
- 5. Does induced current flow in the



primary or secondary of a transformer?

- 6. Explain, in a few sentences, how a transformer works.
- If a transformer has 500 turns in the primary winding and 1000 turns in the secondary, what a.c. voltage is required at the input to produce 12 volts a.c.?
- 8. What will be the output voltage of the transformer on the right?



## ANSWERS TO LAST MONTH'S QUESTIONS





#### 5. 33µ

6. Since the capacitor is uncharged the current is a maximum at the instant the switch is made, it is limited only by the resistance in the circuit.

#### **7**. 10V

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27128250nS12.5-21vpp 74HC27 22p 74HC786	52p	74HCT4016	95p	1mF 100V	7n 1 Ohm - 1 Mest	ice Plust 1990
225p 74HC32 22p 74HCT93	80p	74HC14040	950	2.2nF 100V	7p E12 Range 5%	36 Way Centronica
2716 125p 74HC42 40p 74HCT12	3 80p	741014051	950	4.7nF 100V	7p 1 off 2p	Phug 155p Shand Comment 10 5p
2732 150p 74HC74 39p 74HCT13	8 60p	74HCT4511	1200	10nF 100V	7p 10 off 17p	COLORD COLLEG
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400p 74HC136 50p 10° 7415		74LS123	500	100mF 100V 1	op 10 off 35n	Black Stick on Feet 0.4W 210 K MO 1% 2p
CAPACITORS 74HC157 50p 74LS00	200	74LS125	30p	10015 1004 1	100 off 300p	10mm x 10mm 4 off Resistor
New Ministure Radial 74HC161 62p 74LS02	20p	74LS138	40p	ELECTROLYTICS	CARBON FILM 0.25W	25p 1.0W 47 onm MO 2% 2p
10uF 50V 5p 74HC163 65p 74LS04	20p	74LS151	45p	1.auF 63V	7p 10 Ohm - 1 Meg	PCB 20mm Fuse Holder DILNetworks 15:3.3K200
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Tur 35v rant Adia 50 74HC193 58p 74LS10	20p	7415157	450	4.7uF 16V 1	Op loff lp	STOCK ITEMS BY RETURN.
74HC195 54p 74LS13	30p	7415164	5000	10uF 16V 1	Op 10 off 9p	ADD 75n P&P
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Everyday Electronics, June 1989



THIS month we shall be devoting the whole of *On Spec* to the subject of interrupts. For the practically minded, we

whole of *On Spec* to the subject of interrupts. For the practically minded, we shall be including details of a practical Interrupt Controller designed by one of our regular readers.

#### Interrupts

Interrupts are essential to the operation of all real-time control systems as they provide an efficient means of both alerting the processor to the occurrence of a particular event such as a key depression or end-ofconversion signal. The ability to handle interrupts at various levels, to differentiate between them, and to make an appropriate response, is thus crucial to any microprocessor-based control system. Unfortunately, the Spectrum was designed as a low-cost home computer (rather than a real-time controller) and thus it is very poorly catered for as far as interrupt control is concerned.

Regular readers may recall that I provided some information on the use of interrupts (together with a simple demonstration routine) in the January 1988 instalment of On Spec. (If you missed out on this there is no need to despair as I have included the relevant details in the current On Spec Update!).

**Bill Buick** (from Newtyle, Perthshire) has gone one step further and has provided a novel solution to the problem of lack of interrupt control for the Spectrum. Bill's solution is based upon the Z80 Counter/ Timer Circuit (CTC) and the complete circuit of his Interrupt Controller is shown in Fig. 1. Bill writes:

The design of the Interrupt Controller centres on the Z80-CTC which is easy to interface to the ZX Spectrum. Address lines A5, A6 and A7 are used to select the device and program the internal registers. This allows the user to realise unique and flexible capabilities for his or her system, using the full power of vectored interrupts and machine code speed.

The Z80-CTC has four independent counter/timer channels. Each channel can be individually programmed with two bytes, a Channel Control Byte and a Time Constant Byte. The Channel Control Byte configures each channel either as a counter or as a timer. The Time Constant Byte sets an internal counter which is decremented each time a pulse occurs on the CLK/TRIG inputs to



the chip. These pulses can be selected by the programmer to be rising or falling edges (positive or negative going). When the internal counter reaches zero, the CTC interrupts the CPU in the vectored interrupt mode. A5 and A6. A7 operates as the chip select line. The CTC is unique in that it can modify the vector supplied to the CPU in such a way that the CPU can jump directly to the Interrupt Service Routine specific to the channel that caused the interrupt. The channels have an order of priority, Channel 0 has the high-

The channels are numbered 0 to 3 and are selected by the binary information on lines



Fig. 1. Complete circuit diagram for the Interrupt Controller.

est and Channel 3 has the lowest. The vectored interrupt mode is a method of combining information from the CPU's Interrupt (I) register with a byte from the interrupting device.

Construction of the Interrupt Controller is quite straightforward and should follow the same general lines as that recommended for our previous *On Spec* projects. Bill makes the following points:

As there is only one chip, construction is relatively easy but care has to be taken as errors may damage the Spectrum. The use of an i.c. socket is advisable. Notice also that the CLK/TRIG pins are pulled to 0V by resistors. These are essential to prevent undesired operation of these inputs.

Also, the IEI pin (Interrupt Enable In) must be pulled to +5V via a resistor. This is because the "daisy chain" method is used by Zilog in assigning priority to interrupting devices; the device which is electrically closest to the CPU has the highest priority.

#### Interrupt software

The Interrupt Controller is designed to take advantage of the most powerful of the Z80's interrupt modes. This mode caters for a variety of different responses to interrupts; the source of the interrupt can be identified by the processor and an appropriate interrupt service routine executed. Bill offers the following explanatory notes:

Unfortunately, the Spectrum is initialised at power-up to a non-vectored interrupt mode. It therefore has to be "fooled" into accepting IM2 (vectored interrupt) mode. First of all, a specific sequence of events has to occur to program the CTC as an interrupt controller. This involves programming the registers described previously, initialising all four channels as counters and setting the Time Constant Register to 01H. Also, the interrupt service routines for each channel have to be created and identified.

This process involves the creation of a "vector table". This table is a series of memory locations which contains the address of the area in memory which the CPU will jump to when an interrupt occurs. The location of the vector table in memory is very important. On receiving an interrupt, the contents of the CPU's Interrupt (I) register is joined with the byte supplied from the CTC to make a pointer to the location in memory which has the address of the interrupt service routine(s).

This is then loaded into the Program Counter in the CPU and program execution continues from there. Notice also, that all interrupt service routines must terminate with the **RETI** instruction which will allow further interrupts to occur properly.

To ensure that the Spectrum will operate properly, an additional "dummy" routine has to be programmed. This is because every 20ms the ULA is interrupting the CPU and when the CPU is programmed to Interrupt Mode 2 (IM2), it will be looking for the 8-bit vector normally supplied by the interrupting device.

However, the ULA does not supply this and the data bus lines are pulled to +5V by resistors. Thus, the pointer formed in this case will be the contents of the interrupt register joined to FFH. Therefore, it will be necessary to include this to avoid a "crash".

Once the CTC has been correctly programmed, interrupts on the Spectrum must be disabled while the interrupt modes are changed. The final instruction in the initialisation program is JP 0038H, which

LOAD BOOM: 9000 F5 F1 F19H AF 9001 F5 F19H H1 9002 21 57 80 LD H, CH0 9002 22 57 80 LD H, CH0 9002 22 25 77 80 LD H, CH0 9002 21 57 90 LD H, CH0 9001 22 22 97 F1 D19F72H1, HL 9001 22 22 97 F1 D19F72H1, HL 9011 22 22 47 97 LD 19F72H1, HL 9012 22 25 97 LD 19F72H1, HL 9013 22 25 97 LD 19F72H1, HL 9014 21 78 90 LD H, CH3 9015 22 25 47 97 LD 19F72H1, HL 9020 52 20 11 00 H, 213 100AD VECTOR TD CTC 9022 D3 1F 901131,A 9020 52 20 11 00 A, 213 100AD VECTOR TD CTC 9022 03 15 00 A, 213 100AD VECTOR TD CTC 9022 03 15 00 A, 213 100AD COUNTER WITH 01 9020 52 03 05 00 LD A, 213 8026 53 05 01 LD A, 213 8026 53 05 01 LD A, 213 8026 53 05 01 LD A, 213 8027 53 00 UT (52),A 8035 55 01 LD A, 213 8035 15 01 LD A, 41 8037 55 00 LD A, 213 8035 15 01 LD A, 41 8037 55 00 LD A, 213 8035 15 01 LD A, 41 8037 55 01 LD A, 41 8040 F1 PDF H1 :RESTORE REGISTERS 8040 F1 PDF H1 :RESTORE REGISTERS 8040 F1 PDF H1 :RESTORE REGISTERS 8040 F1 PDF H2 8045 F5 00 WT D1 8045 F5 00 WT D1 8045 F5 00 WT D1 8045 F5 00 WT D1 8045 F6 E1 8040 F1 PDF H2 :SIGMAL MAIN SCREEN 8050 75 CH1 LD A, "B" 8050 F0 D0 RETI :REPEAT FOR OTHER CHANNELS 8050 75 CH1 LD A, "B" 8050 F0 D0 RETI :REPEAT FOR OTHER CHANNELS 8050 F7 F2 CH2 20 F6 RES 0, (17422) 8051 F3 CH1 D1 RETUR :REPEAT FOR OTHER CHANNELS 8052 75 CH1 D1 RETUR :REPEAT FOR OTHER CHANNELS 8055 75 CH1 D1 RETUR :REPEAT FOR OTHER CHANNELS 8056 F5 0 D1 RETI :REPEAT FOR OTHER CHANNELS 8056 F5 0 D1 RETI :REPEAT FOR OTHER CHANNELS 8057 F3 CH1 D1 RETUR :REPEAT FOR OTHER CHANNELS 8056 F5 0			200		
9000         F5         FUSH AF         :SAVE REGISTERS           9002         22         25         7         80         10         14           9008         52         22         27         F1         10         17         13           9008         22         27         F1         10         14         13         13           9009         72         22         7         10         17         13         13         14         14         14         14         14         14         14         14         14         14         15         14         14         15         15         15         15         15         15         15         15         15         15         15         15         14         13         14         14         14         14         14         14         14         14         15         16         1			LOAD	BOOOH	and things and specialized at a second
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Bodd         ESAVE         REGISTERS           Bodd         Z1         S7         Bodd         SAVE         REGISTERS           Bodd         Z2         Z3         F         DIGFZ2HJ, H         Bodd           Bodd         Z1         Z3         Bodd         DIA         Clip         Save         REGISTERS           Bodd         Z1         Z3         Bodd         DIA         Clip         Save         REGISTERS           Bodd         Z2         Z3         F         DIGFZ2HJ, H         Save         REGISTERS           Bodd         Z2         Z4         P         DIA         Clip         Save         REGISTERS           Bodd         Z2         Z4         P         DIA         Clip         Save         REGISTERS           Bodd         Z2         Z4         P         DI         PFFID.         H           Bodd         Z2         Z4         P         DI         PFFID.         H           Bodd         Z3         LD A         Z1         LDA         CDNFIGURE CHANNEL AS COUNTER           Bodd         S3         Clip         A         DI         DI         DI           Bodd         <	8000	F5		PUSH AF	
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B0003       22       26       97       10       10       10       10         B0008       22       22       97       10	8002	21 57	80	LD HE, CHO	
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Big 21         50         10         HI, CH2         1SET IP VECTOR TABLE           B011         22         24         F         LOTG72410, IL           B011         21         78         B0         10         H, CH3           B011         22         24         F         LOTG72410, IL         B0           B020         35         F         DUTG311, A         CONFIGURE CHANNEL AS COUNTER           B022         D3         IF         OUTG31, A         ECONFIGURE CHANNEL AS COUNTER           B022         D3         CUTG31, A         ECONTER WITH 01         B0           B022         D3         CUTG32, A         ECONTER WITH 01         B0           B023         D3         CUTG44, A         ECONTER         ECONTER           B033         E0         LD A, 4         ECONTER         ECONTER	8008	27 22	96	10(9E22H) . H	
1011       22       24       0F       LD 0F7241D, HL         1011       21       78       80       D H, CH3         1011       21       78       60       D H, CH3         1011       21       51       60       D H, DMMY         1011       21       51       60       D H, CH3         1011       21       51       60       D H, CH3         1012       22       F       9F       LD 0FFFID, HL         1012       21       F       9F       LD 0FFFID, HL         1012       21       F       0011313, A       CONFIGURE CHANNEL AS COUNTER         1022       25       F       LD A, 21       CONTER WITH 01         1022       25       D A, 21       REPEAT FOR DTHER CHANNEL AS COUNTER         1022       25       D A, 21       REPEAT FOR DTHER CHANNELS         1023       A       0011313, A       CONTERWITH 01         1024       B44       F       D LD A, 61         1025       26       D UT(44), A       CONTERWITH 01         1035       26       D UT(46), A       CONTERWITH 01         10364       57       F       D A, 61       CONTERWITH 01	9005	21 60	80	ID HL. CH2	SET UP VECTOR TABLE
0014       21       79       80       10       H, CH3         0017       22       24       94       10       H, DMMY         0010       22       25       97       10       10       FF00         0011       22       25       97       10       97       10       97         0011       22       25       97       10       97       10       97         0022       03       17       QUI(33),A       10.0A       52       10.0A       10         0022       03       17       QUI(33),A       10.0A       COUNTER WITH 01       00         0023       05       10       A, 213       COUNTER WITH 01       00       00       00         0026       05       10       A, 213       COUNTER WITH 01       00	8011	22 24	9F	LD(9F24H), H	L.
9017       22       24       9F       LD (9F26H), HL         9010       22       FF       9F       LD (9FFFH), HL         9020       3E       20       LD A, 32       :LDAD VECTOR TO CTC         9021       22       FF       9F       LD (9FFFH), HL         9022       3E       20       LD A, 21       :CONFIGURE CHANNEL AS COUNTER         9022       3E       DI H, 0H (3)   A       :CONFIGURE CHANNEL AS COUNTER         9022       3E       DI H, 0H (3)   A       :CONFIGURE CHANNEL AS COUNTER         9022       3E       DI H, 0H (3)   A       :CONFIGURE CHANNEL AS COUNTER         9022       3E       DI H, 0H (3)   A       :CONFIGURE CHANNEL AS COUNTER         9022       3E       DI H, 0H (3)   A       :CONFIGURE CHANNEL AS COUNTER         9022       3E       DI H, 0H (3)   A       :CONFIGURE CHANNEL AS COUNTER         9023       3E       DI LD A, 213       :CONFIGURE CHANNELS         9034       D3 + 0       DUT(43)   A       :CONFIGURE CHANNELS         9035       SE       DI LD A, 213       :SWITCH INTERRUPT MODES         9035       G6       DUT(4)   A       :SWITCH INTERRUPT MODES         9045       FB       EI       :D A, 213	8014	21 78	80	LD HL, CH3	
Bell 21 51 80       1.D.H., DMMY         Bell 22 FF 9F       LD 94, 32       :LDAD VECTOR TO CTC         Bell 22 FF 9F       DI (151), A       :CONFIGURE CHANNEL AS COUNTER         Bell 23 1F       DUT(31), A       :CONFIGURE CHANNEL AS COUNTER         Bell 23 1F       DUT(31), A       :CONFIGURE CHANNEL AS COUNTER         Bell 23 20       DI (1013), A       :CONFIGURE CHANNELS         Bell 23 20       DUT(32), A       REPEAT FOR DTHER CHANNELS         Bell 23 20       DUT(44), A       :CONFIGURE CHANNELS         Bell 24 20 3 40       DUT(44), A       :SUITCH INTERRUPT MODES         Bell 25 51       DA, 213       :SWITCH INTERRUPT MODES         Bell 26 11       DA, 97H       :SWITCH INTERRUPT MODES         Bell 25 10 A, 213       :RESTORE REGISTERS         Bell 25 10 A, 213       :RESTORE REGISTERS         Bell 25 17 30 MMY: DI       :RESTORE REGISTERS         Bell 25 16 10       :DO NOTHING         Bell 25 17 00 JF       :DO NOTHING         Bell 25 26 27 CH1: DI : CHANNEL 1 INTERRUPT SERVICE ROUTINE         Bell 26 26 27 CH1: DI : CHANNEL 1 INTERRUPT SERVICE ROUTINE         Bell 26 10 DF       :DO NOTHING         Bell 27 CH2: DI RETI : RETURN FROM INTERRUPTS         Bell 26 10 DF       RES 104	8017	22 26	9F	LD (9F26H), H	h de la companya de l
0010       22 FF 9F       LD (9FFFID, HL         0020       3E       20       LD A, 32       :LDAD VECTOR TO CTE         0022       3E       20       LD A, 213       :CONFIGURE CHANNEL AS COUNTER         0022       3E       D1       LD A, 01       :LDAD COUNTER WITH 01         0022       3E       D1       LD A, 01       :LDAD COUNTER WITH 01         0022       3E       D1       LD A, 01       :LDAD COUNTER WITH 01         0022       3E       D1       LD A, 01       :LDAD COUNTER WITH 01         0022       3E       D1       LD A, 01       :LDAD COUNTER WITH 01         0024       3E       D1       LD A, 01       :REPEAT FOR DTHER CHANNELS         0025       3E       D1       LD A, 01       :REPEAT FOR DTHER CHANNELS         0036       3E       D1       LD A, 01       :REPEAT FOR DTHER CHANNELS         0036       SE       D1       LD A, 01       :REPEAT FOR DTHER CHANNELS         0037       SE       D1       LD A, 01       :REPEAT FOR DTHER CHANNELS         0036       SE       01       LD A, 01       :REPEAT FOR DTHER CHANNELS         0040       E1       D A, 97H       :SWITCH INTERRUPT MODES         <	801A	21 51	80	1.D HI # DMMY	
8020       3E 20       LD A, 32       #LDAD VECTOR TO CTE         8022       D3 IF       DU1(31),A       #CONFIGURE CHANNEL AS COUNTER         8024       AS D5       LD A, 23       #CONFIGURE CHANNEL AS COUNTER         8025       D3 IF       OUI(31),A       #LDAD COUNTER WITH 01         8026       D3 IF       OUI(31),A       #LDAD COUNTER WITH 01         8027       D3 20       OUT(32),A       #REPEAT FOR OTHER CHANNELS         8028       D3 20       OUT(32),A       #REPEAT FOR OTHER CHANNELS         8028       D3 40       OUT(43),A       #REPEAT FOR OTHER CHANNELS         8030       S0 40       OUT(44),A       #REPEAT FOR OTHER CHANNELS         8043       E01       LD A,01       #REPEAT FOR OTHER RUPT MODES         8044       F5       DI       #RESTORE REGISTERS         8045       SE 9F       LD A,07FH       #SWITCH INTERRUPT ROUTINE         8044       F5       DI       #RESTORE REGISTERS         8045       SE 90       JP 0P AF       #RESTORE REGISTERS         8046       F5       IM2       #RESTORE REGISTERS         8045       F6       E1       #RESTORE REGISTERS         8046       F8       E1       #REST	8Ø1D	22 FF	9F	LD (PEFEID , H	L
0022       D3 IF       DJ1 (31),A         0024       D5       LDA,23       :CONFIGURE CHANNEL AS COUNTER         0026       D3 IF       DJ1 (31),A       iLDAD COUNTER WITH 01         0027       D3 IF       DJ1 (31),A       REPEAT FDR DTHER CHANNELS         0026       D3 IF       DJ1 (32),A       REPEAT FDR DTHER CHANNELS         0027       D3 D5       D4,A,213       REPEAT FDR DTHER CHANNELS         0028       D5       LDA,213       REPEAT FDR DTHER CHANNELS         0027       D3 40       DJ1 (43),A       REPEAT FDR DTHER CHANNELS         0038       D3 40       DJ1 (43),A       REPEAT FDR DTHER CHANNELS         0037       D3 40       DJ1 (43),A       REPEAT FDR DTHER CHANNELS         0038       D3 40       DJ1 (43),A       REPEAT FDR DTHER CHANNELS         0045       D4       DJ1 (14),A       REPEAT FDR DTHER CHANNELS         0046       D5       LD A,01       INTERRUPT MODES         0046       D5       D1       REPEAT FOR	8020	3E 2Ø		1.0 A, 32	ILDAD VECTOR TO CTC
B024         SE D5         LD A, 213         CONFIGURE CHANNEL AS COUNTER           B026         D3 IF         OUI (31),A         iLDAD COUNTER WITH 01           B026         D3 IF         OUI (32),A         REPEAT FOR DTHER CHANNELS           B026         D3 IF         OUI (32),A         REPEAT FOR DTHER CHANNELS           B026         D3 20         OUT (32),A         REPEAT FOR DTHER CHANNELS           B026         D3 20         OUT (32),A         REPEAT FOR DTHER CHANNELS           B026         D3 20         OUT (32),A         REPEAT FOR DTHER CHANNELS           B036         SE 01         LD A, 01         REPEAT FOR DTHER CHANNELS           B037         D3 40         OUT (44),A         REPEAT FOR DTHER CHANNELS           B038         D1         LD A, 01         REPEAT FOR DTHER CHANNELS           B048         ED 5         LD A, 01         REPEAT FOR OTHER CHANNELS           B044         F0         D1         LD A, 01           B044         F0         D1         REPEAT FOR OTHER CHANNELS           B044         ED 5         LD A, 01         REPEAT FOR OTHER CHANNELS           B044         F0         D1         REPEAT FOR OTHER CHANNELS           B045         F0         F0         P	8022	D3 1F		QUT (31) ,A	
0022       D3 IF       011 (31),A       ILDAD COUNTER WITH 01         0022       D3 IF       011 (31),A       REPEAT FOR DTHER CHANNELS         0022       D5 IF       011 (32),A       REPEAT FOR DTHER CHANNELS         0022       D5 20       017 (32),A       REPEAT FOR DTHER CHANNELS         0023       D5 20       017 (32),A       REPEAT FOR DTHER CHANNELS         0034       E01       LD A,01       REPEAT FOR DTHER CHANNELS         0035       SE 01       LD A,213       REPEAT FOR DTHER CHANNELS         0036       D3 40       017 (44),A       REPEAT FOR DTHER CHANNELS         0037       SE 01       LD A,01       REPEAT FOR DTHER CHANNELS         0038       E01       LD A,01       REPEAT FOR DTHER CHANNELS         0043       E05       LD A,01       REPEAT FOR DTHER CHANNELS         0044       F3       D1       LD A,01         0042       E3 60       017 (45),A       SWITCH INTERRUPT MODES         0044       F3       D1       J.4       RESTORE REGISTERS         0046       F1       POP AF       E1       REPEAT FOR OTHER CHANNELS         0045       F3       DMY: D1       IA (40), (40)       SIGNAL MAIN SCREEN         0055	8024	3E D5		LD A, 213	CONFIGURE CHANNEL AS COUNTER
B022       3E 01       10 A, 01       11.0AD COUNTER WITH 01         B022       32 F       011 (33), A       REPEAT FOR DTHER CHANNELS         B022       3E 01       LD A, 213       REPEAT FOR DTHER CHANNELS         B022       3E 01       LD A, 01         B032       3E 01       LD A, 01         B033       3E 01       LD A, 01         B034       3E 05       LD A, 213         B035       80 1       LD A, 01         B036       35 40       OUT (64), A         B036       93 40       OUT (64), A         B037       B3 60       OUT (64), A         B038       80 1       LD A, 01         B047       ED 5       LD A, 01         B048       SE 9F       LD A, 97H         B044       F3       D1         B045       3E 9F       LD A, 97H         B046       E1       POP H       :RESTORE REGISTERS         B047       ED 55       IM2       DI         B045       G0       JP 0030H       :JUMF TD MASKABLE INTERRUPT ROUTINE         B045       F3       DMY: D1       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         B0455       F8       E1       DA, "A"     <	8026	D3 1F		OUT (31),A	
B022A       D3 IF       OUT(31),A         R02C       D3 20       OUT(32),A         R030B       SE D5       LD A,01         R030B       SE 01       LD A,01         R043B       SE 01       LD A,01         R044       F3       D1         R044       F3       D1         R045       SE 9F       LD A,9FH         R044       F3       D1         R045       E0 47       LD A,9FH         R046       C3 30       00       JF 0059H         R047       E0 47       LD A,9FH       :SWITCH INTERRUFT MODES         R047       E0 40       JF 0059H       :JUMF TD MASKADLE INTERRUFT ROUTINE         R046       F3       D1       :CHANNEL 1 INTERRUFT ROUTINE         R0457       F3       CH0:	8028	3E Ø1		LD A, Ø1	1LOAD COUNTER WITH 01
8022 3: D       CD A, 213       REPEAT FOR DIMER CHANNELS         8026 3: 20       DUT (32), A         8030 3: 6 01       LD A, 01         8033 3: 6 01       LD A, 01         8034 3: E D5       LD A, 213         8035 0: 6 01       LD A, 01         8036 3: 6 01       LD A, 01         8037 0: 7 00       A, 213         8038 3: 6 01       LD A, 01         8037 0: 7 0: 7 0: 7 0: 7 0: 7 0: 7 0: 7 0	802A	D3 1F		OUT (31),A	SEDEAT FOR STUES OUANNELS
B022       D3 20       D01 (32), H         B033       D3 20       DU1 (32), A         B034       D3 20       DU1 (32), A         B035       D3 20       DU1 (32), A         B036       D3 40       DU1 (4), A         B037       D3 60       DU1 (6), A         B038       D3 60       DU1 (6), A         B036       D3 60       DU1 (6), A         B037       D3 60       DU1 (6), A         B038       D3 60       DU1 (6), A         B047       ED 47       LD A, 91         B047       ED 47       LD A, 97H         B048       F6       E1         B049       F6       E1         B047       ED 47       LD A, 97H         B048       F6       E1         B049       F6       E1         B041       POP AF       POP AF         B042       C3 39 00       JP 0030H       : JUMP TO MASKABLE INTERRUPT ROUTINE         B052       F3       CH0:       D1       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         B053       SE 41       LD A, "A"       :SIGNAL MAIN SCREEN         B054       C3 39 00       JP 00336H       :SIGNAL MAIN SCREEN	8020	JE D5		LD A, 213	REPEAT FOR OTHER CHANNELS
G0320       JC 01       LD 0,01         G0334       JS 20       OUT (32),A         G034       JS 20       ID A, 213         G035       JS 01       LD A,01         G036       JE 01       LD A,01         G037       JS 01       LD A,01         G037       JS 04       OUT (46),A         G038       S01       LD A,01         G044       JS 04       OUT (46),A         G044       FS       DI         G044       JS 04       OUT (46),A         G044       SE 9F       LD A,01         G045       JS 9F       LD A,01         G046       GD 47       LD I,A         G047       GD 5E       IM2         G048       FB       FE         G047       FS       FE         G046       C1       POP H       :RESTORE REGISTERS         G046       FS       FE       ID       NOP         G046       FS       FE       ID       NOP         G040       FS <td>802E</td> <td>103 20</td> <td></td> <td>1D 4 04</td> <td></td>	802E	103 20		1D 4 04	
B032       D3       D0       101.32.17         B034       D3       40       DUT(64),A         B036       D3       40       DUT(64),A         B037       D3       40       DUT(64),A         B038       D3       40       DUT(64),A         B036       D3       60       DUT(96),A         B037       D3       60       DUT(96),A         B042       D3       60       DUT(96),A         B042       D3       60       DUT(96),A         B042       D3       60       DUT(96),A         B044       F3       D1       LD A,01         B045       E9       LD A,97H       :SWITCH INTERRUPT MODES         B044       F3       D1       REFEDER       E0         B045       ED 5E       IH2       B047       ED 47         B047       ED 47       LD 1,A       POP 4F       JUMP TO MASKABLE INTERRUPT ROUTINE         B045       F3       DMMY       D1       B054       EO       D0         B054       C3       38       00       JP       0038H       SUTINE       ERVENTINE         B055       F3       CH3       E1       LD	8030	JE 01		CUT(32) A	
B036       D1       D1       D1         B036       D3       H0       DUT(64),A         B038       D3       H0       DUT(64),A         B038       D3       H0       DUT(64),A         B038       D3       H0       DUT(64),A         B034       D3       H0       DUT(64),A         B035       D3       H0       DUT(64),A         B047       D3       H0       DUT(64),A         B047       D3       H0       DUT(64),A         B047       D3       H0       DUT(76),A         B047       D3       H0       DUT(76),A         B047       E0       FF       E1         B047       E0       H2       HD         B047       E1       HD       HE         B055       F3       D1 <td>90-7A</td> <td>35 05</td> <td></td> <td>LD A. 213</td> <td>1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1</td>	90-7A	35 05		LD A. 213	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
B030       32 01       LD A,01         B033       D3 40       DUT(64),A         B035       S2 D5       LD A,213         B035       S2 D5       LD A,01         B040       S2 05       LD A,01         B042       D3 60       DUT(96),A         B044       F3       D1         B045       3E 9F       LD A,9FH         B044       F3       D1         B045       3E 9F       LD A,9FH         B046       FD       F1         B047       ED 47       LD A,9FH         B048       F8       E1         B047       ED 5E       IM2         B048       F8       E1         B047       ED 47       D1 (F6),A         B047       ED 47       LD 1,A         B047       ED 5E       IM2         B048       F8       E1         B045       F3       DMY D1         B046       F3       DMY D1         B055       F8       E1         B054       C3 38 00       JP 0039H         B055       F8       E1         B056       SE 41       LD A,"A"         B057 </td <td>9034</td> <td>D3 40</td> <td></td> <td>DUT (64) A</td> <td></td>	9034	D3 40		DUT (64) A	
B03A       DJT(64),A         B03C       3E       DJ         B03E       3E       0         B042       3E       01         B044       F3       DI         B044       F3       DI         B044       F3       DI         B044       F3       DI         B045       3E       PI         B047       ED       A,9FH         B048       FD       F1         B049       FD       F1         B041       F1       POP AF         B042       F3       DMMY: DI         B053       FB       E1         B045       F3       DMMY: DI         B052       F3       CH0: DI       : CHANNEL 1 INTERRUPT SERVICE ROUTINE         B053       FB       E1       B045       B0       B0         B055       F3       CH0: DI       : CHANNEL 1 INTERRUPT SERVICE ROUTINE       B058         B056	8038	JE ØI		LD A.ØI	
93C 3E 05       LD A, 213         903C 3E 01       LD A, 213         9040 3E 01       LD A, 01         9040 3E 01       LD A, 01         9044 3E 01       LD A, 01         9045 3E 07       LD A, 97H         9047 5D 5F       LD A, 97H         9048 5B       FI         9047 5D 5F       LD A, 97H         9048 7B       FI         9047 5D 5F       M2         9048 7B       FI         9047 5D 5F       M2         9048 7B       FI         9047 5D 5F       M2         9048 7B       FI         9047 5D 5F       M172         9048 7B       FI         9040 71       POP AF         9041 71       POP AF         9042 61       POP AF         9045 75       DMMY: DI         9052 00       NOP         9053 FB       EI         9054 C3 38 00       JP 0038H         9055 FB       EI         9056 FB       EI         9057 F3 CH0: DI :       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         9058 FB       EI         9059 FB       EI         9059 FB       EI	8Ø3A	D3 40		DUT (64) .A	
803E       D3 60       DUT(96), A         9040       3E       01       LD A,01         9042       D3 60       OUT(96), A       B044         9047       ED 4,9FH       SWITCH INTERRUFT MODES         9047       ED 47       LD 1, A         9049       FB       E1         9040       NDP       ID A,97H         9041       POP HL       :RESTDRE REGISTERS         9042       F3       DMMY         9053       F3       DMMY         9054       C3 38       00       JP 0038H         9055       F3       CH0:       DI       :CHANNEL 1         9055       F8       E1       DA,"A"       :SIGNAL MAIN SCREEN         9055       F8       E1       B042       F3       CH1:       DI         9062       F3	8Ø3C	3E D5		LD A, 213	
8040       3E 01       I.D A,01         8042       D3 60       OUT(76),A         8044       F3       DI         8045       3E 9F       I.D A,9FH       :SWITCH INTERRUPT MODES         8047       ED 47       I.D I,A         8048       FB       EI         8049       ED 5E       IM2         8040       F1       POP H.       :RESTDRE REGISTERS         8040       F1       POP AF         8041       F1       POP AF         8042       F3       DMMY:         8045       SE 00       JP 0038H         8045       SE 00       JP 0038H         8052       00       NOP       :DD NOTHING         8053       F3       DMMY:       D1         8054       C3 38       00       JP 0038H         8055       F3       CH0:       D1       : CHANNEL 1 INTERRUPT SERVICE ROUTINE         8055       F3       CH0:       D1       : CHANNEL 1 INTERRUPT SERVICE ROUTINE         8055       F3       CH0:       D1       : CHANNEL 1 INTERRUPT SERVICE ROUTINE         8055       F3       CH0:       D1       : CHANNEL 1 INTERRUPT SERVICE ROUTINE	803E	D3 6Ø		OUT (96) ,A	
0042       03 & 60       OUT (76), A         0044       F3       DI         0045       3E       9F       LD A, 9FH       :SWITCH INTERRIFT MODES         0047       ED 47       LD I, A       10       14         0047       ED 47       LD I, A       10       14         0047       ED 47       LD I, A       10       14         0047       ED 5E       112       10       14         0047       ED 5E       112       10       14         0047       ED 5E       112       10       14         0040       F1       POP H       :RESTORE REGISTERS       900         0051       F3       DMMY: DI       10       100       11         0052       00       NOP       :DD NOTHING       100       11         0053       F8       EI       10       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         0054       C3 39       00       JP 0039H       :PORAT       100         0055       F3       CH0:       DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         0055       F0       D82 86       G1 (1Y+02)       :SIGNAL MAIN SCREEN         00405       F0	8040	3E Ø1		LD A,01	
8044       F3       DI         8045       3E       9F       LD A,9FH       :SWITCH INTERRUPT MODES         8047       ED 47       I.D I,A       1.0 I,A         8047       ED 47       I.D I,A         8048       ED 5E       IM2         8049       ED 5E       IM2         8040       F1       POP AF         8040       F3       DMY: D1         8051       F3       DMY: D1         8052       F8       E1         8053       F8       E1         8054       C3       38 00       JP 0039H         8055       F3       CH0: DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         8055       F8       E1       ED A,"A"         8056       F0 CB 02 86       RES 0,(IY+02)       :SIGNAL MAIN SCREEN         8056       F0 CB 02 86       RES 0,(IY+02)       :SIGNAL MAIN SCREEN         8057       F8       E1       :RETURN FROM INTERRUPTS         8042       F3       CH1: DI       :RETURN FROM INTERRUPTS         8042       F3       CH1: DI       :REPEAT FOR OTHER CHANNELS         8042       F3       CH1: DI       :RETI         8042       <	8042	D3 60		OUT (96),A	
8045       3E 9F       LD A,9FH       :SWITCH INTERRUPT PUDES         8047       ED 47       LD I,A         8049       ED 5E       IH2         8049       ED 5E       IH2         8049       ED 5E       IH2         8040       F1       POP AF         8041       F3       DMMY: D1         8052       60       NOP       :DO NOTHING         8053       F8       E1         8054       C3 38 00       JP 0038H       :JUMP TD MASKABLE INTERRUPT ROUTINE         8052       60       MOP       :DO NOTHING         8053       F8       E1       B057         8054       C3 38 00       JP 0038H         8055       F3       CH0:       DI         8056       JP 0038H       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         8057       F3       CH0:       DI         8058       E0 4D       RETI       :RETURN FROM INTERRUPTS         8056       E0 4D       RETI       :REPEAT FOR OTHER CHANNELS         8065       F0 CB 02 86       RES 0, (IY+02)       RETI         8065       F0 CB 02 86       RES 0, (IY+02)       RETI         8066       SE 43	8044	F3		DI	
8047       ED 47       1.D 1,4         8047       ED 5E       1H2         8049       ED 5E       IH2         8040       E1       POP AF         8040       F1       POP AF         8041       C3 38       00       JP 00339H         8051       F3       DMMY: D1         8052       F3       DMMY: D1         8053       FB       E1         8054       C3 38       00       JP 00339H         8055       F3       DMMY: D1         8055       F3       CH0: D1       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         8055       F3       CH0: D1       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         8056       SE 41       LD A, "A"       POP AF         8057       F3       CH0: D1       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         8056       SE 41       LD A, "A"       POP INT         8057       F3       CH0: D1       :PRINT         8058       SE 41       LD A, "A"       POP INT         8059       F5       E1       PRINT       PRINT         8056       F0 CB 02 86       RES 0, (IY+02)       RETI       PRETI         8045       <	8045	3E 9F		LD A,9FH	SWITCH INTERRUPT MODES
B049       FB       FI         B049       FB       FI         B040       FI       POP AF         B041       FI       POP AF         B042       FI       POP AF         B045       FI       POP AF         B052       ØØ       NOP       :DD NOTHING         B052       ØØ       NOP       :DD NOTHING         B053       FI       EI       LD A, "A"         B054       FJ       CH0:       DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         B055       FJ       EI       LD A, "A"       :SIGNAL MAIN SCREEN         8056       D7       RST 10H       :PRINT       :SIGNAL MAIN SCREEN         8056       ED 4D       RETI       :REPEAT FOR OTHER CHANNELS         80643       SE 42       LD A, "B"       :RES 0, (IY+02)         80645       FB	8047	ED 47		1.0 1, A	
00475       FB       FI       FI       POP AF         00405       F1       POP AF       POP AF         00405       F3       DMMY: D1       POP AF         0052       00       NOP       :DD NOTHING         0052       00       NOP       :DD NOTHING         0053       FB       E1         00545       C3 38 00       JP 0038H         00557       F3       CH0:       DI         0056       SE 41       LD A, "A"         00576       FB       E1         00577       F3       CH0:       DI         00578       FB       E1         00579       F3       CH0:       DI         00570       F3       CH0:       DI         00576       FB       E1       INTERRUPT SERVICE ROUTINE         00576       FB       E1       PRINT         00577       RST 10H       :PRINT         00576       FD       C2 86       RES 0, (IY+02)         0062       F3       CH1:       DI       :REPEAT FOR OTHER CHANNELS         0063       SE 42       LD A, "B"       PA       PA         0064       FB <td< td=""><td>8047</td><td>EN DE</td><td></td><td>1M2</td><td></td></td<>	8047	EN DE		1M2	
B040       E1       POP AF         B044E       C3 38 00       JP 0038H       :JUMP TO MASKABLE INTERRUPT ROUTINE         B051       F3       DMMY: D1       B052         B052       F3       DMMY: D1       D0 NOTHING         B053       FB       E1       B053       B0         B054       C3 38 00       JP 0038H       E1         B055       F3       CH0: DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         B056       JF4       LD A, "A"       :SIGNAL MAIN SCREEN         B055       F5       E1       :SIGNAL MAIN SCREEN         B056       JF6       E1       :SIGNAL MAIN SCREEN         B057       F5       E1       :SIGNAL MAIN SCREEN         B056       JF7       RST 10H       :PRINT         B057       F5       E1       :SIGNAL MAIN SCREEN         B065       D7       RST 10H       :PRINT         B065       F0       CB 02 86       RES 0, (IY+02)         B064       F1       E1       :SIGNAL MAINELS         B065       F0       CB 02 86       RES 0, (IY+02)         B064       F1       E1       :SIGNAL MAINELS         B064       F1       E1<	8048	F 8			RESTORE REGISTERS
804E       C3 39 00       JP 0030H       :JUMP TD MASKABLE INTERRUPT ROUTINE         8051       F3       DMMY: D1         8052       00       NOP       :DD NOTHING         8052       00       NOP       :DD NOTHING         8053       FB       E1         8054       C3 38 00       JP 0030H         8055       F3       CH0:       D1         8056       JF       CH0:       D1         8057       F3       CH0:       D1         8058       3E 41       LD A, "A"         8056       D7       RES 0, (IY+02)       :SIGNAL MAIN SCREEN         8056       D7       RET 10H       :PRINT         8057       F3       CH1:       D1       :REPEAT FOR OTHER CHANNELS         8060       ED 4D       RET 1       :REPEAT FOR OTHER CHANNELS         80647       F3       CH1:       D1       :REPEAT FOR OTHER CHANNELS         80647       D7       RST 10H       :REPEAT FOR OTHER CHANNELS         80648       FB       E1       :D A, "C"         80649       D7       RST 10H       :RETI         80648       ED 4D       RETI       :D A, "C"         8070	8040	E1		POP AF	
9051       F3       DMMY: D1         9052       00       NOP       :DD NOTHING         9053       FB       E1         9054       C3       39       00       JP 0038H         9057       F3       CH0:       DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         9058       SE 41       LD A, "A"       :SIGNAL MAIN SCREEN         9056       SE 41       LD A, "A"         9057       F3       CH0:       DI         9058       SE 41       LD A, "A"         9059       SE 41       LD A, "A"         9057       F3       CH0:       DI         9058       SE 41       LD A, "A"         9059       SE 41       LD A, "A"         9057       FB       EI         9058       SE 41       DI         9059       RST 10H       :PRINT         9062       F3       CH1:       DI         9063       SE 42       LD A, "B"         9064       FB       EI         9065       FD       CB 72         9066       SE 43       LD A, "C"         9070       FD CB 02 86       RES 0, (IY+02)         9074 </td <td>804E</td> <td>C3 38</td> <td>00</td> <td>JP 0038H</td> <td>JUMP TO MASKABLE INTERRUPT ROUTINE</td>	804E	C3 38	00	JP 0038H	JUMP TO MASKABLE INTERRUPT ROUTINE
9052       00       NDP       :DD NDTHING         9053       FB       EI         9054       C3 38 00       JP 0038H         9057       F3       CH0:       DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         9057       F3       CH0:       DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         9058       SE 41       LD A, "A"       :SIGNAL MAIN SCREEN         90550       JP 0038H       :PRINT       :SOGNAL MAIN SCREEN         9055       FB       EI       :DA, "A"         9056       D7       RST 10H       :PRINT         9056       ED 4D       RETI       :REPEAT FOR OTHER CHANNELS         9064       ED 4D       RET I       :REPEAT FOR OTHER CHANNELS         9065       FD CB 02 86       RES 0, (IY+02)       RST 10H         9067       F3       CH2:       DI         9068       ED 4D       RETI       :         9069       F3       CH2:       DI         9076       F3       CH2:       DI         9076       F3       CH2:       DI         9076       F3       CH3:       D         9077       F8       EI       EI      <	8051	F3	DMMY:	DI	
9053       FB       E1         9054       C3 3B 00       JP 003BH         9055       F3       CH0:       D1       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         9056       3E 41       LD A, "A"         9057       F3       CH0:       D1       :SIGNAL MAIN SCREEN         9058       FD CB 02 86       RES 0, (IY+02)       :SIGNAL MAIN SCREEN         9056       D7       RST 10H       :PRINT         9057       F8       E1         9058       F8       E1         9054       CD 02 86       RES 0, (IY+02)         9057       F8       E1         9062       F3       CH1:       D1         90645       F0 CB 02 86       RES 0, (IY+02)         90645       FD CB 02 86       RES 0, (IY+02)         90647       O7       RST 10H         90648       ED 4D       RETI         90649       D7       RST 10H         9078       F3       CH2:       DI         9079       E44       LD A, "C"         9070       F3       CH3:       DI         9079       F4 4       LD A, "D"         9079       F4 4       LD A, "D" </td <td>8052</td> <td>00</td> <td></td> <td>NOP</td> <td>:DO NOTHING</td>	8052	00		NOP	:DO NOTHING
9054       C3 38 00       JP 0038H         9057       F3       CH0:       DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         9058       3E 41       LD A, "A"       :SIGNAL MAIN SCREEN         9054       FD CB 02 86       RES 0, (IY+02)       :SIGNAL MAIN SCREEN         9055       FB       EI       :PRINT         9056       ED 4D       RETI       :REFURN FROM INTERRUPTS         9062       F3       CH1:       DI       :REPEAT FOR OTHER CHANNELS         9063       SE 42       LD A, "B"       :See 7.00000000000000000000000000000000000	8053	FB		EI	
8057       F3       CH0:       DI       :CHANNEL 1 INTERRUPT SERVICE ROUTINE         8058       3E       41       LD A, "A"       B058       SERVICE ROUTINE         8057       FD CB 02 86       RES 0, (IY+02)       :SIGNAL MAIN SCREEN         8056       D7       RST 10H       :PRINT         8057       FB       EI         8058       ED 4D       RET I       :RETURN FROM INTERRUPTS         8062       F3       CH1:       DI       :REPEAT FOR OTHER CHANNELS         8063       SE 42       LD A, "B"       B045       FD CB 02 86       RES 0, (IY+02)         80643       SE 42       LD A, "B"       B045       FD CB 02 86       RES 0, (IY+02)         80645       FD CB 02 86       RES 0, (IY+02)       RST 10H       B064       FB       EI         80646       FB       EI       B070       FD CB 02 86       RES 0, (IY+02)       RST 10H         8070       FD CB 02 86       RES 0, (IY+02)       RST 10H       B075       FB       EI         8070       FD CB 02 86       RES 0, (IY+02)       RST 10H       B075       FB       EI         8077       FD CB 02 86       RES 0, (IY+02)       RST 10H       B077       FD CB	8054	C3 38	00	JP 0038H	
9057       F3       CH0:       DI       : CHANNEL 1 INTERRUPT SERVICE ROUTINE         9058       3E       41       LD A, "A"       DA, "A"         9054       FD CB 02 86       RES 0, (IY+02) :SIGNAL MAIN SCREEN         9055       FB       EI       PRINT         9054       ED 40       RET 10H :PRINT         9055       FB       EI         9062       F3       CH1:       DI         90640       ED 40       RET I         9065       FD CB 02 86       RES 0, (IY+02)         90647       FD CB 02 86       RES 0, (IY+02)         9065       FD CB 02 86       RES 0, (IY+02)         9065       FD CB 02 86       RES 0, (IY+02)         90647       F3       CH2:       DI         90648       ED 40       RET I         90649       F3       CH2:       DI         90647       FD CB 02 86       RES 0, (IY+02)         9074       FD CB 02 86       RES 0, (IY+02)         9075       FB       EI         9076       ED 40       RETI         9077       3E 44       LD A, "D"         9078       F3       CH3:       DI         907					
0058       3E       41       LD A, "A"         0058       FD CB 02 86       RES 0, (IY+02) : SIGNAL MAIN SCREEN         0055       FD       EI         0056       ED 4D       RET I I RETURN FROM INTERRUPTS         0057       FB       EI         0058       ED 4D       RET I RETURN FROM INTERRUPTS         0062       F3       CH1:       DI         0063       SE 42       LD A, "B"         00643       SE 42       LD A, "B"         0065       FD CB 02 86       RES 0, (IY+02)         00647       FD       CH2:       DI         00648       FB       EI         00649       F7       CH2:       DI         00647       F0       CB 02 86       RES 0, (IY+02)         00648       ED 4D       RET I         00649       F3       CH2:       DI         00640       F3       CH2:       DI         00647       FD       CB 02 86       RES 0, (IY+02)         0070       F5       EI       ID A, "C"         0070       F3       CH3:       DI         0075       FB       EI       ID A, "D"         0077	8 <mark>057</mark>	F3	CH0:	DI	:CHANNEL 1 INTERRUPT SERVICE ROUTINE
005A       FD       CB       02       96       RES       0,(1Y+02)       :SIGNAL       MAIN       SURVEY         005E       D7       RST       10H       :PRINT         005F       FB       EI         0060       ED       4D       RETI       :RETURN       FROM INTERRUPTS         0063       SE       42       LD A, "B"       0045       FD       CB       02         00645       FD       CB       02       86       RES       0,(1Y+02)       0045         00645       FD       CB       02       86       RES       0,(1Y+02)         00647       FD       CB       02       86       RES       0,(1Y+02)         00648       FD       4D       RETI       00       86       1D       1         00647       FD       CB       02       86       RES       0,(1Y+02)       00         0070       FD       CB       02       86       RES       0,(1Y+02)       00       00       10       10         0070       FD       CB       02       86       RES       0,(1Y+02)       00       10       10         0075	8058	3E 41		LD A, "A"	
805E       D7       RST 10H       PRINT         805F       FB       EI         9060       ED       4D       RETI       :RETURN FROM INTERRUPTS         8062       F3       CH1:       DI       :REPEAT FOR OTHER CHANNELS         80643       SE 42       LD A,"B"       80645         80645       FD       CB 02 86       RES 0,(1Y+02)         80645       FD       CB 02 86       RES 0,(1Y+02)         80646       FB       EI         80647       F3       CH2:       DI         80648       FD       4D       RETI         80649       F3       CH2:       DI         80640       F3       CH2:       DI         80645       3E 43       LD A,"C"         80740       FD       RST 10H         80750       FB       EI         8076       ED 4D       RETI         8077       F3       CH3:         8078       F0 CB 02 86       RES 0,(1Y+02)         8079       SE 44       LD A,"D"         8079       FD CB 02 86       RES 0,(1Y+02)         8077       FD CB 02 86       RES 0,(1Y+02)         80777	805A	FD CB	02 86	RES 0, (IY+02	2) ISIGNAL MAIN SCREEN
805F       FB       E1         9060       ED       4D       RETI       IRETURN FROM INTERRUPTS         9062       F3       CH1:       DI       :REPEAT FOR OTHER CHANNELS         9063       SE       42       LD A, "B"         90643       SE 42       LD A, "B"         90645       FD CB 02 86       RES 0, (1Y+02)         90647       D7       RST 10H         90648       ED       4D       RETI         90649       D7       RST 10H         90649       D7       RST 10H         90649       ED       4D         90760       FD CB       02 86         9077       FB       EI         9078       F3       CH3:         9079       SE 44       LD A, "D"         9079       FD       CB         9076       FB       EI         9077       FS       02	805E	D7		RST 10H	PRINT
8062       F3       CH1:       DI       :REPEAT FOR OTHER CHANNELS         8063       3E       42       LD A, "B"         9065       FD CB 02 86       RES 0, (IY+02)         9069       D7       RST 10H         8064       FB       EI         9065       ED 4D       RET I         8064       FB       EI         9065       ED 4D       RET I         8064       F3       CH2:       DI         9065       ED 4D       RET I         8064       F3       CH2:       DI         9076       FD CB 02 86       RES 0, (IY+02)         9074       D7       RST 10H         9075       FB       EI         9076       ED 4D       RET I         9077       3E 44       LD A, "D"         9078       F3       CH3:       DI         9077       3E 44       LD A, "D"         9078       FD CB 02 86       REE 0, (IY+02)         9077       D7       RST 10H         9076       FB       EI         9077       PC B0 286       REE 0, (IY+02)         9078       F3       EI         90	805F	ED 4D		RETI	RETURN FROM INTERRUPTS
8062       F3       CH1:       DI       :REPEAT FOR OTHER CHANNELS         8063       3E       42       LD A, "B"         9065       FD CB 02 86       RES 0, (IY+02)         9069       D7       RST 10H         9064       FB       EI         9065       FD CB 02 86       RES 0, (IY+02)         9064       FB       EI         9065       ED 4D       RETI         9066       F3       CH2:       DI         9076       FD CB 02 96       RES 0, (IY+02)         9077       FB       EI         9076       ED 4D       RETI         9077       3E 44       LD A, "D"         9078       F3       CH3:       DI         9077       7       RES 0, (IY+02)         9078       FD CB 02 96       REE 0, (IY+02)         9077       7       RST 10H         9080       FB       EI         9081       ED 4D <t< td=""><td>0.000</td><td>20 40</td><td></td><td>·</td><td></td></t<>	0.000	20 40		·	
8063       3E       42       LD A, "B"         9063       FD CB 02 86       RES 0, (IY+02)         90645       FD CB 02 86       RES 0, (IY+02)         8064       FB       EI         9065       FD 4D       RETI         9066       SE 43       LD A, "C"         9066       3E 43       LD A, "C"         9070       FD CB 02 86       RES 0, (IY+02)         9074       FD CB 02 86       RES 0, (IY+02)         9075       FB       EI         9076       ED 4D       RETI         9077       F3       CH3:         9078       F3       CH3:         9079       F4       LD A, "D"         9079       F0 CB 02 86       RES 0((IY+02))         9079       F0 CB 02 86       RES 0((IY+02))         9079       F0 CB 02 86       RES 0((IY+02))         9079       F0 CB 02 86       RES 0(IY+02)         9070       F0 CB 02 86       RES 0(IY+02)         9070       F8       EI         9081       F0 4D       RETI	8062	F3	CH1:	DI	REPEAT FOR OTHER CHANNELS
0045       FD       CB       02       04       RES       0,(1Y+02)         0049       D7       RST       10H         0046       FB       EI         0046       FB       EI         0046       FD       RETI         0046       FD       4D         0047       FD       CB         0046       SE       43         0047       FD       CB         0070       FD       CB         0074       F7       RST         0075       FB       EI         0076       ED       4D         0077       F3       CH3:         0078       F3       CH3:         0079       SE       44         LD       A, "D"         0078       FD       CB         0079       FD       RE	8063	3E 42		LD A, "B"	
0069       D7       RST 10H         0069       FB       EI         0068       ED 4D       RETI         0068       ED 4D       RETI         0069       F3       CH2:       DI         0067       FD CB 02 86       RES 0, (IY+02)         0074       D7       RST 10H         0075       FB       EI         0076       ED 4D       RETI         0077       F3       CH3:         0078       F3       CH3:         0079       FD CB 02 86       RES 0, (IY+02)         0075       FD       RE 20, (IY+02)         0076       F3       CH3:         0077       T0       RETI         0078       FD 202 86       RES 0, (IY+02)         0075       D7       RST 10H         0080       FB       EI         0081       ED 4D       RETI	8065	FD CB	Ø2 86	RES 0, (IY+02	2)
B06A       FB       E1         B06B       ED       4D       RETI         806D       F3       CH2:       DI         806E       3E       43       LD A, "C"         8076       FD       CB       02       86         8077       FD       CB       02       86         8077       FD       CB       02       86         8077       FD       CB       7       RST         8076       ED       4D       RETI         8077       SE       44       LD A, "D"         8078       F3       CH3:       DI         8079       SE       44       LD A, "D"         8079       FD       CB       02       86         8079       FB       EI       80       80         8079       FB       E	8069	D7		KST 10H	
806D       F3       CH2:       DI         806E       3E       43       LD A, "C"         8076       FD CB 02 86       RES 0, (IY+02)         8074       D7       RST 10H         8075       FB       EI         8076       ED 4D       RETI         8077       3E 44       LD A, "D"         8078       F3       CH3:         8079       3E 44       LD A, "D"         8079       FD CB 02 86       RES 0, (IY+02)         8077       D7       RST 10H         8078       F3       CH3:         8079       SE 44       LD A, "D"         8079       FD CB 02 86       RES 0, (IY+02)         8077       D7       RST 10H         80800       FB       EI         80810       FB       EI         80811       ED 4D       RETI	806A			RETI	
806D       F3       CH2:       DI         806E       3E       43       LD A, "C"         8070       FD CB 02       86       RES 0, (IY+02)         8074       D7       RST 10H         8075       FB       EI         8076       ED 4D       RETI         8077       3E       A4         8077       3E       44         8077       D2       86         8077       D2       86         8077       B0       286         8077       TD       RES 0, (IY+02)         8077       B0       286         8077       B7       DA, "D"         8078       FJ       CH3:         8079       FD CB 02       86         8079       FD CB 02       86         8079       FD CB 02       86         8071       D7       RST 10H         8080       FB       EI         8081       ED 4D       RETI					
8076       FD CB 02 86       RES 0, (IY+02)         8071       D7       RST 10H         8075       FB       EI         8076       ED 4D       RETI         8077       3E 44       LD A, "D"         8078       F3       CH3:         8079       3E 44       LD A, "D"         8079       FD CB 02 86       RES 0, (IY+02)         8077       D7       RST 10H         8078       FD       CH3:         8079       FD CB 02 86       RES 0, (IY+02)         8077       D7       RST 10H         80800       FB       EI         8081       ED 4D       RETI	806D	F3	CH2:	DI	
8074       D7       RST 10H         8075       FB       EI         8074       ED 4D       RETI         8077       SE 44       LD A, "D"         8078       FD CB 02 86       RES 0, (IY+02)         8077       D7       RST 10H         8078       FD CB 02 86       RES 0, (IY+02)         8079       FD CB 02 86       RES 10H         8080       FB       EI         8081       ED 4D       RETI	806L	SE 45	02 94	RES 0 (IV+0.5	2)
8074       D7       Horizon         8075       FB       EI         8076       ED 4D       RETI         80779       3E 44       LD A, "D"         8078       FD CB 02 86       RES 0, (IY+02)         8077       D7       RST 10H         8080       FB       EI         8081       ED 4D       RETI	00/0	10 LB	02 00	RES 0, TITUZ	.,
8076         ED 4D         RETI           8078         F3         CH3:         DI           8079         SE 44         LD A, "D"           8079         FD CB 02 86         RES 0, (IY+02)           8077         FD CB 02 86         RES 0, (IY+02)           8077         D7         RST 10H           8080         FB         EI           8081         ED 4D         RETI	8075	FB		EI	
8078 F3 CH3: DI 8079 3E 44 LD A,"D" 8079 FD CB 02 86 RES 0,(IY+02) 807F D7 RST 10H 8080 FB EI 8081 ED 4D RETI	8076	ED 4D		RETI	
B079       F.3       CH3:       DI         8079       3E       44       LD A, "D"         8079       FD CB 02       86       RES 0, (IY+02)         8075       D7       RES 10H         8080       FB       EI         8081       ED 4D       RETI					
B079         SE         44         LD         A, "D"           8079         FD         CB         02         86         RES         04, (1Y+02)           8075         D7         RST         10H         8080         FB         EI           8080         FB         EI         8081         ED         4D         RETI	8078	F3	CH3:	DI A REEL	
807F D7 RST 10H 8080 FB EI 8081 ED 4D RETI	80/9	SE 44	02 04	DEC A (TVAA	
8080 FB EI 8081 ED 4D RETI	9075	D7	WZ 80	RST 10H	
8081 ED 4D RETI	80,80	EB.		EI	
	8081	ED 4D		RETI	

DRG BRARH

causes the Spectrum to return to the "Maskable Interrupt Routine" and it continues operation from there, unaware of the mode change.

#### Demo. program

The demonstration assembly language program shown in Fig. 3 allows users to test the interrupt controller. The program may be entered using an assembler or with the aid of our own Machine Code Loader (provided in the Update package). The demonstration program will print A, B, C, or D on the screen, depending on which one of the CLK/TRIG pins of the CTC is taken to +5V.

Next month: We shall be providing details of a simple mother-board for the

Spectrum. This novel arrangement allows users to extend the Spectrum's bus system so that a number of external modules can be simultaneously connected. We shall also be reviewing the quaintly named "Pick-POKE-It" software package available from MGT.

In the meantime, please drop me a line enclosing a large (250mm×300mm) adequately (i.e. 42p UK postage) stamped addressed envelope if you would like to receive a copy of our On Spec Update. Please note that I can no longer provide individual replies to queries but instead will do my best to provide answers through On Spec or through the Update. Mike Tooley, Faculty of Technology, Brooklands Technical College, Heath Road, Weybridge, Surrey, KT13 8TT.



# CABLE & SATELLITE '89

Where the increased importance with which the industry now views itself, the annual broadcasting and communications industry show has transplanted itself from Wembley, where it started in 1987, to Olympia. The new and more spacious venue accommodated more than 120 exhibitors for *Cable & Satellite* '89 (March 16-19).

All aspects of the industry were representedprogramme makers, satellite operators, receiver manufacturers, signal distributors, aerial installers and video service companies. More than two dozen new products, mostly satellite receivers, were either launched or previewed.

#### FERGUSON

Ferguson exhibited its SAP1 Astra receiver with 64cm dish. The package will sell for around  $\pm 300$  (or  $\pm 330$  with the larger 80cm dish needed to cope with the weaker signals reaching the north of the Kingdom). The 32-channel receiver, equipped with an infra-red remote control, comes with Astra's 16 channels already programmed in.

Ferguson also announced that it will be supplying the Videocrypt decoder for use with Sky Television's subscription channels. Videocrypt is a signal scrambling system developed by Ferguson's parent company, Thomson Consumer Electronics.

The decoder will be used in conjunction with a Smart Card, a plastic card with a microchip embedded in it, developed by News Datacom a subsidiary of News International. Inserting the card in the decoder authorises the decoder to unscramble Sky Movies and the Disney Channel. The decoder can be used on a simple subscription basis or pay-per-view or pay-per-time. It will be available from this autumn for around £80.

Ferguson's BSB (British Satellite Broadcasting) reception package, the SBP1, will be available from September for about £300. It consists of a 10-programme remote control receiver with digital stereo sound decoder and built-in Eurocypher decoder, and of course the famous (or notorious) "Squarial" flat plate antenna.

#### NOKIA

Nokia Consumer Electronics showed its new range of satellite products, including the 5902 Astra compatible stereo receiver. This is available in the UK as the ITT- Nokia Sat 1100 and the Salora 5902. Its 48-channel capacity will cater for the two additional 16-channel Astra satellites planned for launch in the next few years. The current Astra satellite's 16 channels are pre-programmed into the receiver.

Receiving dishes are available in a range of sizes, from 55cm to 85cm, to meet different requirements in different parts of the country. The Sat 1100 with a 55cm dish will sell for about £379. In addition, ITT-Nokia is offering TV sets with built-in satellite receivers. The 66cm 7190 and 59cm 6390 also have a built-in FM radio, radio/TV remote control and 60 station presets.

#### **ECHOSPHERE**

American manufacturer Echosphere International unveiled two new receivers for Astra-the Echostar SR-1 and SR-2. Both are pre-programmed with the Astra frequencies. The SR-1 has a 10-keypad remote control, onscreen graphics, mono sound and video fine tuning. The SR-2 has full-featured stereo sound, parent lock and a 24key remote control.

The European market in satellite television products has such a growth potential that the Colorado-based company has now opened a new office in Almelo in The Netherlands.

#### SENTRA

Sentra Electronics describes itself as "a determined newcomer to the satellite scene". Its SX1000 Astra receiver, launched at Cable & Satellite '89, has 16 channel presets, infra-red remote control and a 60cm dish with a wall mounting kit—and all at the competitive price of £259.

#### THE SATELLITE OPERATORS

Astra's operator, Société Européenne des Satellites, occupied a modest stand beside British Telecom. The two companies have a close business relationship too. SES took the plunge and launched Europe's first privately-owned television satellite in December 1988. After a perfect launch by Ariane rocket, the satellite performed faultlessly and went operational in February this year. SES has relentlessly and aggressively promoted Astra to programme makers, hardware manufacturers and the High Street dealers who will sell the receivers.

BSB's stand, twice the size of Astra's, reflected the more difficult circumstances in which BSB finds itself. Despite the small numbers of viewers with satellite dishes and receivers, Astra seems to be well on its way to commercial success. BSB is coming along after Astra, with a new broadcasting system, requiring a different type of receiver, and with a satellite in a different position in the sky, requiring a second dish (or a single motorised dish). There must be a question-mark over the viewers' willingness to shell out for what amounts to a second satellite system if they are reasonably satisfied with Astra's output. There have also been technical problems with the flat receptor, the Squarial, that BSB has made so much of; no one has yet seen a working Squarial!

BSB demonstrated a comparison of PAL and D-MAC. PAL is the broadcasting system used by all terrestrial TV stations and until the end of the year all the satellite stations too. D-MAC is a new broadcasting system that will be used by BSB when its three channels begin broadcasting this autumn (with two additional channels due to start in January/February of next year). As soon as D-MAC receivers are available in sufficient quantities, some of Astra's channels will change over to it too.

Sky Television has committed itself to sticking with PAL. In BSB's comparative demonstration, the D-MAC pictures were undoubtedly clearer, sharper and interference-free compared to the PAL picture of the same image. Whether or not the same striking improvement in picture quality will be obtained in the home using a PAL television set has been the subject of debate, sometimes acrimonious, between interested parties.

#### THE PROGRAMME MAKERS

However appealing the technology is, it isn't worth a row of beans if the programmes beamed down from the satellites aren't worth watching. The programme makers and providers were represented in strength to convince showgoers of their value. The ground floor of Olympia's National Hall was dominated by the Sky Television and BSB stands.

Ferguson's SBP1 receiver and the Squarial flat plate antenna should be available in time for broadcasts from the BSB satellite in September.





## One of the new Astra receivers from the American company, Echosphere—the Echostar SR-1.

Sky Television is now offering six channels from Astra-Sky Channel, Sky News, Sky Movies, Eurosport, Sky Arts and The Disney Channel. All channels can be seen free at the moment, but in the autumn, Sky Movies and The Disney Channel will be scrambled. To unscramble them, a decoder will be necessary and a subscription of £12 per month will be charged for access to the two channels. At Cable & Satellite '89, The Children's Channel

At Cable & Satellite '89, The Children's Channel announced that it would begin broadcasts from Astra on April 1st. The channel, which started in 1984, is currently available to over one million cable TV viewers in Europe and Scandinavia. These low-power broadcasts via Intelsat will continue alongside the direct-to-home Astra service.

MTV (Music Television) was born in the USA in 1981. There, it now reaches 40 million homes. MTV Europe was launched in 1987 via a low power Intelsat satellite to a cable TV audience of 1.6 million households. At Cable & Satellite '89, they announced that by January this year that had grown to five million.

In February, MTV Europe began broadcasting by Astra-24-hour music, pop-star interviews, news, video releases and programmes covering contemporary music tastes from Hip-Hop to Heavy Metal. The channel is a big hit with the youth audience and with the advertisers who want to sell to them.

During the summer, W. H. Smith Television will begin broadcasting two channels in PAL via Astra-Lifestyle (Europe's first women's channel) and Screensport. But at the end of the year, they plan to abandon PAL and switch over to D-MAC. Then, both channels will be encrypted.

BSB will kick off with three channels—The Movie Channel, Galaxy (family entertainment) and Now (news and current affairs). In January or February 1990, two more will be added—The Sports Channel and The Power Station (pop and style). All BSB's channels will be encrypted and The Movie Channel will charge an additional subscription.

#### REFORM

The Government seems determined to reform British broadcasting for good or ill. In the more competitive environment that will inevitably come, there will be more broadcasters, more cable television companies, more satellite channels... more of everything televisual. But expertise in broadcasting doesn't come overnight.

To meet the needs of a de-regulated Britain, Philips Consumer Electronics has set up a new Videocommunications Group to offer a range of professional cable and satellite systems, equipment and training. With 30 years in the broadcast television business, Philips is well-placed to provide this sort of service to the UK cable and satellite industry.

Cable & Satellite '89 provided an interesting window on the satellite television industry. The number of manufacturers introducing products and providing services gives the industry an air of solidity and credibility. A visit provided reassuring evidence that there is more to satellite TV than Rupert Murdoch's Sky Television and Alan Sugar's Amstrad, despite the national press's fascination with these two personalities and their companies. This relatively new branch of the entertainment industry appears to be growing and gathering momentum. TEACH-IN SERIES

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# Millimetre-wave Multichannel Multipoint Video Distribution Service

**By George Hylton** 

N THE little Suffolk township of Saxmundham, chimney stacks are sprouting poles carrying 15centimetre aerial dishes. These are directed, not towards some space satellite, but to the roof of the local telephone exchange. There, a number of tiny transmitters relay television programmes—one transmitter for each channel.

This is an experiment in local TV distribution by microwave relays. It's called a Millimetre-wave Multichannel Multipoint Video Distribution Service, or M<sup>3</sup>VDS for short. This inelegant title masks some neat engin/sering by British Telecom, who are installing the Saxmundham project as a demonstration and field test of a TV distribution system which could carry 25 channels, is relatively cheap and is ideal for small towns.

The idea is to collect TV programmes from a number of sources (satellites, local BBC and commercial stations, cable links, video recorders, local studios), convert them to micro-wave on about 30GHz and distribute them by means of low-power (100mW) transmitters with a range of a mile or two. The customer would get high-quality pictures and Telecom would rent the distribution facilities.

## FREQUENCY

The novel feature of this system is the choice of frequency. Most proposals for microwave TV systems (and some working systems abroad) use frequencies of a few GHz (wavelengths of a few centimetres). Telecom's choice of 30GHz (wavelength 10 millimetres) is based on the belief that only by going to this band will it be possible to find enough channel space for a national system. The essential engineering need is to avoid co-channel interference from transmitters in the next town. With (ultimately) hundreds of local systems in simultaneous use this means keeping frequencies used in an area well separated. Only at 30GHz and above is there enough available bandwidth to achieve adequate frequency separation and so avoid co-channel interference.

## COST

Millimetric radio is expensive. The higher the frequency the higher the cost. Telecom research laboratories (at Martlesham near lpswich) have made great strides towards getting the costs down. They have designed integrated circuits (using gallium arsenide as a substrate) which carry out the functions required to down-convert the millimetric frequencies to what can be handled by a domestic TV fitted with a translator for transforming the f.m. used for distribution into a conventional TV signal. (Translators will be mass-produced for satellite TV reception so should be inexpensive.)

At present, Telecom is using several GaAs i.c.s at each receiving point, but it is hoped to coalesce these into just one or two. The estimated cost of a domestic user's extra equipment is about the same as for satellite TV reception, but of course the potential number of channels is larger.



*M*<sup>3</sup>VDS. A test system has been established by British Telecom Research Laboratories to demonstrate feasibility, practicability and the readiness of economic MMWIC (monolithic millimeter-wave integrated circuit) technology. Circuit Card Project

# SIGNAL INJECTOR

# **ROBERT PENFOLD**

An easy to build, inexpensive item of test gear that could prove itself invaluable to any reader.

OST electronic testing takes the form of initial checks to narrow down the area of the fault to one particular stage, followed by more detailed checks to determine exactly which component is faulty. A signal injector is a very useful device that is primarily used for the narrowing down process, but which can sometimes be used for more precise checks. Basically all it does is to generate an audio frequency signal that can be coupled into various stages of audio frequency equipment.

Most signal injectors, including the present design, produce strong harmonics (multiples) of the fundamental audio frequency. These extend well into the radio frequency spectrum, and enable the unit to be used for checks on some kinds of radio receiver (including long and medium wave broadcast sets).

## FAULT-FINDING BASICS

A signal injector is used to test a piece of equipment that has a series of amplifiers or other signal processing stages (tone controls, etc.) The general idea is to inject the signal at the output first, and then gradually work forwards towards the input, injecting the signal at strategic points.

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Each test should produce an output from the loudspeaker, headphones, or whatever is being used to monitor the output signal. However, if there is a fault in the unit, at some point the signal will be injected and no signal (or perhaps an inadequate signal) will be forthcoming from the loudspeaker. The fault then lies somewhere in the region of this last test and the previous one.

In fact you can work the other way round, starting at the input and working towards the output of the unit under test. It is then a matter of injecting the signal at various points until a proper signal is obtained from the loudspeaker. Again, the fault will lie somewhere in the region of the ultimate and penultimate test points.

There is a body of opinion in favour of making the initial test point somewhere in the middle of the signal chain, with subsequent checks being ahead of or after this point, depending on the result of this initial check. Whichever of these three methods you adopt, or if you adopt a random approach to selecting the test points, the basic idea is still to find successful and unsuccessful test points close together in the signal chain.

Fig. 1. Typical circuit which can be tested with the Signal Injector.



## EXAMPLE

A circuit of the type that can be checked using a signal injector is shown in Fig. 1. This is a common emitter amplifier based on TR1 and a two stage highpass filter having IC1 as the buffer amplifier. Feeding even a weak signal to the input should produce a strong output due to the high gain of the amplifier. Assuming that this test fails to give a suitable output and that the circuit is faulty, the next injection point would be at the base of TR1. If this gives a proper output from the circuit, then C1 is clearly not coupling the signal through to TR1, and has presumably gone open circuit (or perhaps it is connected via a "dry" joint)

CIRCUIT CARD F

perhaps it is connected via a "dry" joint). If this check is successful, the next test point is at the collector of TR1. When using a signal injector you need to bear in mind that connecting the output of the injector to the output of a stage in the test circuit is not necessarily a good idea. It could conceivably result in damage to the injector or the circuit being tested. This is unlikely, but it is quite probable that the output will heavily load the output of the injector so that only a low output level is obtained.

In this case the output impedance of TR1 is relatively high, and the injector should have no difficulty in inserting a fairly high signal level here. It should also have no difficulty in injecting a signal into the subsequent test points at the junctions of C3/ C4, C4/C5, and C5/R8.

The output of IC1 is a different proposition, and operational amplifiers (and most other integrated circuit amplifiers for that matter) have a very low output impedance. It would not be advisable to inject a signal into the output of IC1. A much better approach would be to disconnect the positive terminal of C6 from the circuit board, and to inject the signal into this lead.

If a circuit has a lot of stages with low output impedances it might be better to adopt an alternative method of fault finding, such as using a signal tracer (as described elsewhere in this issue).

## CONCLUSIONS

With any electronic testing you should try not to jump to conclusions. There is a very big difference between jumping to conclusions and reaching reasoned conclusions. For example, if applying a signal to the right hand end of C5 produces an output signal, but injecting the signal at the left hand end does not, a fault in C5 is the obvious conclusion. There is another possibility

Everyday Electronics, June 1989



Fig. 2. Circuit of the Signal Injector.

though, which is that R7 has gone short circuit, and is therefore short circuiting this test point to the output of IC1.

Signal injecting will often only indicate the general area of the fault, and some further testing may be needed in order to locate it precisely. In this example one end of R7 could be disconnected. If this restores the output signal, then it is R7 that is faulty. If not, then the defective component is indeed C5. Alternatively, a multimeter could be used to check the resistance of R7.

## THE CIRCUIT

The circuit diagram of the Signal Injector is shown in Fig. 2. This is little more than a 555 timer integrated circuit used in the standard astable configuration. There is some advantage in using the TLC555CP low power version of the 555 as this gives somewhat lower current consumption and stronger radio frequency harmonics on the output. The circuit will work quite well with the standard 555 though. Timing com-



LUIV	IPUIVEIV I S
<b>Resistors</b> R1 R2 0.25 watt 5	4k7 100k % carbon
Potention VR1	neter 1k lin.
Capacitor: C1, C4	s 100µ axial elect. 10V (2 off)
C2	4n7 polyester 7.5mm
C3 C5	150p ceramic plate 47 $\mu$ axial elect. 10V
Semicond	luctor
IC1	NE555P or TL555P timer
S1, S2	s.p.s.t. sub-min
B1	9 volt (PP3 size)
SK1	battery 3.5mm jack socket (see text)
Battery	connector; 8 pin d.i.l.
i.c. holder	; case; Free EE Circuit
knob: wire	e; etc.
Approx, cos	t <b>CO</b> plus
Guidance or	

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ponents R1, R2, and C2 give a roughly squarewave output at a middle audio frequency of approximately 1kHz.

With S1 in the "RF" position the output signal is coupled via C3 to the output level potentiometer (VR1). The small value of C3 results in the audio frequency content on the output signal being severely attenuated. In theory the audio frequency content of the signal should be irrelevant when the signal is injected into the r.f. or i.f. stages of a radio receiver. In practice a strong audio frequency signal can break through to the output and give misleading results.

When S1 is closed, C4 is switched in parallel with C3, and it then provides full coupling of the output signal through to VR1. C5 provides d.c. blocking at the output so that connecting the unit to a test circuit will not upset the biasing of that circuit. The unit can provide quite a strong output signal, and it is suitable for testing loudspeakers and headphones.

## CONSTRUCTION

Details of the circuit board and wiring are provided in Fig. 3. Please read the Using Your Circuit Cards item before going ahead with circuit assembly. Construction of the board is fairly straightforward, but be careful to get the orientations of C1, C4, and IC1 correct. There is a crossover connection on the underside of the board between IC1's two rows of pins. Put in one of these wires and then cover it over with insulation tape at the appropriate place so that it is insulated from the wire that is taken over the top of it.

In places there are several wires running close together. It is important to keep these wires quite taut so that there is no risk of them accidentally short circuiting to one another.



The components should fit into virtually any small plastic case. The controls and SK1 are mounted on the front panel, with the circuit board mounted on the base panel. I used a 3.5 millimetre jack socket for SK1, but virtually any two-way socket can be used here. Two single-way sockets such as 2 millimetre types are also suitable. They might be less fiddly to wire up.

The board can be mounted on standoffs, or it can be fixed in place using M3 or 6BA bolts. If it is bolted in place you must include short spacers over the mounting bolts. Without these, the components will

Circuit Card Project

be forced from the circuit board as the mounting nuts are tightened!

The point to point wiring is not too difficult. The connections to the board are made via the Easiwire plugs which are "crimped" onto the leads. The other ends of the leads are bound to the component tags using the Easiwire "pen". Note that C5 is mounted off-board, and is wired direct to SK1 and VR1. Take care to connect it the right way round as it is a polarised type.

#### TESTING

With the unit switched on and connected

to a loudspeaker, headphones, or earpiece you should hear the audio output tone. Adjustment of VR1 should control the volume. With S1 switched to the "RF" position the tone will probably still be audible. However, it should sound very "thin" with most of the fundamental and lower harmonics being filtered out. It may be barely audible if the output is fed to a low impedance loudspeaker.

A more useful check is to connect a set of test leads to the output of the unit, and to place the non-earthy lead very close to a radio receiver tuned to the long or medium wavebands. The radio should pick up the harmonics and produce the audio tone regardless of the setting of the tuning control (except that strong transmissions might operate the receiver's automatic gain control circuit and leave the tone barely audible).

## USE

In use the earthy test lead connects to the chassis of the piece of equipment being checked, and the other test lead is applied to the test points. It is best to keep VR1 well backed off and to only advance it when applying a signal to a part of a circuit that really requires a high signal level. Otherwise there is a risk of the signal breaking through to the output and giving misleading results.

One final but important point is that only those who are suitably experienced should service mains powered equipment. Using this device to test mains powered equipment that does not incorporate an isolation transformer could prove lethal.

# SIGNAL TRACER



# **ROBERT PENFOLD**

The last of our Easiwire Free Circuit Card projects, but not the least. This useful device will find many applications.

SIGNAL tracer is used in much the same way as a signal injector (as described elsewhere in this issue), but I suppose could be regarded as the inverse of an injector. Rather than generating a signal, it takes a signal from the test circuit, amplifies it, and feeds it to a loudspeaker. In other words, it is just a reasonably sensitive audio power amplifier and a loudspeaker. It could be regarded as the electronic equivalent of a stethoscope.

Apart from use as a signal tracer, this unit is one of those general purpose items of equipment that no electronics hobbyist should be without. A device of this type proves to be indispensible on numerous occasions when testing projects or just dabbling with circuits. It can save hours of time being held up by what turns out to be a simple problem with a broken socket, short circuited plug, etc., as well as sorting out more difficult problems.

#### TRACING TECHNIQUÉ

As when using a signal injector, the basic idea of signal tracing is to find a break in the signal chain. It is used for testing the same types of equipment, which means linear circuits having a series of amplifiers or other signal processing stages.

If a signal tracer was used to check the test circuit in the *Signal Injector* article (Fig. 1 of that article), the first requirement would be that a suitable signal should be applied to the input of the circuit. A signal tracer must have a signal to trace! This signal could be provided by a signal injector or generator, but where possible I prefer to



#### Fig. 1. Circuit diagram of the Signal Tracer.

use the normal signal source for equipment. One advantage is that this automatically provides the equipment with an input of the correct amplitude. Also, you will not always be searching for a complete break in the signal path. The problem might be one of distortion, and any distortion will probably be more noticeable on a speech or music signal than on a simple test signal.

In common with signal injection testing, you can start at the input, the output, or in the middle. For this example we will assume that the starting in the middle technique is to be adopted. An acceptable first test point would therefore be at the junction of C3 and C4 (Fig. 1, Signal Injector article). If a suitable signal is detected here, then the fault lies at some later point in the circuit. If no signal is present at the test point, then the fault is here, or at some earlier point in the circuit.

This test point is at the output of a high gain amplifier, and it would be reasonable to expect a reasonably high signal level here. If the volume control of the amplifier needs to be advanced more than a few degrees from its minimum setting, this would tend to indicate a fault in TR1 and its associated components.

For the sake of this example we will assume that the signal is either not detected, or is at a grossly inadequate level. We must therefore try earlier points in the signal path in an attempt to find one that does provide a signal. If (say) the signal is present at both sides of C1, but appears at the collector of TR1 and in subsequent stages at only about the same level, this would suggest that TR1 is failing to amplify the signal properly and is faulty.

The problem could be due to a fault in one of the other components in this part of the circuit though. C2 going open circuit or not being connected properly would result in low gain through TR1. It is easy to test for this using the signal tracer, and it is just a matter of checking for a signal at the emitter of TR1. The decoupling effect of C2 should result in no significant signal here.

If the signal level at TR1's emitter is much the same as the signal level at its base, this would suggest that C2 is not having any effect on the circuit. A signal tracer is just as useful for detecting signals where there should be none present, as it is for finding signals that are "absent without leave"!

#### CIRCUIT

There are a number of small audio power

amplifier integrated circuits which could be used as the basis for a unit of this type. I chose the TBA820M as it is readily available, has quite a good specification, and is well suited to battery operation. It will work on supply voltages as low as three volts, and it has a low quiescent current consumption of only about four milliamps. The full circuit diagram for the Signal Tracer appears in Fig. 1.

The input signal is coupled via d.c. blocking capacitor C2 to volume control VR1. From here the signal is coupled straight into the non-inverting input of IC1. No d.c. blocking capacitor is needed here. In fact VR1 acts as the input bias resistor for IC1, and so it is essential that no input coupling capacitor should be used.

Capacitor C5 provides a small amount of high frequency attenuation which aids the stability of the circuit. This does not significantly affect the unit's audio frequency response. C4 is a decoupling capacitor that helps to avoid problems with instability due to feedback through the supply rails (as does the main supply decoupling capacitor, C1)

#### GAIN

The voltage gain of the circuit is controlled by an internal feedback resistor, and discrete feedback components R1 and C3. The specified value for R1 gives a voltage gain approaching 200 times, and an input of only about ten millivolts r.m.s. will drive the amplifier to full output power.

In this application there is an advantage in high sensitivity and high input impedance as this enables very weak signals to be detected. The gain and input impedance could be made higher by raising the value of VR1 and reducing the value of R1. The specified values probably represent the best compromise for these components, since boosting the gain and input impedance would risk instability due to stray feedback, and would reduce the output quality. The sensitivity is quite good any-Remember that 10 millivolts is way. needed for full output power, but inputs of well under a millivolt will produce a clearly audible output.

Capacitor C6 couples the output signal to a high impedance loudspeaker (LS1). The maximum output power into a high impedance speaker is not very high, and is less than 100 milliwatts r.m.s. This should be sufficient for the present application, but an eight ohm impedance loudspeaker can be used if a higher output power is

CUIV	PUNENIS
Resistors R1 39 R2 1 0.25 watt 5	% carbon see page 398
Potention VR1 10	leter D0klog.carbon
Capacitors C1, C3 10 (2	s )0μ radial elect. 10V coff) 20 s as busines 7.5 s as
C2 4 pi C4 4 C5 18 C6 10	tch $J_{\mu}$ axial elect. 10V 30p ceramic plate $20\mu$ axial elect. 10V
C7 22 pi Semicond	20n polyester 7.5mm tch
Miscellane	eous
SK1 Si (s	andard jack socket ee text)
LST 64 lo 50	udspeaker, about 0-80mm diameter
B1 9 S1 s. sv	volt battery (PP3 size) p.s.t. sub-min toggle witch
Battery i.c. holder; <i>Card</i> or E knob; wire	connector; 8 pin d.i.l. case; Free <i>EE Circuit</i> asiwire board; control e; etc.
Approx. cos	t <b>f6</b> plus

O MADO ALEALTO

required (at the expense of current consumption and therefore battery life). R2 and C7 are needed in order to prevent high frequency instability.

Although the quiescent current consumption of the circuit is typically only four milliamps, the current drain rises significantly at high volume levels. Using a high impedance loudspeaker the average current consumption is still only likely to be about 10 milliamps or so, and a PP3 size battery should be adequate. I would recommend a higher capacity type (such as six HP7 size cells in a holder) if an eight ohm impedance loudspeaker is used.

## **CONSTRUCTION**

The circuit and wiring are shown in Fig. 2—please read the Using Your Circuit Cards piece before going ahead with construction. Assembly of the board presents few problems, but be careful to get IC1 and the electrolytic capacitors fitted to the board the right way round.

There are no cross-overs in the underside wiring, but the wire that goes between IC1's two rows of pins comes quite close to one of the pins to which it does not (and must not) connect. It is advisable to curve it slightly so that it is kept well clear of this pin (pin 6). It is not necessary to completely cover the circuit card with Easiwire doublesided adhesive backing material—a small piece between IC1's rows of pins should suffice. In fact a small piece of insulation tape placed over this wire is probably the easiest way of holding it in position.



Fig. 2. Construction of the Signal Tracer.



Note that the negative supply rail cannot be handled by a single length of wire. However, there is no difficulty in fitting the main wire and then adding the branch wire which carries the earth connection to LS1.

The unit should fit into most small plastic cases, but be careful to choose one that will provide sufficient space for the loudspeaker. A grille for the loudspeaker must be made in the case. The easiest way of doing this is to drill a matrix of small holes (about five millimetres in diameter will suffice). Take care to position the holes accurately, as it easy to make a slightly sloppy job of this.

Miniature loudspeakers rarely have provision for screw fixing. Consequently, it will almost certainly have to be glued in place using a good quality general purpose adhesive. Avoid smearing any adhesive onto the diaphragm as this could seriously impair the audio output.

Try to arrange the layout so that the wiring to VR1 and SK1 is not intermingled with that to the loudspeaker. This would encourage stray feedback and instability. The input wiring should be kept as short as possible in order to discourage stray feedback and the pick up of mains "hum" or other interference. I used a standard jack for SK1, but any audio connector should suffice. You could even just drill an entrance hole for the test leads in the case and connect them direct to the circuit board.

## IN USE

Ideally the test lead should be a screened type, such as those used with oscilloscopes. Ready-made test leads of this type can be quite expensive though, and two ordinary (multimeter type) test leads are a lower cost solution. With these there will inevitably be a certain amount of background "hum" and other pick up, especially when the leads are not connected to a signal source, or are connected to a weak high source impedance signal. With a little ingenuity you will probably be able to make up your own screened test leads.

Connect the earth test lead to the chassis of the equipment under test, and connect the other lead to the various test points. Unless you have the necessary experience and are sure you know what you are doing, do not try fault finding on mains powered equipment. To do so is potentially fatal.

Although the unit cannot be used to measure signal levels and calculate voltage gains, you will probably find that after some experience using it you will be able to roughly gauge whether or not test circuits have the correct signal levels. Remember that the more VR1 has to be backed off in order to prevent overloading, the stronger the signal at the test point.

# USING YOUR CIRCUIT CARDS HE CIRCUIT CARDS attached to the front cover of this issue

have been specially designed for easy, solderless construction of projects using the BICC-Vero Easiwire system.

## HOLE PUNCHING

Carefully remove your Circuit Cards from the cover taking care not to damage them, then cut them in half along the centre line. Next, using the pointed end of the Easiwire unwrap tool, make holes through the board for the component leads. This is best done by placing the Circuit Card, component side up on a piece of thick cardboard or a pad of scrap paper then push the point through the Circuit Card at all the points marked with a "●".

Once all the holes are made you can use the Circuit Card, as described in the special articles in this issue, to build your projects. If you do not have a BICC-Vero Easiwire kit see the special offer on page 441.



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Everyday Electronics, June 1989

Constructional Project

# SPECTRUM SPECTRUM SPECTRUM SPECTRUM

# KEN TAYLOR

Construct your own add on EPROM Programmer for your Spectrum. Will program 2716 to 27128's with the exception of 2732.

s THE uses for EPROMs increase and the prices come down many serious computer users find a justification for a programmer. But the commercial ones aren't cheap so why not construct an add-on unit for your Spectrum? Before we start what do you know of the currently available UV eraseable EPROMs? Perhaps a run through the main features would be useful.

## HISTORY

In the dim past, when most memories required three voltages, the 2708 was born. It's only claim to fame was its first two numbers-27. Since then there have been a profusion of EPROMs almost all starting with these two numbers-with the rest of the type number providing information on the size and type of the EPROM.

The next memory to appear was the universally popular 2716. This was double the capacity, having 2 kilobytes of 8 bit words. Now you see what I mean about the ending numbers indicating the size. The sequence has gone on until the present with 27512 and the more recent CMOS type 27C1024. Although this last EPROM is organised as a 64k  $\times$  16 bit memory so it does start a change in the rules.

The next bit of standardization was the pin-out. Fig. 1. illustrates the range from 2716 to 27128 and you can see that although they have changed from 24 to 28 pins the output signals on the pins have remained almost unchanged. This amazing consistency of both type identification and pinout has applied to almost all of the many manufacturers. However before we start to celebrate an electronic "first" there is one feature which has seen changes. This is the programming system.

## **PROGRAMMING SYSTEM**

An EPROM memory unit is a transistor device with an isolated control gate. During programming the gate acquires electrons attracted by the "high" V<sub>pp</sub> voltage and this inhibits the output when the cell is being read. Thus the programmed cell indicates a zero and the unprogrammed cell a one.

It is immaterial whether the electron charge is built up slowly or acquired in one burst as long as the cell is fully charged. So the original system of giving one 50mS pulse has now changed in favour of a number of short pulses with a monitor check between each to see if the bit has taken. When it has, a few extra pulses are given to fully charge the gate. Changes have also taken place in the

Changes have also taken place in the value of the programming voltage  $V_{pp}$ . The 2716 EPROM required 25 volts on the  $V_{pp}$  pin but as the chip density increased, cell dimensions decreased and to avoid damaging the cell junction, the voltage has been reduced first to 21 volts and latterly to 12.5 volts. This is a critical voltage and under no circumstances must the maximum rating be exceeded.

In fact most manufacturers' data sheets emphasise that even spikes on the  $V_{pp}$  line must be eliminated. Which is fine if you know the EPROM  $V_{pp}$  voltage but this is one parameter not included in the type number. Yet even this is being improved, for while there is still a problem deciding which are which between the 25 and 21 volt EPROMs, the 12.5 volt ones are being marked with the voltage.

One last change in the programming operation. The 12.5 volt EPROMs require that the normal memory supply voltage  $V_{cc}$  is increased during programming from 5 to 6 volts. Thus adding one more requirement in the design of the programmer.

To sum up then, we need to be able to program EPROMs from 2716 to 27128 in either normal NMOS or CMOS. The 2716 must be included because there are so many in service but in the interest of rationalisation we can leave out the 2732 which would involve a further problem switching  $V_{pp}$ . Three  $V_{pp}$  voltages 12.5, 21 and 25 volts will be required and the program should be able to provide either one shot or fast programming pulses. So let's see what the circuitry required for such a programmer looks like.

## **CIRCUIT OPERATION**

The programmer has two main operations; read and program. The read process is simply a case of applying an address and enabling the EPROM output; but the programming as we have already seen is more complex. First the programming voltage  $V_{PP}$  (and mabye  $V_{CC}$ ) has to be set up, then the address and data has to be latched in and finally the program pulse has to be output. This has to be in the correct sense i.e. low high low for 2716 types high low high for 2764 and 27128.

The address and data lines must remain latched for the duration of the pulse or the programming will fail. But as we need to verify the data, the address should stay latched at least until the EPROM has been

27C1 2712	28 27C64 8 2764	27C32 2732	27C10 2716	5]	28	PIN		27C16	27C32 2732	27C64 2764	27C128 27128
Vnn	Vnn	_		1	24		28			V	V
A12	A12		_	2	1 -		27	$\sim$		PGM	PGM
A7.	A7	A7	A7	3	1.	24	26	Vcc	Vec	NC	A13
A6	A6	A6	A6	4	2	23	25	AŜ	A8	A8	A8
A5	A5	A5	A5	5	3	22	24	A9	A9	A9	A9
A4	A4	A4	A4	6	4	21	23	Vpp	A11	A11	A11
A3	A3	A3 -	A3	7	5	20	22	OÉ	OE/V <sub>pp</sub>	OE	OE
A2	A2	A2	A2	8	6	19	21	A10	A10	A10	A10
A1	A1	A1,	A1	9	7	18	20	CE/PGN	1 CE	CE	CE
A0	A0	A0	A0	10	8	17	19	D7	D7	D7	D7
D0	D0	D0	D0	11	9	16	18	D6	D6	D6	D6
D1	– D1	D1	D1	12	10	15	17	D5	D5	D5	D5
D2	B2/	D2	D2	13	11	14	16	D4	D4	D4	D4
GND	GND	GND	GND	14	12	13	15	D3	D3	D3	D3

Fig. 1. Comparison of EPROM Pin-Outs.



#### Fig. 2. Circuit diagram of the Spectrum EPROM Programmer.

read and verified. If the data is correct the system moves to the next address, or carries out the extra pulses if in the fast program mode.

## **SUPPLIES**

The circuit to operate this sequence is shown in Fig. 2. It connects to the Spectrum expansion port with the connector leads on the left and has its own power supply. This includes regulated 5 volt and 6 volt supplies and a  $V_{PP}$  supply which can be set to any of the three standard voltages. This  $V_{PP}$  voltage is set by switch S3 but is controlled by the computer program via IC2 pin 31.

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A high (5 volts) on this line turns on TR1 which pulls down its collector and the base of TR2 thus turning TR2's output off and allowing the TR3 base voltage to be governed by the setting of S3a. The use of the computer and manual control in this way ensures that VPP is never in excess of 5 volts unless programming is in progress. When the voltage is increased it is brought up slowly by the presence of C4 which prevents the overshoot that otherwise might take place.

It's worth mentioning here that to avoid any possibility of the  $V_{PP}$  voltage being present without  $V_{CC}$ , the EPROM must never be removed unless all the voltages are switched off by S1. Switch S3 has the further function of switching the EPROM V<sub>CC</sub> line from 5 to 6 volts (S3c) when V<sub>PP</sub> is set to 12.5 volts. The 6 volt supply is developed from IC6 which is a standard 5 volt regulator increased by having R4 in its common line.

## INTERFACING

All the computer interfacing including the latching of the address and data lines is carried out by the two Z80 PIOs. These-in case you aren't familiar with them-each have two 8 pin ports which can be set to either input or output under the control of the computer program. Each PIO is



Fog. 3. P.C.B. layout and wiring shown full size.



## COMPONENTS

Resistors R1,R8 R2 R3 R9 R4 R5, R6, R7 All ¼ watt ±!	4k7 (2 off) 47k 10k (2 off) 220 180 (3 off) 5%
Canacitors	
C1 2 C2 1 C3 2 C4 1 C5 4 C6, C7, C91 C10, C12 c C8 1 C11 2 C13 4	200 $\mu$ elect. 16V 000 $\mu$ elect. 16V 20 $\mu$ elect. 63V 00 $\mu$ elect. 25V $\mu$ 7 elect. 63V 00n 16V disc eramic (5 off) 00n 40V ceramic $\mu$ 2 elect. 100V 70 $\mu$ elect. 16V
Semiconduc	tors
D1, D2, D3,	
D4	1N4001 diode (4 off)
D5	1N414 <mark>8</mark> d <mark>iode</mark>
D6	BZY88 C22
	and C3.6*
D7	BZY88 C18
	and C3.6*
D8	BZY88 C9.1
	and C3.6*
*Zener o	diodes, see text
D9	5mm red l.e.d.
D10	5mm green
	l.e.d.
D11	5mm yellow
TD1 TD2	1.e.d. 2N/2704 tran
101,102	sistors (2 off)
TRS	TIP31
1113	transistor
IC1 IC2	Z80 PIO parallel
101,101	interface (2 off)
IC3	74LS00 guad
	NAND gate
IC4	74LS138
	decoder
IC5.IC6	7805 5V 1A

voltage regu-

lator (2 off)

## Approx. cost £45 plus guidance only £45 case

#### Switches

<sup>-</sup> S1	d.p.d.t. toggle
S2	4-pole 3-way rotary
	stop set for 2-way
S3	3-pole 4-way rotary
S4	d.p.d.t. toggle 240V
	a.c. rating



#### **Plugs and sockets**

28 pin zero insertion force (ZIF) socket; 14 pin i.c. socket; 16 pin i.c. socket; 40 pin i.c, socket (2 off); 10 pin p.c.b. plugs\* (2 off); 8 pin p.c.b. plugs\* (2 off); 8 pin p.c.b. plugs\*; 4 pin p.c.b. plugs \* (2 off); 10 pin cable sockets\* (2 off); 8 pin cable socket\*; 4 pin cable sockets\* (2 off); double sided 2×28 way edge connector; pin extension p.c.b \*0.1 pitch plugs and sockets "Minicon" or similar.

#### **Miscellaneous**

T1 Multitapped transformer, providing a minimum of 30 volts a.c. at 1A with a tapping at about 10 volts.

Heatsink, size 22×19×19mm high; pointer knobs (2 off); ribbon cable; l.e.d. clips; board pins; small piece of Veroboard for mounting ZIF socket (see text); plastic box minimum internal dimensions 210×130×70mm high (see text)-recommended box is type M8007 desk console 237×182×55/88mm high; printed circuit board available from *EE PCB Service*, Code EE628; software program tape, see Shop Talk.



uniquely addressed using the Spectrum adress lines A3 to A7. Lines A5, A6 and A7 are decoded by IC4 and switch either IC1 or IC2. The other two address lines select the A or B port and set it to send or receive data.

Outputs from IC1 go to the 13 EPROM address lines using both of its ports but each of the ports of IC2 has a different function. Port A is connected to the EPROM data lines and is used to program the EPROM or read the contents of the stores while port B carries out all the control operations.

The port is set so that lines B1, B3, B4 and B7 are in the output mode and B6 is input. B1 outputs the Chip Enable (CE) signal and B3 the Output Enable (OE) signal. B4 operates the  $V_{pp}$  voltage while B7 controls the program pulse. The B6 line is pulled high by R8 and switch S3b returns this to 0 volts whenever a  $V_{pp}$  voltage is selected. The computer monitors this line to check the switch status.

The layout of the switches, indicators and zero insertion force (ZIF) socket on the top panel of the unit is shown in the photographs. The upper switch S2 selects the two different connection patterns for the EPROMs. As Fig. 1 showed, only pins 20, 23 and 26 require switching because the 2716 EPROMs do not occupy the upper socket positions and the lower pins are common to all the EPROMs.

The lower switch S3, is the V<sub>PP</sub> voltage selector. In the off position it shorts TR3's base to 0 volts effectively switching the transistor off but in the other positions, one of the Zener diodes sets the base voltage and therefore the  $V_{pp}$  voltage. S3 is mounted between the two indicators D10 and D11. These are used to indicate "V<sub>PP</sub> ON" and "Programming in progress" respectively. The ZIF switch S1 isolates the EPROM socket by turning off both the V<sub>CC</sub> and V<sub>PP</sub> supplies. The ZIF indicator (D9) acting as a second reminder to switch off before releasing the ZIF socket lever.

## CONSTRUCTION

The main components are assembled on the p.c.b. shown in Fig. 3. This is obtainable from the EE PCB Service, order code EE628. The component layout of the board and the links are shown. Sockets are recommended for all the i.c.'s and the top panel wires should be connected to the board with Minicon or similar plugs and sockets (A to E).

The heatsink shown can be replaced with a simple aluminium channel if you experience any difficulty in obtaining it. Details of the Zener diode values are given in the next section.

If cost is a prime consideration the unit can be built in an MB6 plastic box although you must check that the transformer used will fit. It will almost certainly have to be fastened on its side, to the side of the box. The recommended case is the larger sized "Desk Console" which is only a few pounds extra and, with a sloping top panel, has a very professional appearance. However the top panel is metal and the only way to secure the ZIF socket is by soldering the pins. So if you use this type of box, solder the ZIF to a piece of Veroboard suitably spot faced and fasten the Veroboard over a large clearance hole cut in the panel.

The transformer used is a multitapped one. There are a number available with tappings at various voltages. A minimum of 30 volts is required for the  $V_{pp}$  supply and a tapping at about 10 volts for  $V_{cc}$ . One amp is more than sufficient current for both.

## **CONNECTOR**

The Spectrum edge connector is wired to one side of the p.c.b. and the ribbon cables brought out through the side of the box. Although it may not look quite as neat it is better to cut the hole in the box wide enough to pass the connector through. However if you use the recommended desk console box you will find the base is removable which greatly simplifies the problem. The slot for the cables can be made just deep enough to trap them and prevent loads on the soldered joints.

The connector on the prototype has a pin extension p.c.b. fitted. This enables an additional unit such as the printer interface described in the January 1989 copy of EE to be connected to the Spectrum. It also facilitates the fitting and removal of the connector to the Spectrum as it is otherwise very difficult to avoid pulling the wires. It is sensible therefore to solder either the extension p.c.b. or a small piece of Veroboard to the connector pins and fasten it securely to the connector with a small fillet of epoxy resin added sparingly between the pins (Fig. 4). The pins and wires can best be insulated with a silicon bath or sink sealer. This sealer is easy to obtain and it is readily removable if a soldered connection has to be remade.

The top panel connections are shown in Fig. 5. Layout is not critical but if the box you are using is restricted in height make sure the switches aren't going to foul components on the board or the transformer. When connecting to the l.e.d.s remember the longest leg is the anode. Take care when mating the plugs and sockets and remember that the A and B, and D and E have the same number of pins. Mixing A and B will only give wrong addresses but mixing D and E could destroy IC2. Pins D1 and E7 aren't used so you could make the plugs non-interchangeable if you wish.







#### REMAINING EPROM SOCKET CONNECTIONS

ZIF PIN	CONNECTS TO SOCKET
2	C/6
3	B/4
4	B/3
5	B/2
6	B/1
7	A/4
8	A/3
9	A/2
10	A/1
11	D/2
12	D/3
13	D/4
15	D/5
16	D/7
17	D/8
18	D/9
19	D/10
21	C/4
24	C/3
25	C/2

## FINAL ASSEMBLY and TESTING

Connecting the top cover to the p.c.b. and, before fitting the i.c.s, check the complete system, edge connector to i.c. sockets and i.c. sockets to ZIF socket. Then connect the transformer and with the Vpp switch set to OFF check the 5 and 6 volt supplies and make sure that the  $V_{pp}$  supply is at least 28 volts at TR3 collector. With IC2 removed TR3 can be turned on by the V<sub>pp</sub> switch allowing you to monitor the V<sub>pp</sub> voltages at pin 1 of the ZIF socket. The recommended voltages are 12.5V±0.5V, 21V±0.5V and 25V±1V.

Unfortunately Zener diodes are made in standard voltages and they vary considerably depending on their current so the best way to get these voltages is by trial and error with two Zeners in each position. Use a 9.1, 18 and 22 volt with a 3.6V in series in each case and check the output voltage-at 30mA if you can. Use a couple of other values e.g. 3.3 volt and 3.9 volt to adjust the output if necessary. Finally fit the four i.c.s and the unit should be operational.

## **OPERATION**

The Spectrum program for this unit is over 15 kilobytes long and is being made available on tape-see Shop Talk. However, for those of you who wish to write your own, data is provided at the end of the article to assist. For the rest here is a simple system to check out the unit without blowing up any EPROMs.

Make up a 28 pin header to fit the ZIF socket, with eight resistors of any value 1k to 22k. Common one end of all the resistors and join the other ends to the Data pins i.e. 11, 12, 13, 15, 16, 17, 18 and 19. Now if you join the common to pin 28 (Vcc) all the pins will be high and will indicate 255, or if joined to pin 14 (GND) they will all be low and equal to zero. If you use an 8 way d.i.l. switch you can set up any value.

Enter the program given in the software section and you will be able to check the PIO operation albeit in a simple form. Used with the software tape this device will enable most of the Main Menu options to be checked even "Program EPROM"

## SOFTWARE TAPE OPERATION

The software consists of a Basic program operating the menu and the slower manual

40000	(#9040)	09	29	AO	00	OE	6E	0A	13	
40008	(#9048)	40	83	4F	29	9C	49	49	49	
40016	(#9050)	BC	1 B	01	BA	74	4F	9F	00	
40024	(#9C5B)	29	9C	08	00	01	00	D8	29	
40032	(#9060)	9E	08	00	00	CC	68	AA	80	
40040	(#9668)	29	9E	08	40	85	C4	FD	00	
40048	(#9070)	28	87	00	ЗF	00	AA	13	CO	
40056	(#9C7B)	80	80	80	FF	00	80	00	FF	
40064	(#9080)	02	4F	80	FF	00	00	06	01	
60073	(#9(99)	57	30	54	68	AA	6E	02	80	

#### Fig. 6a. Memory contents.

ALTER / CHECK MEMORY STORE	A
LOAD MEMORY STORE from BASIC	в
CHECK CONTENTS of EPROM	¢
CALL DISASSEMBLER	D
HEX to/from DECIMAL	н
LOAD TAPE into MEMORY	L
PROGRAM EPROM	P
SAVE MEMORY to TAPE	S
TRANSFER EPROM into MEMORY	т
• Enter Code for Item required	•
Fia. 6D. Menu.	

commands and a machine code program to carry out the operations requiring high speed. The two programs effectively occupy the Spectrum memory up to address 40000. Storage of the EPROM contents starts at this address and allows plenty of room for the 16 kilobytes without interfering with the space required for the disassembler.

The machine code consists of a number of related programs each one devoted to a specific task. Addresses or data entered in response to requests in the Basic program are loaded into storage addresses for use in these machine code programs. Similarly information is returned to the Basic program to provide the results of the machine code operation.

To operate the programmer with the taped software first connect the programmer to the Spectrum and switch it on. Then switch on the Spectrum and Load the program. It checks that the V<sub>PP</sub> switch is OFF and requests the EPROM type being used. Then it presents the main menu shown in Fig. 6b and the following is an explanation of the options:

- A. Entering here produces a further option "S" Screen display or "A" Alter. Fig. 6a. illustrates the former and the data can be selected ineither decimal or hex. "P" will provide a print-out-more later-and ENTER produces the next block of data. Alter displays the selected address and requests the new data which is then entered.
- B. This is an alternative method of load-
- ing data into consecutive addresses. A further "C" will display the C. A EPROM contents in a similar form to that of the memory (Fig. 6a.). An "E" will verify that the EPROM is fully erased or will quote the first failure address.
- D. The program is compatible with the Hisoft disassembler "Mons" and this command transfers to it.
- This provides hex to decimal or decimal to hex conversion.
- Loads the tape into the memory addresses specified.

- P. The programming command. Single addresses or a block of 16K can be loaded. A 50mS or "Fast Program" sequence can be chosen and if the latter, any pulse length can be selected in 1mS increments and the number of pulses at each address can be set.
- S. Saves the selected memory addresses onto tape
- T. The EPROM contents are read and transferred into the computer memory providing a very fast method of duplicating EPROMs. The 2K bytes of a 2716 can be transferred in about two seconds.

The program is user friendly, having default addresses needing only ENTER to select and there is a quick return sequence if the program should crash to Spectrum Basic.

The program is written for the parallel printer interface and program previously articled in EE. If a serial printer is used operating from the RS 232 port of Interface 1 the following mods must be made to the program:

Change line 9993 to read: FORMAT "t";b: OPEN #3;"t" where b is the baud rate of your printer. The printer auto line feed should be OFF.

Note: Microdrives must be disconnected before connecting the unit.

## PROGRAMMING THE UNIT

Although a comprehensive suite of machine code programs is required to efficiently operate the unit it is possible to carry out many operations with a few sim-ple commands. The most essential subroutine is the one used to initiate the system. It must be called whenever the unit is switched on **BEFORE** an EPROM is fitted. It sets the PIO's in a "read" state ready to receive further commands. It is given below along with a Basic program which reads and prints a block of EPROM addresses.

The "initiate" program is in machine code but as it is only 23 bytes long it isn't worth providing a code loader. It should be poked into memory starting at address 39168.

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The code is as follows: 62, 63, 211, 183, 211, 191, 62, 255, 211, 223, 62, 101, 211, 223, 62, 127, 211, 215, 62, 0, 211, 207 & 201. The Basic program is: 10 RANDOMIZE USR 39168 20 FOR n=0 TO 255 30 OUT 167,n 40 PRINT n;" ";IN 199 50 NEXT n

This program reads the first 256 addresses. For simplicity I have cheated a bit because I have only called the "A" port of IC1 and relied on all the other address lines being zero. For the next 256 addresses you would have to increment the "B" port thus: OUT 175,1

and of course both ports should be called every time.

For those of you who want to do your own programming here is a list of the PIO addresses and commands.

Port A Control 183 Set to Output 63 Data 167 —

Port B	Control	191	Set to Output 63
	Data	175	
IC2	ĸ.		
Port A	Control	215	Set to Output 63 or
			Input 127
	Data	199	-
Port B	Control	223	Set to Mode 3 255
	Control	101	This 2nd Control
			word sets:-
			B0, B2, B5 and B6
			to Input
			B1, B3, B4 and B7
			to Output.
	Data	207	

Both Port A and Port B of IC1 are always output because they are sending the addresses to the EPROM but the ports of IC2 both alternate. Port A sends and receives the EPROM data and Port B line 6 Inputs the  $V_{pp}$  switch signal. Line B4 controls  $V_{PP}$  (output "1"=on), B1 and B3 output the CE and OE signals respectively (both active low) and B7 outputs and sets the duration of the PGM program pulse which is always low-high-low.



#### **Design Package**

Last month we mentioned the excellent p.c.b design kit from Kemsoft, this month comes news that Number One Systems of Huntingdon have won a British Design Award for their EASY-PC printed circuit board design package. The EASY-PC is a computer-aided design program that enables preparation of p.c.b. masters and also circuit diagrams to be produced "on screen" before committing to a final printout.

Aimed at the hobbyist user, they have now produced son of EASY-PC. Called TINY-PC it operates at about one tenth

EVERYDAY

the speed of "big brother" and has half its capacity in terms of number of tracks, pads, symbols etc. per board.

For further information and prices of both packages contact: Number One Systems, Dept. EE, Harding Way, Somersham Road, St. Ives, Huntingdon, Cambs, PE17 4WR. 20 0480 61778.

#### **Capacitor Tester**

We do not expect readers to experience any component buying problems for the *Capacitor Tester* project.

The 10 megohm resistors are listed in most component catalogues and can

certainly be found in the one per cent "metal film" range. To make up the 100 megohm resistor R2, which is not available as a single unit, you should, of course, follow the authors guide lines and wire up ten 10 megohm resistors in *series* to obtain the required value.

The CMOS low power op. amp type ICL7611 may prove a little troublesome to locate locally, but it is currently listed by Cricklewood and Omni Electronics.

#### Spectrum EPROM Programmer

Most of our advertisers now carry the Parallel Interface (PIO) chips required

## USING THE PROGRAMMER

One of the greatest difficulties is determining the programming requirements of a strange EPROM. Here is my recommended procedure. 1. Set VPP to 12.5 volts and try program-

- 1. Set  $V_{PP}$  to 12.5 volts and try programming 00 into a few individual addresses with first 1mS for 10 pulses and if that fails 10mS for 5 pulses.
- 2. If the above fails repeat with 21 volts and then 25 volts.

You could also try a 50mS pulse at the 21 and 25, volt step. If all this fails, the EPROM is probably faulty.

## INTERFACE 1 AND MICRODRIVES

Interface 1 can be used with the unit but due to a clash of addresses any Microdrive must be completely disconnected before the unit is fitted.

for the Spectrum EPROM Programmer and this should not cause local sourcing problems. This also applies to the various connectors and ribbon cables, although the "zero insertion force" socket is quite an expensive device.

The main printed circuit board for the programmer is available from the *EE PCB Service*, code EE628 (see page 409). The small extension printed circuit board may be purchased from **Maplin** and should be ordered as part code GB81C.

Special arrangements have been made with the designer to supply the software tape at a reasonable price direct. The cost of this tape is £2 (please enclose a sturdy 6in.×4in. stamped (34p) addressed envelope) and orders should be sent to Mr. K. Taylor, 15 Lindsay Road, Horfield, Bristol, BS7 9NP. Overseas readers should add 50p to cover additional postage.

#### **Bat Detector**

The dual-balanced mixer i.c., type SL1640C, used in the *Bat Detector* is really intended for applications in radio communications and is likely to prove quite troublesome in locating a source of supply. The one used in our prototype model was purchased from **Cirkit** (**3** 0992 444111) and should be ordered as stock number 61-01640.

The 40kHz ultrasonic transducer is usually sold as part of a pair and some advertisers may be persuaded to part with single items. However, if readers do experience difficulties we understand that **Magenta** are prepared to sell this device separately.

The printed circuit board for the Bat Detector is available through our *PCB Service*, code EE647 (see page 409).

#### Signal Injector/Tracer

We cannot foresee any component buying problems for readers undertaking the two Circuit Card projects, *Signal Injector* and *Signal Tracer*. All components are standard "off-the-shelf" items and the circuit cards are attached to the front cover of this issue. The BICC-Vero "Easiwire" kit is

The BICC-Vero "Easiwire" kit is stocked by many of our advertisers and details of a £1 off Special Offer appear on page 411.

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THINK that it is true to say that computer projects are nowhere near as popular as they were a few years ago when home computing was at the peak of its popularity. Nevertheless, there are still plenty of computer projects being constructed, and this remains a popular aspect of the hobby. There have been several bargain computers on sale over the last two years or so, and these have provided many constructors with a low cost means of entering the world of computer add-ons.

I suppose that on the face of it there is no difference between a computer project and any other type. This is something that really depends on the project concerned, but there are terms and components which are much used in computing but are virtually unused in any other field of electronics construction. In this month's Actually Doing It article we will consider the building of computer add-ons, paying particular attention to the unusual components and construction techniques which are often used in these projects.

#### CONNECTORS

One initial point that is well worth making is that a project which connects direct to the buses of a computer (which generally means one that connects to the main expansion bus) is less than ideal for a beginner. It is possible to do considerable (and very expensive) damage to a computer if a mistake is made with this type of project.

In fact modern computer components are quite tolerant to such things as short circuits to one or other of the supply rails. Even so, a complete beginner to electronics construction would probably be well advised not to start with a project of this type. One that connects to something like a printer, serial, or user port is a much safer starting point.

One problem with computer projects is getting the right connectors. When I first started on computer projects I soon accumulated a number of connectors where I had mistakenly ordered the wrong type, or thought I had ordered the wrong thing but had actually been sent the wrong type of connector by the supplier.

Fortunately, the connector terminology now seems a little more standardised and matters are not quite so hit and miss. On the other hand, there seems to be almost as many types of computer connector as there are different computers, and you still need to be a bit careful when ordering.

#### EDGE CONNECTOR

Probably the most common type of connector for home computers is the so-called "edge connector". The male connector is the one which is normally at the computer end of the system, and this is just some fingers of copper at the edge and on both sides of the board. There are actually such things as singlesided edge connectors, but I must admit that I have never encountered one. Some popular home computers which use this type of connector for their expansion buses are the Electron (2×25-way), Amstrad CPC series (2×25-way), Amstrad series Spectrum series (2×28-(2×25-way), way) and ZX81 (2×23-way).

Note that a connector having (say) two rows of 25 connectors is normally referred to as a  $2 \times 25$ -way type rather than a 50-way connector. The latter would be a single-sided 50-way connector.



#### **PITCH AND POLARISING**

When ordering connectors there are a few pitfalls to avoid. One of these is that there are various pitches for edge connectors. Most computer projects seem to use the 0.1 inch (2.54 millimetre) type, but there are also 0.156 inch and 2 millimetre types (although the 2mm variety do not seem to be listed in any of the electronic component catalogues). As far as I am aware, the only home computer which uses the 0.156 inch type is the Commodore VIC-20, and these are now a rarity in catalogues.

Most computer edge connectors are designed to take sockets that are fitted with a polarising key. This arrangement is pretty basic, and just consists of a slit cut into the male connector (Fig. 1) plus a metal rod fitted into the female connector. The rod and slot match up correctly and permit the two connectors to fit together, but as they are offset from centre, this is only the case if the female connector is fitted the right way up.

Since making the connections to an expansion bus the wrong way round could be decidedly dangerous for the computer or the add-on project (or both), the use of a connector with the correct polarising key has a lot to recommend it. It is essential to have a polarising key only where one is called for, and then to have one in the right position. Otherwise the key will "lockout" the connector. Where possible it is advisable to buy an edge connector that is specifically advertised for use with the appropriate port of your computer.

Do not make the classic mistake of reversing all the connections so that although the edge connector is fitted to the computer the right way up, it is effectively up-side-down! All wiring should be carefully checked before any finished project is switched on and tested, but this is especially important with computer projects. If you have to use an edge connector which does not have a polarising key, clearly mark the top and bottom edges of the connector.

#### MAKING CONNECTIONS

Most ordinary connectors are made for soldered connection of leads to their tags. There are alternatives with computer connectors though, and these are designed to make life easier with what is very often a very large number of interconnections. These alternatives

are not invariable used, and there are plenty of computer and plugs sockets which have ordinary soldertags. These are connected to the printed circuit board of the project via a piece of ribbon cable. As its name implies, this is a flat, ribbon-like cable, which is in effect a number of multistrand insulated wires laid side-by-side and joined with an overall plastic sheath.

You may require something like a 28way cable, but only be able to obtain 26- or 30-way cable. The answer to this prob-

lem is to obtain the 30-way type and strip off two wires from this in order to give the required 28-way type. Ribbon cable usually pulls apart without too much difficulty, and there should be no bother at all in trimming a piece down to the required number of ways.

When making the connections to a computer plug or socket the usual rules apply. Strip the insulation from the ends of all the leads and tin them with solder. Then tin all the tags of the connector with solder, and only then start making the connections.

This is inevitably going to be a slightly awkward job as there are likely to be dozens of connections to make to pins that, are only about 2.5 millimetres apart. The spacing between rows is not usually very great either.

The solder needs to be used sparingly in order to avoid solder-blobs short circuiting adjacent tags, but obviously it needs to be used in sufficient quantity to produce a good joint. If a short circuit should occur, clear away all the solder from both tags and start again.

#### PROBLEMS

When wiring up edge connectors I have encountered two main problems. One of these is simply getting wires crossed over. This is where the multicoloured "rainbow" ribbon cable is very much better than the monochrome variety. It is easy to identify each wire and spot any mistakes that are made. The "rainbow" ribbon cable is usually a little more expensive, but it is probably well worth the extra money for this type of application.

Problem number two is that of getting to the end of wiring up a connector only to find that you are a wire short! What can easily happen is that when connecting one lead you also inadvertently connect the one next to it to the same tag. From there on you are connecting the wrong lead to each tag. The only remedy if this should happen is to disconnect everything back to the point where the mistake was made, and to start again from there.

#### **ON BOARD**

Some edge connectors are intended for direct connection to the printed circuit board of the add-on project. These connectors come in the straight and right-angled varieties, as shown in Fig. 2. With this type of connector the project plugs straight onto the back of the computer. This is very neat, but it is a method of construction I tend to avoid.

Having the project attached to the rear of the computer does not necessarily place it where it is convenient in use. Perhaps of greater importance, reliability is not always all it could be. Slight movement of the computer can tend to crash the system! Some very popular computers and add-ons have had severe problems of this type.

There should be no great difficulty in fitting a right angle edge connector to a printed circuit board. It is very much like fitting a large integrated circuit or other multi-pinned component. As with any component which has many closely spaced pins, take due care not to produce any solder bridges. Carefully check for and remove any accidental short circuits that are produced.

An important point to watch is that the connector is pushed right down onto the board before you start soldering it into place. As pointed out in several previous *Actually Doing It* articles, it is important that any component is properly pushed down onto the board prior to being soldered into position. The physical strength of construction is dependent on it. With a gap between the body of the component and the board, any downwards force on the connector tends to rip the copper pads and tracks away from the board.

The large number of pins of an average computer connector aids physical strength, but this is offset by the considerable force that is likely to be exerted on some of the pins each time the project is fitted to the computer or removed from it.

The straight pinned connectors are a bit more tricky. The pins on these are often quite long, and may in fact be too long for the particular printed circuit board you are using. The pins are easily trimmed back using wire clippers though. As a point of interest, trimming the pins back very short will produce a connector with what are effectively simple soldertags, and which can be connected to a piece of ribbon cable without too much difficulty.

For maximum strength the board should be pushed right up to the connector. The pins will almost certainly need to be bent upwards/downwards slightly in order to bring them very close to the surface of the board. Use plenty of solder when connecting each pin, and make sure that the connector is correctly positioned and aligned when making the first few connections. place with the shaft of a large screwdriver will normally suffice.

I have only once encountered tough ribbon cable that proved to be difficult to fit to an IDC socket. This needed a small hammer to press it down into the teeth, but this type of thing is usually unnecessary. When fitting a cable to an IDC connector make sure that the cable does not slip sideways one lead out of position. This would leave one lead and one tooth unconnected, and is easily done. If a mistake should be made it is not difficult to pull the cable away from the connector and then try again.

Note that it is only the proper (0.05 inch pitch) IDC ribbon cable that is suitable for use with this type of connector. There is a larger type which is not suitable (but which is excellent for the point to point wiring of projects). IDC connectors are often available ready fitted with a piece of ribbon cable, and I always obtain them in this form whenever possible.



#### IDC

When dealing with computer connectors you will often encounter the term "IDC". This stands for "Insulation Displacement Connector". It is a slightly vague term in that there is a range of connectors which go by the name "IDC" connectors (as used on the BBC and Oric computers for example). It is also a general term which can be applied to any connector which uses this method of connection to the cable. It is a form of solderless connector.

There are rows of small teeth with U shaped cutouts on the rear face of the component. The spacing of the teeth is such that it matches up with the lead spacing on the appropriate type of ribbon cable.

Fitting the cable to an IDC connector is just a matter of pressing one end of the cable down onto the teeth and then fitting a simple clip mechanism which holds the cable in position. The teeth cut through the insulation and make reliable connections to the leads in the cable. The cable can be pressed into place using special tools, but a small vice or even just pressing the cable into

#### **THE REST**

There are several computer connectors other than the edge and IDC types, but these mostly come in the same three forms (solder tags, straight or right angled printed circuit mounting, and IDC). Probably the most common of these are the "D" type connectors. Amongst other things, these are used for Atari/Commodore style games ports, the BBC computer's analogue port, and serial/parallel ports on many computers.

A less common type is the DIN41612 96-way connector (as used on the Sinclair QL's expansion bus). These can be a bit confusing as most connectors of this type have less than the full 96-ways. It is a 0.1 inch pitch connector which has three rows of 32 terminals.

However, many types only have one or two rows, giving 32 or 64-ways. The rows are named "A", "B", and "C", and a 64-way type can have either wide row spacing (an "AC" type) or narrow row spacing ("AB" type). Obviously you need to know what you need and what you are buying when obtaining a 64way DIN41612 connector.



## **DEMONSTRATION STATIONS**

A number of museums around the country have demonstration amateur radio stations, some using modern equipment and some using older historic sets. Usually run by volunteers, these stations offer visitors an opportunity to see and hear various aspects of amateur radio "in action", while their operators are more than willing to answer any questions.

If you have occasion to visit the Science Museum in London, for instance, make a point of visiting station GB2SM which has an impressive layout. The Chalk Pits Museum in West Sussex has station, GB2CPM, together with historic amateur radio displays. In the Isle of Wight, the National Wireless Museum at Newport has the call BG3WM; the Imperial War Musuem at Duxford Airfield has GB21WM and HMS Belfast near the Tower of London has GB2NR.

If you are holidaying in Scotland this year look out for the Scottish Tourist Board Radio Amateur Expedition Group. This group sets up amateur radio stations with world wide capabilities at historic, scenic, cultural or other sites of interest (e.g. famous distilleries!) throughout Scotland.

Operating on behalf of the National Trust for Scotland, the Scottish Tourist Board, Historic Buildings and Monuments, and other organisations, they aim to make the public more aware of the hobby of amateur radio. They operate with speech, radio teletype (RTTY), Morse (CW), and this year hope to demonstrate packet radio as well. They set up special explanatory displays for visitors and offer special awards to other amateur stations who contact them over the air.

They have a very full itinerary mapped out for the coming year. Look out for GB2RB (Robert Burns) at Burns House Museum, Mauchlin, May 27/28. GB2RBC (Royal Balmoral Castle) operating, with royal permission, from the castle's stable block, June 24/25.

An ambitious event will be a Five Nations National Trust weekend, with stations operating July 29/30 from National Trust properties in Scotland, Northern Ireland, Wales, England and Eire. On August 26/27, the group will be at Edradour Distillery, Pitlochry, with the call GB2SSD (Scotland's Smallest Distillery) and they are in the process of arranging a station to operate at Dumfries and Galloway Aviation Museum. on September 23/24/25.

If you come across any of these demonstration stations, don't hesitate to ask the operators or stewards about what's going on.

## ANTENNA DEFINED

It occurred to me recently that although, like most other experimen-

ters, I had some knowledge of antenna theory which certainly helps when I construct and try out a new antenna, I didn't have any formal definition of what exactly a transmitting antenna is.

I dug deep into my textbooks to find the following: "It is basically an electrical circuit. In ordinary circuits the dimensions of the components are small compared with the wavelength corresponding to the frequency in use. Most of the electro-magnetic energy remains in the circuit to perform some specific task, or is converted to heat.

In a transmitting antenna, the dimensions of the wiring or the components become significant in terms of wavelength and some of the energy escapes in the form of radiated electromagnetic waves. Efficient antennas are designed to ensure that the major part of the energy in the circuit escapes and is radiated in this manner."

Conventional theory has been much the same for many years, with the ideal antenna physically dimensioned to match the frequency to be used and located high in the air (at least half a wavelength high) and clear of surrounding objects. This means, for instance, that an antenna for use on the 80 metre band should, ideally, be about 40 metres (i.e. half-wavelength) long and the same distance in height.

Because of these large dimensions there has always been an interest in reducing size without losing too much in performance and any number of "specials" have emerged over the years. I have mentioned some of these in previous columns. In all cases however, reduction brings some loss or disadvantage compared to a full-size, full height, antenna. In some cases, though, a properly designed small-size antenna can provide satisfactory performance where space is limited.

#### MAGNETIC LOOP

There has been a lot of interest recently in a type of small antenna called a "magnetic loop". This is widely used in professional and military communications but has only recently been seriously considered for use by amateurs, although individuals have been experimenting with and writing about various versions over the last 20 years.

The amazing thing about this antenna is its size. A loop of approximately 750mm diameter will cover all amateur bands 20 metres to 10 metres, without a normal antenna tuning unit (ATU), and needs to be mounted just 2.5m above ground. By comparison, a standard antenna for 20 metres is about 10m long and the same distance above ground.

A loop covering the 80, 40, and 30 metre bands is about 1.2m diameter, again 2.5m above ground. As mentioned, the comparable standard length

and height dimension for the 80m bana is about 40 metres.

Conventional theory indicates that such miniaturisation will result in an antenna which is extremely difficult to "load up", enabling it to radiate radio waves without reflecting r.f. energy back to the transmitter via the feed line, in the form of "standing waves". Such waves can destroy the output stages of solid-state transmitters, yet with the magnetic loop properly used this does not occur.

The loop is claimed to have a radiation efficiency not far short of a halfwave dipole half a wavelength above ground, and a better performance than such a dipole less than a half wavelength high. It is little affected by nearby objects so it can be used indoors, and on receive there is a significant reduction in electrical interference or from strong transmissions on nearby frequencies.

It does have some disadvantages. Being so small it has a very high *Q*, the characteristic that ensures such good interference rejection, but this also means that the antenna has to be retuned every time the transmitting frequency is changed more than a few kHz. Additionally, a very high r.f. voltage develops across the antenna with a risk of r.f. burns so caution needs to be observed when using the antenna indoors with high power.

#### NOT SO EASY TO MAKE

As can be imagined, this antenna is not as simple to make as a classic dipole, which can be just a length of wire suspended in the air with an electrical break in the centre to form a feed point. The loop is constructed of metal tubing about 25mm diameter, and requires workshop facilities for satisfactory construction. The loop is tuned to the required frequency by a high-quality tuning capacitor linked to a d.c. motor, with reduction gear, all mounted on the antenna and remotely controlled from a control box at the transmitter.

These loops can be obtained commercially from Cap. Co Electronics Ltd, Unit 6, Peel Road Industrial Centre, Peel Road, West Pimbo, Skelmersdale, WN8 9PT, but they are expensive. The smaller 20 to 10 metres version, for example, costs about £300, but I believe there is a "receive only" model for less than £100 for the serious short-wave listeners.

Whether there will be wide-spread use of the magnetic loop by amateurs remains to be seen. It certainly seems to offer a performance never achieved before with such small dimensions, and it could be the answer to the problems of limited space. I imagine that as time goes on there will be other cheaper versions put on the market, perhaps as d.i.y. kits.



The books listed have been selected as being of special interest to everyone involved in electronics and computing. They are supplied by mail order direct to your door. Full details are given on the last book page.

For another selection of books see next month's issue.

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PRACTICAL MIDI HANDBOOK

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**S**OFAR in this series of articles the 1MHz Bus has received relatively little attention. We will now rectify matters with an in-depth look at this port, and its possibilities for user add-ons. As we will see, these possibilities are substantial.

The 1MHz Bus certainly offers much greater expansion potential than user and analogue ports. In theory at any rate, dozens of add-ons could be simultaneously connected to and operated from this port.

#### **The Buses**

Connection details for the 1MHz Bus have been provided in a couple of previous *Beeb Micro* articles, including the last one, and so this diagram will not be provided again here. This is a list of the 1MHz Bus pin functions:

PIN	FUNCTION	PIŅ	FUNCTION
1	GND	18	D0
2	R/W	19	D1
3	GND	20	D2
4	1MHz Clock	21	D3
5	GND	22	D4
6	NNMI	23	D5
7	GND	24	D6
8	NIRQ	25	D7
9	GND	26	GND
10	NPGFC	27	A0
11	GND	28	A1
12	NPGFD	29	A2
13	GND	30	A3
14	NRST	31	A4
15	GND	32	A5
16	AUDIO IN	33	A6
17	GND	34	A7

The 1MHz Bus provides most of the lines you would expect to find on the expansion port of a 6502 based computer. One of these is the 1MHz clock signal. This could be useful for add-ons that require an accurate clock signal which can be derived from the 1MHz signal. This signal is also needed by many standard 65<sup>\*\*</sup> and 68<sup>\*\*</sup> series peripheral devices.

Although the 6502 in the BBC computers operates with 2MHz a clock signal, some internal circuitry reduces the clock speed to 1MHz when input/output devices are accessed. This enables standard 1MHz 65\*\* and 68\*\* peripherals to be used within the computer. This includes the 6522 VIA which provides the user port lines, and the 1MHz Bus is also run at the lower rate (from which its name is derived).

The eight bit data bus is available, and this is obtained from the microprocessor bus via a bidirectional tristate buffer. This provides the port with plenty of drive, and ensures that it does not heavily load any input devices connected to it.

Another important point in favour of this buffering is that it reduces the risk of any serious internal damage to the computer in the event of a fault in any add-on connected to the 1MHz Bus, or if a device is connected incorrectly. Many of the other lines are also buffered incidentally.

...Beeb...Beeb...Beeb...Bee

There are the usual control and interrupt lines available. NRST provides a negative reset pulse at switch-on, and when the BREAK key is pressed. The usual read/ write and two interrupt lines are included, but the latter are really only something for experienced users. There is a useful audio input, or "analogue" input as it is sometimes confusingly called in Acorn literature.

#### Addressing

Only the eight least significant address lines are available, but these are all that are needed as the upper eight address lines are decoded by internal circuits of the computer. The decoded outputs are NPGFC and NPGFD, which pulse low when any address in pages &FC and &FD (respectively) are accessed. This gives two pages of 256 addresses for expansion purposes, which should be considerably more than enough to satisfy even the most ambitious BBC add-on builder.

On the BBC model B both of these lines tend to suffer from spurious pulses which can result in unreliable results with most add-ons. This problem can be overcome by adding a simple clean-up circuit (which was provided in the previous *Beeb Micro* article). Note that this circuit is not needed with the Master 128 computer.

In order to make use of all 512 available addresses a considerable amount of address decoding would have to be applied to NPGFC, NPGFD, and A0 to A7. In most cases only one or two decoded outputs will be needed, and it is then quite in order to use NPGFC and NPGFD without any decoding of the eight available address lines.

Acorn have divided up pages &FC and &FD into areas which are given over to

specific purposes, but this is not to say that you have to keep within their guidelines. You should certainly do so if you wish to connect commercial add-ons to the 1MHz Bus, and to also have your own circuits connected to this port at the same time.

Probably most users will not wish to do this, and will only fit their own circuits to this port, or will only have commercial addons or their circuits connected to it at any one time. Anyway, details of how this address range has been divided up are provided below:

ADDRESS	ASSIGNMENT	
&FC00-&FC0F	Test Hardware	
&FC10-&FC13	Teletext	
&FC14-&FC1F	Prestel	
&FC20-&FC27	IEEE488	
&FC30-&FC3F	Cambridge Ring	
&FC80-&FC8F	Test Hardware	
&FCC0-&FCFE	User Add-ons	
&FCFF	Extended Page	
	Register	
&FD00-&FDFF	Extended Page	
	Memory	

At first sight this may seem to be a bit restrictive, since out of some 512 addresses only 63 are available for user add-ons. If you should decide to keep within these guidelines, then I think it is unlikely that this would really prove to be a serious limitation. After all, this is sufficient address space for up to three 6522 VIAs, with sufficient spare address range for a few 6821s and 6850s. The main problem if you adhere to this system is that it will necessitate the use of some additional address decoding.

#### **Paging Jim**

For reasons best known to themselves, Acorn decided that page &FC should be called "Fred", and page &FD should be called "Jim". While Fred is intended for



Fig. 1. The basic method of using the 1MHz Bus to provide 64K of extended memory.

use with a number of add-on devices, Jim is intended to provide 256 pages of extended memory. An extra 64k of memory in other words

It does this in conjunction with the uppermost address (&FCFF) in Fred. Jim on its own only provides 256 addresses, but the idea is to switch between various banks (or pages) of 256 addresses using the extended page register (address &FCFF).

In other words, you have an arrangement like Fig. 1, where an address decoder and latch provide an 8-bit output from address &FCFF. This is used as the upper eight address lines for the extended memory. You write to ?&FCFF the value for the eight most significant address lines, and then read/write at addresses in Jim (?&FD00 to ?&FDFF)

The eight least significant address lines act as such for the extended memory, with NPGFD being used to activate this memory when the appropriate address range is accessed. The bottom 32k of this extended memory is reserved for system use, but the upper 32k is free for user applications.

Like all forms of memory which extend the limits of the microprocessor, this system is relatively slow. It is not aided by the fact that each block of extended RAM is just 256 bytes. Most other systems operate with a block size of something more like 16k, like the BBC computer's sideways RAM. In practice you are unlikely to bother with this form of extended memory, and page &FD is then available for other purposes, as is address &FCFF.

Various other recommendations about devices connected to the 1MHz Bus are made by Acorn. One of these is that each device should load the bus by no more than LS TTL input. Also, devices which connect

to this port should have their own mains power supply unit.

This explains the lack of any power sup-ply output on the 1MHz Bus. The recommended connecting lead length is 600 millimetres, and add-ons should have a standard 1MHz Bus socket so that a series of devices can be strung together using the "chain" (series) method of connection.

It is obviously important for producers of commercial add-ons to adhere to the Acorn recommendations, to ensure that their units are, as far as reasonably possible, compatible with other 1MHz Bus addons. The home constructor is free to decide for his or herself whether or not to follow these recommendations.

If you have lots of add-ons on other ports, including disk drives powered from the power port, trying to power 1MHz Bus add-ons from the computer would probably be a bit risky. On the other hand, if you only have a couple of small add-ons connected to the user and analogue power ports, plus self-powered disk drives or a cassette based system, it is perfectly acceptable to power 1MHz Bus add-ons from the computer.

Tapping off power from one of the other ports should not prove to be difficult. Similarly, there would seem to be no point in providing a 1MHz Bus for other peripherals if you only intend to use a single device on this port.

#### Expansion

This is something you have to weigh-up and decide for yourself. The 1MHz Bus not only has tremendous expansion potential, but it is easy to use if you take a few short cuts such as minimal address decoding,

As an example, standard chips in the 65\*

\*and 68\*\* series of peripherals can be interfaced to this port with little or no extra circuitry being required. Basically you just connect each terminal of the peripheral chip to the corresponding line on the 1MHz Bus

Either NPGFC or NPGFD should be used to drive a negative chip select input of the peripheral chip, or a positive chip select input should be driven from one of these lines via an inverter. Any other chip select inputs should be wired to their active logic levels.

In this way it is possible to connect two peripheral devices to the 1MHz Bus without the need for any additional address decoding. One device occupies page &FC while the other occupies page &FD. Of course, with the BBC model B computer, clean-up circuits are needed for both the decoded page-select outputs of the port, but this requires nothing more than a couple of quad gate integrated circuits costing a few pence each.

Peripheral chips not intended for use with the 6502 microprocessor might not be so easily interfaced to this port. Where possible it is probably best to use 68\*\* or 65\*\* series peripheral chips rather than those for (say) the 8080 series of microprocessors.

However, most computer peripherals can be interfaced to the 1MHz Bus, including such things as UARTs, analogue to digital converters, and digital to analogue converters. Like any general purpose computer expansion bus, you can interface practically anything to it.

Next Month: We will take a detailed look at ways of connecting a range of peripheral devices to this port, and using them in practice.

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#### FORGOTTEN DREAMS

THE short history of the small robot is littered with the bright hopes and dreams of investors and inventors which have come to nothing. Colne Robotics with the Armdroid, L. W. Staines and their Ogres and IGR's Zero are only a few examples of products which briefly shone before they were eclipsed.

Armdroid was more a "now-you-seeme-now-you-don't" as a variety of companies attempted to keep it on the market. It is in a "you-don't" phase at the moment but that is not to say that it will not reappear in the near future.

Even some of those robots which have survived have had a far from easy time. Valiant Research had to become Valiant Technology in order for the turtle to continue.

#### "RETURN"

Now, after disappearing for a number of years Remcon's Teach Robot has returned to the scene. The basic hardware is much the same but this time it is being offered completely built instead of as a kit and it is linking its fortunes to the various versions of the IBM PC.

Another change is that it is being offered this time as part of a package, the other components comprising an interface and control software.

Geoff Chapman of Remcon said that he thought the time was now right for another effort at making the machine a success. He emphasised that the Teach Robot had not disappeared in the intervening period. Sales, though not at the level of its first year, had been steady to various parts of the world.

It had been decided to give it an extra push when the IBM software and interface had become available. At a price of about £1,000, the West German-built arm has five axes plus gripper. The feature which sets this arm apart from the rest is that four axes are driven by linear rams. The rams are driven by lead screws but Remcon says that they are representative of hydraulic or pneumatic rams as used in full-size industrial machines.

The rams, which work on the waist, shoulder, arm and wrist up and down, are powered by d.c. motors with optical shaft encoders to provide feedback. Waist rotation and gripper movements are driven by worm and worm wheel. All axes have limit switches and ramping facilities to prevent damage and provide smooth movement.

Movement can be limited by the use of the rams and the shoulder, arm and wrist up and down can move through only 90 degrees. The waist on the original machines also only had 90 degrees of movement but this has been increased to 270 degrees. The wrist can also turn through 270 degrees and the gripper can open 180 degrees. It can carry a maximum load of 200 gms.

#### INTERFACE

When the robot first appeared about five years ago it was offered with a general interface and a limited amount of software, centred on the Spectrum. It could also be controlled by a keypad.

Chapman said that there had been an attempt to get BBC software so as to be able to enter the education market but the German software house did not show much interest as the BBC did not have a large market outside Britain.

He thought that the decision to link the arm to IBM and IBM XT and AT compatibles would make it much more appealing. There are also plans for creating a work cell-the conveyor is ready but other items are still at the design stage. It is hoped that there might be something to show at the CDT exhibition in the autumn.

Remcon claims the software has been developed to give a simple and user friendly system. To enable inexperienced users to get an idea of how the system works the sotware contains a short "demo" program showing simultaneous and single step operation.

A double-sided interface card plugs into any of the free ports of the computer. They do not interfere with the working of the computer allowing it to be used for other purposes while the card is plugged in.

Chapman said that software was being developed to allow eight arms to be controlled at the same time. That would require eight cards to be plugged into the port.

As usual programs can be saved, edited and downloaded to and from the disc storage. The system provides for a screen display of the programs stored, the display being in three sections, one across the top of the screen with two columns beneath.

The left-hand column shows the program being run, with a cursor indicating the part of the program in operation. The top section gives the state of the six motors, both the called for position and the actual position. The rest supplies information on the various key functions. Remcom says that knowledgeable users should be able to change this section so as to show their own required information.

#### ATARI/ROBOKIT

As mentioned by Richard Pawson in his interview (EE September 1988) the Atari version of his company's-Personal Robots Ltd-Robokit robotic development and teaching system is now available from Atari, or its distributors, at a cost of about £80.

For that users, and it is said to be ideal for secondary schools, get an interface for the ST range, 520 and 1020, to drive d.c. motors, lights and solenoids, plus eight inputs for microswitches or simple sensors and two inputs for microswitches or simple sensors and two outputs for Lego Technic shaft encoders, a manual and software. The manual contains instructions for building five projects from Lego Technic.

The company says that it is the software which really sets the system apart from the rest. It is based on the larger, more complex version which has been developed over four years at a cost of £1.2m. Making use of the Gem interface it starts with a picture of the model on screen, control is effected by adding control icons and assigning messages to them. From there it is possible to move onto building sequences of messages and developing event-driven applications.

During the next two years a number of add-on packages are planned including a vision processing system. A more complex version, Professional Robokit, is being prepared and, at a cost of about £2,000 for the software, is expected to go on sale in 1990. As might be expected from the price it is thought to be most suitable for universities and commercial development laboratories.

The Remcon Teach Robot is "driven" by an IBM PC and XT or AT compatibles.





Printed circuit boards for certain constructional projects (up to two years old) are available from the PCB Service, see list. These are fabricated in glass postage and packing. Add £1 per board for overseas airmail. Remittances should be sent to: The PCB Service, Everyday Electronics Editorial Offices, 6 Church Street, Wimborne, Dorset BH21 1JH. Cheques should be crossed and made payable to Everyday Electronics (Payment in £ sterling only.)

Readers are advised to check with prices appearing in the current issue before ordering.

NOTE: Boards for older projects-not listed here-can often be obtained from Magenta Electronics, 135 Hunter St., Burton-on-Trent, Staffs DE14 2ST. Tel: 0283 65435 or Lake Electronics, 7 Middleton Close, Nuthall, Nottingham NG16 1BX. Tel: 0602 382509.

NOTE: please allow 28 days for delivery. We can only supply boards listed in the latest issue. Boards can only be supplied by mail order and on a payment with order basis.

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#### LINEARIZING RECTIFIERS

MULTIMETERS often have special scales for their lowest a.c. voltage ranges. The scale markings are not evenly spaced, but become progressively closer towards the zero end. (See Fig. 1). The explanation lies in the behaviour of the built-in rectifier which changes a.c. to the d.c. required to give a meter reading.

The semiconductor diodes used for this job have voltage/current characteristics of the general shape indicated. If a very low d.c. voltage is applied with the polarity which tends to make the diode conduct (often called a *forward* voltage, as opposed to the *reverse* or *inverse* voltage which inhibit conduction) nothing happens.

As the forward voltage is increased, current begins to flow. Further increases produce even more current.

If the straight portion of the curve is extended downwards it cuts the voltage axis at a particular forward voltage, called the forward Zener voltage. This varies with the material and fabrication method used for the diode.

For a normal silicon junction diode at room temperature it's about 700mV. For a silicon Schottky diode it's about half this, for a germanium junction about 240mV and for an old-fashioned copper oxide rectifier about 100mV. Point-contact diodes give more rounded curves with a much less pronounced "knee".

#### RECTIFICATION

When a.c. voltages are applied (as shown below the graph) very small voltages have no perceptible effect. At intermediate voltages there is some conduction near the positive peaks, where the voltage begins to get into the knee region. At high voltages there is conduction over most of each positive half cycle.

This explains the meter scale. Suppose the voltage range is 10V full-scale. Large inputs (say over 3V) produce conduction over most of the positive half cycles and the current is controlled by the range-setting resistance R1.

With small inputs such as 1V conduction occurs only near the peaks and the pointer deflection is less than it would be with a perfect rectifier. (A perfect diode, in this sense, would offer no resistance to forward voltages, however small, and infinite resistance to reverse voltages, large or small.)

On high-voltage ranges (such as 100V full scale) virtually all readable voltages are large enough to clear the knee and the scale markings are most regular. In fact, for high voltages the same markings are often used for both a.c. and d.c.

The diagram Fig. 1 shows sine wave inputs. For other waveforms the response is different but the effect of the knee is always present to some extent. Asymmetrical waves, where the positive half cycles have a different shape from the negative may give different meter readings when the meter leads are reversed (if the rectifier is half-wave as shown). There's something to be said for measuring both ways then averaging the result.

#### BIASING

One way to improve matters is to apply a small d.c. forward bias to the diode. Suppose this sets the working point to *P*. Then a small a.c. signal can produce a significant current. Of course, the meter shows a small deflection when there's no input but this can be tolerated.

A problem is that diodes are strongly affected by temperature. A bias voltage that is right at 20°C will give too much current at 30°C and not enough at 10°C.

So biasing isn't the complete answer. One possibility is to use a transformer to step up low a.c. voltages so that they become high enough to lie in the linear part of the characteristic. This works, but the effect of the transformer is to reduce the impedance of the meter to the input signal. This may lead to low readings when the meter is used on a high impedance circuit. A good transformer is also expensive.

#### AUTOMATIC BIAS

The effect of these considerations on design is that the lowest a.c. voltage range on the cheaper sort of meter is around 10V full-scale, and a specially calibrated scale is used for this 10V range. In electronics, however, it is often desirable to measure a.c. voltages down to a few millivolts.

Some sort of automatic bias system which adjusts itself precisely to the signal level and gives perfect compensation for the knee of the diode curve would be very handy. This is a tall order for a simple passive multimeter but as soon as the meter is made active, by incorporating an amplifier, the problem is greatly eased.

With an amplifier small voltages can be boosted into large ones without the sort of penalties incurred by using transformers. Also, there is a circuit trick which has much the same effect as the wished-for automatic biassing.

In Fig. 2, back-to-back diodes are connected as the negative feedback path of a difference amplifier such as an op. amp. When a diode is non-conductive its high resistance blocks the feedback path. With no negative feedback the amplifier gives its full normal gain.

When a large signal is applied the diodes conduct, closing the negative feedback loop and since feedback is

Fig. 1. Diode characteristics. Small forward voltages are not sufficient to cause conduction. When a sine wave is applied current flows over a portion of the positive half cycles which increases with signal amplitude. This explains why a.c. meter scales are cramped at the low voltage end.



Fig. 2. Diodes as feedback controllers. The amplifier gives normal gain until the output signal overcomes the diode "knee" voltage and D1 or D2 conducts. The feedback path is then completed and gain falls to 1. The effect is to place each half-cycle of output on a pedestal equal to the knee voltage, if this output is rectified by other diodes it gives an almost exact measure of the signal amplitude.



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now 100 per cent reducing the gain to 1. The effect of the diodes is to permit normal gain until the knee voltage is reached then reduce gain to 1.

Suppose the signal is 100mV peak and the amplifier gain is 100, and the knee voltage is 500mV. As the signal wave rises from the zero line full gain is then obtained until the output reaches 500mV, when gain falls to 1.

With our values, an input of 5mV gives 500mV output. So the first 5mV of our 100mV signal is fully amplified and the other 95mV is not, appearing at the output as a replica of the input wave. The effect of all this is to produce an output wave in which each half cycle of the input stands on a pedestal equal to the knee voltage.

If this output is now applied to diodes identical with D1 and D2 the voltage pedestals overcome the knee almost instantaneously and conduction then follows a replica of the input wave. In other words, the diode feedback arrangement creates something very like the automatic bias we've been looking for.

#### **PRACTICAL CIRCUITS**

In describing the principal I have as usual made some simplifying assumptions. One is that the knee is abrupt, when in practice of course it is gradual.

In practice, however, the Fig. 2 type of circuit works very much as described. The important thing is that in effect the knee voltage is reduced by the gain of the amplifier. A number of practical circuits exist in which this is made use of to make rectifiers give a more faithful response to a.c. signals.

One very convenient arrangement is shown in Fig. 3. This is just a familiar class-B complementary output stage driven by an op. amp. Negative feedback is taken from the load RL to the inverting input. To get there the feedback has to have passed through the base-emitter junctions of the output pair TR1 and TR2.

These junctions are of course diodes and so the circuit behaves like Fig. 2 as far as feedback is concerned and large signals are subject to a gain of 1. The load current is  $V_{out}/R_L$ , and this is the same as  $V_{in}/R_L$  when gain is 1.

The complementary output stage is unbiassed so there is no standing current through the transistors. The meter in the collector of TR2 reads zero.

When a signal large enough to overcome the knee voltage arrives, TR1 and TR2 conduct on alternate half cycles. Each transistor thus acts as a half wave rectifier. The meter reads half the load current.

As before, only a tiny portion of a large signal is lost in overcoming the



Fig. 3. Practical active rectifier, negative feedback stabilizes the gain and linearizes the transistor base—emitter characteristic.

knee voltage. Once overcome, the amplifier is gain stabilized by the negative feedback and this evens out the frequency response.

So long as the amplifier has enough gain the rectification is linearized. In practice this means that with an ordinary op. amp performance just begins to fall off towards the upper frequency limit of the audio band.

#### **RANGE SETTING**

The op. amp acts as a buffer with a fairly high input impedance. Range setting can be carried out with a voltage divider at the input, giving a constant input resistance on all ranges.

The current in R1 is the input/output voltage divided by R1. Since TR2, which supplies the meter current, carries only alternative half cycles the meter current is half the load current. It can be set to whatever the meter needs (within reason) by selecting or adjusting R1.

Suppose the input voltage at full scale is to be 1V and the meter takes 1mA at full scale. Then the load must carry 2mA.

At first sight this seems to call for a load resistance of 500 ohms. However, this is true only for square wave signals. For sine waves load current is less than 2mA away from the peaks and the value of R1 has to be reduced to about 0.9 times the apparent resistance to allow for this.

The diode D1 across the meter is not for rectifying but for meter protection. If the voltage across the meter coil resistance exceeds the knee voltage D1 conducts and bypasses the meter. With some meters it will pay to increase the coil resistance by adding a series resistance so that D1 comes on earlier. With others, D1 may conduct too soon and to avoid this two diodes in series may be needed.

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