INCORPORATING ELECTRONICS MONTHLY FULLY S.O.R.

FREE INSIDE! 48 page GREENALED summer sale catalogue EXPERIMENTAL ELECTRONIC PIPE DESCALER Hard Water? Build this experimental descale for under 220.

TRI-STATE THERMOMETER Keep an eye on your engine temperature

AUDIO AMPLIFIED DESIGN ENGINEERING OR ALCHEMY? PART 1 John Linsley Nood investigates

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AUGUST 1993

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THE No. 1 INDEPENDENT MAGAZINE for ELECTRONICS, TECHNOLOGY and COMPUTER PROJECTS

AMP KITS

30 WATT SINGLE CHANNEL

30 watt rms into 80hm, 20HZ-20KHZ harmonic distortion 0.1%, sensitive to 150MV, mic sensitivity 50MV, 36V x 2 psu's required complete with base, treble and volume controls and heat sink.

Our	Price	:	£8.00 each
Our	Ref	:	EE/W8P7

120+120 WATT STEREO AMP

240 wattsrms into 4 ohm, 10KZ-20KHZ, harmonic distortion less than 0.01%, sensitivity 3MV (phono), 130MV (aux), 2 x 30V psu's required. Our Price : £30.00 each

Our Ref : EE/W30P3

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30 watts rms into 80hm, harmonic distortion .1%, sensitivity 3MV (phono), 130MV (tuner), signal to noise 80db, 36V x 2 psu's required.

: £16.00 each Our Price Our Ref : EE/W16P1

300 WATT MONO AMP

Our Ref

300 watt rms output, harmonic distortion less than 0.05% 10KHZ. 2 x 60V psu's required. Our Price : £40.00 each

: EE/W40P3

WE HAVE HUNDREDS AND HUNDREDS MORE STOCK LINES - TOO MANY TO LIST IN ONE ADVERT ! HERE ARE JUST A FEW OF THE

PRODUCTS LINES WE STOCK

MICROWAVE CONTROL PANEL. Mains operated, with touch switches. Complete with 4 digit display, digital clock, and 2 relay outputs one for power and one for pulsed power (programmable). Ideal for all sorts of precision timer applications etc. Now only $\pounds4.00$ ref 4P151. Good experimenters board.

FIBRE OPTIC CABLE. Stranded optical fibres sheathed in black PVC, Five metre length £7,00 ref 7P29R or £2 a metre. 12V SOLAR CELL. 200mA output ideal for trickle charging etc. 300

mm square. Our price £15.00 ref 15P42R. Gives up to 15v PASSIVE INFRA-RED MOTION SENSOR. Complete with day-

light sensor, adjustable lights on timer (8 secs -15 mins), 50 range with a 90 deg coverage. Manual overlde fadility. Complete with wall brackets, bulb holders etc. Brand new and guaranteed, Now only £19.00 ref 19P29

Pack of two PAR38 bulbs for above unit £12.00 ref 12P43R VIDEO SENDER UNIT. Transmit both audio and video signals from either a video camera, video recorder or computer to any standard TV set within a 100' rangel (tune TV to a spare channel). 12V DC op. £15.00 ref 15P39R Suitable mains adaptor £5.00 ref 5P191R, Turn your camcorder into a cordiess cameral

FM TRANSMITTER Housed in a standard working 13A adapter (bug is mains driven). £26.00 ref 26P2R. Good range.

MINATURE RADIO TRANSCEIVERS. A pair of walkie talkies with a range of up to 2 titlometres. Units measure 22x52x155mm. Complete with cases and earpieces £30.00 ref 30P12R

FM CORDLESS MICROPHONE. Small hand held unit with a 500' rangel 2 transmit power levels. Reqs PP3 battery, Tuneable to any FM receiver. Our price $\pounds15$ ref 15P42AR.

12 BAND COMMUNICATIONS RECEIVER, 9 short bands, FM. AM and LW DX/local switch, tuning 'eye' mains or battery. Comp with shoulder strap and mains lead, £19 ref 19P14R, Ideal for listening all over the world.

CAR STEREO AND FM RADIO. Low cost stereo system giving 5 watts per channel. Signal to noise ratio better than 45db, wow and flutter less than .35%. Neg earth. £19.00ref 19P30

LOW COST WALIKIE TALKIES. Pair of battery operated units with a range of about 200°. Our price £8.00 a pair ref 8P50R. Ideal for garden use or as an educational toy.

7 CHANNEL GRAPHIC EQUALIZER plus a 60 watt power amp! 20-21KHZ 4-BR 12-14v DC negative earth. Cased, £25 ref25P14R, NICAD BATTERIES. Brand new top quality. 4 x AA's £4,00 ref 4P44R, 2 x C's £4,00 ref 4P73R, 4 x D's £9,00 ref 9P12R, 1 x PP3 £6.00 ref 6P35R Pack of 10 AAA,s £4,00 ref 4P92R.

MENINERAL COOLDS OR DISC 5 100 TRADE OR DISC FROM GOVERNMENT, SCHOOLS, UNIVERSITIES & LOCAL AUTHORITIES WELCOME. ALL GOODS SUPPLIED SUBJECT TO OUR CONDITIONS OF SALI AND UNLESS OTHERWISE STATED GUARANTEEL FOR 30 DAYS.

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THIS MONTHS SPECIAL OFFERS

COMMODORE 64 & PSU

Once again available due to a bulk purchase. Commodore 64 together with Powet Supply, supplied and tested in working order.

Our Price: £50.00 Ref: EE/W50P1

PIR MOVEMENT DECTECTOR

Once again we have acquired stocks of this popular line and are able to offer you a very high quality and professional detector at only ***£15.00***. Range: 20 Mtrs with a 90° arc. Day and Night Mode Dimensions: 15cm x 9cm x 11cm. New and boxed complete with installation guide

Our Price:£15.00 Our Ref :EE/W15P2

ELECTRIC MOTOR - 3 PHASE

GEC Alpak Induction Motor. This is an extremely well made industrial 3 phase motor. BRAND NEW & UN-USED. Supplied with full installation and maintenance literature

Our Price: £60.00 Our Ref : EE/W60P1

PORTABLE ALARM SYSTEM

'PAL' Portable Multi Bean Scanning System. Lockable Stand-alone PIR unit with removable keys(3 supplied). This unit uses a PP3 battery and when activated emits a piercing SHRILL! The units scans the room and memorises the layout. Should this change, the alarm is triggered. There is a 60 second exit delay.

Our	Price:	£17.00
REF	;	EE/W17P1

CAR PHONE HANDSETS YUPPIES LOOKOUT!!

Brand New High quality handsets originally intended for use with Phillips Mobile Phone System. We have acquired a number of these handsets, supplied in two parts, speaker and combined phone rest unit plus handset with LCD display, LED indicators and push button dialling. 6 additional keys as well as the conventional 12 numeric keypad. No transmitter /receiver. 5 Mtr 12 core black cable attached, Excellent Dummy Mobile Phone!!!

Price: £6.00 each Ref : EE/W6P3

MINI MULTI TESTER

Mini multi-tester with 9 ranges. AC Volts up to 1000. DC Volts up to 500.

Ohms/mA ranges

1K ohm/V. Ideal for pocket or toolbox use and at only £5.00 each - replaceable!!

Our Price: £5.00 Our Ref : EE/W5P2

HEAT DETECTOR

This is a Japanese made combination type heat detector which could be linked to a suitable fire alarm panel. 105mm dia, 30V operational.

Our Price:£4.00 OurRef:EEW4P1

SOME OF OUR PRODUCTS MAY BE UNLICENSABLE IN THE UK



USEFUL POWER SUPPLY - £5.00 14.5V DC @ 400Ma battery charger Plug in type with sleeved pins. Lead terminates in 3 pin din plug, easily removeable. Originally intended for mobile phone charging. (Large quantities available) Our Price : £5.00 each Our Ref 1 EE/W5P8

AMSTRAD PRINTER MECHS

We have both Amstrad LQ3500 and Amstrad DMP4000 printer mechanisisms No case or electronics but complete with print head. Ideal for replacement. The mechanism comes with two high quality stepper motors which together with the print head are worth much more than we are selling the whole unit for !!!! This unit could also be utilised for a number of other projects including coil winding, laser scanning(*!) etc.

(Quantity Prices available on application) AMSTRAD LO3500 units

ALIS.	river n?	200	oo unites
Our	Price	:	£10.00 each
Our	Ref	:	EE/W10P5
AMS	TRAD DI	œ4	000 units
Our	Price	:	£20.00 each
Our	Ref	:	EE/W20P3

MANY MORE SPECIAL OFFERS IN OUR REGULAR NEWSLETTERS

PC CORNER

PC CASES desk top case +psu £51.60 ref BPC/C1.Deluxe slimline case +psu £60.00 ref BPC/C2, Minitower case +psu £51.60 ref BPC/C3, Deluxe midi case +psu £90.00 ref BPC/C4. MONITORS Mitac 14* SVGA .39DP £174.00 ref BPC/MO2, Mitac 14" SVGA .28DP £202.00 ref BPC/MO1.

MEMORY 256K Simm 70ns £8.40 ref BPC/MI1, 1MB Simm 70ns £26.40 ref BPC/MI2, 4MB Simm 70ns £96.00 ref BPC/MI3 MICE 2 button serial mouse with 3.5* s/ware. £8.40 ref BPC/MI6, 3 button serial mouse with 3.5" s/ware £9.60 ref BPC/MI7 KEYBOARDS 102 AT UK standard keyboard £18.60 ref BPC/ MI4, Deluxe keyboard 102 AT UK £26.40 ref BPC/MI5. SOFTWAREMS DOS 5 DEM version £39.60 ref BPC/MI8, MS

WINDOWS 3.1 OEM version £42.00 ref BPC/MI9, 6 286-16 Headland chipset £46.80 ref BPC/MB1, 386SX-33 Acer

chipset £82.80 ref BPC/MB2, 386SX-40 UMC with 64K cache £110.00 ref BPC/MB3, 4865X-25 UMC with 64K cache £191.00 ref BPC/MB4, 486DX-33 UMC with 256K cache £378.00 ref BPC/ MB5, 486DX-68 UMC with 256K cache £515.00 ref BPC/M86. FLOPPY DRIVES 1.44mb 3.5* drive £32.34 ref BPC/DD05, 1.2n 5.25* drive £38.40 ref BPC/DD02, 3.5*-5.25* mounting kit £5.00 1.2mb

ref BPC/DD07 HARD DRIVES 42MB IDE 17ms £99.00 ref BPC/DD01, 89MB

IDE 16ms £166.00 ref BPC/DD02_130MB IDE_15ms £215.00 ref BPC/DD03, 213MB IDE 14ms £298.00 ref BPC/DD04. VIDEO CARDS 256K C&T 8 bit SVGA card £22.20 ref BPC/

VC01, 512K Trident 9000 16 bit SVGA card £31.20 ref BPC/ VC02, 1MB Trident 8900 16 bit SVGA card £45.00 ref BPC/VC03, 1MB Cirrus AVGA3 16.7M colours £48.00 ref BPC/VC04, 1MB Tseng multimedia £82.80 ref BPC/VC05 ADD ON CARDS Multi I/O card 2s, 1p, 1g, 2fdd, 2hdd£11.00 ref

BPC/AOC01, ADLIB sound card with speakers £37,00 ref BPC/ AOC02, Orchid sound card with speakers £53,00 ref BPC/ AOC03

EXAMPLES OF COMPLETE SYSTEMS 386SX-33 SYSTEM

3865X-33 board at £82,80, case £51.60, 2MB ram £52.80, 42MB drive £99.00, SVGA colour monitor £174.00, 102 keyboard, £25 build fee if required. Total £579.34 486DX-33 SYSTEM

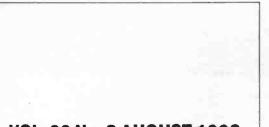
486DX-33 board £378.00, case £51,60, 2MB ram £52.80, 89MB drive £166.00, 512 SVGA card £31.20, 3.5° FDD £32.34, multi I/O card £11.00, SVGA monitor £174.00, 102 keyboard £18.60, £25 build fee if required. Total £939.84

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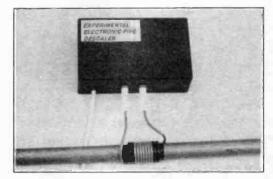
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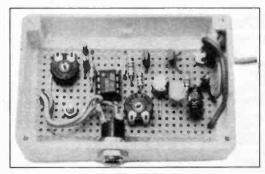


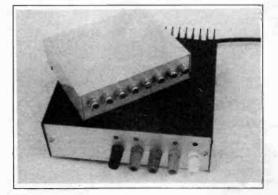
The No. 1 Independent Magazine for Electronics, Technology and Computer Projects

ISSN 0262 3617 PROJECTS ... THEORY ... NEWS ... COMMENT ... POPULAR FEATURES ...









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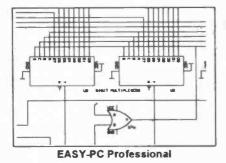
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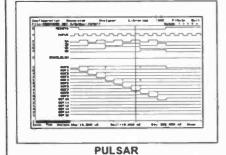
Electronic Designs Right First Time?

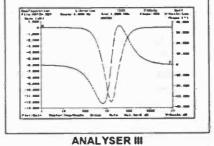
From Schematic Capture -

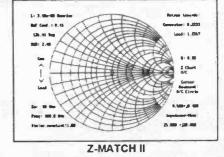


Create your schematics quickly and efficiently on your PC using **EASY-PC Professional**. Areas of the circuit can be highlighted on screen and simulated automatically using our **PULSAR, ANALYSER III** and **Z-MATCH II** simulation programs.

through Analogue and Digital Simulation -





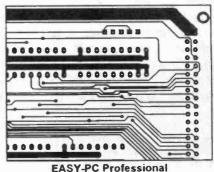


If the results of the simulations are not as expected, the configuration and component values of the circuit can be modified until the required performance is achieved.

to Printed Circuit Board Design!

The design, complete with connectivity, can then be translated into the PCB. The connectivity and design rules can be checked automatically to ensure that the PCB matches the schematic.

Affordable Electronics CAD				
EASY-PC: Low cost, entry level PCB and Schematic CAD.	\$195.00	£98.00		
EASY-PC Professional: Schematic Capture and PCB CAD. Links directly to ANALYSER III and PULSAR.	\$375.00	£195.00		
PULSAR: Low cost Digital Circuit Simulator ~ 1500 gate capacity.	\$195.00	£98.00		
PULSAR Professional: Digital Circuit Simulator ~ 50,000 gate capacity.	\$375.00	£195.00		
ANALYSER III: Low cost Linear Analogue Circuit Simulator ~ 130 nodes.	\$195.00	£98.00		
ANALYSER III Professional: Linear Analogue Circuit Simulator ~ 750 nodes.	\$375.00	£195.00		
Z-MATCH II: Smith Chart program for RF Engineers - direct import from ANALYSER III.	\$375.00	£195.00		
We operate a no penalty upgrade policy. You can upgrade at any time to the professional version of a program for the difference in price.	US\$ prices include Post and Packing	Sterling Prices exclude P&P and VAT.		



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CHARGE-15

SOUND ACTIVATED CAMERA TRIGGER

Most cameras have a self-timer facility which enables photographs to be taken about five to ten seconds after the release button has been activated. This facility is included primarily to enable the photographer to take self portraits. In many instances a simple self-timer facility is quite adequate, but the delay sometimes proves to be too short. In particular, it is often inadequate when the photographer has to position him or herself in a group of people. The delay can also be too short if the photographer wishes to pose with a prop, such as "the one that did not get away", or some sort of cup or trophy.

With this unit the general idea is to get into position and then shout to activate the trigger. After a brief delay to allow the operator to compose him or herself, the camera is then automatically triggered.

Quarter-hour charging for AA batteries

Charge-15 is a mains-operated unit designed to charge a set of the popular "AA" type (HP7 size) nickel-cadmium cells in about 15 minutes – hence the name. This time compares with 15 hours for commercial standard charging units and between two and five hours for the fast charge variety. It will thus be found useful whenever batteries are likely to let you down suddenly or where it is essential to have ready access to a fully-charged set.

Ultra-fast charging is not thought to be particularly good for batteries and it is possible that they may suffer from some shortening of life. However, in tests on the prototype unit, cells were repeatedly charged and discharged over 50 cycles and were found to be still in perfectly good condition at the end with no significant loss of capacity. Since a nickel-cadmium cell costs little more than the throw-away alkaline variety these days (about £1 compared with 70p) any shortening of life is thought to be of little consequence even taking into account the much lower energy content (one fifth approximately) of rechargeable cells compared with alkaline ones.

LIGHT WORK

A handy aid for checking trailer or caravan lights

Light Work provides a convenient way of testing the operation of rear lights on a caravan or trailer. It will thus be found useful for any reader who owns a caravan, boat or motorcycle trailer, horse box, etc. With this unit you can check the rear lights from the drivers seat without assistance, trailing wires or mirrors etc. It's all done with electronics!



SURVEIMENCE PROFESSIONAL QUALITY KITS

Whether your requirement for surveillance equipment is amateur, professional or you are just fascinated by this unique area of electronics SUMA DESIGNS has a kit to fit the bill. We have been designing electronic surveillance equipment for over 12 years and you can be sure that all of our kits are very well tried, tested and proven and come complete with full instructions, circuit diagrams, assembly details and all high quality components including fibreglass PCB. Unless otherwise stated all transmitters are tuneable and can be received on an ordinary VHF FM radio.

UTX Ultra-miniature Room Transmitter

Smallest room transmitter kit in the world! Incredible 10mm x 20mm including mlc. 3-12V operation. 500m range. £16.45

MTX Micro-miniature Room Transmitter

Best-selling micro-miniature Room Transmitter

Just 17mm x 17mm including mic. 3-12V operation. 1000m range......£13.45 STX High-performance Room Transmitter

Hi performance transmitter with a buffered output stage for greater stability and range.

VT500 High-power Room Transmitter

Powerful 250mW output providing excellent range and performance. Size 20mm x 40mm. 9-12V operation. 3000m range..... £16.45

VXT Voice Activated Transmitter

Triggers only when sounds are detected. Very low standby current. Variable sensitivity and delay with LED indicator. Size 20mm x 67mm. 9V operation. 1000m range...£19.45

HVX400 Mains Powered Room Transmitter

Connects directly to 240V AC supply for long-term monitoring. Size 30mm x 35mm. 500m range £19 45

SCRX Subcarrier Scrambled Room Transmitter

Scrambled output from this transmitter cannot be monitored without the SCDM decoder connected to the receiver. Size 20mm x 67mm. 9V operation. 1000m range......£22.95 SCLX Subcarrier Telephone Transmitter

Connects to telephone line anywhere, requires no batteries. Output scrambled so

SCDM Subcarrier Decoder Unit for SCRX

Connects to receiver earphone socket and provides decoded audio output to headphones. Size 32mm x 70mm. 9-12V operation ..£22.95

ATR2 Micro Size Telephone Recording Interface

Connects between telephone line (anywhere) and cassette recorder. Switches tape automatically as phone is used. All conversations recorded. Size 16mm x 32mm. Powered from line £13.45



BLTX/BLRX Radio Control Switch

Remote control anything around your home or garden, outside lights, alarms, paging system etc. System consists of a small VHF transmitter with digital encoder and receiver unit with decoder and relay output, momentary or alternate, 8-way dil switches on both boards set your own unique security code. TX size 45mm x 45mm. RX size 35mm x 90mm. Both 9V operation. Range up to 200m.

Complete System (2 kits)	£50.95
Individual Transmitter DLTX	£19.95
Individual Receiver DLRX	£37.95

MBX-1 Hi-Fi Micro Broadcaster

Not technically a surveillance device but a great idea! Connects to the headphone output of your Hi-Fi, tape or CD and transmits Hi-Fi quality to a nearby radio. Listen to your favuurite music anywhere around the house, garden, in the bath or in the garage and you don't have to put up with the DJ's choice and boring waffle. Size 27mm x 60mm. 9V operation, 250m range £20.95

UTLX Ultra-miniature Telephone Transmitter

Smallest telephone transmitter kit available. Incredible size of 10mm x 20mm! Connects to line (anywhere) and switches on and off with phone use. All conversation transmitted. Powered from line. 500m range. £15.95

TLX700 Micro-miniature Telephone Traesmitter

Best-selling telephone transmitter. Being 20mm x 20mm it is easier to assemble than UTLX. Connects to line (anywhere) and switches on and off with phone use. All conversations transmitted. Powered from line. 1000m range £13.45

STLX High-performance Telephone Transmitter

High performance transmitter with buffered output stage providing excellent stability and performance. Connects to line (anywhere) and switches on and off with phone use. All conversations transmitted. Powered from line. Size 22mm x 22mm. 1500m range £16.45

TICX900 Signalling/Tracking Transmitter

Transmits a continous stream of audio pulses with variable tone and rate. Ideal for signalling or tracking purposes. High power output giving range up to 3000m. Size £22.95 25mm x 63mm. 9V operation.

CD400 Pocket Bug Detector/Locator

LED and piezo bleeper pulse slowly, rate of pulse and pitch of tome increase as you approach signal. Gain control allows pinpointing of source. Size 45mm x 54mm. 9V operation . £30.95

CD600 Professional Bug Detector/Locator

Multicolour readout of signal strength with variable rate bleeper and variable sensitivity used to detect and locate hidden transmitters. Switch to AUDIO CONFORM mode to distinguish between localised bug transmission and normal legitimate signals such as pagers, cellular, taxis etc. Size 70mm x 100mm. 9V operation ... £50.95

OTX180 Crystal Controlled Boom Transmitter

Narrow band FM transmitter for the ultimate in privacy. Operates on 180 MHz and requires the use of a scanner receiver or our QRX180 kit (see catalogue). Size 20mm x 67mm. 9V operation. 1000m range... £40.95

QLX180 Crystal Controlled Telephone Transmitter

As per QTX180 but connects to telephone line to monitor both sides of conversattions. 20mm x 67mm. 9V operation. 1000m range..... ...£40.95

QSX180 Line Powered Crystal Controlled Phone Transmitter As per QLX180 but draws power requirements from line. No batteries required. Size 32mm x 37mm. Range 500m£35.95

QRX180 Crystal Controlled FM Receiver

For monitoring any of the 'Q' range transmitters. High sensitivity unit. All RF section supplied as a pre-built and aligned module ready to connect on board so no difficulty setting up. Outpt to headphones. 60mm x 75mm. 9V operation ... £60.95

A build-up service is available on all our kits if required.

UK customers please send cheques, POs or registered cash. Please add £1.50 per order for P&P. Goods despatched ASAP allowing for cheque clearance. Overseas customers send sterling bank draft and add £5.00 per order for shipment. Credit card orders welcomed on 0827 714476.

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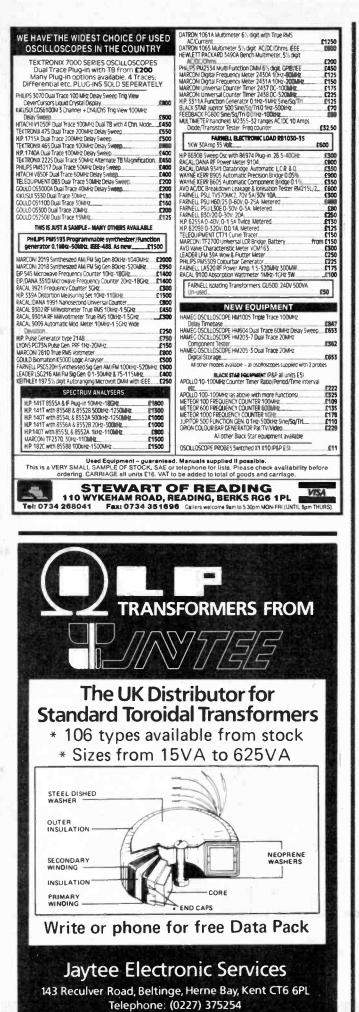
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Everyday with Practical Electronics, August, 1993



SUMMER 1993 CATALOGUE



The new enlarged **Cirkit Catalogue is out now!**

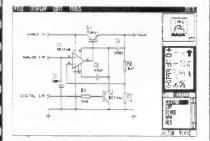
- ➤ 32 more pages
- New range of Kenwood 'scopes
- ► The latest scanning receivers and accessories
- New section of low cost security products
- Extended range of Velleman kits including: 250W 12Vdc to 220Vac inverter, in-car amplifier power supply, 200 and 400W amplifiers, suppressed lamp dimmer, halogen lamp dimmer, day/night thermostat and telephone remote control unit
- New test equipment, includes: 2.3GHz bench frequency > counter, EPROM emulator/programmer, portable 'scopes and bench function generators
- ► Host of new components, including: compression trimmers, variable capacitors, connectors, fuses, and fuseholders, potentiometers, IC's, soldering irons and lead free solder
- Published 27th May 1993
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ISIS - SCHEMATIC CAPTURE

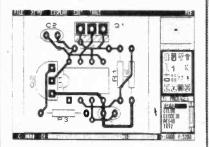
Easy to use yet extremely powerful schematic entry system with all the features you need to create input for ARES or other CAD software. Now available in a super-fast 32 bit version capable of handling huge designs even on A0-sized sheets.



- Graphical User Interface gives exceptional ease of use - two mouse clicks will place & route a wire
- Automatic wire routing, dot placement, label generation.
- 2D drawing capability with symbol library.
- Comprehensive device libraries.
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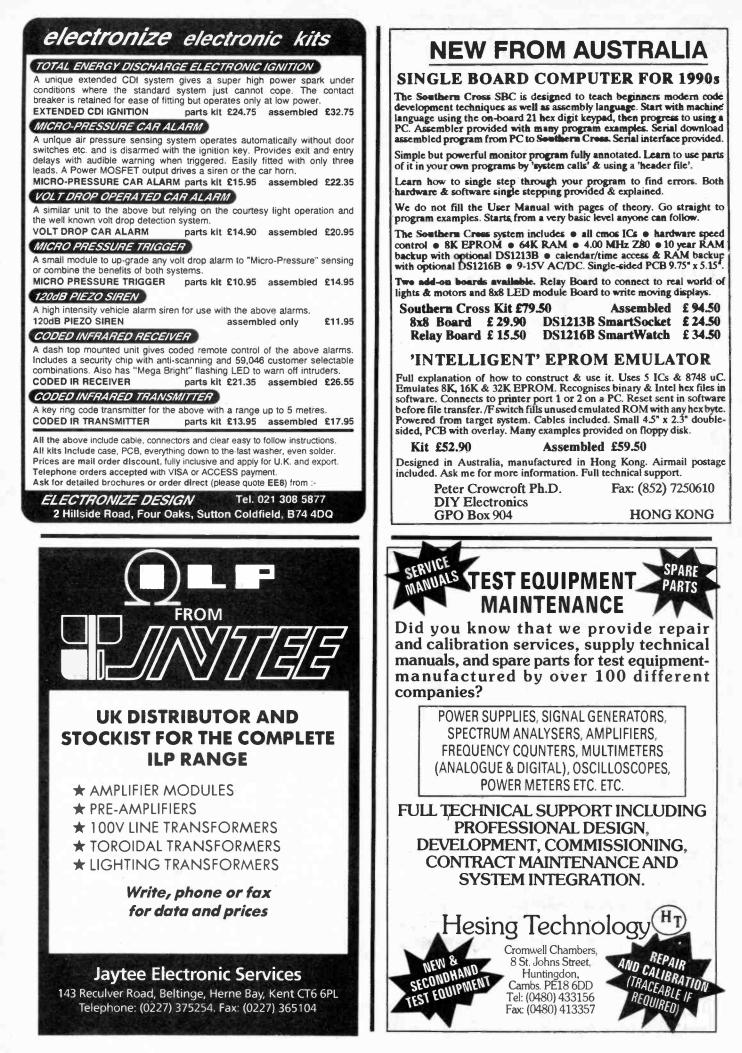
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VOL. 22 No. 8

AUGUST '93

GOLDEN EARED

When you get to the end of John Linsley Hood's Audio Amplifier Design -Engineering or Alchemy series, you will find the following paragraph. "Much of what is claimed by the 'golden eared' fraternity, does exist - but is much, much smaller than they think - and when it exists, and it sometimes doesn't, there are good reasons why."

I feel it is one of those statements that will be well quoted over the coming years. The series leads up to this conclusion and explains it "stage by stage", looking first at tests, both listening and engineering, and going on to see how components, even wire, can affect the results. Few designers are as open minded or enlightened as John and his examples and conclusions are interesting and stimulating. The series will make you think and hopefully understand the various views proffered within the hi-fi fraternity.

We are sure that not everyone will agree with everything he says and your letters on the subject are welcome, we must however ask that you keep them as short as possible. We hope to provide a couple of John's amplifier designs to round off the series.

DESCALER

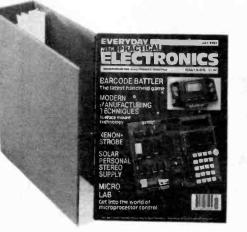
Everytime you look around at the moment the subject of the effects of magnetism on fluids and gases is causing controversy. We have seen various articles and tests on magnets used for car fuel enhancement and even on beer to reduce the requirements of cleaning pipes in Public Houses. We are sure there is more to come on this subject.

We will certainly be interested to hear of readers' experiences with our Experimental Electronic Pipe Descaler and hopefully such information will lead to a better general understanding of the subject by Joe Public. Once again your letters are welcome.

While the action of magnetism on water seems to be well accepted, claims for other magnetic devices aimed at fuel saving are certainly not yet proven, or at least not yet generally accepted as being proven. Anyone want to try a Descaler on their car fuel line or boiler gas pipe?

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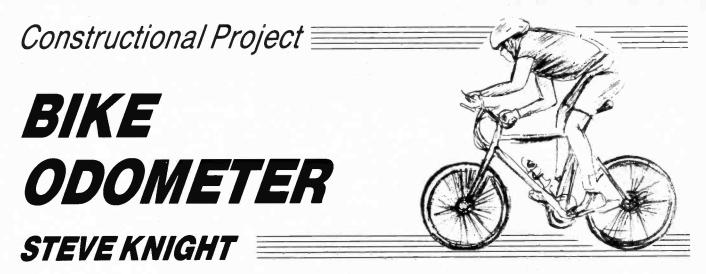
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We would like to advise readers that certain items of radio transmitting and telephone equipment which may be advertised in our pages cannot be legally used in the UK. Readers should check the law before using any transmitting or telephone equipment as a fine, confiscation of equipment and/or imprisonment can result from illegal use. The laws vary from country to country; overseas readers should check local laws.



Pedal power takes on a new meaning with this digital readout distance recorder.

YCLING is good for us all, so they say, and so a year or so ago the author abandoned his car and bought himself a two-wheeled machine with which he took to the leafy lanes whenever the sun was in the sky and strong winds were in another part of the country. All very nice and refreshing, too, but liking to know how far I had gone on each excursion so that my progress (or lack of it) could be quantified, I felt the need for some kind of distance measurer, or "odometer" as they call it in the trade.

Simple mechanical counters affixed to the front fork of the bike and tapped around by a projection attached to one of the spokes were common some years ago and perhaps still are. However, the making of an electronic system was enough of a challenge to get me started in that direction.

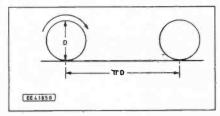


Fig. 1. For each revolution of the wheel the distance travelled is π times the diameter.

There is available a fully computerised odometer from "bike" shops which gives us not only distance but average speed, elapsed time, maximum speed and clock time; no doubt this will be used by entrants for the *Olympics* and the *Milk Race*, but something a little more basic (with component availability) will suit the rest of us.

BASIC PRINCIPLES

Basically any distance measuring system must depend upon counting the number of revolutions a wheel, of known circumference, makes during a particular journey, and interpreting this number in terms of the total distance travelled. For a wheel of diameter D as shown in Fig. 1, the distance travelled per revolution is πD , hence the number of revolutions per mile (or km) is easily found by dividing the mile (or the km) by the distance πD , both in the appropriate units. Knowing by some means how many revolutions have been made over the journey, the total distance can then be determined. To keep things uncomplicated we will work in miles, but anyone who wishes to work in metric may do so by following the same approach to the problem and making the necessary adjustments to the dividing circuitry in the general design.

There are three basic questions to ask before beginning an odometer design; what kind of display should be used; what kind of accuracy is required to make the building of the unit worth the while, and what maximum mileage needs to be recorded?

Well, answering the last question first. Not being an entrant for the *Milk Race* and being completely satisfied with a leisurely ride of perhaps twenty to thirty miles with a country pub somewhere about the halfway mark, I plumped for a maximum readout of 100 miles (161km), in steps of 0 1 mile.

So returning to the first question, what kind of display do we want, this can now be a three-digit l.e.d. system reading up to 99.9 miles. And the accuracy? Well, as we shall see later on, the basic accuracy at the 0.1 mile points can be less than one per cent; however, the *readable* accuracy must lie within the 0.1 mile minimum step.

For instance, at the end of a ride, if the display indicates, say, 23.4 miles, the actual distance may be anything from 23.4 miles to 23.49 miles; you may have only just passed the 23.4 mark or you may be just short of the 23.5 mark. But an "error" of 176 yards (0·1 mile) is not a lot to worry about on a journey of several miles and the added complication of another decimal place to read to an accuracy of 0·01 mile is hardly justified. So down to a practical design.

BASIC CALCULATIONS

The cycle to be fitted with the odometer had 24in. diameter wheels, which are common enough, and the overall diameter with the tyres, measured from hub to ground with the machine upright was 26.2in. Working from this figure, the wheel circumference is $26 \cdot 2\pi$ or 82.3 in. In travelling 0.1 mile, or 6336in., therefore, the wheel will rotate 6336/82.3 = 77 times. What we want, then, is a circuit system based on the block diagram of Fig. 2.

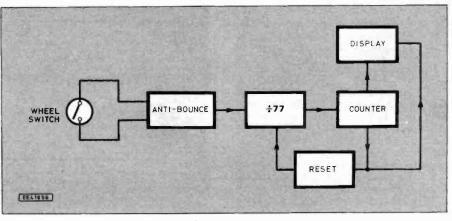
Some sort of wheel operated switch sends a pulse for each rotation of the wheel, via an anti-bounce circuit, to a system that will divide the input by 77, so that an output pulse is obtained for every 77 input pulses. Hence an output pulse is received for each 0 l mile travelled. By counting these pulses and showing the result on a conventional display, the mileage covered will be indicated in steps of 0 l mile up to the maximum of 99 9 miles.

Slight variations in tyre size will, of course, affect things somewhat, but even for changes in the overall wheel diameter of up to 0.5 in., the error will be quite small and of little significance on the accuracy of the readout.

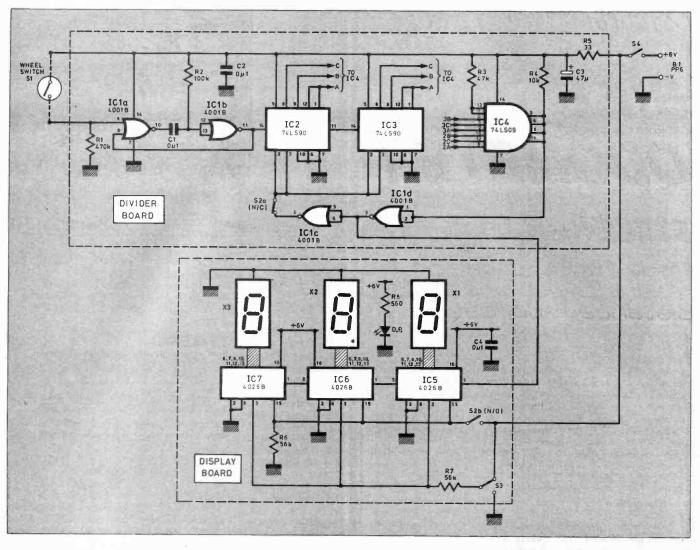
CIRCUIT DESCRIPTION

Division by 77 is not quite so straightforward as division by, say, five or 10. There

Fig. 2. The block diagram for the Cycle Odometer designed for nominal 26in. wheels.



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are a number of ways of achieving the division but the one used employs a couple of 74LS90 decade counter i.c.s followed by a 74LS09 quad 2-input AND gate i.c., with open-collector outputs.

It is possible to use the reset-to-nine facilities on the 74LS90 to divide by 7, but it is easier in this circuit to use the AND gate method. A 4001B quad-input NOR chip additionally serves as a debouncing circuit for the wheel generated pulses and as a resetting and isolating buffer for the dividing circuits.

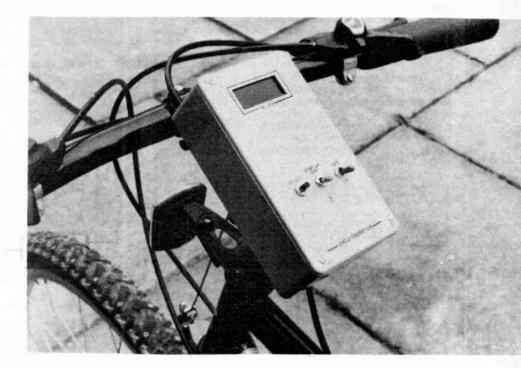
The complete circuit diagram for the Bike Odometer is shown in Fig. 3. This includes both the divider and the l.e.d. display systems. The assembly is on two small p.c.b.s (printed circuit boards), measuring 82mm by 55mm and 82mm by 45mm respectively. These are indicated by the broken outlines on the diagram; the upper being the divider system, debouncer and reset facilities, and the lower the display and the display counter and driver i.c.s.

DIVIDER CIRCUIT

The wheel operated switch S1 (of which more later) closes once per wheel revolution; the resulting pulse passes through the conventional debouncer, IC1a, IC1b, to the input pin (14) of the first of the dividers, IC2. The first counter IC2 divides by 10 in the usual manner and on the tenth pulse input passes an output, from pin 11, to the input pin of IC3. This in turn is connected as a divide-by-10, so although in a normal situation this would enable a count up to 99 to be achieved, the output this time cannot exceed 77 before resetting to zero. Fig. 3. Full circuit diagram for the Bike Odometer. The circuit is built in two stages – Divider board and Display board.

This is easily explained by looking at the circuit again. We have only a two-digit number to deal with and it is convenient to express this as two groups of binary, that is, as 0111 0111.

When the count of 77 is reached, therefore, the A, B, and C binary coded outputs of *both* dividers are all high. When these highs are applied to the inputs of the AND gate IC4, the output of this gate will also





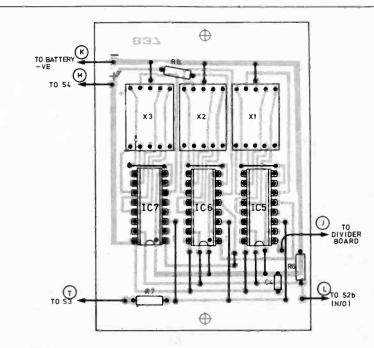


Fig. 5. Display printed circuit board component layout and full size copper foil (opposite) master pattern.

go high, hence a single pulse output is obtained for each group of 77 input pulses (or wheel revolutions) from the wheel switch.

The AND gate used is a 74LS09 (IC4) which as mentioned above comprises four 2-input AND gates with open-collector outputs. This last requirement is necessary as the four outputs can then be connected together with a single load resistor, R4, giving us an 8-input, single-output gate.

We need only six of these inputs for our present purpose, the remaining pair simply being held permanently high by returning them to the positive line by way of resistor R3. An output is then obtained only when all eight inputs are simultaneously high.

The output pulse is then passed to the counter display board by way of a buffering inverter IC1d and also to the reset pins of IC2 and IC3 via a further inverter, IC1c, and the manual reset switch S2a which is normally closed (N/C). Opening this switch momentarily will reset the dividers to zero and is normally used only at the start of a journey.

DISPLAY BOARD

The display board houses the three 7segment l.e.d. displays together with the associated counter-divider chips, IC5 to IC7. The CD4026B counters not only do the necessary counting of the input pulses from the AND gate but also include an output decoder which converts the binary information about the count into the form necessary to drive the l.e.d. displays directly.

The pin-out information is shown in Fig. 4. The circuit is therefore very economical in components and runs satisfactorily from a 6V supply.

The display must be zeroed at the start of a journey and this is accomplished by a momentary closing of the reset switch S2b which is ganged to S2a, the reset for the 7490 dividers. For economy of the battery supply, the display itself can be turned off if necessary by the Display Enable switch S3 during the journey or part of a journey. All the time the readout is switched off, the count continues and the distance travelled will be fully logged and restored when reactivating the display l.e.d. segments. The decimal point on the centre digit is permanently activated via resistor R8 and can be used, when the display itself is off, as an indicator that the circuit is switched on.

Total current consumption averages 140mA with the display enabled and 40mA with the display off.

CONSTRUCTION

A home built electronic system cannot compete for size with the small mechanical or computerized devices mentioned earlier, but the ABS plastic box used for this project does fit neatly on the crossbar of the bike and is sufficiently waterproof to resist anything but a tropical downpour. Those who wish may, of course, use a diecast box of similar dimensions.

A smaller box can be used if the battery is separated from it and carried, say, in a

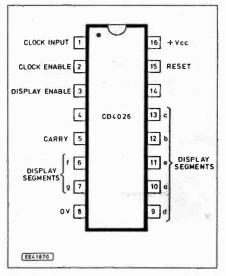


Fig. 4. Pin-out details for the CD4026 counter/driver i.c.

rear pannier. In fact, the whole box can be carried in a pannier but access to it can then only be made at the end of a trip or a stop-off at a pub!

So these constructional notes will cover only the existing boxed assembly as used on my own machine; any enthusiastic constructor can of course feel free to do his own thing with the basic circuit and make the disposition of things and the attachment to the bike to suit his or her particular liking. This kind of project can be quite open-ended.

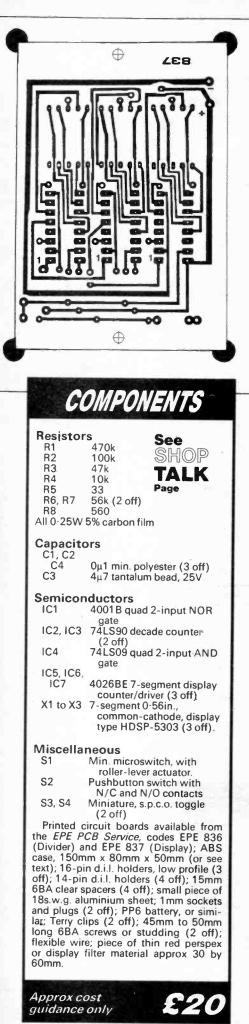
The Divider and Display printed circuit boards topside component layouts and underside full size copper foil master patterns are shown in Fig. 5. and Fig. 6. These boards are available from the *EPE PCB Service*, codes EPE 836 (Divider) and EPE 837 (Display). If you make your own, some care is needed particularly with the display board to avoid shorting between the copper foil paths.

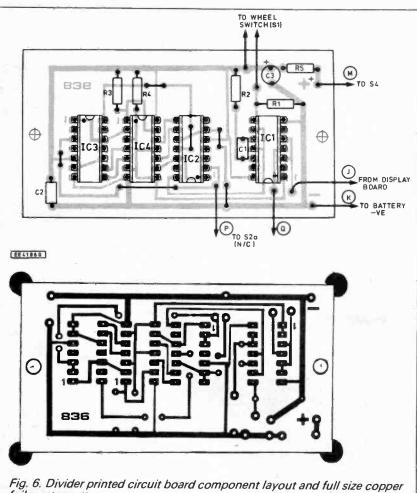
The Display board layout suits the specified 7-segment displays which have 0.56in. high digits with overall dimensions 0.7in. by 0.5in. Alternative displays will need adjustment to the board pin spacings, but see Shop Talk.

Assemble the boards carefully; there are quite a number of links, particularly on the display board. Notice particularly that there is a link which goes *under* IC2, insert this link *before* mounting the i.c. or its holder.

Next mount the resistors and capacitors. If you decide to use holders for the i.c.s, make these low profile types and when fitting the displays, space these about 2mm from the board surface so that their upper faces are slightly higher than the tops of the i.c.s.

There is only one board interconnection to be made later, that from the "commoned" 3, 5 and 6 pins of IC1c and IC1d to the input (pin 1) of IC5. All other leads go to the various switches mounted on the lid of the box. Use solder pins for the battery connection points and flying leads for the other outgoing wires, leaving these about 6 inches (150mm) for the time being.





foil master pattern.

BOX MOUNTING

The two boards, when completed, are mounted one above the other as shown in Fig. 7, using 45mm. 6BA screws or studding with plain spacers and the fixing holes provided on the boards. Care must be taken to ensure that the spacers, if metal types are used, do not touch the copper foil.

With the board interconnection made, this assembly is mounted in the ABS case; two Terry clips of appropriate size are then bolted under the box, using the fixing points of the board screws, so that the box can be clipped to the crossbar or to some other part of the bike.

The two leads to the wheel switch are brought out by way of two one millimetre type miniature sockets at the side of the box. The holes for these should be drilled before the boards are fitted.

It is now necessary to prepare the lid of the box. The switch and display positions are indicated in Fig. 8. The exact position for the display cut-out cannot be given as there will be small variations in the board positions from one project to another.

However, having mounted the boards in the box, there should be no problem in marking out the correct position of the cut-out and taking it out with an ordinary fret saw. Obtain a piece of display filter material (or a thin piece of red Perspex, which is cheaper) and glue it behind the cut-out, taking care not to get the glue on the viewing area.

Now mount the three switches in the positions shown in the back panel view of Fig. 9. With the lid now positioned close to the box, wire up the switches to the board leads, leaving these just slack enough to

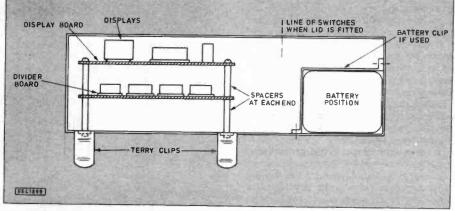
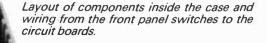


Fig. 7. See-through view of the board and battery mounting in the case. The face of the displays should be about 1mm below the top of the case.

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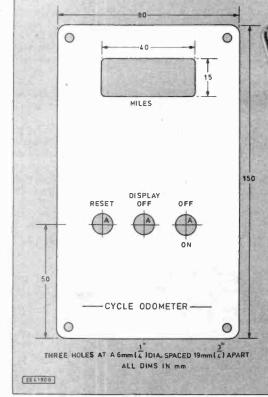


Fig. 8. Cut-out, drilling and legends for the front panel.

enable the lid to be turned over and screwed to the box once the wiring is complete.

Be especially careful in the wiring to the two switches (S1a and S1b) which are in a single push-button switch unit. There are four tags on this switch, opposite pairs of which go to one normally-open (N/O) switch and one normally-closed (N/C) switch. Which is which is easily checked with a continuity tester, and they must be wired in accordance with the circuit diagram or resetting will be impossible. Don't forget to wire the side terminals which will later connect the wheel switch.

The 6V battery, if it is to be mounted in the box, can be a PP6 type or $4 \times AA$ cells in a suitable holder. This fits into the lower end of the box and can be held by a simple metal clip or by a couple of adhesive pads.

TESTING

The Odometer can be *bench* tested by connecting the wheel switch (a roller-lever type microswitch) to the side terminals. Switch the unit on; the display and the single decimal point should light up, and by pressing the Reset button, the reading should show 00-0. Check, using the Display Off switch, that the display can be blanked but that the decimal point remains on to act as a reminder that the unit is still "live".

Return the display and now operate the microswitch by hand, counting the number of contacts made. On reaching 77, the display should indicate 00.1, representing one-tenth of a mile.

Carrying on in this way, the next total of 77 operations should bring up a reading of 00.2 and so on. There is no need to carry this tedious procedure too far; if the changes take place up to, say, 00.3, it is most likely that there is no fault on the unit.

To check things quickly, of course, the input can be derived from a pulse gener-

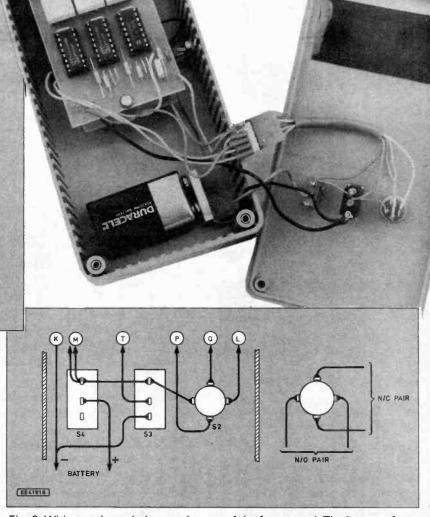
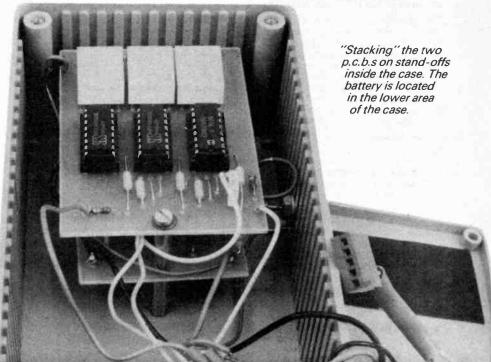


Fig. 9. Wiring to the switches on the rear of the front panel. The letters refer to locations on the two p.c.b.s.

ator giving an output of about 5V at a frequency of some 10Hz to 50Hz, and this will check the display reading right through to 99.9. But it is *still essential* to make sure (by the hand-operated switch method) that the display moves up for each group of 77 input pulses.

OTHER WHEEL SIZES

If you are making your own board, it is not too difficult to make small changes to the Divider section so that other wheel sizes can be accommodated in the design.



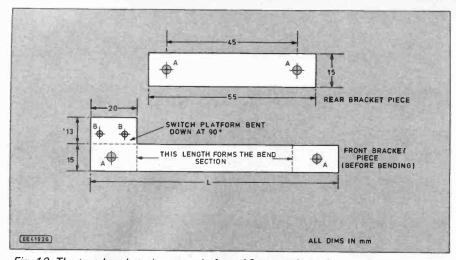


Fig. 10. The two bracket pieces made from 18s.w.g. aluminium. The bend position can be judged from Fig. 11. Length L to suit size of fork but approx. total length before bending is 80mm. Four holes at "A" are matched after bending front bracket piece (4BA). Two holes at "B" drilled to suit microswitch.

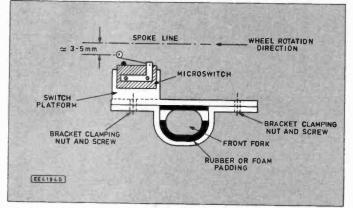


Fig. 12. How the completed bracket and microswitch are fitted to the front fork. The roller should clear the nearer spokes by the amount indicated.

This involves alternative arrangements to the inputs of the AND gate, IC4, hence the necessity of modifying the board.

Suppose we have a 21-inch wheel, then the circumference will be 21π or 66-inches and the number of revolutions made in 6336-inches (0·1 mile) is 96. We can write this as 1001 0110 in binary groups, hence we need to connect the A and D outputs of IC2 (which are high on 9) to two of the AND gate inputs; and the B and C outputs of IC3 (which are high on 6) to two other IC4 inputs.

The actual inputs are not important. The remaining inputs to IC4 are all held high by way of resistor R3 as in the circuit diagram. Note that the D output (most significant digit) of the counter i.c.s is pin 11. In a similar way, other wheel sizes can be accommodated.

It is also possible to "fine tune" the display readout on the 26-inch wheel circuit by such adjustments to IC4 inputs as outlined above. If, for example, you find on a test ride over a known mileage (possibly checked by a car drive) is over or under the correct figure by, say, more or less than 0.2 mile and you would like to be closer, then the count of wheel revolutions can be altered slightly to compensate for the error.

Instead of counting to 77, a count of perhaps 76 or 78 will make the necessary adjustment. To do this, use the same method as already described for different wheel sizes. The equivalent group binary for 76, for example, will be 0111 0110; the count by IC2 will be unaffected but the ABC outputs of IC3 will be 011 which means that only the BC outputs will go to inputs on IC4 while the then unused input which otherwise accepted the A output is strapped to the positive line via R3.

WHEEL SWITCH

This is that part of the project where individual ideas can prosper. We need a device that will give us an output pulse for each wheel revolution. The two most obvious systems that come to mind are a magnetically operated reed switch (a non-contacting method) and a microswitch operated by some kind of stud on the wheel (a contacing system).

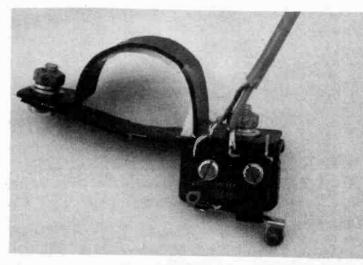
I did get a reed switch to work successfully but the setting and the consequent assembly was critical and, I felt, not really capable of being described in a manner that would be repeatable for every situation or style of machine. This does not mean that any constructor has to abandon this method; once again there is scope for the enthusiast to dabble for himself.

A possible objection to the mechanically operated switch is that it makes a clicking sound as the wheel rotates. With the method used here, the sound output is negligible as the contact is very light and any frictional effect on the rotation of the wheel is infinitesimal.

The use of a roller-lever microswitch helps in this way; avoid cheap devices which have a primitive form of hinge for the lever and which results in a floppy free movement of the lever. This can lead to problems when cycling at speed (but nothing dangerous!) as rebounds of the lever could strike the spokes and lead to false counting and a wrecked microswitch. A two-piece bracket has to be made to position the switch properly, but this is easy to fabricate and fit and can be formed from a piece of 18s.w.g. aluminium. In the diagrams illustrating this bracket the fixing hole positions are approximate and must be adjusted as necessary to suit your own machine, but large variations are very unlikely to be needed.

The two pieces needed to fabricate the wheel switch bracket are shown in Fig. 10, these being cut out neatly from the aluminium sheet and any burrs or rough edges smoothed off with a fine file. Now the switch actuator is most conveniently fitted to the wheel reflector, so the right position for mounting the bracket on the fork is in line with the reflector as the wheel rotates. Fig. 11 illustrates this point.

So, using a piece of wood or tube simi-



The wheel microswitch mounted on the completed fork bracket.

lar in size and shape to the wheel fork at the correct position (I used the left-hand front fork) shape the front bracket piece around it to conform finally to the configuration shown in Fig. 12. Line the inside of the loop and the mid-part of the back bracket piece with rubber or foam protective padding (as indicated); this takes care of any slight inaccuracies or slackness and prevents damage to the paintwork. It was found that draught excluder strip, sticky on one side, was ideal.

The bracket is secured to the fork by two 4BA screws with nuts and spring washers; try the fitting out before fitting the microswitch and ensure that it will clamp on to the fork very firmly with no tendency to slide or rotate. The rear piece of aluminium will bow *slightly* as the nuts are tightened

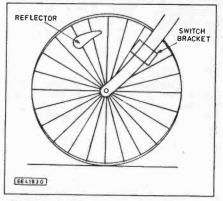


Fig. 11. The switch bracket should be positional on the same radius as the reflector.

but this helps the tightness of the assembly.

Any severe bowing will indicate that your front bracket piece needs readjusting to match the shape of the fork better. Aim for a good job, poor and inaccurate work must be avoided.

Now fit the microswitch to the platform in such a position that the roller, when it is in its normal "rest" position, clears the nearer spokes as the wheel is rotated by some 3mm to 5mm.

ON STRIKE

All that remains now is to provide some kind of "striker" mechanism which will close the microswitch momentarily on each rotation of the wheel. This is where the wheel reflector comes in; by fitting a shaped piece of material on to the reflector, a ready made support is available, so avoiding any

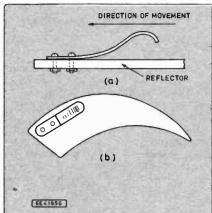
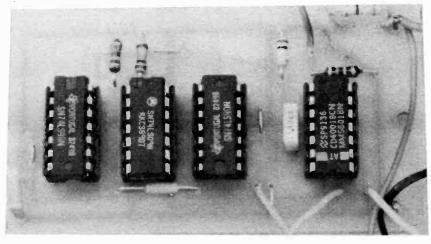


Fig. 13. General configuration of the "striker peg" and its attachment to the wheel reflector.



Layout of components on the completed Divider board. Do not forget the link under IC2.

other form of fixing the striker to the spokes.

A springy striker is best and a piece of 30s.w.g. brass did the job admirably and has so far survived for several hundred miles of travel. A piece of spring steel, salavaged from an old electric bell, and some 26s.w.g. aluminium have been tried and these have worked successfully as well. There is nothing too critical about either the material used or its thickness (within reason, that is) for this application.

The general shape needed for the striker and its mode of fixing is shown in Fig. 13a. Two fixing holes are necessary, and the final degree of bending is best done after assembly.

The reflector is drilled through 6BA and the striker screwed to it as shown in Fig. 13b). Make sure that the striker is in line with the microswitch lever as it passes and not at some oblique angle.

The bend of the striker is finally adjusted in situ; arrange the contact with the roller of the microswitch so that only the *minimum* movement of the lever occurs to close the switch contacts as the striker passes by. This need be no more than 2mm to 3mm; the lever *must not* be forced down to make contact with the body of the switch or both the striker and the lever will soon be distorted.

Having finalised the fitting, two leads are then soldered to the normally-open contact tags on the microswitch and taped to the frame as they are brought up to the side terminals on the main box. Happy cycling!

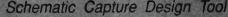
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3.1

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Innovations

A roundup of the latest Everyday News from the world of electronics

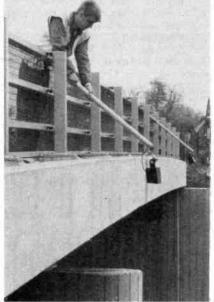
SORTING OUT TRAFFIC JAMS

TRAFFICMASTER TO THE RESCUE

By Hazel Cavendish A British firm has scored an important world first with "Trafficmaster", a novel electronically-based in-car traffic information service, extended nationwide in Spring this year after a triumpahnt two-year pilot scheme which proved itself on the M25 – an area suitably chosen for its monumental traffic snarl-ups at peak times. General Logistics is the UK company behind this innovative system which is designed to help drivers avoid time-wasting traffic jams and escape dangers ahead.

The Trafficmaster unit is a speciallydesigned, purpose-built portable radio receiver with a clear, easy-to-read screen showing a map of the motorways covered, which fits neatly into a subscriber's car or lorry. A simple group of control buttons enables details to be viewed in close-up and there is a supporting narrative which can be reviewed as required. It provides an information system which tells the motorway user accurately, quickly and simply when the road is clear and when it is congested, showing the extent of the congestion and the average speed of the traffic.

With this information available the driver can schedule journeys when the roads are at their least crowded, can choose alternative routes when likely delays are shown up on his screen, and know the extent of the delay if there is no option but to sit it out.



IR sensors being fitted to a motorway bridge.



Trafficmaster display inside a vehicle.

SPEED MONITORS

The key to the system is the installation of traffic speed monitors on 450 motorway bridges and gantries across the country's motorway system at a cost of £3 million over the last two years. These high-tech sensors are mounted in groups, for each direction, on motorway bridges and gantries, or on lamp-posts and signposts in more urban situations. Unlike Gatso speed-check cameras used by the police, these all-weather infra-red devices sense the vehicles passing underneath and measure their speed, although it is emphasised they cannot identify individual vehicles.

In order to give comprehensive coverage, the sites are on average only two miles apart. Each bridge site has its own microprocessor which calculates a three-minute rolling average speed of traffic, providing an accurate measurement of speed of flow as well as measuring the number of vehicles passing along the motorway.

Every bridge site is programmed to send information to the Control Centre in Luton when the average speed drops below a preset threshold of 30 m.p.h., the speed at which it is estimated congestion and delay build up on a busy road. Police, motoring organisations, road repair contractors and local government supply the information for the accompanying narrative which indicates speed, direction and extent of congestion, and will also give a reason for the problem occurring – "red Mini overturned at final roundabout before entering Norwich" – giving a driver the opportunity to re-route their journey.

DISPLAY

When motorway information is transmitted from the sensor via the Control Centre to the Trafficmaster the problem is displayed on the map as a flashing square. In this square an arrow indicates the direction of the hold-up and a number which is the average speed in miles per hour of the traffic at that point. This information is updated every three minutes. The speeds shown in the squares start at 25 m.p.h. and range down in units of 5 m.p.h. to zero. The user should soon become adept at assessing the delay a particular hazard may imply; the length of the tailback can be assessed by the number of squares flashing.

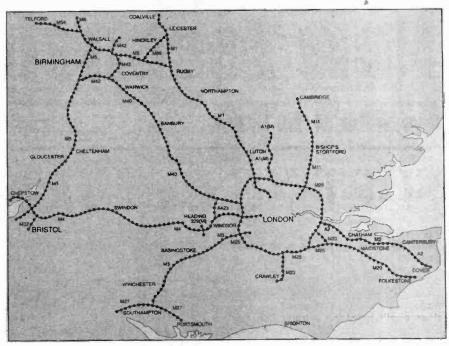
As an optional extra, Trafficmaster can double as a message pager, storing messages that can be read at a convenient time. On the large screen complete messages can be seen at a glance, making reading simple. As the Trafficmaster is portable – each unit being located in a specially-installed holster from which it can be removed for security – paging messages can be received in the office, at meetings or in restaurants and can be automatically transferred from a cellular phone to the screen.

A supplementary service covering the London area inside the M25 provides a comprehensive view of closures and serious incidents, showing the location of major accidents on the screen with supporting information in a separate "memory" which can be recalled on demand.

U.S. BELTWAY

Already used by a quarter o the FT-SE 100 index companies in Britain – the list reads like an inventory of Britain's top comapnies ranging from Grand Met and ICI to Virgin Atlantic and Marks and Spencer – Trafficmaster's technology and expertise has been seized upon by the Americans for their equivalent of our M25 – The Washington DC "Beltway" – which will try out the scheme in September this year following a significant deal with Westinghouse Electric Corporation of Baltimore. This gives the American company exclusive use of General Logistics' technology on a 15-year licence.

Trafficmaster is poised to benefit from a worldwide expansion into driver information systems as public authorities use their road allocation funds more effectively. These systems are known as Road Traffic Informatics (RTI) and Intelligent Vehicle Highway Systems (IVHS). A recent report estimated that the UK RTI market will be worth £5 billion by the year 2010, and the Clinton administration has funded IVHS with \$925 million over the next four years. General Logistics would seem to be in on a winning ticket.

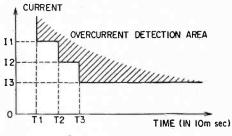


The present Trafficmaster network.

In his capaicty as Seretary of State for Transport, John MacGregor praised Trafficmaster as a remarkable private sector initiative, saying "Trafficmaster is a major breakthrough in driver information systems which many other countries are watching closely. It is an important aid to business efficiency, and helps to reduce costs and stress." In the UK General Logistics has this year extended the M25 trial scheme to cover more than 1,000 miles of motorway in the south and midlands, including the M27 between Portsmouth and Southampton, and is planning a substantial development in Scotland over the next two years. It is also involved in RTI projects in Europe, the Middle East and the Far East.

THE THINGS PEOPLE PATENT!

The following abstracts are taken from recent UK patent applications in the general electrical/electronics area. British Patent Specifications can be ordered from the Patent Office, Sales Branch, Unit 6, Nine Mile Point, Cwmfelinfach, Cross Keys, Newport, Gwent, NP1 7HZ.



Detecting overcurrent

In UK patent 2257314 Kabushiki Kaisha Toyoda Jidoshokki Seisakusho describe a means of detecting with high accuracy a current which may adversely affect an electric circuit if continued for an extended time. The apparatus periodically samples an output of a current sensor connected to the circuit and includes a memory, storing data of current values, (I1 to I3) regarded as representing overcurrents, in consideration of their respective durations, T1 to T3.

The current values are stored in pairs with time values of the durations. The sampled value is compared sequentially with the current values stored in the memory while duration of the sampled value is compared with the time value paired with that current value in order to determine whether or not an overcurrent has occured. The circuit may be used to drive the motor of a battery-powered fork lift truck.

Motor control system

In UK patent 2258094 Racal Health & Safety Ltd. describe a control system for an electric motor driving a fluid moving device, such as the air fan in a powered respirator. The system senses the speed of the motor by the back e.m.f. which it produces and senses the running current of the motor by the voltage drop across a fixed series resistor.

From these parameters, the prevailing rate of flow from the fan can be calculated in a microprocessor and compared with a specified value. The resultant error signal is used to modulate the widths of power pulses suplied from a drive to the motor so that the fan is controlled to produce a substantially constant rate of flow, in spite of variations of flow resistance caused by progressive blockage of associated filters.

COVER PIC

Acknowledgement: we appologise for forgetting to credit David Lee of Barton-on-Humber with last month's excellent front cover photograph of the Micro Lab.

TOOL-UP

A recent price reduction makes a good value 15 Piece Students Tool Kit from Maplin a real bargain. The kit includes a 25W soldering iron with two interchangeable bits (flat and pointed), a detachable hook and small fold-up stand, desoldering tool, a supply of solder and a pot of flux.

For working on p.c.b.s and delicate equipment, a "helping hands", "scraper" and a wire wrap tool are included. To help in assembly and repair of projects, two crosspoint screwdrivers, two standard screwdrivers, a pair of pliers, a pair of wire cutters and a pair of tweezers are also supplied.

All the tools are housed in a smart, tough carrying case with preformed sockets to hold them in place during transit. The cost of this kit is now £14.95 including VAT, it is available from Maplin shops and by mail order Tel. 0702 554161.



New Technology Update In Poole reports on developments in battery technology.

NCREASING demands are being placed on battery technology nowadays. People who use batteries want new types which pack more power into a smaller space whilst being cheaper and environmentally friendly.

It is this last point which is becoming increasingly important. Virtually all batteries in use at the moment include chemicals which can be hazardous in one way or another. Even the standard zinc-carbon battery contains an acidic electrolyte which can be quite corrosive as anyone who has had them leak into a radio will know. They also contain manganese dioxide which is toxic.

NiCad

NiCad cells seem to be the answer to many problems. They can be recharged time after time thereby reducing the amount of chemicals which are needed. However even these cells do not last for ever. It is quoted that they can have up to 1000 charge/discharge cycles, but in reality their life is somewhat less.

They are seldom used under ideal conditions: they may be overcharged, totally discharged, or used in a battery where it is possible for one or more cells to become reverse charged. All of these factors will lead to a reduction in the life of the cell by a considerable degree. There are very few applications where a cell will reach its maximum expected life.

Environmentally NiCad cells are much worse than standard cells. They contain potassium hydroxide which is corrosive. Worse than this is the cadmium they contain. Such are the fears about the disposal of these cells that some countries are considering a ban on their use. A number states in the USA are actively considering this, and the European Community is legislating to control their disposal,

Whilst NiCad cells are one of the worse environmental offenders other types of battery in everyday use also cause problems. The lead acid battery used in every car uses a corrosive electrolyte as owners of older cars can testify from the rust around the battery. In addition to this the lead is also a significant hazard.

Lithium cells are not particularly good either because they use some strong oxidising agents. In fact many of today's batteries give cause for concern in one way or another.

Green

The green lobby is growing rapidly but there is also an increasing reliance on batteries for everything from cam-corders and personal stereos to professional test equipment. These factors mean that there is a great need for new, safe batteries.

This presents a magnificent opportunity for any company which can develop a suitable battery to fill the need. As a result a number of organisations are investing large amounts of money into this field.

NMH

Already there are a number of new batteries which are being developed. Some years ago rechargeable lithium cells appeared on the horizon as the solution to the problem, but despite all the coverage in the technical media they have not reached the market.

Now another type of cell called the Nickel Metal Hydride or NMH cell has appeared and it is already being used in some applications. The cell has several advantages, but as the NiCad has been in use over 20 years and has been well refined, there is still a way to go before the NMH cell can perform as well in all respects.

Fortunately the NMH cell is very similar to a NiCad in its performance. This means that given enough development it could actually replace the NiCad in the near future, it has the advantage of being far more environmentally friendly. In addition to this it is much lighter and has a higher storage capacity. As an example a typical NiCad type AA cell has a capacity of around 0.5Ah whereas the new NMH cell can store just over 1Ah.

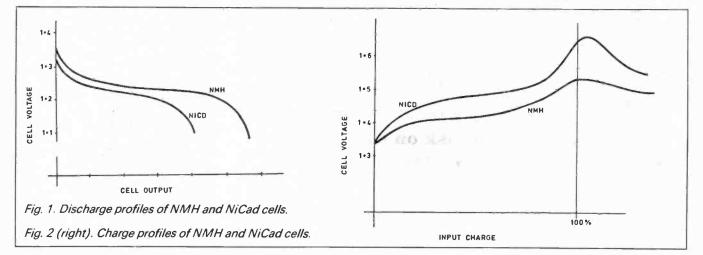
The output voltage is very similar to that of a NiCad. The open circuit voltage is between 1.3V and 1.4V falling to just over 1.2V under moderate loads. In fact the output voltage is only about 0.05 volts higher than a NiCad. The cell also maintains its output voltage well through the discharge cycle, falling rapidly once it becomes discharged as shown in Fig. 1. The main difference between the two cells is the additional charge which the NMH cell can hold.

The charge profiles are also very similar. One feature which is very useful for NiCad chargers is an increase in the cell voltage once full charge is reached. This is often used by so called intelligent chargers to detect when a cell is fully charged. A similar effect is also exhibited by the NMH cell, but it is less marked (Fig. 2).

Drawbacks

Whilst these new cells may seem ideal in many respects there are a number of drawbacks and problems which still need to be solved. The first is that NMH cells surprisingly have small traces of cadmium in them at the moment. This should not remain the case for much longer because manufacturers have stated that this will be removed shortly, enabling these cells to be disposed of without any major environmental concerns.

A more serious long term problem is their cost. Currently they are about two. and a half times the price of an equivalent NiCad. However when the increased capacity and cell life-time is considered the cost seems more reasonable.



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One area which is receiving the most development is that associated with loss of charge over a period of time when the cell is not in use. Currently NMH cells discharge at about twice the rate of NiCads which in turn is much higher than that for ordinary non-rechargables. Fortunately development is beginning to bear fruit and it is expected that it will be possible to reduce self-discharge rates to acceptable levels within the foreseeable future.

Charging of these new cells also creates a problem. Whilst standard NiCad rechargers can almost always be used, great care has to be taken not to overcharge them. If this is done then the capacity of the cells is greatly reduced.

As a result of the dangers of overcharging, these cells are not available as separate items. Instead they are built into manufactured equipments which contain intelligent chargers to prevent overcharging. Currently the main users are lap-top computers and mobile phones. In both of these applications weight and capacity are of great importance. On both of these counts the new NMH cells win over NiCads.

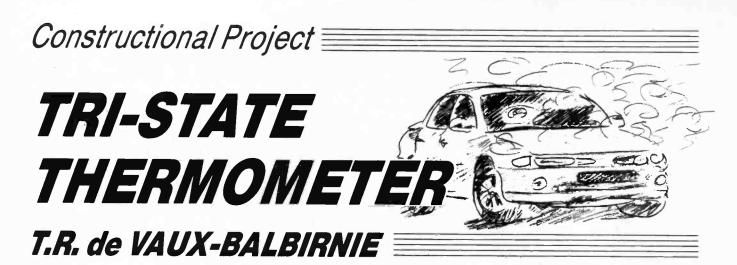
Whether NMH cells become as widely used as the standard NiCad cells depends to a large extent on other battery developments. At the moment a number of industrial battery manufacturers are making them or developing their own versions. However as yet there are none available separately on the domestic market.

Like any cell, whether rechargeable or primary, the new NMH cell has two electrodes, one positive, one negative, and between them there is an electrolyte.

From the name of the cell it can be deduced that one electrode (the positive one) is made from nickel compounds. In fact it is very similar to that used in a NiCad. The electrolyte is also very similar, being an alkaline solution.

The main difference occurs in the area of the negative electrode. Instead of using cadmium the new cells use a new alloy which can store hydrogen. The reason for this is that towards the end of the charge cycle, oxygen and hydrogen are given off. To overcome this problem a secondary electrode is added to absorb the oxygen, whilst the hydride in the primary electrode is able to store the hydrogen. In this way the cell can be sealed, only having a vent for emergencies.





Don't get all steamed up! Stay cool – build this overheating alert for your car.

G ONE are the days when car dashboards sported an array of chromebezelled instruments to monitor the engine's state of health. Along with the fuel gauge, there was often an ammeter, oil pressure gauge, water temperature gauge and possibly a voltmeter.

There may have been others indicating inlet manifold vacuum, oil temperature and so on. Car manufacturers tell us that today's motorist does not want or need such instruments and many drivers would not understand the readings anyway.

ILLUMINATING

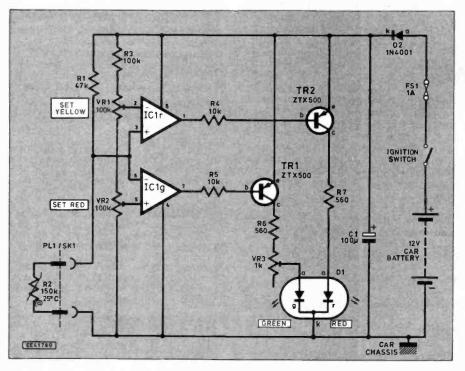
Pointer-type instruments were always something of a problem since the driver's eyes had to be taken off the road to see and interpret the readings properly. Also, traditional instruments do not positively signal attention – for example, a slowly decreasing engine oil pressure reading would probably be missed. A further point is that these instruments were not very effective at night.

In most cars warning lights are now provided. These remain normally off unless there is a fault. Thus, a red light comes on when the battery is not charging, another if the engine overheats and so on. There may be other lights to indicate worn brake pads, etc.

Lights have the advantage of attracting attention much more readily than a meter and are also more effective at night. However, unlike instruments, most warning lights have only two states - on and off.

In the case of a water temperature warning light, the operating point often appears to be set on the high side (so that it usually remains off to avoid alarming the driver). Unfortunately, instead of providing a timely warning, the light comes on when it is obvious – in the form of boiling

Fig. 1. Complete circuit diagram for the Tri-State Thermometer.



noises and steam seen issuing from beneath the bonnet – that overheating has already taken place.

It may not be possible to stop the car there and then yet driving even a short distance may have serious and expensive consequences – indeed, damage may already have been caused. If the cylinder head is made of light alloy – very common in today's cars – even moderate overheating can cause distortion which, in turn, can lead to loss of compression and water entering the cylinders past the cylinder head gasket.

The cost of dismantling, possibly having the cylinder head machined true, cleaning and re-assembling can be very high. If the warning had been given earlier – that is, at a slightly lower temperature, the car could have been driven gently off the road and the cause investigated. This may turn out to be simply due to an inexpensive radiator hose having burst but the problem could be compounded into a costly repair.

COLOURFUL VIEW

This project is a type of thermometer but instead of a pointer-on-scale meter, it uses a special tri-colour l.e.d. for the display. This lights up green for "cool", yellow to signify "normal" operating temperature and red to warn of "overheating". The only part on view is the l.e.d. in a small plastic box so the appearance of the project is very small and neat.

The operating temperatures are freely adjustable at the setting-up stage. In the prototype unit, yellow may be made to show at any temperature between 35° C and 70° C and red from 70° C onwards. The red l.e.d. will normally be set to operate just a few degrees above normal engine operating temperature.

The Tri-State Thermometer draws current from the 12V car supply and is only active while the ignition is switched on. Current consumption is about 10mA which may be regarded as negligible.

CIRCUIT DESCRIPTION

The full circuit diagram for the Tri-State Thermometer is shown in Fig. 1. Before going on to explain how it works, it will be helpful to understand the operation of the tri-colour l.e.d., D1. This device contains two separate l.e.d.'s – one red and one green – connected with common cathode (k) in a milky white package.

When the l.e.d.'s are used individually, they emit red or green light in the usual way but if they are operated *together*, the impression given to the eye is that of *yellow* light. This is because red and green are

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primary colours and these add to give a secondary colour - in this case, yellow.

In the following description, D1g refers to the green l.e.d. and Dlr, the red one. Referring to Fig. 1, the temperature is detected by thermistor, R2, which is placed in thermal contact with a suitable part of the engine which becomes hot in operation (details for this are given later).

The thermistor has a negative temperature coefficient - that is, as its temperature increases, its resistance falls. The way in which it does this is shown graphically in Fig. 2 and it will be noted that the response is non-linear, that is, it does not provide equal changes in resistance for equal changes in temperature.

Thermistor R2, in conjunction with fixed value resistor, R1 form a potential divider connected across the nominal 12V supply. Thus, as the temperature of R2 rises, the voltage across it falls and this is applied to IC1 pins 3 and 6. It is this changing voltage which operates the rest of the circuit.

IN COMPARISON

Integrated circuit, ICl, contains two identical voltage comparators (a comparator is a form of operation amplifier). These are referred to in the text as the red and green sections (IC1r and IC1g respectively) according to which l.e.d. the output is responsible for.

Each comparator has two inputs; an inverting (-) and a non-inverting (+) one (pins 2 and 3 respectively for IC1r, and pins 6 and 5 respectively for IC1g). It also has an output (pin 1 for the red and pin 7 for the green). The voltages applied to the two pairs of inputs are compared - hence the name of the device - comparator.

If the voltage applied to an inverting input exceeds that at the corresponding non-inverting one, the respective output will be low. In fact, the situation is slightly complicated because the output of this particular device is in the form of an open collector transistor which becomes low when the device is actuated.

This fact demanded certain considerations in design. With either comparator output low, the transistor for which it is responsible, TR1 or TR2, will switch on and light the appropriate l.e.d., D1r or Dlg. This is because the transistors are of the pnp type and operate when the base is low compared with the emitter which

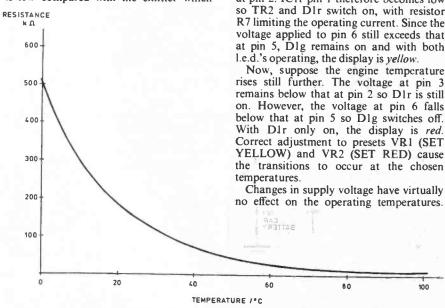
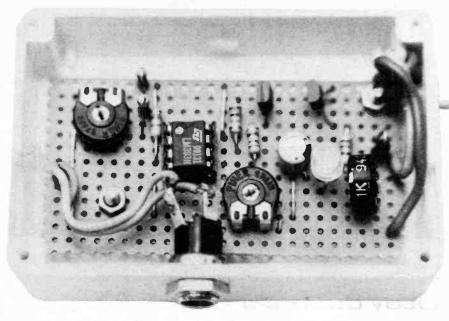


Fig. 2. Response of the thermistor resistance to change in temperature.



Layout of components inside the small plastic case.

is maintained at supply positive voltage. Resistors R4 and R5 limit the base current.

HOWITWORKS

Suppose thermistor, R2, is at a low temperature (the car has only just had its engine started and has not warmed up yet). The voltage applied to pin 3 will be fairly high (approaching supply positive voltage). Presets VR1 and VR2 form potential dividers whose sliding contacts apply certain voltages to pins 2 and 5 respectively ac-cording to their adjustment.

With a cold thermistor and with VR2 correctly adjusted, the voltage applied to pin 6 will exceed that at pin 5 so IClg output, pin 7, will be low. Transistor TR1 hence Dlg will therefore operate. Fixed resistor, R6, in conjunction with preset potentiometer, VR3, limit the operating current.

Meanwhile, with VR1 correctly adjusted the voltage applied to pin 3 (the same as that at pin 6) will exceed that at pin 2 so transistor TR2 and hence D1r will remain off. With Dlg on and Dlr off, the display is green

As the temperature sensed by R2 rises, the voltage applied to pin 3 falls below that at pin 2. IC1r pin 1 therefore becomes low so TR2 and D1r switch on, with resistor R7 limiting the operating current. Since the voltage applied to pin 6 still exceeds that at pin 5, Dlg remains on and with both

Now, suppose the engine temperature rises still further. The voltage at pin 3 remains below that at pin 2 so D1r is still on. However, the voltage at pin 6 falls below that at pin 5 so D1g switches off. With D1r only on, the display is red. Correct adjustment to presets VR1 (SET YELLOW) and VR2 (SET RED) cause the transitions to occur at the chosen

no effect on the operating temperatures.

This is because IC1 is comparing pairs of voltages applied to its inverting and noninverting inputs. Since these are all derived from the same supply they will all rise and fall with supply variations in like manner. Thus, the operating conditions remain unchanged.

With the arrangement of presets VR1 and VR2 as shown, the green l.e.d. goes off

COMPONENTS
Resistors R1 47k R2 miniature bead thermistor, nominal resistance
at 25°C 150k R3 100k R4, R5 10k (2 off) R6, R7 560 (2 off) All 0·25W carbon, except R2 TALK
Capacitors Page C1 100μ radial elect., 16V
Potentiometers VR1,VR2 100k sub-min preset, horizontal (2 off) VR3 1k sub-min preset, vertical
Semiconductors D1 5mm tri-colour common. cathode l.e.d. D2 1N4001 50V 1A rect. diode TR1, TR2 ZTX500.pnp silicon (2 off) IC1 LM393 dual comparator
Miscellaneous PL1/SK1 2 1mm power-type plug and matching socket or 2·5mm mono jack plug and socket - see text FS1 In-line car-type fuseholder and 1A fuse to fit Stripboard 0·1in. matrix, size 13 strips x 25 holes; 8-pin d.i.l. socket; plastic box, size 75mm x 50mm x 25mm; potting box size 28mm x 18mm x 14mm; 3A auto-type wire; light-duty stranded wire; car-type connectors; rubber grommets as required; fibreglass insulation; quick-setting epoxy resin adhesive; solder; etc.

Approx cost guidance only



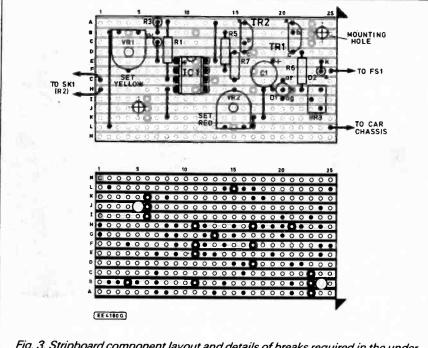


Fig. 3. Stripboard component layout and details of breaks required in the underside copper tracks. Note that there is NO break between pins 3 and 6 of IC1.

after the red one has come on – that is, on rising temperature the sequence will always be green, yellow and red. Preset VR3 allows for some adjustment to D1g operating current and this will be used to provide the best yellow colour at the end of construction. It seemed in tests that the green l.e.d. always appeared brighter than the red one so VR3 allows the current through D1g to be reduced so allowing the brightness levels to be balanced.

Diode D2, in conjunction with capacitor, C1, smooth the supply obtained from the car charging system. D2 also prevents damage if the supply is connected with the wrong polarity. Fuse FS1 provides protection in the event of faulty connections or a short-circuit.

A plug and socket arrangement, PL1/SK1 is used to connect the remote thermistor sensor to the main unit. In the prototype a $2 \cdot 1mm$ power-type connector was used but it would be possible to use a $2 \cdot 5mm$ mono jack plug and socket or any other similar connector.

CONSTRUCTION

Construction of the Tri-State Thermometer is based on a circuit board made from a piece of 0-1in. matrix stripboard, size 13 strips x 25 holes (see Fig. 3). Cut the material to size, drill the two mounting holes and make all track breaks as indicated. Note the *intact* copper strip between IC1 pins 3 and 6.

Follow with the topside inter-strip link wires and finally add the soldered on-board components including the i.c. socket. Do not insert the i.c. at this stage, however.

Take care to observe the polarities of the electrolytic capacitor Cl, and diodes Dl, D2. Note that the tri-colour l.e.d. Dl has its two outer leads bent in different shapes to denote the red and green anodes. The common cathode (k) is the centre lead – see Fig. 4.

The l.e.d. should be soldered in position so that it stands with its tip 20mm approximately above the circuit board. This should ensure that the end will protrude through a hole which will be drilled in the lid of the box later. It would be possible to mount the l.e.d., remotely if desired so that the box containing the circuit panel could be placed out of sight and the l.e.d. mounted alone on a small bracket.

Solder 5cm pieces of light-duty stranded wire to strips G and H on the left-hand side of the circuit panel. Solder a 20cm piece of light-duty *red* stranded wire to copper strip F and a similar piece of *black* wire to strip L on the right-hand side.

Solder one section of fuseholder, FS1, to the free end of the red wire. Make a careful check for errors particularly to ensure that all cut racks are *completely* broken and that there are no solder "bridges" formed between adjacent copper strips.

CASE DETAILS

Drill the circuit board mounting holes in the base of the box and the hole in the side for the two supply wires leading through from the circuit board. Drill the hole for the thermistor connecting socket SK1 (see photograph).

Mount the circuit panel temporarily and measure the position of the l.e.d. carefully. Drill a hole in the lid, having the same diameter as the l.e.d., to correspond with it.

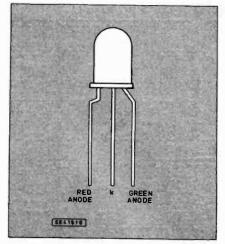


Fig. 4. Pin connection details for the tri-colour l.e.d.

An alternative method is to put a dot of ink on the top of the l.e.d. using a felttip pen. Immediately place the lid on top aligned with the correct position. The ink will mark the box at the point to be drilled.

Check that the l.e.d. will protrude through the hole when the lid is in position. Careful bending of the l.e.d. leads is permissible to provide small adjustments.

Only the *tip* of the l.e.d. should show through the case. If it protrudes through the hole too far, the separate glow of the green and red l.e.d.'s will be seen when the device is viewed at an angle and this will spoil the effect.

Measure the positions of presets VRI and VR2. Drill two small holes in the lid to correspond with these positions so that these presets may be adjusted using a small screwdriver when the lid of the box is in place.

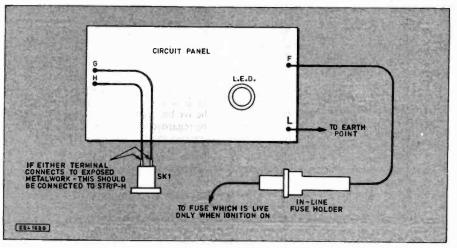
Mount the circuit panel on piece of thin cardboard to provide some padding. Attach the thermistor connecting socket and wire it up. The terminal connected to any exposed metal should be wired to supply negative – i.e. to strip H on the circuit panel (see Fig. 5).

Make sure all connections remain clear of VR2 and other components. Tie a knot in the supply wires to provide some strain relief and pass them through the hole in the box drilled for the purpose leaving a little slack.

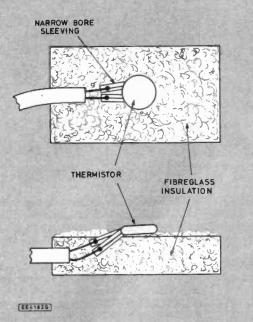
Adjust all three presets to approximately mid-track position. Finally, insert the i.c. into its socket observing the orientation.

Use a short piece of light-duty twin wire to make a temporary connecting lead for the thermistor. Fit one end with the plug

Fig. 5. Interwiring from the circuit board. The "earth" wire is taken to a metal chassis point.



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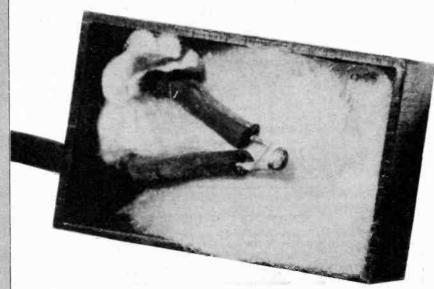


Fig. 6. Suggested method of housing the thermistor inside a small "potting" box.

and twist the other ends on to the thermistor wires. Take care since the thermistor is a delicate component and the wire ends are easily broken off.

TESTING

Initial testing and setting-up are made using a 9V battery instead of the car supply. If a thermometer which reads up to 100°C is available, this may help but is not essential. Testing on the bench is convenient since any small problems which may have arisen are more easily corrected.

Connect the battery with the positive terminal to the red wire and the negative to the black one. The l.e.d. should glow green although a little less brightly than it will be when connected to the car's 12V system.

Warm the thermistor R2 by dipping it in hot water. The l.e.d. should go to the yellow stage (with preset VR1 at mid-track position this should occur at 50°C approximately) and eventually show red towards boiling point.

This is only a basic test – there is no point in trying to set the operating temperatures this way since water conducts electricity well enough to disturb the operating points. In any case, the exact temperatures can only be set with the thermistor under true working conditions.

Instead of using hot water for testing purposes, it would be possible to use a hair dryer instead. With the l.e.d. showing yellow, adjust preset VR3 to provide the best colour and replace the lid of the box.

If all is well, attention may be given to the thermistor housing. The thermistor cannot be simply attached to the engine block in its present form because the wire ends would soon break off.

Also, cool air circulating around the engine would lower the temperature of the thermistor and alter the operating points. This would lead to unpredictable results with the transitions depending on the air temperature, the speed of the car and so on.

It is therefore necessary to provide mechanical protection and also some insulation. This was achieved in the prototype using the arrangement shown in Fig. 6. The thermistor housed in a potting box on a bed of fibreglass insulation.

Obtain a piece of light-duty *stranded* twin wire sufficiently long to reach from the unit to the thermistor position. In the prototype, 4-core burglar alarm wire was used with two of the wires cut off short.

The small plastic box used for the thermistor housing is a *potting box* (see components list). This needs a small hole drilled in the side to accommodate the connecting wire.

One end of the connecting lead is passed through to the inside. The thermistor is then soldered to the free ends with the connections insulated with narrow-bore sleeving or (better) heat-shrinkable sleeving. Some strain relief is needed for the wire inside the box -a piece of thin string tied tightly around it is adequate.

The box is now stuffed with fibreglass – such as a little roof or pipe insulation. The thermistor is left on this bed of fibreglass with the top level or slightly higher than the open end of the box (see photograph).

A suitable site on the engine block now needs to be found which becomes hot in operation. This must be clear of *all mechanical parts* and be easily accessible. The thermistor unit *must not* be placed on or near the exhaust system. The local garage will probably help in advising on a suitable place if necessary.

The area is carefully cleaned of all oil and dirt and then roughened using emery paper. The engine should then be run until the attachment site is warm so that the adhesive will set quickly.

Quick-setting epoxy resin adhesive is applied to the face of the thermistor (this is important to provide good thermal contact between the thermistor and the engine block) and around the rim of the box. Take care to avoid putting adhesive on to the fibreglass. The box is then pressed firmly in position and supported until the adhesive has hardened. The attachment must be regarded as permanent – any attempt to remove the box will probably destroy the thermistor.

INSTALLATION

Before installing the unit, you must disconnect the car battery.

Installation is simply a matter of connecting the thermistor wire to the unit inside the car and making the power supply connections. Choose a suitable site for the unit. With care, the position may be chosen so that only the lower part of the box with the l.e.d. shows.

Pass the thermistor connecting wire to the unit position inside the car. It saves time to use an existing hole and grommet by pushing the new wire through alongside the existing ones but if a new one needs to be drilled make sure a rubber grommet is used to avoid damage.

Leave a little slack in the connecting wire to allow for engine vibration. Cut off any excess wire and solder the plug PL1 to the end (polarity unimportant).

Using a piece of red auto-type wire of 3A rating minimum, connect one end to the output side of a fuse which is live only when the ignition is switched on. Use a proper connector here - do not rely on twisted connections. Often a "piggy-back" converter may be used to add a wire to an existing terminal.

Cut the wire to the correct length and connect the remaining half of the in-line fuseholder – see Fig. 5. Assemble this with a 1A fuse inside.

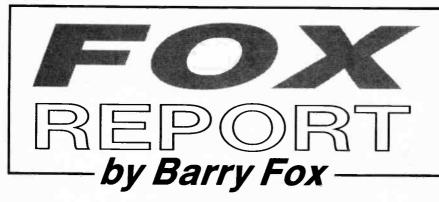
Connect the black wire to an "earth" chassis point. If no existing earth point can be found drill a small hole in a metal part and use an eyelet secured with a self-tapping screw. Support the unit temporarily using a piece of string or an elastic band.

ADJUSTMENT

Re-connect the car battery then switch on the ignition. The l.e.d. should show green.

Run the car until the engine is warm – rather less than normal operating temperature – and adjust preset VR1 (SET YEL-LOW) using a small screwdriver through the hole in the lid so that the l.e.d. just shows *yellow*. Run the car until the engine is at normal operating temperature and adjust VR2 (SET RED) so that the l.e.d. *just* fails to show red under all normal driving conditions. Note that with both VR1 and VR2, clockwise rotation increases the operating temperature.

If the red l.e.d. tends to come on when driving uphill or when waiting in a traffic queue with the engine running, further small adjustments may be made to prevent this from happening. It only remains to attach the unit permanently in position using a small bracket.



INTERFERING INTERFERENCE

In the good old days, anyone suffering radio or TV interference went to the local Post Office to fill in a form for a free check. After the split and privatisation of British Telecom, responsibility shifted to the Radiocommunications Agency, in Waterloo. If someone knows for sure where interference is coming from, the RCA will act free. If the RCA has to track down interference the fee payable is a still-reasonable £31.

The RCA has recently announced that it now has a Central Enquiry number for sufferers to ring (071 215 2150). This made me check back on the previous situation. I checked both the telephone directory and BT's Phone Disc CD-ROM. Neither listed either the RCA or Radiocommunicatons Agency.

I now learn that this was not a mistake. Although the RCA and its enquiry number will now be listed, the Agency has for all these years not been entered under its own name. To find the number a member of the public has needed to know that the RCA is an Executive Agency of the DTI, and then phone the DTI's main enquiry number, 071 215 5000.

Perhaps everyone did know this. Or just perhaps this little oversight had the happy effect of shielding the RCA from a lot of interfering phone calls from people suffering interference.

MS-DOS PATENT BATTLE

Microsoft announced Version 6 of the MS-DOS operating system on 30 March. The launch went ahead despite the fact that Microsoft is being sued for infringement of patents on the data compression system, called DoubleSpace, which Microsoft bought in from Vertisoft and modified to bundle with DOS 6.

Stac Electronics, the owner of the patents and vendor of third party compression system Stacker, has been only too happy to talk about the legal case. Although Microsoft has refused to comment on the case, I read, and reported, a message circulated on electronic bulletin boards by a consultant employed by Microsoft which told how the company is trying to break Stac's patents. Although I normally get all Microsoft's press releases and invites, by some mysterious quirk of the mail I got no invite to the launch "party" for DOS 6. IBM chose Microsoft, in the early

IBM chose Microsoft, in the early eighties, to provide the operating system for its then-new range of personal computers. Apple's stubborn refusal to licence its much more friendly Lisa and Mac operating systems guaranteed the PC's success and made Microsoft's founder Bill Gates the richest man in America. Despite the user-hostility of DOS, over 100 million IBM and IBMcompatible PCs already use it and Microsoft ships more than 20 million units a year. Microsoft earns around \$3 billion a year and employs 12,000 people in 27 countries. But Microsoft knows the market will shrink in the future, because existing owners of PCs will not upgrade their operating system, unless there is a real incentive to do so.

Microsoft believes the best incentive to buy a new version of DOS, is to build in data compression. There are already several third party data compression sytems available, with Stac's Stacker one of the most popular. Stac has grown from a company with 25 employees and a revenue of less than \$1 million in 1989 to a company with over 200 employees and revenue of \$150 million. Stacker takes advantage of the fact that all computer files contain redundant information, for instance repeated use of the same characters, which need not be fully coded.

STACKER STALL?

Stac claims that in late 1991 Microsoft's founder, Chairman and Chief Exective Officer, Bill Gates, saw Stacker winning computer industry awards, and asked Stac's President Gary Clow, about including Stacker technology in MS-DOS. During six months of negotiation Microsoft showed Stac a spreadsheet analysis, which predicted that if MS-DOS 6 incorporated data compression, Stac's sales of Stacker would stall. This view is shared by the computer industry.

In the February 1993 issue of *Personal Computer World*, a review of Stacker predicted that MS-DOS with compression would make the "market for utilities like Stacker.... disappear", and suggested to anyone thinking of buying a data compression system that "waiting for MS-DOS 6 may be no bad thing".

Stac says that Microsoft has refused to pay Stac "reasonable compensation" for using Stacker technology in DOS 6, with negotiations between the two companies breaking down, re-starting and failing several times during 1992. In the second half of the year Microsoft released a Beta test version of MS-DOS 6 for software developers to check for problems or "bugs".

Stac checked this and claims that although Microsoft's DoubleSpace may vary slightly from Stacker, it still infringes its two master patents, US patents 4 701 745 and 5 016 009. The first was filed by John Waterworth of Ferranti of the UK, but is now owned by Stac. Microsoft has retained a consultant who describes it as the "king pin" of a tree of patents on compression, but says he is "completely convinced of the patent's triviality" and that it is "ludicrous" that the patent was ever granted.

The consultant warns that the stakes are high. If a company as large as Microsoft wins, then the whole tree of patents will fall; if Microsoft loses, then it is unlikely the patent will be challenged again and will thus be massively strengthened.

Microsoft's difficulty is that it must now prove that what appears trivial now, was trivial when John Waterworth filed his application in March 1985. The patent claims the basic idea of searching data for sequences of bytes which are identical to sequences already processed, and then storing only code which identifies the location of previously stored identical sequences.

"This is a fundamental and obvious technique in data compression and it is ludicrous that it should be anywhere but in the public domain", claims Microsoft's consultant who has been posting notices on electronic bulletin boards for computer engineers and enthusaists all round the world to read. "If you know of any prior art . . . please contact me" pleads Microsoft's aid.

WILFUL

Microsoft's informal line now is that only 10% of DoubleSpace derives from Vertisoft's original work, and 90% comes from Microsoft's ongoing development; and DoubleSpace, like all compression systems, builds on the same common foundation that Stac is claiming to monopolise. Stac hopes to convince the court in Los Angeles that because Microsoft asked for a licence under its patents, Microsoft's infringement is "wilful". Everything now hangs on the legal validity of Stac's patents.

Stac is now promising a new version of Stacker to be available within 30-60 days from the release of DOS 6. This, says Stac, will hook onto DOS and integrate itself into Microsoft's operating system, by replacing whatever DoubleSpace system comes with DOS 6. The 30-60 day delay was needed because until the official launch of DOS 6, no-one could be sure exactly what DoubleSpace would look like.

Teach-In '93

with Alan Winstanley Keith Dye B.Eng(Tech)AMIEE and Geoff MacDonald B.Sc(Hons) AMIEE

Part 10

Teach-In '93 continues a tradition of offering an interesting and thorough tutorial series aimed specifically at the novice or complete beginner in electronics. The series is designed to support those undertaking either GCSE Electronics or GCE Advanced Levels.

AST month we introduced lots of new terms that are used in the world of microprocessors and their operating programs. We'll now focus on programming and try to avoid too many new terms. We will not be able to delve into complex programming techniques, but hope to provide an interesting overview to commonly used programming techniques. Don't worry if you find it difficult to grasp the idea of programming on your first reading. If you have built the *Micro Lab* have a go at entering programs – you can't damage anything by pressing the keys!

INTRODUCTION TO PROGRAMMING

Machine Code is the basic language understood by the Microprocessor (or machine). It is made up of *byte instructions* followed by *data* or *address locations*. This is the form that a program is stored in a computer's memory, and will be used to enter instructions into the *Micro Lab*. Unfortunately, it is not easy to understand code written in this way as it just appears as a stream on hexadecimal numbers. Higher level languages are easier to use and understand because they use English to describe the program operations. See the separate section on *High Level Languages*.

An improvement on machine code is an Assembler Language

program. Assembler language is the preferred method of entering microprocessor code because it uses the three-character mnemonics from the *instruction set*. As the mnemonic represents the function of the instruction, it helps you to understand a program that has been written by someone else (or yourself if you have an average memory!).

All microprocessors have an instruction set with a set of mnemonics. The instruction sets are different for each manufacturer's devices, although there are families of devices that use similar types of code. Instructions written in "assembler" cannot run on the microprocessor in that form and must be converted into machine code. This is achieved either by hand, or with a program that converts the assembler into the machine code of the target microprocessor. Most desk top computers (e.g. the BBC and IBM machines) come with software that allows a program written in assembler to be converted to machine code.

An example of the program listing from our 6502 assembler is shown in Fig. 10.1. This shows the result after the assembler has been converted into machine code. Assemblers come with some other useful features that allow a programmer to choose where the converted code will be placed in the microprocessor's memory map. In this example the code has been placed at 8080h.

Fig. 10.1 Description of 6502 Program Listings

All the programs written in 6502 assembler language are shown in this format. This is the output of the assembler translation software that produces the 6502 machine code. All the programs can be run on the Micro Lab, but must be entered in machine code. To do this enter the MODify command and type in the memory location – say \$0200 – then press enter. The program is then entered as A9 00 85 90 A9 02 etc.

8080 8082 8084	A900 8590 A902	D_DEMO0:	lda Sta Lda	#\$00 \$90 #\$02	;Set up start address of table
+	÷	+	ŧ	+	+
Memory Address	Op-Code (Machine		inemonic Assembler	Operan	ds Comments

(Machine	Assembler		
Codel	language		

Numbers

In computer programs we need to know how to distinguish between decimal and hexadecimal numbers. We will always place an (H) or h after hexadecimal numbers to distinguish them from decimal numbers. Unfortunately, the output from the 6502 assembler uses a different convention to indicate Hexadecimal numbers. A s is placed in front of hexadecimal numbers as:

Hex number \$200

Memory Address

The memory address is the location of the program in the microprocessor memory map. On the Micro Lab address locations from

\$8000 to \$FFFF are contained in the EPROM. You cannot alter these memory locations.

Op-Codes

The op-codes are the machine code instructions that the 6502microprocessor follows. In the first line the op-code for the LDA (Load Accumulator) instruction is A9. The value to be loaded into the accumulator follows the op-code – in this case decimal 00.

Labels

Labels are names given by the writer of the program to identify particular points in the program. The assembler language can recognise the names as locations to branch to within the program. In this example D_DEMO0 is at address \$8080. An instruction to jump or branch to D_DEMO0 will load the program counter with 8080 and the program will run from this address.

Assembler Language

The instructions entered by the programmer are these assembler language descriptions of the program. The mnemonic is a three character abbreviation of the instruction command name. The operand is the data or address that the instruction applies to. In the second line the accumulator is stored at address 0090. (This is a zero page instruction and only two bytes are needed to describe the memory location.)

Comments

The comments are an optional description that the programmer can enter. They are used to describe the instruction or process operation. Good programmers use extensive comments to aid others to understand their programs.

Fig. 10.2 - 6502 Instructions

BCC - Branch on Carry Clear

NV-BDIZC

Tests the carry flag for 0. If clear, the program will branch forward

	or backwards by the number of bytes specified. If set, the nex program instruction is executed. Displacement range from + 127 to -128 from the next instruction after BCC.					
	Address Mode Relative	Op-Code 90	Example 90 20	BCC PC + 20h		
	BEQ - Branch o	n Z = 1		NV-BDIZC		
	Tests the zero flag for 1. If set, the program will branch forward o backwards by the number of bytes specified. If clear, the next pro gram instruction is executed. Displacement range from +127 to -128 from the next instruction after BCC.					
	Address Mode Relative	Op-Code F0	Example F0 20	BEQ PC + 20h		
	BNE – Branch o	n Not Equal te	o Zero	NV-BDIZC		
	Tests the Z flag for backwards by the gram instruction is -128 from the next	number of byte executed. Disc	es specified. If	set, the next pro-		
	Address Mode Relative	Op-Code D0	Example D0 20	BNE PC + 20h		
	BRK – Break			NV-BDIZC		
	A software interrup stored at FFFE and B flag in the status the stack.	the PCH is set	to the value st	ored at FFFF. The		
	Address Mode Implied	Op-Code 00	Example 00	BRK		
	CPY - Compare t	o Y Register		NV-BDIZC		
	$X \rightarrow X$ Compares the contents of the specified memory or immediate data from the Y register. If the result is zero Z is set. If bit 7 = 1 (positive) then N is set to 1. If the register equals or is greater than the data (negative) the C flag is set. The memory and accumulator contents are unchanged.					
	Address Mode Immediate Absolute Zero Page	Op-Code C0 CC C4	Example C0 20 CC 34 12 C4 20	CPY #20h CPY 1234h CPY 20h		
	DEX – Decremen	t X Register		NV-BDIZC		
1	Deserves 1.6			AX-		

Decrements 1 from the X register.

Address Mode	Op-Code	Example	
Implied	CĂ	CA	DEX
INY – Increment Y Register			NV-BDIZC
			x x -

Adds 1 to the contents of the Y register.

1	Address Mode Implied	Op-Code	Example		
	Implied	C8	C8	INY	
		1			

JMP – Jump to Address NV-BDIZC

Loads the program counter with the address specified. The program will jump to the new address.

Address Mode Absolute Indirect	Op-Code 4C 6C	Example 4C 34 12 6C 34 12	JMP 1234h JMP (1234h)
JSR – Jump to S	ubroutine		NV-BDIZC

Stores the program counter + 2 onto the stack, and loads a new address into the program counter. The program jumps to that address. An RTS is used to return to address stored in the stack.

Address Mode	Op-Code
Absolute	20

Example JSR 1234h

> NV-BDIZC x ---- x -

NV-BDIZC X ----X-

NV-BDIZC X----X-

LDA - Load the Accumulator

20 34 12

Loads the data into the accumulator.

Address Mode Immediate Absolute Zero Page	Op-Code A9 AD A5	Example A9 20 AD 34 12 A5 20	LDA #20h LDA 1234h LDA 20h
(Indirect,X)	AI	A1 20	LDA (20h,X)
(Indirect),Y	BI	B1 20	LDA (20h),Y
Zero Page,X	B5	B5 20	LDA 20h,X
Absolute,X	BD	BD 34 12	LDA 1234h,X
Absolute,Y	89	B9 34 12	LDA 1234h,Y

LDX - Load the X Register

Loads the data into the X register

Address Mode	Op-Code	Example	
Immediate	AŽ	A2 20	LDX #20h
Absolute	AE	AE 34 12	LDX 1234h
Zero Page	A6	A6 20	LDX 20h
Absolute,Y	BE	BE 34 12	LDX 1234h,Y
Zero Page,Y	B6	B6 20	LDY 20h,Y

LDY- Load the Y Register

Loads the data into the Y register

Address Mode	Op-Code	Example	
Immediate	AŌ	A0 20	LDY #20h
Absolute	AC	AC 34 12	LDY 1234h
Zero Page	A4	A4 20	LDY 20h
Zero Page,X	B4	B4 20	LDY 20h,X
Absolute,X	BC	BC 34 12	LDY 1234h,X

LSR-Logical Shift Right

Absolute,X

Absolute,Y

NV-BDIZC --x x

Shifts the contents of the memory or accumulator 1 bit to the right, and saves the result. Bit 7 is set to 0. Bit 0 is stored in the C flag.

Address Mode	Op-Code	Example	and in such
Absolute	4E	4E 34 12	LSR 1234h
Zero Page	46	4620	LSR 20h
Accumulator	4A	4A	LSRA
Zero Page,X	56	20	LSR 20h.X
Absolute,X	5E	5E 34 12	LSR 1234h,X

PHA – Push Accumulator onto Stack

9D

99

NV-BDIZC

Copies the contents of the accumulator onto the stack and decrements the stack pointer.

Address Mode	Op-Code	Example	
Implied	48	48	PHA

PLA - Pull Accu	PLA – Pull Accumiator from Stack NV-BDIZC					
Loads the top byte from the stack into the accumulator and incre- ments the stack pointer.						
Address Mode Implied	Op-Code 68	Example 68	PLA			
STA - Store Acc	umulator in N	lemory	NV-BDIZC			
Copies the accumulator to the specified memory location.						
Address Mode	Op-Code	Example				
Absolute	8D	8D 34 12	STA 1234h			
Zero Page	85	85 20	STA 20h			
(Indirect,X)	81	81 20	STA (20h,X)			
(Indirect),Y	91	91 20	STA (20h),Y			
Zero Page,X	95	95 20	STA 20h,X			

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9D 34 12

99 34 12

STA 1234h,X

STA 1234h,Y

. .

Instruction Sets

Every microprocessor is designed to follow a set of instructions. Unfortunately every manufacturer uses a different set of instructions for their products. This means that a program written for one type of processor will not run on another without some alterations. As more powerful processors are developed, some manufacturers have tried to provide an upward compatibility for existing programs. In the case of Intel's 86xxx series of microprocessors used in IBM compatible personal computers, programs written for the early microprocessors will run on the latest devices. The process does not work in reverse, as later devices usually have additional features built in and programs written to use these will have commands that the early devices cannot understand.

As we have used the 6502 microprocessor for the Micro Lab we have to use its instruction set. Some of the instructions we use this month are shown in Fig. 10.2. The instructions are all given in the same format and we have given examples of how the commands are used.

All the instructions have a one byte code which is the command that the microprocessor understands. This is called the Operation Code, or more usually the Op-Code. A sequence of op-codes make a program that a microprocessor will follow - it can only operate on one op-code at a time.

As one byte is used for the Op-Code there are theoretically 256 different instructions that could be used. The 6502 only uses 150 instructions - codes that are not valid instructions are ignored.

There are only 56 basic instructions for the 6502, but some of these have different ways of being used within programs. Each basic instruction type is given a three letter "name" or mnemonic that helps you to remember the instruction. For example the instruction Branch on Carry Clear is called BCC. If you look at the instruction set you can see that this instruction has only one mode of use. Some of the instructions have eight modes of operation, and we will look at some of these different modes.

Addressing Modes

A computer program will usually spend most of its time reading and writing data to and from memory. There are a number of ways in which the program may need to do this. For example, you may need to set a register to a particular value. This is called immediate addressing, as the register is immediately loaded with the value specified. If you have a program that needs to repeat a section of the program a set number of times you will first have to store the repeat value in a register. As the computer completes this section of the program it will subtract one from the register. If the register is not zero the program will jump to the start of that section again. We will look at these techniques again later.

There are several other ways in which you may want to load and store data to and from registers and memory depending on what you want your program to do. Each different way of accessing the memory is called an addressing mode. The addressing modes available depend upon the micro processor being used, although they all provide the same basic facilities. Each addressing mode of the 6502 is described below. As with most things, some addressing modes are more useful than others and will therefore be used more often. An example is given with each mode. The examples given are of usable programs and may contain several addressing modes.

If you have successfully constructed the Micro Lab, have a go now at entering the programs. The Micro Lab User Manual contains comprehensive details of how to enter your own code and see it operate. Remember, you cannot damage the Micro Lab if you press the wrong keys, just try again! If you have a 6502 based computer (like the BBC B and Acom Master series) you could try to enter the code in assembler or machine code. (Note: the programs may not run as the memory map will be different on these machines.)

A table of Hexadecimal and Decimal numbers and their ASCII equivalents is given in opposite. Use this to help you understand some of the numbers being entered in the programs.

Immediate Addressing

Loads a register (not a memory location) with an immediate value.

:Load accumulator with 20h 0200 A9 20 LDA #20h

The example loads the accumulator immediately with the value 20h.

Implied Addressing

This term is used for instructions which do not have an addressing mode specified. For example pushing the contents of the accumulator onto the stack:

0200 48	PHA	:Save accumulator
UZUU 48	PDA	,Save accumulator

ASCII TABLE HEY DEC ASCIL

VUL SCH STX STX SOT ENQ SEL BS HT LF VT FF CR SO SI DLE DC1 DC2 DC3 DC4 VT SI DLE DC1 DC2 DC3 DC4 VT SI DC2 DC3 DC4 SI SI DC2 SI SI SI SI SI SI SI SI SI SI SI SI SI
STX STX STX SOT SOT SOT SOT SOT SEL BS HT LF VT FF CR SO SI DLE DC1 DC2 DC3 DC4 VT SO CC4 VT SO SI DC2 DC3 DC4 VT STX SO SI SC SC SC SC SC SC SC SC SC SC
TX SOT SOT SOT SNQ CK SEL BS HT LF VT FF CR SO SI DLE DC1 DC2 DC3 DC4 AK STB CC4 CC8 SI DLE DC1 DC2 DC3 DC4 CC4 SC SI DC2 DC3 DC4 CC8 SC SC SC SC SC SC SC SC SC SC
GOT ENQ ACK BEL BS HT LF VT FF CR SO SI DLE DLE DC1 DC2 DC3 DC4 AK SYN ETB CAN EM SC FS GS RS US
ENQ ACK BEL BS HT LF VT FF CR SO SI DLE DC1 DC2 DC3 DC4 AK YN ETB CR SC SC SC SC SC SC SC SC SC SC SC SC SC
ACK BEL BS HT LF VT FF CR SO SI DLE DC1 DC2 DC3 DLE DC1 DC2 DC3 DC4 IAK YN ETB CAN EM SC FS GS RS US
BEL BS HT LF VT FF CR SO SI DLE DC1 DC2 DC3 DC4 DC2 DC3 DC4 VAK SYN CAN EM SUB SC FS GS RS US
BS HT LF VT FF CR SO SI DLE DC1 DC2 DC3 DC4 IAK TB CAN EM SUB SC FS GS RS US
HT LF VT FF CR SO SI DLE DC1 DC2 DC3 DC4 IAK TB CAN EM SUB SC FS GS RS US
LF VT FF CR SO SI DLE DC1 DC2 DC3 DC4 UAK TB CAN EM SUB SC FS GS RS US
VT FF CR SO SI DLE DC1 DC2 DC3 DC4 UAK TB CAN EM SUB SSC FS GS RS US
FF CR SO SI DLE DC1 DC2 DC3 DC4 UAK CAN EM CAN EM SUB SSC FS GS RS US
FF CR SO SI DLE DC1 DC2 DC3 DC4 UAK CAN EM CAN EM SUB SSC FS GS RS US
CR SO SI DLE DC1 DC2 DC3 DC4 UAK CAN EM CAN EM SC SC FS GS RS US
SO SI DLE DC1 DC2 DC3 DC4 UAK VN TB CAN EM SC SS GS RS US
SI DLE DC1 DC2 DC3 DC4 IAK YN ETB CAN EM CUB SSC FS GS RS US
DLE DC1 DC2 DC3 DC4 IAK YN ETB CAN EM EM SC FS GS RS US
DC1 DC2 DC3 DC4 IAK YN TB CAN EM EM EM EM EM SSC FS GS RS US
DC2 DC3 DC4 IAK TB CAN EM EM SUB SC FS GS RS US
DC3 DC4 IAK YN TB CAN EM SUB SC FS GS RS US
DC4 IAK YN TB CAN EM UB SSC FS GS RS US
IAK YN TB CAN EM UB SC FS GS RS US
YN TB AN EM SUB SC FS GS RS US
ETB CAN EM SUB SSC FS GS RS US
EM EM SUB SSC FS GS RS US
EM SUB SSC FS GS RS US
SUB SSC FS GS RS US
ESC FS GS RS US
FS GS RS US
GS RS US
RS US
RS US
US
ALC
!
#
\$
%
&
-
(
)
*
+
,
-
100
1
0
1
2
3
4
5
6
7
8
9
: 4
:
;
; <
;

TTTN	DEC	ASCII
HEX	64	@
40	65	A
		B
42	66	C
43	67	
44	68	D
45	69	E
46	70	F
47	71	G
48	72	H
49	73	1
4A	74	J
4B	75	K
4C	76	L
4D	77	M
4E	78	N
4F	79	0
50	80	P
51	81	Q
52	82	R
		S
53	83	
54	84	T
55	85	U
56	86	V
57	87	W
58	88	X
59	89	Y
5A	90	Z
5B	91	
5C	92	
5D	93	1 i
5E	94	
	95	
5F	1	
60	96	
61	97	a
62	98	b
63	99	c
64	100	d
65	101	e
66	102	f
67	103	g
68	104	h
69	105	i
6A	106	i
6B	107	k
6C	108	1
6D	109	m
6E	110	n
6F	111	0
70	111	
	112	<u>р</u>
71		9
72	114	r
73	115	S
74	116	t
75	117	u
76	118	v
77	119	w
78	120	x
79	121	у
7A	122	z
7B	123	1
7C	124	
7D	125	
7E	126	-
7E 7F	120	DEL
11	1 121	

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Absolute Addressing

This is used to access a particular memory location to either load a register with the location's value or store the register value in the memory location. This may be used to access peripherals such as the keyboard, display, l.e.d.s etc. or using memory for storage of program variables (for example the number of ships the player has left in a game of space invaders, today's date, etc.).

0200 A9 AA	LDA	#AAh	:Load accumulator with AAh
0202 8D 00 80	STA	8000h	Store accumulator in LEDs

This loads the accumulator with AAh (10101010 in binary) and stores it at the memory location used by the l.e.d.s. This has the effect of turning on every other l.e.d. Try this out on the Micro Lab and see the l.e.d.s light up.

Offset Addressing

Offset addressing is only used by the 6502 branch instructions. These instructions change the program counter if a particular condition is true or false (e.g. if the last arithmetic result was zero). If the condition is met, the program counter is added to the parameter. Note that when the instruction is read from memory, the program counter is pointing to the first byte of the next instruction. This means that an offset of zero will have no effect. An offset of three will branch forwards, skipping three bytes. An offset of -2 (or FEh) will branch to the start of the branch instruction - looping continuously if the condition is met.

The above example will always jump to address 0207h then stop.

0200 A0 10	LDY #16	;Load IY with 16	
0202 88	DEY	;Subtract one from IY	
0203 D0 FD	BNE 0202h	;Jump to 0202h if not zero	
0204 00	BRK	;Stop program	

The above example will loop for sixteen (10h) times then stop.

Zero Page Addressing

...

Zero page addressing is a subset of absolute addressing. The address specified is only eight bits wide. The upper eight bits of the address are set to zero (hence the name). Therefore, only addresses between 0000h and 00FFh may be accessed with this addressing mode. The advantage of using zero page addressing is that the instruction uses less memory (a considerable advantage a few years ago when memory was very expensive, but not so today). Using less memory means that the processor can read the instruction from the program faster.

0200 A5 90 LDA 90h	LDA	90h ;Load accumulator with the	;Load accumulator with the
	contents of address 0090h		

Absolute Indexed Addressing

This is a more complex addressing mode than those previously described, but it is probably one of the most often used. A base address is specified, along with an index register (either IX or IY). The value held by the index register is added to the base address and the resulting address is the one used by the instruction.

The following example stores 12h in address 0410h using absolute indexed addressing. This may seem pointless, as the same result can be obtained using simple absolute addressing, however a better use of this addressing mode will be described later when we talk about program loops. This addressing mode is denoted by a ",X" or ,Y'' after the base address.

0200 A9 12	LDY	#12h	;Load accumulator with 12h
0202 A0 10		#10h	;Load IY with 10h
0204 99 00 04	STA	0400h,Y	Store acc. at 0410h

Zero Page Indexed

Again, this is a subset of the absolute indexed addressing mode. The base address specified must be in the range 00h to FFh and the upper eight bits of the resultant address are always 00h. This means that, for example, if the base address is F0h and the IX register is 20h, the address accessed will be 0010h, not 0110h.

0200 A2 20 0202 XX F0	LDX #20 LDA F0h	,

Indirect Addressing

This one's a bit more complicated! The processor looks at the

contents memory at the address specified and then at the contents of the one after it. The two values read are then used as the resultant address. This is best shown by an example. Indirect addressing is shown by placing the address in brackets.

Address 0400h holds the value 00h Address 0401h holds the value 05h

Then the instruction 0200 6C 00 04 JMP (0400h)

Will make the program jump to address 0500h

Indexed Indirect Addressing

Indexed Indirect Addressing simply adds the value of the IX register to the address which is to be looked at to get the resultant address! This is shown by inserting a ",X" after the address.

If

Address 0096h holds the value 00h Address 0097h holds the value 05h Address 0500h holds the value 34h

Then the instructions 0200 A2 06 LDX #06h 0202 XX 90 LDA (90h,X)

Will load the accumulator with the value 34h (held at address 0500h).

Indirect Indexed Addressing

This mode looks at contents of the two bytes specified (as with indexed addressing) and then adds the value of the IY register. This addressing mode is shown by placing a ",Y" after the close bracket.

```
Address 0090h holds the value 00h
Address 0091h holds the value 05h
Address 0504h holds the value 56h
```

Then the instructions #04h 0200 A0 04 LDY 0202 XX 90 LDA (90h),Y

Will load the accumulator with the value 56h.

The last three addressing modes may seem a bit complicated at first, but you'll soon get the hang of it! They may also seem a bit pointless, but they do have their uses, especially when you are using text strings, look-up tables or large areas of memory (greater than 256 bytes). Again, we will go into this in more detail later. Don't worry if it seems a bit incomprehensible at the moment. We hope that the Micro Lab will help you to clarify things.

REGISTERS

In order to run a program, the microprocessor needs to keep track of such things as where the next program instruction can be found, or the result of the last arithmetic operation, etc. These values are stored in registers. There are two types of register - user registers may be used by the user's program to store values, pass information, etc. Specific registers are used to control the way in which the processor operates or are set by the processor to inform the user program of various events.

The 6502 has six registers, five of which are eight bits wide. (See last months Fig. 9.10 – CPU Internal Architecture.) So, binary numbers between 00000000 and 111111111 may be stored in them. In decimal, the range is 0 to 255. The exception to this is the program counter register. This register is sixteen bits wide (as the address bus is sixteen bits wide) and, thus, may store decimal values between 0 and 65535.

There are three specific registers, the *program counter (PC)*, the *processor status word (PSW)* and the *stack pointer (SP)*.

The program counter holds the memory address of the next instruction that the processor is to execute. As each byte of the instruction is read from memory, the value in the PC is incremented by one. The user may set the PC to a specific value when required. This has the effect of making the program branch or jump to a new address.

Unlike the other registers, processor status word is not used as an eight bit number. Instead is a collection of single bit flags which indicate various states within the processor. For example, the N flag is set to a 1 if the result of the last arithmetic operation was negative (less than zero) - one way in which this could happen is if a register containing zero was decremented by one.

The stack pointer holds the lower eight bits of the next free stack address. The upper eight bit of the stack address are always 00000001 (i.e., the stack is stored in page one of the memory

MACHINE CODE AND HIGH LEVEL LANGUAGES

Computer languages come in all shapes and sizes. Most of you will be aware of languages such as BASIC, Pascal and C, which are examples of *high level languages*. Conversely, machine code is known as a *low level language*. A high level language uses complex language to describe the program. This makes the program easier to understand as it reads like a description of the operations to be performed. A low level language uses terse instructions made from the basic computer commands. These are often difficult to follow and understand as they have no easy flow of language, and the instructions are not easily remembered.

A computer can not directly run programs written in a high level language, they must first be translated into machine code. This can happen in one of two ways - *interpreting* and *compiling*. To illustrate the difference, consider a piece of text written in French which a person who only understands English wishes to read. An interpreter may be hired to read the text to the person every time he or she wishes to read it, or a translator may be hired to rewrite it in English.

Although the second option has an initial time overhead whilst the translator rewrites the text, the text may then be read quickly by the English speaking person and, indeed, any other English speaking person without the need for a translator. This is equivalent to *compiling* a program, the first option is equivalent to *interpreting* it. Compiled programs run faster than interpreted ones but there is a time penalty each time the program is compiled which can slow down program development. Additionally, interpreted programs need the interpreter present on every computer in which they are to run. With modern computers, the time taken to compile a program is quite small and, so, there are very few interpreted programming languages around today.

There are several advantages to writing programs in a high level language instead of machine code:

Program development is much easier and quicker

It is easier for other people to understand your programs

A program may be made to run on a system with a different processor by passing it through a compiler for that processor (similar to using a different translator to rewrite the French text in German). A machine code program will need to be completely rewritten for the new processor.

However, programs written directly in machine code will run much faster than their compiled counterpart and the final code will also be much smaller. Computer games tend to be written mainly in machine code for these reasons, as they are very demanding on the microprocessor as they are continually calculating new positions and updating the display whereas a word processor, for example, spends most of its time waiting for the user to press a key.

The choice of high level language or machine code depends upon the application. You should remember, however, that it is always possible to mix the two.

space). The stack is used to temporarily store numbers and addresses. When a number is *pushed* onto the stack, the number is stored at this address and the address is decremented by one. For example, if SP = FFh and the value 05h was pushed onto the stack, then address 01FFh would hold the value 05h and the SP would be FEh. When a number is *pulled* from thestack, the opposite occurs – i.e., SP is incremented by one and the contents of the stack address are read. This type of stack is known as a *last in, first out (LIFO) stack.* This means that the last item pushed onto the stack will be the first item pulled from it. This is similar to placing dinner plates in a pile. You only place a new plate on the top of the pile and may only remove the top plate in the pile.

The three remaining registers are the user registers. These are named the accumulator (A), the X index register (IX) and the Y index register (IY). These registers may be used in any way by the user's program, the only restriction being that the 6502 uses only the accumulator for the arithmetic and logic functions.

For example, when adding two numbers together, the first number must be stored or *loaded* into the accumulator and the second must then be added to it. The result is then placed back in the accumulator.

COMPARISONS AND LOOPS

A useful computer program will often need to skip sections of code or jump to particular sections depending upon one or more *conditions*. As an example, to set memory locations between 0400h and 044Fh to zero, you could use a program like this:

0200 A9 00 LDA 0202 8D 00 04 STA 0205 8D 01 04 STA	#00h 0400h 0401h	;Load acc. with zero ;Set address 0400h to zero ;Set address 0401h to zero
02F2 8D 4F 04 STA 02F5 00 BRK	044Fh	;Set address 044Fh to zero ;Stop program execution

And so on, until every memory location is set to zero. But, obviously, you wouldn't want to do it this way! Instead, you would write a program which would repeat the code to store the accumulator at a particular memory location the required number of times:

0200	A9	00	LDA	#00h	;Load acc. with zero
0202	A2	00	LDX	#00h	;Load IX with zero
0204	8E	00 04	STA	0400h,X	;Set loc. 0400h + IX to zero
0207	E8		INX		Add 1 to IX
0208	E0	50	CPX	#50h	;IX = 50h?
020A	DO	F8	BNE	0204h	No - Jump to 0204h
020C	00		BRK		Stop program execution

Note the use of indexed addressing to store the accumulator in memory and offset addressing to branch back in the program.

The comparison is performed by the CPX instruction. This instruction compares the IX register with the immediate value of 50h. If the two are equal, then the *zero flag* in the PSW is set, otherwise it is cleared. The BNE instruction looks at the zero flag and jumps to the specified address if it is clear (i.e. IX is not equal to 50h). The opposite of this is the BEQ instruction which jumps if the zero flag is set.

Branch instructions on the 6502 always use an offset addressing mode. This addressing mode restricts you to branching forwards by a maximum of 127 bytes and backwards by a maximum of 128 bytes. There will be occasions when this range is not adequate. So, it is normal to test for the opposite condition to skip over an absolute jump to the address required. As an example, the following program looks at the DIPswitches on the *Micro Lab* and jumps to address 0500 if they are all on.

0203	C9	FF			#FFh	;Get switch settings ;Switches all on?
0205					020Ah	;No – skip over the jump
		00	05	JMP	0500h	;Yes – jump to 0500h
020A	00			BRK		;Stop program

LOOK-UP TABLES

Look-up tables are used at some point by most programs – for writing text, reading keyboards, determining the number of days in a month, etc. In 6502 machine code, a look-up table is normally accessed using absolute indexed addressing.

To demonstrate this, we will first show you how to determine the number of days in a month then how to write a text string to the l.c.d.

Determining the Number of Days in a Month

A table of twelve numbers is generated. The first number being the

number of days in January and the last being the number of days in December (we will ignore leap years).

In decimal, this table is:

31, 28, 31, 30, 31, 30, 31, 31, 30, 31, 30, 31

Converting this into hexadecimal, we get:

1F, 1C, 1F, 1E, 1F, 1E, 1F, 1F, 1E, 1F, 1E, 1F

This table must be stored in memory at a known start location, say 0300h.

If the IX register is loaded with the number of the month we wish to look at (from 0 to 11), a single instruction may be used to load the accumulator with the number of days in that month. You may use the REGS command on the *Micro Lab* to set the IX register before running the program.

0200 BD 00 03 LDA 0300h,X ;Get no. of days in month 0203 00 BRK ;Stop program

When the program has been run, the l.c.d. on the *Micro Lab* shows the register values, the IX register will contain the number of the month and the accumulator will contain the number of days in this month.

There are two ways to change the program to number the months from 1 to 12:

The first is to set the base address of the table to one less than the real start of the table:

0200 BD FF	02 LDA	02FF,X	;Get no. of days in month
0203 00	BRK		;Stop program

The second is to subtract one from the IX register before looking at the table:

0200 CA DEX		;Subtract one from IX
0201 BD 00 03 LDA	0300h,X	;Get no. of days in month
0204 00 BRK		;Stop program

Writing Text to an LCD

To write text to an l.c.d. (or, indeed, any type of display) you need to make a table containing the ASCII values of the characters which you wish to write. (See the table in Fig. 10.10 for a list of ASCII characters.) The l.c.d. on the *Micro Lab* will not display all of the ASCII codes, as some are only used in controlling computer terminals. Try some of them out – you can't damage the l.c.d. by giving it characters it cannot display.

An index register is set to zero so that the first character from the table may be read. This character is then sent to the l.c.d. The index register is then incremented by one to point to the next character. This character is also sent to the l.c.d. This is repeated as many times as necessary. For text with a known size, you can compare the index register with the number of characters which you are to display in order to end the cycle, however it is normal to place a byte which does not represent a display character (say 00h) at the end of the text table:

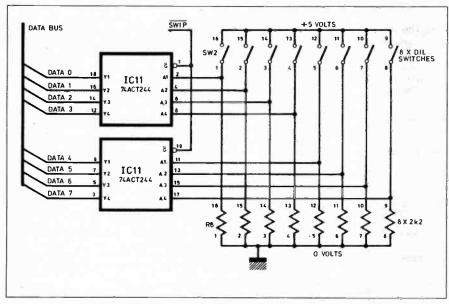


Fig. 10.3. Input switches

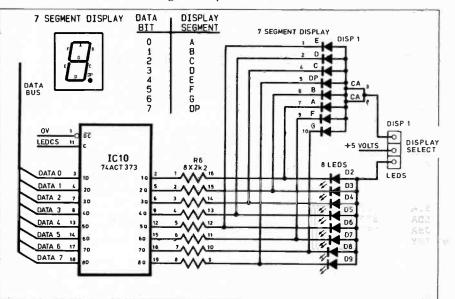


Fig. 10.4. Output LEDs

0200 A2 00 LDX	#00h	;Point to start of table
0202 BD 00 03 LDA	0300h,X	;Get next character
0205 F0 07 BEQ	020Eh	;If Char. = 00h, end loop.
0207 20 06 00 JSR	0006h	;No - Send char. to I.c.d.
020A E8 INX		;Point to next char.
020B 4C 02 02 JMP	0202h	;Loop back
020E 4C 0E 02 JMP	020Eh	;Loop forever
0300 48 65 6C		;Text table for "Hello"
0303 6C 6F 00		;With a 00h at the end.

Note that unlike previous examples, there is not a BRK instruction at the end of the program. This is because a BRK instruction returns the user to the *Micro Lab monitor* program, which displays the CPU registers on the l.c.d., overwriting the previous display. The **JMP 020Eh** instruction simply loops back to itself so that the only way to stop it is to reset the processor or press **STOP** on the *Micro Lab* keyboard.

This program can only display text beginning at a particular area of memory (0300h). Next month, we will show you how to write a *subroutine* which can be called by your programs to display text from anywhere in the computer's memory, using indirect indexed addressing.

MICRO LAB INPUT AND OUTPUT CIRCUITS

That's enough programming technique for this month! Lets take a look at some *Micro Lab* Input and Output (I/O) circuits to see how data can be transferred to and from a computer system.

Switch Input Circuit

The switch input circuit is shown in Fig. 10.3. This allows the microprocessor to "read" the state of the eight input switches. The eight inputs are pulled to ground via eight 2k2 resistors. When the switches are closed the voltage across the resistors rises to five volts. IC11 is an octal tri-state line receiver whose outputs (O/Ps) are connected to the computer's data bus. The switch data can only pass through the tri-state receiver when the enable input on pin 1 and pin 19 is taken low. This is driven by the address decoding circuits and synchronised to allow the switch data to be placed on the data bus when no other data is present.

The SWIP enable signal is activated when the CPU reads from addresses 8010h. This "read" instruction allows the CPU to load the contents of the data bus into the accumulator, and the only data on the bus will be the switch settings.

L.E.D. Output Circuit

The LED output (O/P) circuit of the *Micro* Lab is shown in Fig. 10.4. In this case the CPU writes data to IC10 – an octal tri-state latch. The data is loaded into the latch when the LEDCS control line goes low. Again this is synchronised by the decoding circuit to only allow data intended for the l.e.d.s to be latched.

The eight outputs drive the high efficiency l.e.d.s via eight 2k2 current limiting resistors. The outputs are connected to both 8 separate l.e.d.s and a 7 segment display. A link is used to apply five volts to the required display.

The LEDCS signal is activated when the CPU writes to address 8000h.

Keyboard Input Circuit

The keyboard I/P circuit for the *Micro Lab* is shown in Fig. 10.5. This circuit uses a dedicated keyboard scanning i.c. (IC4) to constantly check the keypad for a key press. When a key is pressed IC4 generates an interrupt signal from its IRQ output. This goes to the CPU's Non Maskable Interrupt (NMI). When the NMI goes low the CPU enters an interrupt program to read IC4 and determine which key has been pressed.

The keys are arranged in a matrix of rows and columns. Pressing any key links a row to

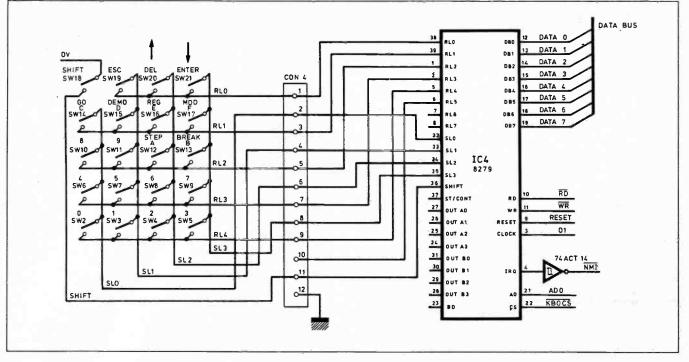


Fig. 10.5. Keyboard input.

a column and this is decoded in IC4 into a binary value. This is read from IC4 via the data bus when decoder O/P (KBDCS) goes low. Again the decoder circuit ensures that only the data from IC4 will be on the data bus during this read operation. The keyboard registers are located at 8060h and 8061h. IC4 can also drive l.e.d.s and has a separate write (WR) line to allow data to be written to the display registers. We do not use this feature on the *Micro Lab*.

INTERFACING MICROPROCESSORS TO THE "REAL WORLD"

We are now at the stage where we can apply some of the topics discussed. This month, we will cover simple input and output (I/O) devices – switches and I.e.d.s. The programs used are already con-

tained within the *Micro Lab* monitor. Three topics are covered which drive simple l.e.d.s and 7-Segment displays and read multiple switch inputs. These use the I/O circuits previously described.

Demonstration 0:

This program shows you how to illuminate eight LEDs in a pre-defined sequence. The use of look up tables, indirect indexed addressing, comparisons and branching is demonstrated. Fig. 10.6 gives the program listing.

On the *Micro Lab*, the LEDs are mapped to address 8000h. Writing a number to this address will change the pattern displayed. Each number will produce a different pattern. Some examples are given below:

			Fig. 10	.6. Demons	tration 0	
8080			*****	***********	*******	***
8080		;***				***
8080		;*** Everyd	ay Electron	nics Micro-Lab		***
8080			-			***
8080		;*** Nam	e:	Demonstratio	on 0	***
8080		;*** Ver	sion:	1.0		***
8080		;*** Aut	hor:	Geoff MacDon	hald	***
8080		;*** Dat	e:	13/01/1993		***
8080		;*** Las	t Update:			***
8080		;***	-			***
8080		*********	********	************	***************************************	***
8080						
8080			;Read	a sequence bea	ginning at address \$0200, of length	
8080					send the sequence to the LEDs, with	1
8080					0301 (in 1/100 sec) between each out	
8080						
8080	A900	D DEMOO:	LDA	#\$00	;Set up start address of table	
8082	8590	-	STA	\$90	•	
	3002		LDA	#\$02		
8084	AJUZ					
8084 8086			STA	\$91		
	8591	DEMOOA:			;Get byte to send to LEDs	
8086 8088	8591	DEMOOA: DEMOOB:	STA	\$91	;Get byte to send to LEDs ;Key pressed?	
8086 8088	8591 A000 A55D		STA LDY	\$91 #\$00	;Key pressed?	
8086 8088 808A	8591 A000 A55D D014		STA LDY LDA	\$91 #\$00 KBSIZE		
8086 8088 808A 808A	8591 A000 A55D D014 CC0003		STA LDY LDA BNE	\$91 #\$00 KBSIZE DEMOOC	;Key pressed? ;Yes - terminate demonstration ;End of table?	
8086 8088 808A 808C 808E	8591 A000 A55D D014 CC0003 F0F5		STA LDY LDA BNE CPY	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA	;Key pressed? ;Yes - terminate demonstration-	
8086 8088 808A 808C 808C 808E 808E	8591 A000 A55D D014 CC0003 F0F5 B190		STA LDY LDA BNE CPY BEQ LDA	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA (\$90),Y	;Key pressed? ;Yes - terminate demonstration ;End of table? ;Yes - restart sequence	
8086 8088 808A 808C 808E 8091 8093 8095	8591 A000 A55D D014 CC0003 F0F5 B190 8D0080		STA LDY LDA BNE CPY BEQ LDA STA	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA (\$90),Y LED	<pre>;Key pressed? ;Yes - terminate demonstration ;End of table? ;Yes - restart sequence ;Set up LEDs</pre>	
8086 8088 808A 808C 808E 8091 8093	8591 A000 A55D D014 CC0003 F0F5 B190 8D0080		STA LDY LDA BNE CPY BEQ LDA	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA (\$90),Y LED \$0301	;Key pressed? ;Yes - terminate demonstration ;End of table? ;Yes - restart sequence	
8086 8088 808A 808C 808E 8091 8093 8095 8098 8098	8591 A000 A55D D014 CC0003 F0F5 B190 8D0080 AD0103 201800		STA LDY LDA BNE CPY BEQ LDA STA LDA JSR	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA (\$90),Y LED	<pre>;Key pressed? ;Yes - terminate demonstration ;End of table? ;Yes - restart sequence ;Set up LEDs ;Delay for specified time</pre>	
8086 8088 808A 808C 808E 8091 8093 8095 8098	8591 A000 A55D D014 CC0003 F0F5 B190 8D0080 AD0103 201800 C8		STA LDY LDA BNE CPY BEQ LDA STA LDA	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA (\$90),Y LED \$0301	<pre>;Key pressed? ;Yes - terminate demonstration ;End of table? ;Yes - restart sequence ;Set up LEDs</pre>	
8086 8088 808A 808C 808E 8091 8093 8095 8098 8098 8098	8591 A000 A55D D014 CC0003 F0F5 B190 8D0080 AD0103 201800 C8 4C8A80		STA LDY LDA BNE CPY BEQ LDA STA LDA JSR INY	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA (\$90),Y LED \$0301 DELAY DEMOOB	<pre>;Key pressed? ;Yes - terminate demonstration ;End of table? ;Yes - restart sequence ;Set up LEDs ;Delay for specified time ;Point to next value</pre>	
8086 8088 808A 808C 808E 8091 8093 8095 8098 8098 8095 8095	8591 A000 A55D D014 CC0003 F0F5 B190 8D0080 AD0103 201800 C8 4C8A80 202100	DEMOOB:	STA LDY LDA BNE CPY BEQ LDA STA LDA JSR INY JMP	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA (\$90),Y LED \$0301 DELAY	<pre>;Key pressed? ;Yes - terminate demonstration ;End of table? ;Yes - restart sequence ;Set up LEDs ;Delay for specified time ;Point to next value ;Clear keyboard buffer</pre>	
8086 8088 808A 808C 808E 8091 8093 8095 8095 8098 8098 8095 8095 8095	8591 A000 A55D D014 CC0003 F0F5 B190 8D0080 AD0103 201800 C8 4C8A80 202100	DEMOOB:	STA LDY LDA BNE CPY BEQ LDA STA LDA JSR INY JMP JSR	\$91 #\$00 KBSIZE DEMOOC \$0300 DEMOOA (\$90),Y LED \$0301 DELAY DEMOOB KBCLR	<pre>;Key pressed? ;Yes - terminate demonstration ;End of table? ;Yes - restart sequence ;Set up LEDs ;Delay for specified time ;Point to next value</pre>	

Number Written	Pattern Displayed	Writing a sequence of numbers to the LEDs, pausing between
00h 01h	· · · · · · · · · · · ·	each, will produce a changing pattern. The following sequence will produce a single dot moving from left to right:
FEh		7Fh, BFh, DFh, EFh, F7h, FBh, FDh, FEh
FDh	• • • • • • • • • •	 Referring to listing 1, the first four lines of the program store the
FBh		address of the look up table in locations 90h and 91h.
F7h		• The table will be read using an LDA (90h), Y instruction. So, the
55h	1	next line sets the IY register to zero so that the first value in the
AAh	*******	 table will be read. The program then checks to see if a key has been pressed. If one
	where " · " = LED off " • " = LED on	has, then it jumps to the end of the program, where the key is read, then i.e.d.s are turned off and the program stops (the BRK instruction).

Fig. 10.7. Demonstration 1

BOAB	;********	*******	**********	***************************************
BOAB	;***			· · · · · · · · · · · · · · · · · · ·
BOAB	;*** Everyda	y Electro	nics Micro-La	ab +++
BOAB	;***			***
BOAB	;*** Name	::	Demonstrat	tion 1 ***
BOAB		sion:	1.0	***
OAB	;*** Auth	or;	Geoff MacI	Donald ***
OAB	;*** Date	::	13/01/1993	3 ***
OAB		: Update:		***
OAB	;***	-		***
OAB	· · * * * * * * * * * * *	*******	**********	*****
OAB				
OAB		;Read	a sequence h	beginning at address \$0200, of length
OAB		;store	ed at \$0300 a	and send the sequence to the 7 segment LEI
OAB		;wait:	ing for a key	press between each output.
OAB				
0AB A900	D_DEMO1:	LDA	#\$00	;Set up start address of table
0AD 8590	_	STA	\$90	, and of course during of ourse
0AF A902		LDA	#\$02	
OB1 8591		STA	\$91	
0B3 A000		LDY	#\$00	;Get byte to send to LEDs
0B5 CC0003	DEMO1A:	CPY	\$0300	;End of table?
0B8 F00C		BEQ	DEMO1B	;Yes - terminate
0BA B190		LDA	(\$90),Y	
OBC 8D0080		STA	LED	;Set up segment LEDs
OBF _201B00		JSR	KBWAIT	;Wait for key to be pressed
ÒC2 C8		INY		Point to next value
0C3 4CB580		JMP	DEMO1A	Forme to ment varat
OC6 A9FF	DEMO1B:	LDA	#SFF	;Clear 7-segment LED
0C8 8D0080		STA	LED	Clear /- Segment Lab
0CB 00		BRK		 In the second secon second second sec

Fig.	10.8.	Demonstration	2
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80CC		;*****	**********	**********	****
80CC		;***			
80CC		;*** Ev	eryday Electro	nics Micro-Lab	**
80CC		; * * *	•		**
80CC		;***	Name:	Demonstration	**
80CC		;***	Version:	1.0	
80CC		;***	Author:	Geoff MacDona	**
80CC		2***	Date:	13/01/1993	
80CC		2***	Last Update:	13/01/1993	**
80CC			opunce.		**
80CC			***********	*************	**
80CC					**********************************
80CC			This	demonstration .	
80CC			tand o	demonstration i	eads the settings on the DIP-switches
80CC	-		/ 411/2 (lisplays the sta	ites on the LCD
80CC	A200	D DÉMO2	LDX	#0	
80CE	A001	-	LDY	#1	Position cursor at left side
80D0	A900		LDA	#1	of bottom row
80D2	200900		JSR	CURSOR	;Turn cursor off
80D5			LDA	DIPSW	
80D8			LDX		Read the DIP switch states
80DA		DEMO2A:	LSR	#8	;Set up number of switches
BODB	48	DENOZA.	PHA	A	;Get state of single switch
BODC					
	A94F		BCC	DEMO2B	
BOED			LDA BNE	#\$4F	;Switch is on - display "O"
30E2		DEMO2B:		DEMO2C	
30E4		DEMO2C:	LDA	#\$2D	;Switch is off - display "-"
30E7		DEMOZC.	JSR	OPCHAR	as the and the second sec
BOE8			PLA		Co. Alexandre
30E9			DEX		;Decrement number of swiches left
OEB			BNE	DEMO2A	Repeat if not finished
OED	201800		LDA	#10	;Delay for 1/10 sec.
	A55D		JSR	DELAY	
BOF2	FOD8		LDA	KBSIZE	;Key pressed?
OF4	202100		BEQ	D_DEMO2	;No - start again
30F7	202100		JSR	KBCLR	;Empty keyboard buffer
OF /	00		BRK		

- If no key has been pressed, then the IY register is compared with address 0300h. This address holds the number of values in the table. If the IY register equals this value, the program jumps back to the line where it is loaded with zero.
- Next, the accumulator is loaded with the IYth number in the table. This is then copied to the l.e.d. address.
- The program then pauses by calling the monitor's DELAY routine.
- Finally, the IY register is incremented by one and the program jumps back to the line which checks for a key press,

Before running the demonstration, you should use the *Modify* command to store the desired sequence beginning at address 0200h. Next, the length of the sequence should be stored at address 0300h and the delay between changes (in 0.01 seconds) should be stored at address 0301h. Then enter *Demo 0* and the l.e.d.s will show the entered sequence.

There is no danger of accidentally damaging these demo programs stored in the EPROM. This is a read only device, and cannot be changed by the user. If you get into a muddle just press the reset button to start again!

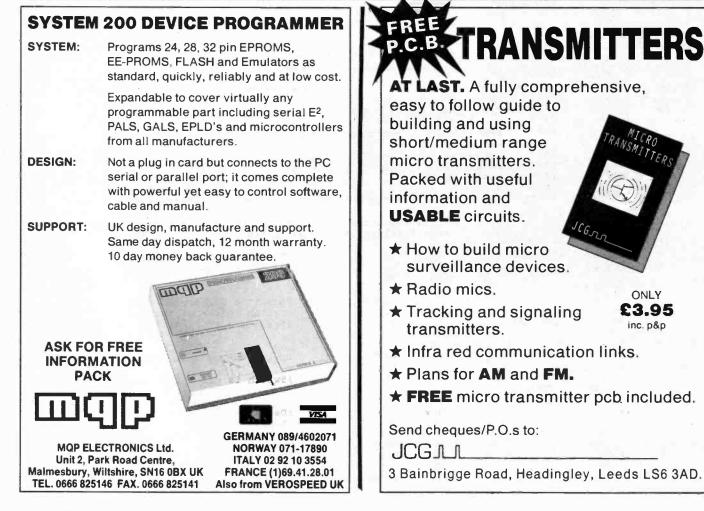
DEMONSTRATION 1

Segment displays, as used in digital clocks, etc. are made from seven l.e.d.s arranged in an "8" and another l.e.d. forming a decimal point to the right. These l.e.d.s may be driven in the same way as normal l.e.d.s as shown earlier. The *Micro Lab* seven-segment l.e.d.s are addressed at 8000h (a link is used to select between the normal l.e.d.s and the seven-segment display). Fig. 10.4 shows which bit of the data bus controls which segment. You should note that setting a bit to 1 will turn the corresponding segment off.

Demonstration program 1 is similar to demonstration program 0, except that it waits for you to press a key after copying a value from the look up table to the l.e.d.s and it stops when the end of the table has been reached. Fig. 10.7 gives the program listing.

Before running this program, you should set up the look up table starting at address 0200h, storing the length of the table in 0300h, to output the numbers 0 through to 9 (address 0300h should be set to 0Ah in this case).

To get you started, the number "0" is displayed with binary 001111111 (or 3Fh) and "1" is displayed with binary 00000110 (or 06h).



Everyday with Practical Electronics, August, 1993

DEMONSTRATION 2

In order to be useful, a computer system must have some form of input. On the *Micro Lab*, there are four basic input devices – the keyboard, the DIP switches, the analogue input and the 6522 VIA. The simplest of these to use are the DIP switches, which are mapped directly into memory at address 8010h. Fig. 10.8 gives the program listing.

To read the switches, all you need to do is load a register with the contents of address 8010h (e.g. with LDA 8010h).

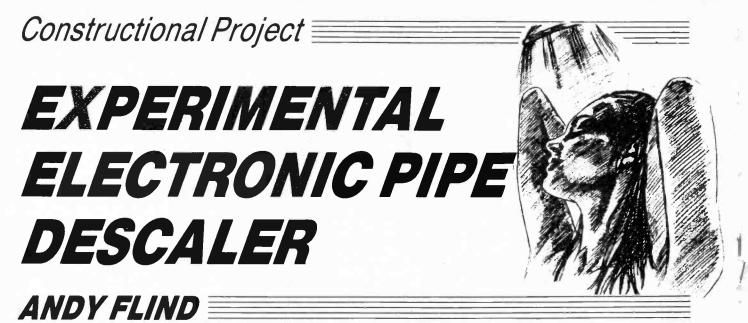
The following program simply reads the switches and copies the result to the l.e.d.s:

0200 AD 10 80	LDA	8010	;Get switch value
0203 8D 00 80	STA	8000h	;Store in I.e.d.s
0206 4C 00 02	JMP	0200	;Repeat forever

Demonstration program 2 is similar to this, except that the switch settings are displayed on the l.c.d. (a "·" means that the switch is off, and a "O" means that the switch is on). Monitor routines are called to write to the display. Unlike the simple program shown above, to display the switch settings on the l.c.d., each switch setting must be displayed separately. There are two ways to do this. One way is to AND the switch reading with 01h (to read the left most switch) and compare the result with zero. Next, you would AND the switch settings with 02h and compare with zero again. You would copy this for all eight switches.

This method is most useful when you are only interested in the value of one particular switch at a time (if you were writing a game and the switches were push buttons – left, right and fire – then you would use this method to test each switch in turn), however, it is not really suited to this particular task. A neater way is to load the switch settings into the accumulator then use the LSR instruction to shift the lowest order bit into the carry then test the carry flag (using BCC or BCS) to read the state of the left most switch and display it on the l.c.d. This is repeated eight times in a loop, once for each switch. The resulting program is much smaller and does not contain large amounts of repeated code.

Next month: We take a look at data logging, analogue measurement and controls, and digital-to-analogue conversion. We demonstrate further routines to help you grasp further principals of microprocessor systems.



A mains powered "black box" to make your calcium less sticky and, in time, your water flow more freely.

DO YOU suffer from furry pipes? Clogged conduits? Calcified kettles? Quite a few readers do, it seems. The problem, of course, is due to "hard" water containing calcium, which tends to precipitate onto the surfaces of pipes, kettles, etc, forming the familiar "scale". In addition it can cause scum and lack of lather, resulting in the need for extra detergent and other problems.

Not so long ago the only cure was a large and expensive chemical filter, but

recently several new claimed solutions have appeared. Some of these are electronic, mains-powered "black boxes" supplying a signal to a coil of wire placed around the incoming water pipe. Since these are usually expensive, EPE has received enquiries from readers concerning the possibility of constructing one for a more modest outlay.

RESEARCH

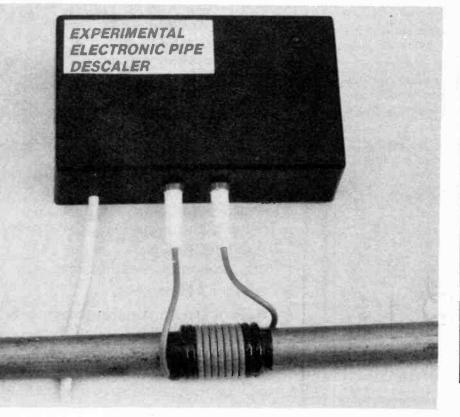
Following "extensive research" the

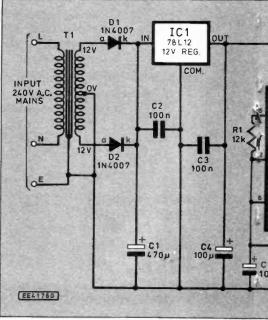
design presented here was arrived at. At the time of writing no definitive results have been obtained as to whether it actually works, but as it is cheap and simple to construct it was decided to publish forthwith for interested readers to try for themselves.

Commercial units produce a square wave output, with frequency swinging between 1400Hz and 2000Hz at a rate of about five Hertz. The output power is surprisingly low.

The wire used for winding around the pipe tends to be thick, around 2.5mm, so it could be assumed that the current would be several amps. However, the output is a twelve-volt peak-to-peak source from a resistance of 10k, so operating into the virtual short-circuit of the short wire coil this gives an average current of just 0.6mA!

It's hard to see how this tiny current produces significant effects of any kind, but suppliers of commercial units state that it most certainly does. The output, by the way, is purely a.c., there is no d.c. component at all. This suggests a blocking capacitor in the output circuit.





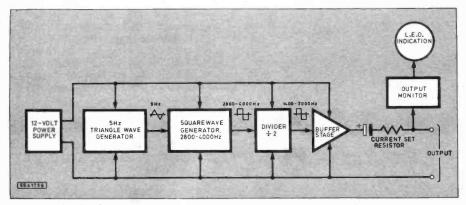


Fig. 1. Block diagram of the Experimental Electronic Pipe Descaler.

ACTION

The claimed action is to render dissolved calcium in the water less "sticky". In other words it's still present, but has less tendency to precipitate out and stick to the surfaces it comes into contact with.

The effect of current passing through a coil of wire around a water pipe is twofold. It produces electrostatic and magnetic fields, both alternating at the frequency of the current.

A metal pipe of any kind would block an electrostatic field. This leaves the magnetic one, which would be significant at this low frequency, although still very small given the tiny current used. However, a lot of older houses (the author's included) have iron supply pipes, which would also attenuate the magnetic field to some extent. It might be worth a mention in passing that other descaling devices on sale consist of a powerful permanent magnet designed for fitting around the water pipe.

CIRCUIT

The aim of this project therefore, is to produce a signal varying at 5Hz between 1400Hz and 2000Hz, initially at 0.6mA, though provision is made for experimenting with increased current. The design has an output buffer stage capable of higher output, controlled by the value of one resistor. A simple block diagram, Fig.1, shows the complete arrangement. Power from a small mains transformer is regulated to twelve volts. A 5Hz triangle-wave generator controls the squarewave generator. Since the output mark-space ratio of this is uneven and variable with frequency, it is produced at double the required rate and then halved by a divider, resulting in a perfectly even output signal.

A buffer stage provides output power, a capacitor blocks the d.c. voltage and a resistor sets the current supplied to the coil around the pipe. A monitor circuit confirms that the oscillator is working and that output connections are intact.

The circuit diagram of Fig. 2 shows how these functions are achieved in practice. Power is supplied by the mains transformer T1 and maintained at a constant twelve volts by regulator IC1. IC2 is an ICM7556, a dual low-power version of the popular 555 timer. Both are used as astable oscillators, although in slightly, different ways.

The first has the output, pin 5, connected through R1 to input pins-2 and 6, with R1 and C5 setting the output frequency to about 5Hz. The signal across capacitor C5 is approximately triangular with an amplitude of 4V peak-to-peak, and this is applied directly to pin 11, the control input of the second timer. This uses the conventional astable timer circuit.

Cl	OMPONENTS	
Resistor	s See	
B1		
R2	12k 22k SHOP	
R3	47k TALK	
R4, R5 R6, R7,	10k (2 off) Page	
R9	100k (3 off)	
R8	8k2 (see text)	
R10	470k	
R11	1k	
	1% metal film	
Capactio		
C1	470μ axial elect. 25V	
C2, C3,		
C8, C9,		
C10	100n polyester layer (5 off)	
C4, C7	100µ axial elect. 25V	
C5 C6	10µ axial elect. 25V 4n7 close tolerance	
Co	polystyrene, 1% type	
C11	10n polyester layer	
C12	470p min. disc ceramic	
Semicor		
D1, D2	1N4007 diode (2 off)	
D3, D4,		
D5	1N4148 diode (3 off)	
D6	green I.e.d.	
TR1, TR3	BC184L npn silicon	
	transistor (2 off)	
TR2	BC214L pnp silicon	
IC1	transitor 78L1212-volt positive	
ici	regulator	
IC2	ICM7556 CMOS 555 dual	
	timer	
IC3	4024B CMOS divider	
Miscellaneous		
T1	12-0-12 volt 100mA sec.,	
	mains transformer	

mains transformer 4mm plugs and sockets; case, ABS plastic box 158mm x 95mm x 54mm; p.c.b. available from the *EPE PCB Service*, order code 839; 2-core mains lead; wire for coil – see text; strain relief bush for mains cable.

Approx cost guidance only

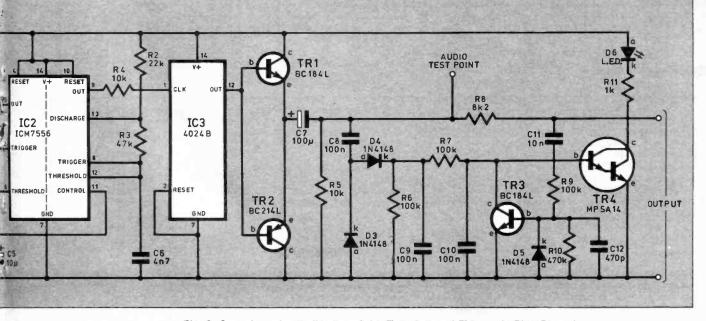
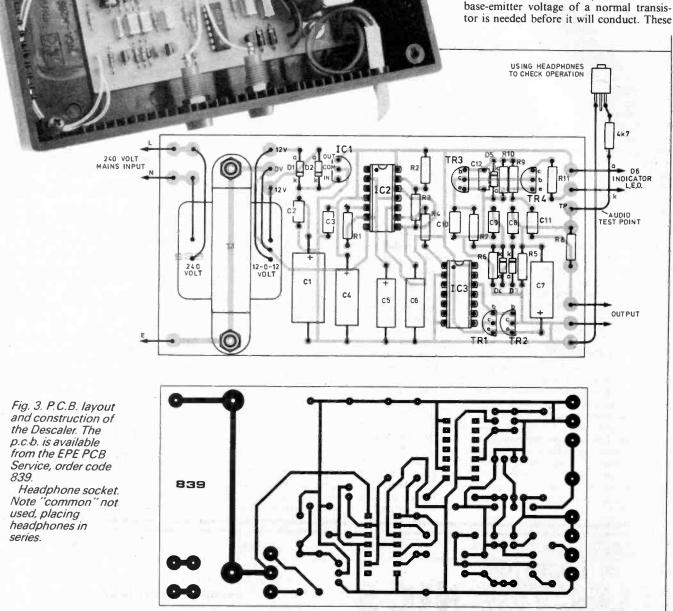


Fig. 2. Complete circuit diagramof the Experimental Electronic Pipe Descaler.

C11 will cause TR3 to conduct on alternate half-cycles. This discharges C10 and there is insufficient time between half-cycles for R7 to charge this capacitor and turn on TR4 again. This means that the output signal must be present and connections to the coil around the pipe must be sound for the indicator to be lit.

A "Darlington" transistor was chosen for TR4 for two reasons. The very high gain ensures adequate drive for the l.e.d. from a small base current, and double the base-emitter voltage of a normal transistor is needed before it will conduct. These



STREET, STREET

HILL

With the values of R2, R3 and C6 used the output, from pin 9, varies between about 2600Hz and 4000Hz. Capacitor C6 is a close-tolerance (one per cent) type to ensure adequate accuracy of these frequencies. As the control voltage affects only half of the output waveform the mark-space ratio varies quite a bit, but passing the signal through divider IC3 results in a final output of 1300Hz to 2000Hz with a perfectly even ratio.

OUTPUT

The buffer transistors TR1 and TR2, provide additional output drive capability. Whilst not really necessary for 0.6mA, it was felt that constructors might wish to experiment with higher currents so this stage can provide over 50mA if desired. C7

blocks d.c. voltage from the output, leaving a pure a.c. signal. Again, the value of 100μ is excessive for the small current, but provides for use of a much higher one.

The output current is set by R8. The value of 8k2 in this design compensates for the small voltage drop across TR1 and TR2. Reducing the value of this resistor increases the output.

The remainder of the circuit provides monitoring to ensure that the unit is working. The a.c. output signal charges C9 through the rectifier circuit D3 and D4. As long as the output is present and the coil connections are sound, d.c. current from C9 passes through R7 to TR4, causing this to conduct and illuminate the l.e.d. D6.

If the output connections become faulty, the resulting increase in voltage applied to. two factors permit practical resistor and capacitor values to be used in this part of the circuit.

Although this monitoring is fairly complex, it was considered necessary as it will normally be the only indication that the unit is actually working. Astute readers will realise that there is still one item not covered, namely operation of the 5Hz oscillator. It's unlikely that this will fail, but the simple addition of a resistor and stereo socket shown in Fig.4 will allow an occasional check with a pair of headphones, as the output has a very distinctive sound.

CONSTRUCTION

All the components of this project are mounted on the circuit board so construction should be straightforward, though step-by-step checking is recommended. All component positions are shown in Fig. 3.

Transformer T1 is secured to the board with two screws. This, with diodes D1 and D2 and capacitor C1 can be fitted first, then powered up and the voltage across C1 checked. Note that 240 volt mains is present on the p.c.b. tracks connecting to the transformer primary, this of course is a hazardous voltage and appropriate precautions should be taken. If this part of the board is suitably insulated, perhaps with a couple of layers of tape, danger should be practically eliminated.

The voltage across C1 should be about 17V, the "peak value" from the transformer. Following this, IC1 and capacitors C2 to C4 can be fitted, the unit powered again and presence of the regulated 12 volt supply across C4 checked.

This design represents a departure from the author's usual recommendation of the use of d.i.l. sockets for the i.c.'s. As it may have to operate for long periods in adverse conditions (where is your rising mains water pipe situated?) and may be encapsulated in resin, it was felt that the possible contact problems arising from the use of sockets would be best avoided. Therefore, IC2 and IC3 are soldered directly into the board.

As these are both CMOS types, and the ICM7556 in particular can be a bit static sensitive, appropriate handling measures are advised. The main precaution is earthing of the constructor. If an anti-static wrist strap is available it should be used. If not, earthing of the constructor is recommended, perhaps by a wire around the wrist with a couple of IM resistors in series for safety.

CHECKING

Integrated circuit IC2 and associated components should be fitted first, and with the circuit powered the voltage at pins 5 and 9 can be checked. Pin 5 should display an average level of about 6V, and with an analogue meter the 5Hz flicker will be observable. Pin 9 will be between 5 and 6 volts. Following this R4 and IC3 can be added. If this operates correctly the d.c. voltage at pin 12, as read with a meter, should be exactly half the supply voltage as it is switching between the supply rails for equal periods of time. Finally the remaining components can be installed, and the l.e.d. connected for testing. When the unit is powered this should not light until the output is shorted to simulate healthy connections to an output circuit. Once it is lit, it could be useful to ensure that it also goes out if pin 1 of IC3 is shorted to negative rail, simulating oscillator failure.

The output can also be tested with headphones and a suitable resistor between the test point and negative; 4k7 should be a suitable value for most headphones, if connected in series.

CASE

The assembled board can be fitted into a case of the constructor's choice. The prototype used an inexpensive plastic box, with the l.e.d. mounted at the front and two 4mm sockets for output on the side. If it is to be used in a hostile environment, such as an area subject to damp, it might be advisable to "pot" it in resin for protection. In fact this is a good way to secure it in the case, the unit being propped in the centre of the case which is then filled with resin.

Some constructors may wish to use a watertight case and these are now available at realistic prices.

INSTALLATION

To install the descaler, first the incoming water supply pipe, or the pipe supplying an item to be protected, should be located. The unit should be mounted close to this pipe, no more than a metre or so from it, with a suitable coil wound on the pipe for connection to the unit. This consists of eleven turns of thick multi-strand insulated wire, close-wound, with ends secured by tape or cable-ties and terminated with 4mm plugs to fit the descaler.

Suitable wire can be obtained by stripping the cores from some heavy-duty mains flex or from car electrical suppliers. The coil should be on a straight length of pipe, at least 50mm from any fittings or fixings etc. The tails to the descaler should be short, not be more than a metre in length.

There may be installations where the unit has to be fitted in a bathroom or similar area where use of mains electricity is forbidden or hazardous. In such cases, the best approach is to encapsulate the circuit in the case with a mains lead long enough to be routed right out of the room before connection. The cable routing should obviously be chosen to protect it from possible damage.

In all cases the unit should be connected to the mains supply via a suitable 2A fuse either in a 13A mains plug or a switched mains flex outlet.

IN USE

If the unit works as intended, scale buildup should stop immediately and existing scale should eventually start to dissolve. There is a possibility of apparent increased water hardness at first, as existing scale re-dissolves back into the water, but this should clear after a few weeks of use.

Remember that the calcium is still there and will be seen in the kettle or on taps when the water has dried, it simply will not stick so much and therefore not build up.

As stated earlier, this project is experimental in nature. Unlike commercial versions it's cheap, so it will cost little to test on plumbing systems plagued by hard water. It might be worth trying more than one, with additional units fitted to supply pipes to hot water cylinders, dishwashers etc. Although the output power is very low, it may be increased by altering the value of resistor R8. Constructors are welcome, indeed encouraged, to try this.

The minimum suggested value of R8 is 100 ohms, which will give an output current of 56mA, although with 47 ohms it will push out over 100mA. At this level over half a watt will be dissipated in the resistor, so a suitable wattage rating will be needed.

FEEDBACK

The experience of users of this project, and comments on it's performance etc, are welcomed by the author as from them it may be possible to ascertain it's effectiveness and whether any improvements might be worthwhile.

Details such as the material and diameter of pipe it was used on, location e.g. rising main, input to hot water cylinder etc, and the output current used, together with results obtained and how long they took to appear, should help to build up a picture of the effectiveness of electronic descaling.

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Special Feature

Audio Amplifier Design, Engineering or Alchemy?

JOHN LINSLEY HOOD

Can you trust performance tests?

HERE are two fundamental, but mutually contradictory beliefs in the field of audio, and these can be summarised as:

1. "All competently designed audio amplifiers, used within their ratings, will sound the same," and:

2. "All things in an audio system are important – so that even the nature of the wiring which is used to interconnect the pieces will alter the sound of the system."

By and large the first of these beliefs is that held by professional audio engineers and designers, and the second by the readers and a majority of the editorial staff and contributors to "Hi-Fi" magazines, who I will call the "subjectivists", simply as a convenient group label.

Both of these groups are willing to demonstrate the validity of their points of view – the "engineers" by carefully arranged group listening trials, using "double blind" tests, (those in which neither the listening panel nor the person doing the switching actually knows which amplifier they are listening to at the time) – and the "subjectivists" by "one to one" demonstrations in the comfort of their own homes, on a "now, just listen to this" basis.

SANDWICHED

As an audio amplifier designer, I find myself uncomfortably sandwiched between these two opposing views. Obviously, the possession of test instruments, and some measure of faith in the results they give, is just as essential a requirement for any serious design work as a good understanding of the nature of the technology and its theoretical basis, if only because it is in the nature of mankind to make occasional mistakes, and instruments are helpful in drawing attention to these errors.

On the other hand, it is impossible to predict with absolute confidence just how audible some of the residual performance shortcomings of the hardware – and there always are some, in even the best of designs – will prove to be.

Our understanding of what the human ear is prepared to accept as "accurate" is getting better, but we're not there yet, and I note, with regret, that one still comes across engineers who will flatly deny the existence of technical faults in a system, even when these are so glaring that their own ears object to the nature of the sound they are hearing.

HEAD IN THE SAND

As a relatively recent example of this "head in the sand" approach, I have a book on digital audio systems, written in the mid. '80s, soon after the introduction of the compact disc, in which the author says "of course, digital sound sounds entirely different from analogue, because it is so much more 'accurate'. However, in time we will get used to this different and superior sound quality, and accept it for what it is."

In reality, the big difference, at that time, between, for example, the sound of an LP (analogue) and a CD (digital) replay was due to the unwanted presence of large h.f. phase errors, which ruined transient response, and the pre- and post-transient "ringing" caused by the primitive "brickwall" (steep-cut anti-imaging) filters on the output of the CD players. Because of these problems, some of the early machines really did sound different from analogue systems: they were harsh and strident, and not at all pleasant to live with.

Fortunately, some of the engineers working in this field were prepared to accept that the deficiencies in the sound produced by these early CD systems were real, and were due to measurable technical faults, and they worked to improve things.

So now, with a multiple "over-sampling" type of CD player, which can give good replay phase linearity, and a decent transient response, due to relatively gentle h.f. filter slope characteristics, the only significant audible difference between a CD and an LP of the same music, when the LP is replayed in synchronism on a good turntable (so that the listener can switch over directly from one to the other), will be the greater freedom from "rumble", "clicks" and surface noise on the CD. However, this certainly wasn't the case ten years ago.

PERFECTION?

The history of audio is full of instances when new ideas were introduced, but which didn't work as well as they should, and, because the errors in the design were not apparent in the test results, or were noted but not thought to be particularly important, their designers or manufacturers claimed that perfection had been attained.

Since the more perceptive of the listeners, and you might not need to be very perceptive at that, were very aware that things were wrong, the mistaken claims of the engineers tended to undermine the validity of the very measurements they quoted, and this, sadly, is now the climate of popular opinion in which we audio engineers must work. I say "sadly" because it is essential to make measurements, and the idea that test results are of no value to anybody undermines the whole basis of technical progress.

PART 1

However, one needs to measure the right things. For example, the one measurement that Joe Public will trust, when he goes into the showroom of his local "hi-fi" discount warehouse, is the quoted "Total Harmonic Distortion" (THD) figure, and this, unless you know the specific conditions of the test, really is pretty meaningless. I will explain, later.

SUBJECTIVISTS

Taking the side of the "subjectivists" for a moment, one of the first areas of conflict was about the influence of loudspeaker connecting leads. The hi-fi devotees noted, and tested out for themselves, and supported, the claim that different kinds of loudspeaker connecting wires "sounded different". "Rubbish" retorted the engineers, "provided that the loudspeaker lead resistance is less than a tenth of an ohm, or thereabouts, it can't possibly have any effect with an eight ohm loudspeaker unit".

Indeed, so it shouldn't. But, if you have a pair of adequately low resistance leads and you decide that one of them doesn't need to be as long as the other, and so you chop off a length of it, you will find that you have now altered the apparent position of the centre of the stereo image, and this is evident as the new position required from the amplifier balance control.

Of course, part of this effect is due to the fact that "transient" type signals are very important to the human ear, particularly in assessing the position of the sound source, and most modern loudspeaker units have a very low input impedance to transient type input signals. This just means that we had measured our "eight ohm" loudspeaker impedance in the wrong way. Indeed with an impedance vs. frequency curve that usually looks like a Sherpa's eye view of the Himalayas, it seems a pretty meaningless measurement anyway.

THE PSYCHOLOGY OF TINKERING

I have a close friend with a professional interest in human psychology, and he assures me that it is well known that most

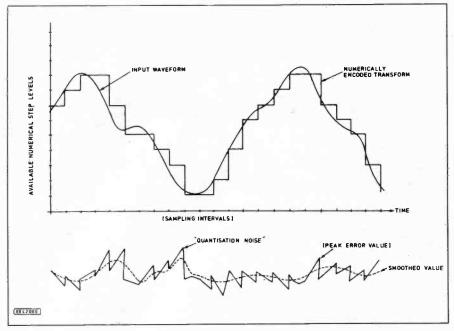


Fig. 1. The origins of "Quantisation Noise" in a digitally encoded signal.

people get great satisfaction from adding their own contributions to any system, and will, because of this, be predisposed to believe that if they make a mod. to their audio system, it will sound better as a result of any changes they have made.

Of course, most hi-fi users will hesitate to take snips or soldering iron within the entrails of their expensive kit, so most of the changes they will be inclined to try will be restricted to various "bolt on" external bits and pieces, to go on their input, loudspeaker or mains power leads, and the difficulty here is the fugitive nature of the acoustic memory, in the interval between one trial and the next.

It is helpful if one has a balanced pair of stereo channels, so one can try one's new gizmo just in one channel, so the two can be given a simultaneous trial, but even this begs the question of what one means by "better".

WHICH WAY IS "UP"?

Some years ago, a highly respected audio systems reviewer set up, as an experiment, an arrangement by which he could introduce controlled amounts of harmonic distortion into his normal listening system, and invited his various visitors and sundry local friends to listen to some music, and indicate which "amplifier" they preferred.

The results he obtained indicated that most of his ad hoc listening panel actually liked the effect produced by the addition of relatively small amounts, say 0.5 per cent, of second harmonic distortion – they said it sounded "richer" – and it so happens to be the kind of distortion that one finds in most traditional valve amplifier systems, which are so much liked for their "rich" sound quality.

The problem, therefore, with basing ones judgment solely on listening tests, is that one's ear can often tell you what it likes best, but not always what is actually better from a technical point of view. Since 0.5 per cent of second harmonic distortion may lead – depending on the conditions of the test and the nature of the system – to one per cent or more of "intermodulation distortion" (which is the kind of distortion which muddles up the sound), the author of these tests might have got a different answer if he had asked which of his "amplifiers" sounded "clearer".

The fact that people like to fiddle with their gear, and will often "hear" improvements in sound as a result of using some gadget or other has, I suspect, made a few small fortunes for the inventors of various magic boxes.

ARE ANY OF THE DIFFERENCES REAL?

It is very easy for the "engineers" – including me – to dismiss the claims of the "subjectivists" that there really are audibly significant effects, in the face of the invariably negative results given by carefully arranged "double blind" listening tests. These negative results have been obtained even when the listening panel was deliberately chosen to include "subjectivists" who had publicly declared their belief that there were big differences between the very amplifers which were being tested, and they could hear these differences without the slightest difficulty.

Is there any plausible explanation for this? I think there is, and that it is based on the length of the tests and on familiarity.

Another friend of mine is a profes-sional recording engineer, of perfectionist inclinations, and he was concerned at the time that he was about to adopt a digital recording system to supplement, and eventually replace, his tried and trusted 15in./sec. analogue tape recorder, that the results from his new digital system might be audibly less good. I was lucky to be one of those he asked to sit, individually, in judgment on this contest, for which the same musical performance had been recorded simultaneously on both analogue and digital recorders, to either of which I could listen, at the flick of a switch, when they were replayed simultaneously in synchronism.

Given a sufficiently long duration sample of each system I was correct in my identification every time, but with 5 to 10 second samples I really could not tell the one from the other. And the reason why? Because, given time, I could hear the differences between the types of background noise characteristics of the two systems. Digital audio encoding and decoding processes lead to the kind of noise called "quantisation noise", shown in Fig. 1, due to the inability of the "staircase" waveform to follow the smooth curve of the input signal.

(What I have shown in the diagram would correspond, approximately, to an audio signal at about 2kHz sampled at 44·1kHz, using a "4-bit" encoding resolution. A thing to be noted in relation to this is that this sampling error will decrease if the sampling interval is reduced or if there are a greater number of permissible quantisation levels – with the example chosen the error "noise" could be as much as 10 per cent of the signal, though this would be reduced by subsequent filtering.)

In digital audio, this kind of noise always accompanies the signal, but it stops when the signal stops. In analogue systems the noise is there all the time. And this was the difference I listened for.

ALL OF A DITHER

Of course, in the early '80's, the modern technique of adding "dither" (a synthesized, amplitude limited, random noise voltage), to the replay signal was not common practice. If it had been used in my friends relatively early digital set-up I really wouldn't have been able to identify them.

However, "dither" is not simply added to baffle attempts to tell digital from analogue, it really does improve the ability of the digital decoding process to resolve fine detail, and reduce the "granularity" (staircase characteristic) of the replay signal, particularly if the sampling frequency is increased. For optimum results, the added dither voltage should be between a half and a third of the size of the decoding voltage step.

The other point about even carefully arranged statistical tests is that small differences in sound quality may become more noticeable if one lives with a system for a little while, and this is an aspect which cannot be included in the inevitably brief "listening panel" trials.

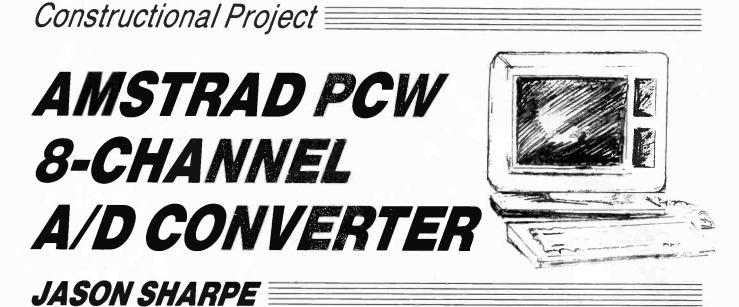
DO COMPONENTS MATTER?

Apart from the claimed importance of connecting leads, the other main plank of the "subjectivists" platform is the difference in sound quality caused by the choice of component type or construction used in the circuitry. Once again, I find myself sandwiched between the "yes there is" and "no there isn't" schools of thought.

Some of the effects are indeed real, and there are good reasons why they should be, and I propose to have a look at these in the next article.



"Most people get great satisfaction from adding their own contributions..."



Eight buffered analogue inputs
Built in Sample/Hold Function
Fast 2.5µS conversion time (0.4 MSPS)

THE WORLD we live in is continuous, for example, seconds can be divided into tenths of seconds, hundredths of seconds, etc. to infinity. But the world of the digital computer is discrete, its shortest time period determined by the clock rate.

Our world is also analogue, for example, it can be very bright, bright, dim, dark etc. In the computer's world it would be either dark or not dark.

A-to-D CONVERTER

The A/D converter enables a digital computer to take discrete samples of the continuous world. The Amstrad PCW 8-Channel ADC circuit is based on the popular AD7828 (IC2), an 8-channel ADC i.c., which contains an 8-channel analogue multiplexer, a sample and hold function, a half flash analogue-to-digital converter, and I/O control logic.

GUICK AS A FLASH

The 7828 uses a half-flash technique to digitise the input signal. A 2-bit flash A/D converter is shown in Fig. 1. The analog input is compared simultaneously with $2^{n}-1$ (where *n* is the number of bits) equally spaced voltage reference voltages.

The comparitor outputs are fed into a 2^n bit priority encoder, which outputs the digital value. This is the fastest method of A/D conversion, using this method speeds of 300 million samples per second (MSPS) can be achieved.

The main problem with the Flash ADC is its price (e.g. an 8-bit, 20 MSPS flash ADC costs approx. £70, 150 MSPS costs approx. £125) this is because of the large number of comparators required $(2^{No. Bits}-1)$. Half-flash conversion is the second fastest conversion technique, it provides near flash speed with a much lower cost, thanks to a neat trick. The block diagram for an eight-bit halfflash convertor is shown in Fig. 2. The input signal is fed into an n/2 bit flash converter, which digitises the most significant part of the voltage, this data is then fed into an n/2 bit DAC.

The DAC output is subtracted from the input voltage and the resulting voltage is digitised by another n/2 bit flash converter, which provides the least significant part of the output. Using this method speeds of up to 20MSPS can be achieved. An eightbit flash converter would need 255 comparators, but with this method only 30 comparators are required.

CIRCUIT DESCRIPTION

The circuit diagram for the 8-channel A/D Converter is shown in Fig. 3. The circuit for the buffer i.c.s, IC3 to IC10, is repeated for each stage.

Address decoding is performed by IC1 (74HC688), a high speed CMOS 8-bit comparator. When each P-input is in the same state as the corresponding Q-input, pin 19

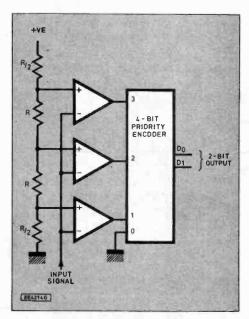


Fig. 1. Two-bit flash converter.

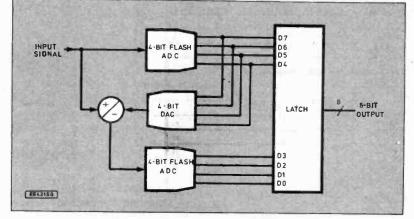


Fig. 2. Block diagram for 8-bit half flash ADC.

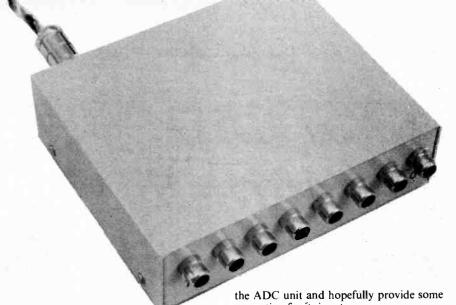
 $(\overline{P=Q})$ goes low enabling IC2. When NOT IOReQuest goes low and the low byte of the address bus (A0 \rightarrow A7) contain an address in the range 176 to 183 (Inclusive) the ADC i.c. is enabled.

As the ADC unit runs from a separate supply, the PCW's 5V line is also fed into ICI to make sure the unit is disabled when the PCW is turned off. The PCWs pull up resistors R4 to R7 are used to bring the TTL level outputs upto CMOS levels (IORQ and A7 are tied high inside the PCW).

The AD7828, IC2, is set to mode 1. In this mode a conversion is started by reading from a port in the range 176 to 183 (B0 to B7 Hex), the channel on which the conversion is performed is determined by the address on lines A0, A1, A2.

Reading from port 176 starts a conversion on Channel 0, through to port 183 which starts a conversion on Channel 7. To get the results of the conversion just read from one of the eight ports, this will also start a conversion on the selected channel.

It was mentioned above that the 7828 has a track/hold function, this is basically achieved by charging 31 internal 1pF capacitors. These capacitors, plus about 14pF of stray capacitance must be charged to the input voltage through the resistance of the input multiplexer (approx. $5k\Omega$) in

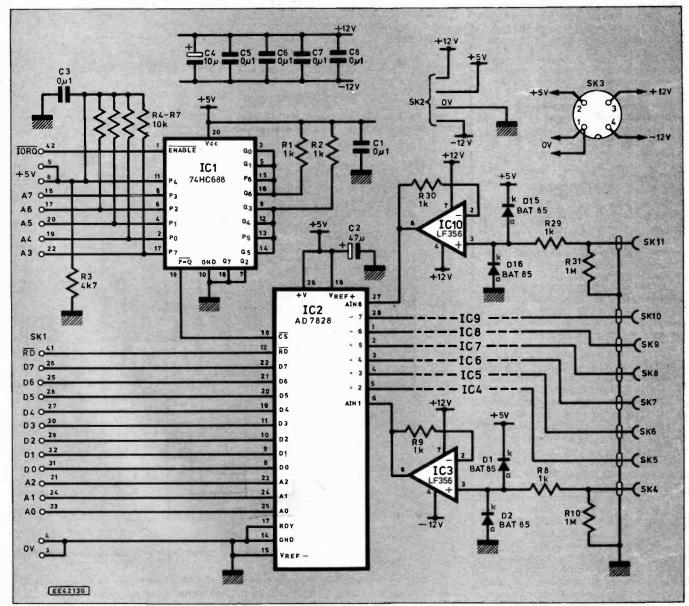


approximately 1µS. The higher the source resistance the longer it takes for the input capacitance to charge.

To make sure the capacitance is charged in time, unity gain buffers IC3 to IC10, with low output impedances, were added to the inputs of IC2. The use of these buffers also increases the input impedance of protection for its inputs.

The op.amps IC3 to IC10 draw their power from a \pm 12V supply to allow a full output voltage swing of 0V to 5V. The output of the op-amps are capable of going within a few volts of the supply, so if say 7V was placed on an input the output would swing to 7V destroying the input of IC2!





To provide some protection, diodes D1 to D16 were added to prevent the input of the buffers from going outside the 0V to 5V range. Schottky diodes are used as these have a low forward voltage drop and fast "reaction time." These diodes only provide limited protection, and care should still be taken to keep the input voltage in the 0V to 5V range.

The op-amps chosen for the buffers have low input offset voltages, so they require no trimming.

CONSTRUCTION

The 8-channel A/D Convertor is constructed on a double-sided printed circuit board (p.c.b.). The board component layout, top and underside full size copper foil master patterns are shown in Fig. 4. This board is available from the EPE PCB Service, code EPE838.

Track pins are used to connect tracks from one side of the p.c.b. to the other. These should be inserted into the holes (without components) marked \bullet on the component overlay. Push them in from the top of the p.c.b. and solder them, then turn the board over and solder them to the other side of the board – see Fig. 5a. After you have finished this check the tracks are actually connected with a multimeter.

Insert and solder the remaining components in height order, flattest up to tallest. The leads marked O on the component overlay should be soldered to the *top* layer as well as the *bottom* – see Fig. 5b. Be careful not to over heat the diodes, also when bending the leads make sure you do not damage the glass cases.

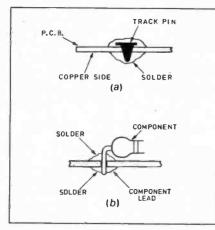
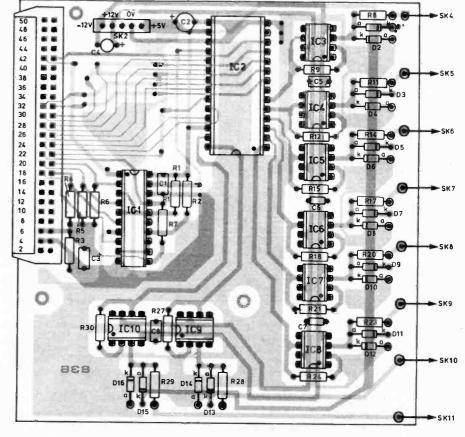


Fig. 5 (a) Connecting one side of p.c.b. to the other with "track pins". (b) soldering component to both sides of board.

Power is connected to the board using a "Minicon" connector. Strip about 5mm of insulation from the end of the wire, and tin it. The wire can then be crimped to the metal connector, this can be carried out using a pair of thin nose pliers. Then push the assembly into the plastic housing until the locking pin clicks into place, as shown in Fig. 6.

The one megohm (R10 to R31) between the inputs and "ground" are soldered between the input phono sockets SK4 to SK11 and their solder tags. One end of the lead is used to connect the p.c.b. input to the phono socket. One of the solder tags should also be connected to the 0V input.

Case construction is shown in Fig. 7. Make the hole for the 50-way connector quite high so that some "grommet strip" can be used to cover the sharp edges and prevent damage to the ribbon cable.





= Track solder pin.



Solder to top and bottom tracks.

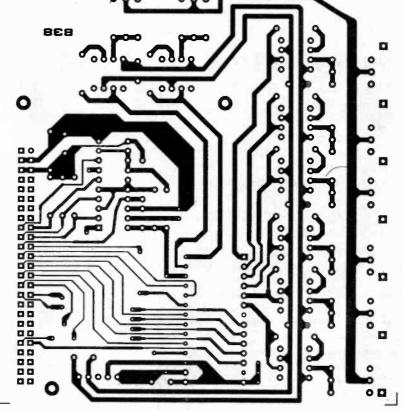
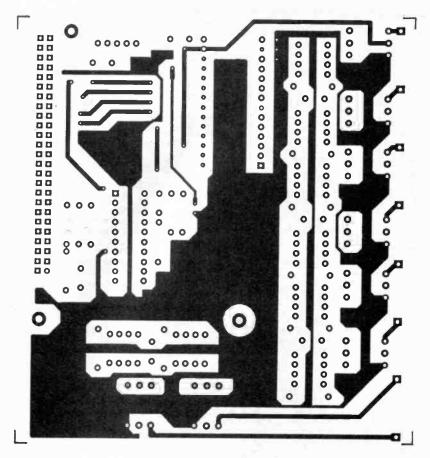


Fig 4. (top) Printed Circuit board component layout, (top right) full size component side copper foil pattern, (above) full size underside copper foil master pattern.

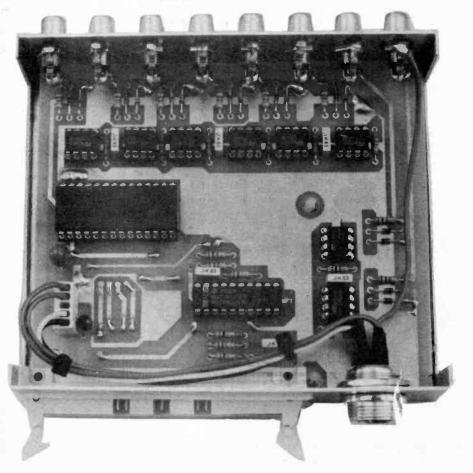


The unit should be connected to the PCW using ribbon cable terminated at one end with a 50-way IDC connector, and at the other with a 50-way IDC edge connector (check when assembling the lead that pin one of the edge connector is connected to pin one of the IDC socket).

The cable should be kept as short as possible (less than 50cm). If longer, ringing and cross-talk may become a problem.

TESTING

Check all joints and connections and make sure there are no shorts caused by



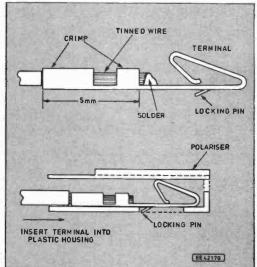


Fig. 6. Assembly of the "Minicon" connector.

COMP	PONENTS	
Resistors R1, R2, R8, R9, R11, R12, R14, R15, R17, R18, R20, R21, R23 R24, R26, R27 R29, R30 R3 R4 to R7 R10, R13, R16, R19, R22, R25, R28, R31 All 0·25W 5% carbon film Capacitors C1, C3, C5, C6, C7, C8 C2 C4	<pre> } 1k (18 off) 47k 10k (4 off) 1M (8 off) See SHOP TALK Page Ou1 polyester (6 off) 47µ tantalum bead, 16V 10µ tantalum bead, 35V</pre>	
Semiconducto D1 to D16 IC1 IC2 IC3 to IC10	Drs BAT85 Schottky diode (16 off) 74HC688 CMOS 8-bit comparator AD7828 High speed 8-bit 8-channel analogue-to-digital converter LF356N f.e.tinput wideband op.amp (8 off)	
Miscellaneous SK4 to SK11 Phono or BNC socket (8 off) Aluminium case, size approx. 125mm x 105mm x 35mm; double-sided printed circuit board available from <i>EPE PCB</i> <i>Service</i> , code 838; Minicon connector; 50-way IDC socket and R/A plug; 50-way IDC edge connector; 50-way ibbon cable; 4-pin locking chassis plug and line socket; 8-pin low-profile d.i.l. socket (8 off); 10-pin low-profile d.i.l. socket; 28-pin low-profile d.i.l. socket; interconnecting wire; plastic self-ad- hesive p.c.b. stand-off pillars (3 off); stick-on rubber feet (4 off); plastic edging; solder etc.		
Approx cost guidance only	£56	

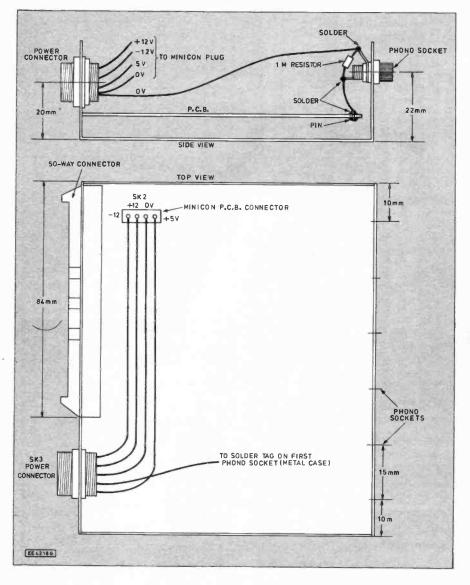


Fig. 7. Interwiring, installation and case drilling details.

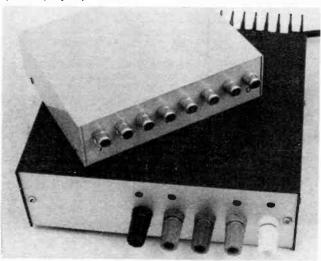
excess solder. Before inserting the i.c.s or connecting to the PCW, connect the power supply and check the voltages at the Mincon connector are correct (with a circuit containing a £20 i.c. this is worth doing!).

If all is okay insert the i.c.s. The 7828 and 74HC688 are both CMOS and so should be handled carefully (wearing an anti-static wrist-strap is recommended).

Connect the unit to the PCW and the power supply. First switch on the external supply, then switch on the computer and "boot up" as normal. If the computer will not boot up or does anything abnormal turn off the PCW then the power supply, unplug the unit and check all joints, orientation of the i.c.s etc.

To test the inputs connect a 10 kilohm or

The completed unit sitting on the "Linear" Power Supply (future project).



20 kilohm potentiometer (multi-turn are best as they give better resolution per turn) between the 0V and +5V outputs of the power supply and the wiper to Channel 0 of the ADC to form a variable voltage source. Enter the test program, Program 1, and then run it.

Enter 0 when the prompt "Which Channel" appears. A list of values will scroll up the screen, which should vary between 0 and 255 as the potentiometer is turned. If the values don't change check you are using the correct channel. The other 7 channels can also be tested in this way.

Program 1: Test Program

10 Input "Which Channel (0 to 7):", channel

20 REM **Read last result, and start next. conversion, then repeat**

30 PRINT INP(176 + channel) :GOTO 30

TAKE CARE

Phono connectors were used for the input sockets SK4 to SK11 as they are widely available and are quite low cost compared to most e her screened connectors. Care should he taken when connecting things to the ADC using phono plugs as the centre conductor mates before the screen (this is not really a very good design). Unless the ADC and the source it is being connected to have a common ground this could cause damage to the ADC unit.

The best ways around these possible problems are:

- a) To connect the phono socket first and then connect the source (with another type of connector).
- b) Use a common earth for the units (only connect the screen to the ground at one end).
- c) Make all the connections before the power is switched on.

PROGRAMMING

This unit is very simple to use. The main thing you must remember is that the value read from the port is the result of the last conversion.

The conversion program, Program 2, is an example of the above, it prints the values of Channels 7 and 0 repeatedly on the screen. First (line 20) the old value is cleared from the ADC and discarded, this also starts a conversion on Channel 7 (port 183).

The result of this conversion is printed at line 40, which also starts a conversion on channel 0 (port 176). The result of this conversion is pinted at line 60, which also starts a conversion on channel 7. The program then loops round to line 40.

Program 2

10 REM **Start conversion on channel 7 (discard value read from port)** 20 Discard% = INP(183)

- 30 REM **Start conversion on channel 0 and print result of last conversion*
- 40 PRINT "Value of Channel 7 < ";INP(176);

50 REM **Start conversion on channel 7 and print result of last conversion**

60 PRINT "> Value of Channel 0 < ";INP(183)" > "

70 GOTO 40 :REM Repeat

There are many uses for this compact analogue-to-digital converter unit. In Part Two, next month, more programming details and some possible applications will be given when we set out and give information on "Using The ADC Unit". For example, how to use the ADC for monitoring/data logging - with simple sensors, its use for sampling signals at higher sampling rates and some elementary signal processing will be outlined.

Also, a future article will present a "Linear" Power Supply designed especially for the A/D Converter. However, the outputs of +12V at 1A; +5V at 1.5A; -12V and -5V at 0.5A make it ideal for many general purpose uses and an invaluable acquisition for the "test gear" collection.

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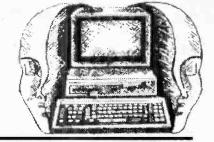
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Everyday with Practical Electronics, August, 1993

QUALITY

AUDIO KITS





A S MOST readers who own PCs will probably be aware, upgrades to MS/DOS version 6.0 have recently become available. This version of DOS has a number of new features, but the most interesting of these is undoubtedly the "DoubleSpace" program. This is a disk compression program which can be used with any disks, but is primarily intended to be a means of effectively doubling the size of a hard disk drive.

Although some reports in the computer press seemed to suggest that this program had been withdrawn from MS/DOS 6.0 due to legal reasons, it is definitely included in the package. It is the utility program that can convert disks compressed using "Superstore" to "DoubleSpace" format that has been withdrawn. Apparently this conversion program is available separately from Microsoft for a small fee.

Crash Drive

Hard disk compression programs have rather poor reputations, and many users seem to run into trouble when using them. On the other hand, other users seem to experience no real difficulties at all.

I tried a hard disk compression program about a year ago, but was far from impressed. When the hard disk became more than half full, and the program started to provide extra disk capacity, the system would seriously crash. In fact it was difficult to regain control of the computer again, and this was only possible by reformatting the disk and restoring all the files from the backup disks.

Another problem is that the way in which most of these programs operate is something less than convenient. The usual arrangement is for drive C: to contain the usual start-up files, plus a huge file generated by the compression program. This huge file takes up practically all of drive C:'s capacity.

A notional drive D: is generated, and this has a capacity of around double the hard disk's actual capacity. It is here that the programs and data are stored. Of course, the huge file on drive C: and the notional drive D: are one and the same.

The problem with this arrangement is that moving the programs and other files from drive C: to drive D: can cause malfunctions, with files not being where programs expect them to be. It is a system that works best if it is used from the start, rather than being applied to a hard disk which is already loaded with your programs and data.

A program such as "DoubleSpace", which is part of the operating system, has a big advantage over an add-on program. The fact that the disk has been subjected to compression can be completely hidden from the user, with drive C: seeming to increase in capacity after the compression program has been run. The programs and other files remain on drive C:.

The only peculiarity of the "DoubleSpace" program is that it generates a drive H:, which has a capacity of about eight megabytes. No compression is used on this drive, and its main purpose seems to be as temporary storage space for Windows. It can also be used for other files that must not be subjected to any compression.

Installation

I obtained the 3.5 inch version of the MS/DOS 6.0 upgrade, which is supplied on three high density disks. If necessary, a set of 720k disks can be obtained by sending off the appropriate card to Microsoft.

Installation is easy since a setup program is provided. Unless a custom installation is required, it is basically just a matter of pressing the right key when requested, and changing disks at the appropriate times.

Like the MS/DOS 5.0 upgrade, some existing files can be automatically loaded onto an "uninstall" disk. This makes it possible to return to the old version of DOS if required.

The "DoubleSpace" program is loaded into the DOS directory, but it is not run during the installation process. If hard disk compression is required, "DoubleSpace" must be run once the installation procedure has been completed.

It is very straightforward to use, and for most users it is just a matter of accepting the defaults. The compression process takes a while, and it took just over an hour to compress my 105 megabyte hard disk drive which was almost full. This includes the time taken for the program to defragment the disk.

Like most programs of this type, "DoubleSpace" provides what in most cases is something less than a two-to-one compression of the data on the disk. However, it still provides a very worthwhile increase in effective disk capacity. With a 105 megabyte hard disk drive I gain about 70 megabytes on drive C:, plus the eight megabytes or so of decompressed space on drive H:.

The amount of space gained by compression depends very much on the contents of the disk. Compression programs mostly use more than one compression technique.

The most popular forms of compression rely on a certain amount of repetition in each file. A long string of binary 1s or 0s might be present, and it will then be possible to save disk space by coding these as X number of 1s or 0s.

This process can be taken a stage further, with repeated binary patterns being coded in much the same way. The longer the binary patterns, and the more often they are repeated, the greater the space saving that can be achieved. After compression, many programs repeat the processing on a second pass in an attempt to produce further space saving.

Compression techniques mostly work quite well with program files, but give much more variable results with data files. Bit maps generally offer excellent scope for quite high degrees of compression, while ASCII and similar files generally lend themselves to moderate amounts of compression.

However, many programs apply one or more compression techniques to saved data, it is probably this factor that results in programs like "DoubleSpace" tending to fall short of their design aim of two-to-one compression. I achieved a compression ratio of about 1.8 to 1, which seems to be fairly typical of these programs.

As anyone who used PC shareware in the early 1980s will remember all too well, not all compression programs manage to decompress files with 100 per cent accuracy. Many of the early efforts crashed half way through the decompression process, or produced errors in the decompressed files.

There was a lot of shareware in circulation which did not run due to decompression errors. Fortunately, modern disk compression programs seem to be totally reliable, and I have certainly not experienced any problems when using "DoubleSpace".

Half Time

There is a potential problem with disk compression programs that work "on-thefly", and this is that they can seriously slow down disk accesses. Every time something is written to disk, or read back from the disk, it must be processed by the compression/decompression program. This process is "transparent" to the user, but it goes on in the background every time the hard disk is accessed, and it takes time.

In practice the problem is less severe than one might think. It has to be borne in mind that hard disks tend to be quite slow by the standards of the rest of the computer. Processing the data takes extra time, but having less data to read and write saves time.

Using disk compression can speed up disk accesses, or slow them down. It all depends on the access time, etc. of the disk, and the speed the data processing is carried out. In my experience of these programs they do not seem to have much effect one way or the other.

Using a 33MHz 80386 based computer and an IDE drive with a quoted 14mS access time, loading large programs is definitely somewhat slower using the "DoubleSpace" program.

Loading large data files into programs seems to give a less significant difference. Overall, any slowing down of the computer would not seem to be of great significance.

MS/DOS 6.0 has several other new [features, including a program which helps to free more RAM, an anti-virus utility, a defragmenter program, an undelete utility, and improved backup programs. Incidentally, the "DoubleSpace" program can be used to compress data on floppy disks, and it is not restricted to use on hard disks.

The MS/DOS 6.0 is available from most PC software retailers, and it currently has a recommended retail price of \pounds 49 plus VAT. It is worth shopping around though, since it can be obtained for about this price, inclusive of VAT.

Either way, it represents very good value for money. Note that this is an upgrade program which can only be installed on a computer which is running an earlier version of DOS, or Microsoft's OS/2 operating system.

Joystick Port

From time to time I receive letters concerning the PC joystick ports. Some readers

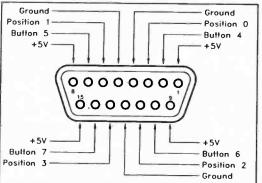


Fig. 1. Pin functions of the joystick port.

wish to use this as a mouse port. As explained in a previous *Interface* article, this is not a practical proposition because the joystick port is incompatible with all the normal types of mice. Most of the others wish to use this port for interface temperature probes, light sensors, or other analogue devices.

This seems to be a more realistic proposition, since the PC joystick port is an analogue type. In this respect it is quite like the BBC computer's analogue port. Unfortunately, it seems to work in a rather different fashion to the BBC computer's analogue port. The latter utilizes a fourchannel 12-bit analogue-to-digital converter which is easily interfaced to various analogue devices.

The PC joystick port does not use a proper analogue-to-digital converter. It seems to work on the basis of using the resistance of each joystick potentiometer in a timer circuit.

Presumably a software counter operates until the end of the each timing pulse. The higher the joystick resistance, the longer the pulse duration, and the higher the count. A resistance of around zero to 140k or so seems to be required.

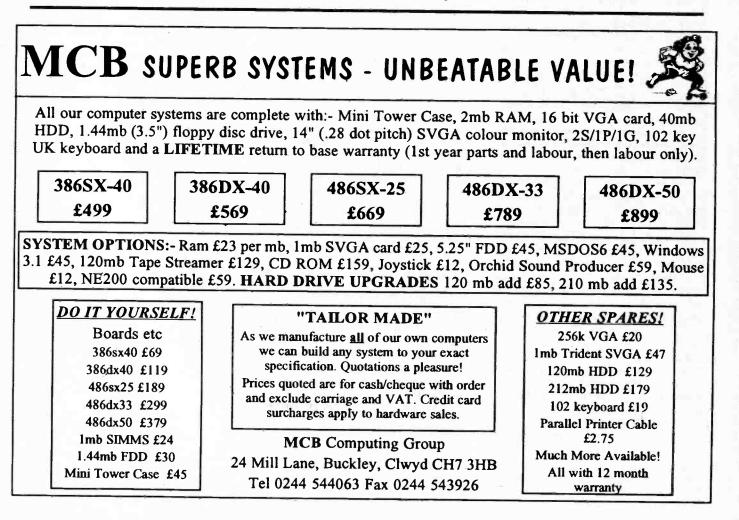
The standard connector for the joystick port is a 15-way D-type socket. The pin functions for this port are shown in Fig.1.

The two potentiometers for joystick 1 connect from + 5V to the "Position 1" and "Position 2" terminals. Presumably the potentiometers in a second joystick connect from + 5V to the "Position 2" and "Position 3" terminals.

It might be possible to use these four inputs with some types of sensors, but the sensors would have to provide varying resistances, and not varying voltages. Also, the linearity of these inputs does not seem to be particularly good. The joystick port can only be used for very basic analogue sensing applications, but it is potentially useful.

There are two firebutton inputs for each joystick. These inputs are "Button 4" to "Button 7", and in normal use they are connected to the earth rail via the firebutton switches. These seem to be four conventional TTL digital inputs which could be used for other purposes.

The interfacing possibilities of the PC games port will be covered in more detail next month.



Everyday with Practical Electronics, August, 1993

Special Feature

Those Amazing Barcodes



ALAN WINSTANLEY

scanned by lasers and interpreted by computers.

ANY advances in terms of technological applications, speed of operation capacity power and size

■ **V** ■ operation, capacity, power and size have been made within the last decade. For example, the computer age is well and truly upon us, as immensely powerful personal computers can now be found in the home, in the office, and even in the briefcase.

DEVELOPMENTS

Facsimile ("fax") machines, which were exclusively sported by the wealthiest of companies six or seven years ago, have tumbled in price, even finding their way into domestic homes as they can now be bought in the high street for as little as £229. A fax machine can even be purchased which is not much larger than an ordinary telephone.

Miniaturisation continues unabated as the manufacturers now proffer pocketsized colour televisions, talking language translators no bigger than a calculator, electronic diaries and address books, palmsized video cameras and hi-fi equipment which is a fraction of the size of its predecessors.

One of our more amazing electronic developments is now taken very much for granted. They pop up all around us in everyday life. Checking out at the supermarket, buying a magazine at a newsagent, selecting a programme to be taped by our video recorder, those striped symbols are now familiar to us all.

Barcodes seem very innocuous. They adorn almost every product you can buy in every store and supermarket. There are thousands of millions of them in circulation, each bearing a computerised code which has revolutionised the way we do our shopping.

CHECK THIS OUT

Queuing up at that supermarket "scanning" check-out (electronics still hasn't solved the problem of those interminable delays at the cash register!). you will now hear the monotones of a computerised cash register which "reads" the barcodes of the produce as the check-out assistant passes it over a **laser scanner**. Each electronic beep is a confirmation that the laser has "seen" the barcode – whether it's on a bag of crisps, a tin of soup or lengths of timber.

In the surface of each check-out counter you may see a small window, probably less than twelve inches square. Look carefully at the window and you may often observe the laser light itself, in the form of a red pin-prick of light. Adjacent you may see both a small red and a green indicator lamp. The green lamp briefly flashes, accompanied by a beep, to confirm that the goods have been scanned correctly by the barcode reader. A price and product description then flashes up on an alphanumeric display nearby. The red indicator lamp occasionally warns the operator to repeat the scanning process.

The laser scanner is quite capable of reading the barcode forwards or backwards, as they scan over the barcode with a matrix of low-power light beams which can be directed by mirrors mounted internally under the window. Thus, the way in which the operator presents the goods to the scanner is not critical, because the multidirectional beams are bound to register the barcode no matter which way the product is presented. The check-out assistant merely passes the goods over the scanner in a casual manner which belies the complexities of the light-directional mechanism and the electronic deciphering system being used behind the scenes.

How does a check-out scanner actually "read" this barcode? When the item is passed over the scanner window, a precise laser light beam is directed by a series of lenses and mirrors over the merchandise and so over the barcode. This beam of laser light – which, unlike those fictional ones which James Bond encountered in the movies, is completely harmless – is actually absorbed by the dark bars of the barcode, but reflected back by the lighter sections.

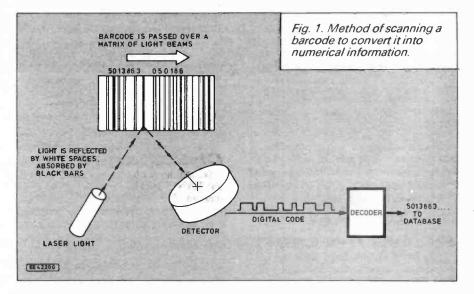
The image which bounces off the barcode is directed back into an electronic detector which decodes the reflection, see Fig. 1. Thus, the check-out computer obtains the product's *digital code* by deciphering the barcode from the series of light pulses which are received by the detector and decoding system.

MEMORIES ARE MADE OF THIS

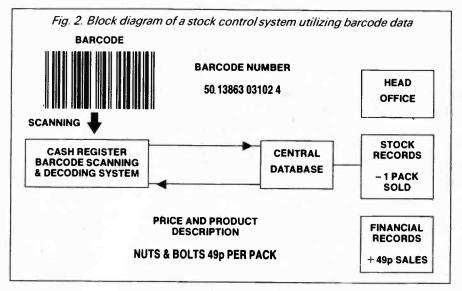
The barcode is in effect a unique "I.D. card" for that particular line of merchandise. The cash register system then reads through its database, looking for a code number which matches the number it has just deciphered from the barcode, see Fig. 2. The system's memory then instantly flashes back to the cash register with a product description, and, more importantly to the housewife, the price to be charged. In addition, the computer now knows that a particular product has just been purchased, and so it also updates its stock records accordingly.

In an instant, the cash register has been updated with a product description, and the product name is printed onto the till receipt along with its price. The customer now has a clear record of the purchase, instead of an unhelpful roll of paper indicating nothing but a string of prices.

Even more amazingly, the senior staff back at the head office – which could be



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anywhere in the country – can also be connected into the system by a network of computer links, and by using their own computer terminal they are able to watch the sales figures of their merchandise clocking up.

Computers analyse product sales by the hour, or offer summaries based on the number of units sold this week if wanted. They can also forecast whether it will be possible to meet any forthcoming demand for a product.

STOCK

Buyers whose responsibility it is to source and replenish stocks, can obtain virtually instantaneous information on sales so that their suppliers can be instructed to deliver replacement stocks. Slow-moving lines are also highlighted to be considered for deletion. Indeed, a retailer's entire operation centres around the movement of stocks. Over-stocking means that cash is tied up needlessly in stocks when it could be better utilised in other aspects of their business – so the raw data provided by those barcodes is actually critical for efficient stock control and market planning.

If the products concerned are of a less manageable nature, such as certain lines found in d.i.y. superstores then it's obviously impossible to pass the cumbersome goods over the scanner window – unless you relish heaving lengths of timber onto the counter! As you may have experienced yourselves at a check-out, in these cases the operator is armed with an electronic "gun" which is "fired" at the barcode label attached to the goods.

A precision light-beam scans over the barcode and then the decoded data is transmitted instantly into the store's computer system, which responds with the product description and value. The useful range of the "gun" can be many centimetres, which enables the cashier to "shoot" the barcode label affixed to the goods in the customer's shopping trolley.

An alternative system is for the checkout operator to hold a book with the barcodes of various bulky items (e.g. bags of potting compost) all printed inside. Whenever a customer buys one of these items, the operator simply scans the appropriate barcode from the book.

SEEING THE LIGHT

Have you ever wondered what the barcode actually means? There are some two dozen or more different types of barcode in common use, but the system which the consumer will mostly see in retail stores is known as the "EAN" system, short for European Article Number. This is generally based on a thirteen-digit code number – the EAN13 format – which is unique to that product. A different system is used for magazine sales.



Fig. 3. A typical EAN13 barcode.

A typical EAN13 barcode is shown in Fig. 3. The thirteen digits of the code can be divided into four groups. Firstly, two digits placed at the beginning of the code indicate the country of origin of the barcode. You will see "50" at the start of all codes originating in the United Kingdom.

Each manufacturer who subscribes to the barcode number system is allocated a unique five-digit code number by the Article Number Association. This number represents their company and cannot be used by any other. That of my own firm is 13863. The code belonging to Halfords Ltd., for example, is 15025. These five numerals follow the country of origin code. So the first half of the code number may be 5013863 or 5015025, for instance.

The supplier or manufacturer is free to allocate another five digit code entirely of their own choice to the product. The code will take into account the packaging of the product: the maker of a particular chocolate bar may offer several sizes, perhaps packed in units of half a dozen bars, as well as individually. Whilst one barcode may appear on the individual bar's wrapper, a different one will appear on the outer wrapping of the six-pack.

CHECK DIGIT

We still have one digit to account for in our EAN13 system – the numeral right at the end. This check digit is included as a built-in test which tells the barcode scanner that it has read the rest of the code correctly. A simple algorithm is utilised in order to validate that the barcode is sound. The panel on the right shows how the algorithm works for the EAN13 system. By ignoring the last (check) digit of the EAN13 code, work backwards through the code number, right to left, adding up *every* other numeral in the code. Multiply the total by *three*.

Go back to the end of the code, and work through the barcode again, adding to your total each digit which you omitted in your initial calculation, and make a grand total. Now simply determine the *next whole multiple of ten* up from your grand total, and calculate the *difference* between your grand total and this multiple of ten.

Your result will be a single numeral which is the check digit. This is appended at the end of the twelve digits to make thirteen in all. It is an in-built check which confirms that the scanner has read the barcode correctly. Have a look at any thirteen-digit barcode and work out the final check digit for yourself.

The EAN13 digital sequence which results is encoded into a precise pattern of stripes, which are reproduced on the packaging to fine tolerances. The width of the bars is precisely controlled, but also has to take into account the fact that printing ink might spread or "bleed" during the printing process – so barcodes might be deliberately printed very slightly narrower than required (using a bar width reduction factor, measured in microns), because they will expand somewhat when the wet ink spreads.



late the final check digit. It can be proved as follows. Starting at the end of the product code (before the "check digit"), go back from right to left and add up every other numeral.

$$+1+0+6+3+0=12$$

Multiply your answer by 3:

2 -

$$3 \times 12 = 36$$

Now go back and add up the numbers which you missed first time:

$$0+3+3+8+1+5=20$$

Add your result to the answer from the first calculation:

20 + 36 = 56

Find the next whole multiple of 10 up from this answer (=60).

The difference between your final answer and the multiple is the check digit:

60-56=4.

COME ON DOWN...

It will not have gone unnoticed that no *price* has so far been mentioned in the barcode itself. In fact, the price of the goods is not denoted by the barcode as such. Whilst a product designer will allow for the EAN13 code to be shown on the packaging, it is left to the *retailer* to allocate a price on' their computer's database. The retailer can change the retail prices (e.g. reduce them in a sale) by altering the computer records of that product code number. Otherwise the barcode contains no price information at all – it's simply a product identifier.

Sometimes you might observe a shorter code, one which contains not thirteen but just eight digits – this is not surprisingly called the EAN8 format. You might see it on small items which are not physically big enough to carry a large EAN13 barcode – a packet of "Polo" mints, for instance. Because the eight digit system cannot accommodate a great number of individual manufacturers and products (the system would soon be filled to capacity), the EAN8 format is only employed by a select few manufacturers who have been lucky enough to be allocated an abbreviated code number.

Barcodes often appear on the edges of shelves of supermarkets, too. A portable computer scanner will be used by a store assistant to read each product designation. Then the assistant will count the stock which is adjacent to the shelf barcode, and type this figure into the portable computer.

The machine may transmit the barcode and stock check figures back to the office computer by remote control, which will then update its "actual" stock level information for that product code. In other systems, the terminal may be plugged into a computer inlet once the stock count has been completed by the operator, thereby transferring the entire stock check into the central computer.

PROBLEMS

Of course, no system is entirely infallible. It is still possible for sophisticated barcode scanners to become a bit confused. Whilst the barcodes themselves are printed to very exact standards – the width of each line and the gap between them is absolutely critical, and has to take the weaknesses of the printing process into account – scanners may sometimes have difficulty reading a shiny object such as a bag of frozen peas.

Alternatively, the barcode may have been damaged or soiled in the store. In these instances, the operator is then able to manually enter the digital product code as printed underneath the barcode: the computer can then "look up" the price and description as normal. That's why there is always a "human recognisable" numerical code underneath the otherwise incomprehensible barcode.

BAH! CODES

Whilst the technology of barcodes has been highly refined over the past few years, problems with the machinery itself, though thankfully rare, can give rise to nightmarish headaches for system users. It was reported that shortly after a new supermarket opened its doors for the first time, the computer system unfortunately "crashed". The system was soon crippled through a fault or "bug" in the scanning equipment.

Because the store used a barcode scanning system, their check-out cashiers

Magazine Barcoding System Magazines are regularly barcoded in order to speed the pricing-up process at check-outs. A barcode on the front cover is scanned by a hand-held pen-like "wand" which reads the barcode into the decoding system. This generates a digital code which can be referred to a computer "look-up" table as usual.

Magazines and serial journals use the ISSN system – the International Standard Serial Numbering system, where every barcode number begins with the digits 977. Then seven digits are used to indicate the ISSN reference number for that journal; two spare digits follow, which can contain the price if needed and a check digit appears at the end. Finally two extra digits can indicate the issue number i.e. 08 for the August issue.

In addition to helping at the checkout the barcode on a magazine is also used for sale or return (S.O.R.) procesing. Unsold magazines being returned to the wholesaler are scanned and the "sales" level for each issue stored on the wholesalers' and distributor's computer systems are corrected and credits given by the distributor.



were unable to ascertain the prices of the customers' purchases: of course, the goods did not bear any price labels! Instead, paralysed by the loss of their computer, the staff were instructed to ask the customers to make a reasonable offer for each item – perhaps the management considered it was worth making a loss if necessary rather than alienate its new patrons!

Apart from retailing, barcodes also crop up in other applications. Recently, the writer was invited to an exhibition of leisure products, and was sent a lapel badge beforehand. This badge, apart from bearing my name, also included a barcode. At the entrance to the exhibition, the barcode on the badge was quickly scanned by a receptionist holding a "wand" scanner like a large pen. This informed the organiser's central computer that I had attended the exhibition.

From the barcode the computer could also determine my company name and address, not to mention my position, and the organisers will then write to me next year with another invitation to the show knowing that I visited their last one.

Additionally, when I called on to an exhibitor's stand, each exhibitor, instead of accepting a business card would scan the barcode on the badge: after the show, they could then decode the information and follow up with a visit or a written quotation to my business address.

SELF PARKING

A Japanese car manufacturer is reported to have designed and built a prototype car which will automatically park itself in the garage: it looks for barcodes fixed to various locations on the garage walls, in order that it can find its "bearings" and correct its position accordingly!

It was recently announced that pension and allowance books will include a barcode, unique to the book-holder, in an attempt to foil any attempts to fraudulently claim an allowance by using another person's book. Each Post Office will probably become equipped with a barcode wand unit so that they can scan the identifying barcode. A false claim will become immediately apparent if the allowance book has been reported as stolen, and a "stop" can be applied instantly by the authorities.

In industry as well as warehousing, barcode information systems are being increasingly used to help central computers to monitor the throughput of stocks or products. By automatically reading a special-format barcode which has been allocated to, say, a motor car during assembly, the progress of the product can be controlled as each unit is logged into and out of various processes.

AUTOMATIC

One large retailer I visited holds some £30 million of stocks stored in Europe's largest automatic warehouse which is integrated into a highly efficient stock control system, but it still depends on barcode systems to monitor the throughput of stock. Merchandise which is destined for a particular store is loaded into plastic miniskips, each box having a unique barcode label on the side. Infra-red detectors read the barcode as the box travels automatically through the warehousing system on an assortment of conveyors and robotic transporters.

Gradually the box is filled with a variety of products all destined for one particular store. An impressive, fully automatic sorting process then eventually diverts these boxes to a suitable loading bay, ready for despatch by lorry. The automated warehouse system sorts the boxes into reverse order: The system is aware of the route which the vehicle will take when it travels around the country, so that the first box to be loaded onto the vehicle will always be the last one off!

A new hand-held l.c.d. game – the Barcode Battler (reviewed last month) – actually utilises barcodes in order to allocate a number of points to a player. Barcodes are "swiped" into the games unit like a credit card at a checkout. The barcode is then transformed into points and the game commences. This has led to a craze in Japan where merchandise is bought simply to snip off and scan the barcode into the machine!

Next time you visit a supermarket, spare a thought for those often overlooked, incongruous barcodes: the electronic industry's little miracles which hold the key to the entire merchandising system. \Box

More Information

Any party considering becoming involved with the barcoding of merchandise for the first time might find the following useful:

The Article Numbering Association controls the issue of company codes, and every supplier using a barcode on packages needs to subscribe with this body.

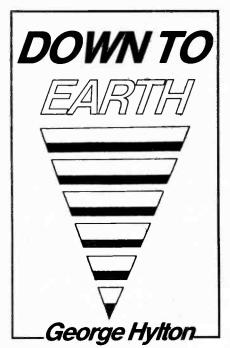
Article Numbering Association (UK) Ltd., 11 Kingsway, London WC2B 6AR Tel. 071 240 3056, Fax. 071 240 8149

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CONDUCTANCE

E VERYBODY is familiar with the idea of resistance in an electrical circuit. We feel happy with "resistance" in an electrical circuit because we are so accustomed to hearing it in everyday, non-technical situations.

This is not true of the technical relatives of resistance: impedance, reactance, conductance, admittance. They bear some resemblance to familiar words but have no obvious meanings. Let's have a look at one of them: *Conductance*.

EARLY DAYS

At one time, there were no volts, ohms, and amperes, only something vague, called "electricity". Electrical researchers were struggling to discover whether the sort of electricity you made by rubbing amber with a dry cloth was the same as the sort that you got from one of Volta's new-fangled piles. In present-day words, they were trying to find out whether static (frictional) electricity and current electricity were two aspects of the same thing or two different things.

Georg Simon Ohm was a pioneer researcher in this field. His now classic paper was called (in German) "The Galvanic Chain". We would now say "The Electric Circuit".

CONDUCTANCE

Ohm was trying to get answers to the question: do some things conduct electrical currents better than others? Note that it was the ability to conduct that Ohm set out to research, not the ability to resist conduction. Resistance hadn't been thought of!

He showed, to his own satisfaction (though for a long time to nobody else's) that "ability to conduct" is a measurable quantity, expressible in precise terms (known to us as Ohm's Law). If the ability of a circuit to conduct is called its conductance, what determines the amount of current that flows?

OHM'S LAW AS HE SAW IT

Ohm saw the current as something governed by the number of cells in his battery and the circuit's ability to conduct. A battery provides an electromotive force (E) and the ability to conduct can be called the conductance.

The intensity (/) of the current is then given by the product of these two quantities. Current = electromotive force times conductance.

That may not look like the Ohm's Law you know and love. That says: Current = Voltage divided by Resistance: I = E/R (or V/R if you call the electromotive force the voltage.

If two things are each equal to a third thing then they must also be equal to one another. In the present case we have two things each equal to the current. It follows that one of them (conductance) must be equal to the other. But what *is* the other? Not R, but 1 divided by R. Conductance is just a 1/R.

For convenience, to avoid having to write 1/R every time, electrical engineers usually give it an alternative symbol, *G*. So I = E.G. Conductance is a sort of mirror image of resistance.

When Ohm started his work nobody really knew whether one conductor was different from another in any electrically important way. The first electrical circuits were just batteries or cells whose terminals were connected together via a length of wire. They were virtually short-circuits.

What limited the current wasn't so much the resistance of the wire as the internal resistance of the battery, which was usually very much larger. It was only as a result of Ohm's painstaking research that the idea of resistance was born.

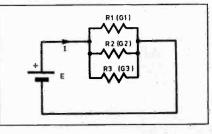


Fig. 1. Simple electrical circuit.

PARALLEL CONNECTION

A circuit like Fig. 1 is easier to understand if the elements are thought of as conductances rather than resistances. Common sense tells you that the more parallel paths there are the more current flows.

In other words the conductances add. The total conductance is G1+G2+G3. Writing 1/R for G, the total conductance is 1/R1+1/R2+1/R3.

Before the computer age these 1/Rs were mathematically a bit awkward. Pocket calculators have now made things easy, and you can work out a conductance by entering the resistance then using the reciprocal (1/X) key.

What is the conductance of a circuit with 29 ohms, 97 ohms and 133 ohms all in parallel? My calculator says 0.00523...

Inverting this (1/X) gives 19.11, which is the equivalent resistance in ohms. (Since conductance is the inverse of resistance, then by inverting again you get back to resistance.)

UNITS

Conductance, as we've seen, is a measure of a circuit's ability to pass current when a voltage is applied. The more volts, the more amps. This leads straight to one unit of conductance, amperes-per-volt. A resistance of 1 ohm, when connected to 2 volts, allows 2A to flow. The corresponding conductance is one amperes-per-volt.

This is a bit clumsy. Back in the Victorian age, William Thompson (Lord Kelvin) made a rather whimsical suggestion. By then, the unit of resistance had been named: the Qhm. Since conductance is the inverse of resistance, said Kelvin, let's just invert the word Ohm and call the unit of conductance the mho. The mho = one ampere-per-volt.

The matter might have rested there. But scientists decided that they must honour another electrical pioneer, Siemens (pronounced ZEEMENS). So they changed

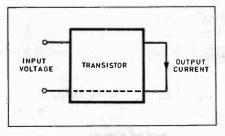


Fig. 2. "Black Box" transistor.

the mho to the Siemens. The result is that, depending mainly on the age of the books you use, you'll find conductance expressed in three ways: amperes-per-volt, mhos, or Siemens. Small wonder that people find it confusing!

MUTUAL CONDUCTANCE

You are most likely to encounter conductance in transistor data. A transistor of any kind can be regarded as a "black box" with input and output terminals (Fig. 2). When a voltage is applied to the input terminals a corresponding amount of current flows (from the battery) through the output terminals.

Voltage causing current flow is what conductance is all about. So conductance is happening in the transistor.

Applying one volt to the input of a silicon power transistor might cause a collector current of 3A to flow. So the conductance here is three amperes-per-volt, or 3mhos, or, nowadays, three Siemens.

However, there's something odd about this conductance. The 3A does not come directly from the 1V at the input. All the 1V does is drive a relatively small base current. This base current then makes the collector current flow.

Processes which link input and output in this way are often labelled with the word "transfer conductance". In the example just given the transfer conductance is 3S.)

You may find that the transistor data adds the word "forward" and quote the "forward transfer conductance" as 3S. "Forward" just means that we are talking about the normal use of the transistor with an input to the base (or gate, if it's a f.e.t.) and output at the collector (or drain). Transfer conductance is often telescoped to make "transconductance".

Some transistor data call the forward transfer conductance the mutual conductance. This is an old term used to describe the same effect in radio valves.

A Siemens is rather a large conductance, and for convenience it can be divided in the usual way, into a thousand milliSiemens (mS) or a million microSiemens (μ S). In the case of *pnp* or *npn* transistors (but not f.e.t.s) there is a useful way of estimating the transconductance.

If the emitter current is 1mA the transconductance is about 40mS. For other emitter currents the transconductance rises or falls in proportion. So for an emitter current of $100\mu A$ (0.1mA) the transconductance is about 4mS.

The accuracy of the estimate diminishes as the emitter current becomes much larger or smaller than its typical value for the transistor in question. For power transistors, the estimated value is often higher than the real value. The transconductance is more or less independent of the current amplification factor.

This quick estimate enables you to work out the voltage gain of a one-transistor amplifier. The gain is: transconductance in mS times load in kilohms. So an *npn* transistor with a load of 2k and an emitter current of 1mA has a voltage gain of 80.

In multistage amplifiers the d.c. collector load is shunted by the input impedance of the next stage. This reduces the voltage gain.





THIS is a piece of advice which has been given in *Everyday With Practical Electronics* many times before, but is one which seems to be more important now than in the past. "Make sure that you can obtain *all* the components for a project before you start buying any of the parts."

Be especially careful if you are building an old design. If a project requires anything that is at all out of the ordinary, it is not a bad idea to obtain the unusual components first, and to progress to the mundane components only when the awkward ones have been obtained.

Difficulties in obtaining components seem to have increased significantly over the last year or so, with a number of components having suddenly disappeared from the market. I have certainly noticed a recent increase in the number of readers letters concerning problems with obtaining components. Some of the problem components are ones which went off the market quite recently, while others have not been available for years but still bring in a steady flow of letters.

Has Beens

I find it surprising that components which have been available for as long as I can remember can suddenly disappear from the market. It seems to be a fact of modern commercial life that unless something can be manufactured and sold in vast quantities, it is often not worthwhile producing it.

This has led to the demise of several components that seemed to be selling quite well. Unfortunately, the specialist nature of many of these "ex" components means that there is no possibility of using substitute components. In a few cases there are suitable alternatives available, for the time being anyway.

The number of readers' letters received concerning problems with defunct components suggest that it would be well worthwhile mentioning some of the components which are now causing problems. Hopefully, this will deter some constructors from starting projects that can never be finished, and will help others to find alternative components where suitable substitutes are still available.

Springline Units

As far as I am aware, the last of the springline units were sold last year and they are no longer in production. It seems unlikely that they will become available again unless some surplus units appear.

A reverberation unit based on a c.c.d. device would seem to be the only alternative. This requires a completely different type of circuit though (a c.c.d. reverberation project appeared in the June 1992 issue of *Everyday Electronics*).

Dual Gate MOSFETS

Popular dual gate MOSFETs of the past such as the MEM616, 40673, 3N140, and 3N141 now seem to be obsolete. Some of the more recent surface-mount types could be used in place of "wired" devices without too much difficulty, but many of these now also seem to be unobtainable. Dual gate MOSFETs appear on the surplus market from time to time, but are often at quite high prices.

The MFE201 from *Cirkit* is new stock, quite cheap, and is pin for pin compatible with the 40673, etc. Electrically it is not an exact equivalent to any of the devices mentioned above, but in practice it seems to work well as a substitute for any of them. Apart from surplus devices, it is quite possibly the only dual gate MOSFET currently available to amateur users.

Bucket Brigades

The Mullard TDA1022 and TDA1097 c.c.d. delay line ("bucket brigade") chips used to be very popular, but despite this the manufacture of both chips was ceased. Readers sometimes enquire about the possibility of using the Hitachi MN30** series of chips in place of the TDA10** devices.

Due to physical and electrical differences there no easy way of achieving such a substitution. However, surplus Mullard "bucket brigade" devices are often available from *Cricklewood Electronics*, who are able to supply many semiconductor devices which have been out of production for some time.

A number of other integrated circuits have gone out of production in recent years. Due to their specialist nature there is little chance of finding a proper substitute for any of these components. Searching for a surplus device is the only course of action which is likely to end in success.

BC650

The BC650 is a very high gain, very low noise, *npn* silicon transistor which was very popular about ten to fifteen years ago. Unfortunately, it has been out of production for some time now, and it is unlikely that any BC650s are still available.

As far as I can gather, no true equivalent to this device is currently available. However, using a BC549 seems to give only a marginal reduction in performance in most applications.

A number of transistors that were quite popular in the 1970s now seem to be unobtainable or in short supply. It is worth bearing in mind that for most low power, low frequency applications, a BC549 (*npn*) or BC559 (*pnp*) transistor will provide satisfactory results. More careful substitution is needed if high frequencies, high currents, or high voltages are likely to be involved.

Denco Coils

The famous Denco Coils have been out of production and unavailable for many years. It is possible to use Toko readymade coils or home wound coils in place of many Denco coils, and this was covered in a previous *Actually Doing It* article.

However, it is now probably best to seek out modern designs which use readily available components, rather than try to resurrect old designs based on Denco components.

TIL209/TIL220

When I.e.d.s first appeared they were mainly sold under type numbers, like most other semiconductors. These days they are normally sold as I.e.d.s of certain sizes, shapes, colours, and efficiencies.

The TIL209 and TIL220 are "bog standard" red I.e.d.s having diameters of 3mm and 5mm respectively. Any modern low cost red I.e.d.s of the correct diameters should be suitable replacements for these devices.

In Stock

I think that it is worth pointing out, yet again, that many readers still complain of difficulties in obtaining components that are readily available. In some cases the problem is simply due to a minor misunderstanding which is easily cleared up. In other cases a source of supply is provided in either the article or the *Shop Talk* feature which is included in every issue of this magazine. Studying the article concerned and the relevant section of the *Shop Talk* feature could save a lot of wasted time and effort!

Many component buying problems seem to be the result of readers not being equipped with a few of the larger component catalogues. With the vast range of components that are currently available, a few large mail order catalogues have to be regarded as an essential part of the hobby. Many of these catalogues are worth the money for the general data and information that they contain. – Not forgetting the ones given *Free* with this magazine from time to time – *Ed*.

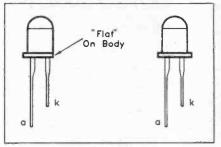


Fig. 1. The polarity of l.e.d.s is normally indicated by a "flat" on the body, and (or) a shorter cathode ("k") lead.

LED Polarity

Determining the correct polarity of l.e.d.s is a subject which crops up from time to time. In the past, most l.e.d.s have had the polarity indicated by a "flat" on the body of the component next to the cathode ("k") terminal. Additionally, the cathode leadout wire is usually shorter than the anode leadout.

Wiring diagrams in magazines and books often indicate the polarity of l.e.d.s by showing the flat on the cathode side of the body. However, many real l.e.d.s now seem to lack the flat on the body, but many still have the shorter cathode leadout wire (Fig.1).

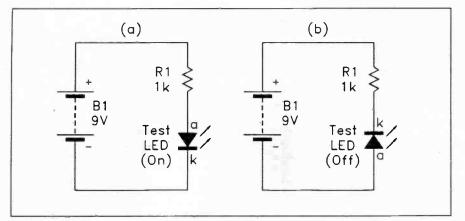


Fig. 2. A simple method of testing l.e.d. polarity.

In fact all the l.e.d.s I have bought in the last year or two have lacked the flat. Looking on the bright side, one advantage is that these l.e.d.s fit into some types of panel holder much more reliably than those which have the flat.

Rather unhelpfully, some l.e.d.s seem to lack both the flat and the shorter cathode leadout wire! It is only fair to point out that a few l.e.d.s having the anode lead as the shorter one have been produced. Consequently, it is not 100 per cent safe to assume that the shorter lead is the cathode terminal.



MINI DILEMMA

Dear Ed.,

Examining notes on your *Micro Lab* project, I have noticed a frequent refference to another project i.e. *Mini Lab* described in Feb. '92 issue, however there is no circuit diagram hence it's difficult to understand its usefullness in connection with *Micro Lab* (the 5V/1A supply can be provided by any other means).

However the 34 way ribbon cable used between both units makes me think that there are some other uses of *Mini Lab* not so obvious without the above mentioned circuit diagram.

Could you perhaps kindly advise.

E. du Puget Oadby

Thank you for your letter concerning the Teach-In Mini and Micro Labs. We hope we can clarify the position for you.

The Teach-In tutorial series lasts for twelve months and is designed to support those undertaking GCSE and GCE "A" Level Electronics studies. Additionally we have tried to frame the material such that it would have broad appeal to regular readers too. In order to demonstrate the basic principles of micro-electronics, we designed a special tutorial unit – the Teach-In Mini Lab.

This is a large printed circuit board roughly the size of a page of EPE Magazine. It is modular in nature, where one section of the p.c.b. is assembled every month to form a handy piece of test equipment relevent to that month's material.

The immensely popular Mini Lab has without a doubt captured the imagination of the beginner in electronics and is the ideal learning centre, to be used in conjunction with the experiments carefully The only certain way of determining the polarity of a l.e.d. is to connect it into a simple test circuit. I generally use the circuit of Fig.2 which can be quickly breadboarded, or put together with the aid of a couple of crocodile clip leads. The l.e.d. lights up if it has the polarity shown in Fig.2(a), or remains off if it has the polarity shown in Fig.2(b).

An *analogue* multimeter set to a low ohms range (about 500Ω mid-scale) can also be used to check the polarity of l.e.d.s. When the l.e.d. lights up, its cathode is connected to the positive test prod. When

detailed in our Teach-In series. A photo of the Mini Lab appears in the Magenta advertisement, who incidentally retail a full kit of parts for both Labs.

Because the Mini Lab consists of many sections, each connected to a versatile power supply, it's not possible to reproduce the individual circuit of the Mini Lab as such, but it should still be possible to obtain the relevant back-numbers starting with Part 1 back in November 1992.

(Unfortunately we have sold out of Dec. '92, Jan and Feb '93 issues but we can provide photostats of the relevent parts for the same price as back numbers – see page 565 for ordering details. Ed.).

After completion, the Mini Lab forms a great test bed for demonstrating and testing electronic circuits, especially as it incorporates a breadboard, simple voltmeter, fixed and variable power supply, signal generator, logic probe, audio amplifier and radio tuner amongst other useful items – everything you need for project development!

In order to demonstrate the function of the microprocessor, a separate Micro Lab has been designed for more advanced readers. We soon saw though that the Micro Lab would not only demonstrate m.p.u.'s but could also act as a stand-alone unit which could be programmed to function as a dedicated m.p.u. controller.

Hence, the Micro Lab can be powered either from a Mini (ab (which incorporates a 5V and 12V supply) where the supply voltage is fed through the ribbon cable; alternatively, experienced readers who are seeking a dedicated stand-alone flexible microprocessor controller for a particular task, would not need the Mini Lab (which includes many features that may not be relevant in many microprocessor applications) and so we included a separate 5V terminal also.

However, the Mini and Micro lab systems can combine together via the ribbon cable to form an extremely versatile microprocessor demonstration and applications unit. If you are interested in it fails to switch on, the cathode is connected to the negative test prod.

A digital multimeter can also be used, but if it has high and low test voltage settings it must be set to the "low" position. Modern digital multimeters seem to use low test currents, so even using the lowest resistance range the l.e.d. might not light up very brightly. Note that with a digital multimeter the cathode is connected to the negative test prod when the l.e.d. lights up, and to the positive test prod when the l.e.d. fails to switch on.

I give all newly received I.e.d.s a quick check of this type to ensure that they are working properly, and that the polarity is as I think it is. Where the leadouts are the same length it is a good idea to determine the polarity of the I.e.d., and then trim the cathode leadout slightly so that the polarity is obvious when it is time to connect the device into a circuit.

If a l.e.d. should be connected into circuit the wrong way round, it is highly unlikely that it will sustain any damage. Consequently, if a project has only one or two l.e.d.s, the correct method of connection can, if necessary, be found by trial and error. With projects that contain numerous l.e.d.s this is not a practical proposition, and it is then essential to determine the polarity of the l.e.d.s before starting to wire them into circuit.

actually learning about and demonstrating microprocessors then the idea is to use the Micro Lab in conjunction with the Mini Lab.

The reason you might need both Labs is that the Mini Lab includes several input/output peripheral devices (e.g. the breadboard, potentiometers, switches etc.) as well as useful modules like the Audio Amplifier, and these will be hooked up via the ribbon cable to the Micro lab for use in certain demonstrations. Most of the microprocessor input/output information is accessible via the 34 way ribbon cable, apart from seperate D/A and A/D signals which are carried through the 4-way screw terminal block. The I.c.d. display shows alphanumeric data too.

To summarise, our Teach-In series can help you as follows:--

• If you are a beginner in electronics (apologies if you aren't), consider the Mini Lab only, in order to gain a basic foundation. The Micro Lab is not really suitable for assembly by beginners.

• If you have a little experience of microprocessors and would like to see how they operate, you will need both the Mini and Micro Labs if you are to benefit from the series fully.

• If you are already familiar with microprocessors (especially the 6502) and would like a versatile 8-bit m.p.u. controller, build the Micro Lab on its own, in which case you will need a separate 5V power supply.

Please note, the Micro Lab is a finelydetailed p.c.b. and it is necessary to be able to use a fine-tipped soldering iron skilfully. Other than that, expect the Micro Lab to work first time if it is soldered neatly – our own models did!

We hope this will help and encourage you to join all our regular readers around the world who enjoy following Teach-In. Best wishes for your success.

Alan Winstanley and Keith Dye - Teach-In '93 Team Micro Lab Software Author: Geoff MacDonald

Home Base

Jottings of an electronics hobbyist – Terry Pinnell

To Catch a Mouse

"We've definitely got mice," said my friend Annie. "They're getting into the pantry somehow. But even if I could bring myself to do it, there's no way the kids will let me use poison or a mousetrap."

It was a Sunday lunchtime and we were sitting at a table in the sun outside the local pub.

"Maybe I could make you an electronic mousetrap," I said, looking up from my newspaper, always alert for a chance to slip into my Eccentric Inventor role.

Oblivious to the tolerant smiles exchanged between the others around the table, my mind was already on the grand design as I reached for my pint. Making a device which would allow overnight capture and morning release would not really be all that complex, but I thought I might stretch it out to a few days if I worked at it.

Although it went against the grain, I did even fleetingly consider an idea that wouldn't actually need any electronics. Suitably lured onto the top of a box, the unsuspecting mouse would step onto a piece of tissue paper or cloth covering a hole, fall through and be unable to climb out. (I probably got the idea as a kid watching Tarzan movies, in which they always seemed to be digging holes in the jungle – to trap man-eating tigers or Tarzan, I don't exactly remember). However, I decided it was most unlikely that a mouse with the minimal survival skills that had got it this far would be stupid enough to fall for (or fall through) such an obvious ambush.

No, it was clear that what I would need was an open box, into which I would lure the unwanted rodent, then trap it. But how to persuade it to venture inside?

Attraction

Focussing now on an all-electronic design, I first consulted the Sound Effects section of my ageing Circuit References Binder, but this yielded no Rodent Attractors or such-like. The closest was a Fish Caller in a late '70s magazine – a multivibrator in a watertight jar – but I dismissed that as unpromising for my more discriminating mammal target. Anyway, why look further than the traditional piece of cheese?

Needless to say my mousetrap had to be electrically operated – a merely mechanical contrivance was not an option for an electronics hobbyist. Battery operation was another obvious criterion; mains sockets aren't usually sited with mouse catching in mind, and the idea of an extension reel was abandoned quickly.

It was clear that I would need a door of some sort which would close when the mouse was about to start its one course meal, remaining shut until its merciful release by A Kind Human some time later, presumably the following morning. So the first interesting design questions were: what sort of door and how to close it?

A vertical freely-sliding type was the obvious choice, given that it had to close quickly. Our small guest was unlikely to linger over the cheddar, watching with interest as a hinged type slowly closed in conventional manner. I decided to use a piece of thin hardboard suspended at the sides in grooves, so that when suitably released it would fall quickly downwards.

Sting in the Tail

This prompted some rough calculations as to how long the box should be. I did not want news of successful capture to be marred by hearing that the mouse's nether extremity had been amputated – a sting in the tail as it were. Unsure as to the upper size bounds of the local mouse populace, I was inclined to be generous. After all, I could limit its width to discourage larger nocturnal prowlers like cats or the occasional hedgehog. The box eventually built was about 35cm long.

The next question was how to contrive the door's release. I decided in favour of a small strip of wood glued to its inner side and resting on a sliding rod of some kind. It would have to allow for the trapdoor to be raised again of course, so this 'ledge' was bevelled as shown in Fig. 1.

For withdrawing the rod so that the trapdoor could fall, a solenoid was my first thought. But I knew that the only ones I had were specified as 24V types. So for a while I experimented with a small d.c. motor, winding a length of nylon line around its spindle so that on powering the motor the line would pull in the rod. However, the snag was that the rod needed to be returned to its holding position.

A light spring was not strong enough to pull it back against the motor's resistance, and a stronger spring would have prevented the rod pulling inwards. Anyway, I decided that this approach was basically inelegant and fault-prone, so abandoned it.

I then experimented with one of my surplus solenoids and found that satisfactory operation could be obtained using an 18V supply made from two 9V batteries, despite the 24V specification. This depended on ensuring that friction between the solenoid's rod and the wooden catch was minimised by careful smoothing, and avoiding too heavy a trapdoor.

Triggering

Then I turned to the method of triggering the solenoid. To minimise current consumption it should only be held in long enough to fully release the trapdoor. This indicated that a monostable would probably be needed. Also, it should only be triggered once; further operation once the door was shut would be redundant and waste power. This indicated using a bistable as well as the monostable.

Triggering should occur when the mouse was fully inside, so that its tail would not be under the falling door. This meant positioning whatever switch was chosen well inside the box and ensuring reliable triggering.

Simplicity and reliability were the main reasons I selected a microswitch as the method of activation. I did briefly consider an interrupted light beam, but widely varying ambient light levels would add complexity and encourage spurious triggering.

Miceofarads

One other option was a proximity switch, which I considered because it could be made relatively unobtrusive. I had in mind something similar to the circuit I'd built into lamps around the house. When you touch or come very close to the antenna of one of these, a piece of metal or metal foil, it is triggered by capacitive attenuation to ground of an h.f. oscillator (around 100kHz). So a small piece of aluminium cooking foil covered with some cloth could possibly activate the solenoid driver circuit.

However, although I was familiar with the sensitivity of this circuit to human proximity, I was by no means sure how it would perform with a mouse. What is the capacitance of mice anyway? (In *Miceofarads of course.*)

Anyway, such a high-tech trigger circuit would have needed continuous current, limiting useful battery life. It was better to go for a circuit with negligible standby current.

So as seen in Fig.1 I arranged a microswitch with its lever in such a way that when the mouse was on the hinged base section it would close the contacts.

Perhaps next month I'll cover the simple electronic circuit and describe its exciting inauguration in my friend's pantry.

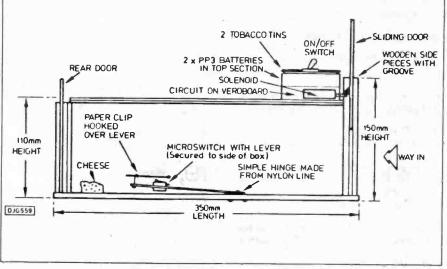


Fig. 1. Basic construction of the electronic mousetrap.

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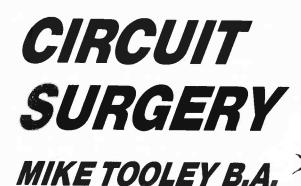
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Regular Clinic 📃



Welcome once again to Circuit Surgery, our regular clinic devoted to readers' problems. This month we return to the world of digital electronics and some interesting and useful circuits. We begin with a fairly common logic problem...

No coincidence?

D. Salmon from Benfleet, Essex, writes: "Logic gates like AND and NAND provide an output whenever both inputs are the same, either both 0 or both 1. I need a circuit which will operate an audible alarm when two input signals (both standard TTL levels) are different (i.e., 0 and 1 together or 1 and 0 together). There doesn't seem to be a standard logic gate that provides this function - can you help?"

Well, Mr Salmon, there is a standard TTL logic gate that provides this 'anti-

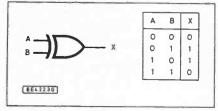


Fig. 1. Symbol and truth table for an exclusive -OR gate.

coincidence' funtion. You actually need an exclusive-OR gate, the truth table and ANSI logic symbol of which is shown in Fig. 1. A standard 74LS86 TTL chip provides four exclusive-OR gates and can be used, together with an external transistor along the lines shown in Fig. 2, to provide an audible warning whenever the two logic states differ. The audible transducer can be any piezoelectric sounder.

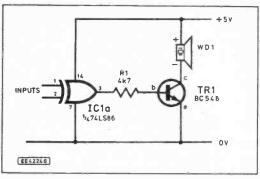


Fig. 2. Complete anti-coincidence alarm circuit.

Fig. 3 (right). Simple pulse generator with individually adjustable "mark" and "space" times.

Simple Pulse Generator

Regular reader, *Matthew Hume*, is looking for a simple variable duty-cycle pulse generator. Matthew writes:

"I have used the 555 timer as a source of square wave pulses but I wonder if there is any way in which I can vary the duty cycle over, say a 10:1 range?"

The 555 must surely be one of the most useful and versatile chips ever invented and, yes, there is a way to vary the duty-cycle using two varable resistors and a couple of diodes, as shown in Fig. 3. VR1 adjusts the "mark" time whilst VR2 sets the "space" time. It is, however, worth noting that the output pulse repetition frequency (p.r.f.) depends on the setting of *both* controls.

The value specified for C1 will produce a p.r.f. of between 1.4kHz (VR1 and VR2 both set to maximum) and 66kHz (VR1 and VR2 both set to minimum). C1 can be scaled up or down to provide different p.r.f.'s alternatively decade values could be selected by means of a simple rotary switch (e.g., 10 μ , 1 μ , 100n) to produce outputs over the entire range from 14Hz to 66kHz.

It is also worth pointing out that the circuit in Fig. 3 will produce a reasonable square wave output whenever both controls have identical settings (assuming that they both obey a similar law).

RAM equivalents

Several readers have asked me to provide data on semiconductor memories and,

COMPONENTS

Resistors R1, R2 1k (2 off) R3 10

All 0.25W ±5%

Potentiometers VR1, VR2 47k linear carbon (2 off)

Capacitors

apacitors		
C1	10n (see text)	
C2, C3	100n polyester (2 off)	
C4	10µ radial elect. 35V	

Semiconductors

D1, D2 1N4148 diode (2 off) IC1 555 timer

Miscellaneous

 SK1
 50 ohm panel mounting BNC socket

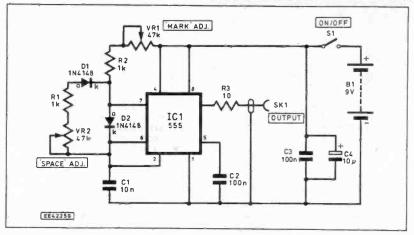
 S1
 S.P.S.T. or s.p.d.t. miniature

toggle switch

Battery holder (to accept 9V battery); knobs (two required); 8-pin low-profile d.i.l. socket; small metal or ABS enclosure (approx. 140mm x 80mm x 45mm); small piece of matrix board (approx. 75mm x 60mm); terminal pins (as required).

Approx cost guidance only





in particular, to recommend equivalent devices. It is important to note that RAM devices are supplied with particular access times. Devices with different access times should not be mixed. Furthermore, access times must be selected correctly to match the speed and timing characteristics of the equipment in which the RAM is to be used. In this respect, devices with very fast access times might be just as unsatisfactory as those with very slow access times.

The following data refers to two of the most popular d.i.l. encapsulated RAM types:

Type Near equivalents

64K × 1bit 9064/8264/4164/4264/4564 256K × 1bit 4256/6256/41256/37256/50256.

Play it again SAM!

Whilst on the subject of RAM, I was interested to see that Texas Instruments (TI) has just announced the first 4Mbit RAM device. This clever chip integrates a 256K x 16 bit dynamic RAM array and a 256 x 16 bit serial access memory (SAM). The chip is supplied in a 64-pin package and volume production should be well under way by the time you read this. Doubtless it won't be too long before we see 8, 16 and 32Mbit devices appearing!

What's in a name?

Andrew Pennington writes from West Sussex with a query concerning the type numbers of TTL logic devices.

"I have some integrated circuits marked '74ALS...', '74LS...', '74C...', etc. Can you explain the meaning of the letters and also the significance of the final leter 'N'?"

Thanks, Andrew for this useful question. To put the record straight, the major 74-TTL sub-families are as follows:

Device Sub-family

Type AC

AC Advanced CMOS technology (improved CMOS device)

- LS Low-power Schottky (the 'standard') ALS Advanced low-power Schottky (improved LS device)
- C CMOS (CMOS version of the TTL gate)



Experimental Electronic Pipe Descaler

All components required to build the *Experimental Electronic Pipe Descaler* are standard "off-the-shelf" items and should be available locally. Even the one per cent polystyrene capacitor should be generally available.

If you change the value of resistor R8 to run the unit at a higher output current you are advised to increase the wattage rating for safety. Talking of safety, you must be careful when handling the circuit board as mains voltages are present on the track pads linking up with the mains transformer primary winding leads.

the mains transformer primary winding leads. The printed circuit board for the pipe descaler is available from the *EPE PCB Service*, code 839.

Bike Odometer

A couple of items called for in the *Bike* Odometer will need particular attention when ordering parts for this project. The "two-inone" pressbutton Reset switch, for instance, was ordered from Electromail (*0536* 204555), code 332-830. Other non-locking types can be used provided the contact sets are identical.

The 0.56in. common cathode displays (type HDSP 5303), used in the prototype, were also ordered from the above company. Most of our regular advertisers will be able to offer suitable devices at very competitive prices.

Seven-segment, 0-5in. high displays can be used without board changes but the body length of these is slightly greater than those of the 0-56in. types, being 19mm instead of 17mm. The width and pin spacings are indentical and there is no reason why 0-5in. displays cannot be used in place of those specified in the parts list.

Some constructors may prefer to use lowcurrent, high-intensity displays; 0.5in. and 0.6in. types are available with compatible pin spacings, for example, the HDSP-E103 in 0.5in. or the HDSP-H103 on 0.56in. but these are of course more expensive than the ordinary types. Most advertisers list comparable types.

Roller-lever microswitches are fairly widely available, avoid devices which have a primitive hinge arrangement for the lever mechanism – check before purchasing. Likewise, the Terry clips will, of course, have to be chosen to suit the dimensions of the particular bike structure. The two small printed circuit boards (Divider-836 and Display 837) are available as a pair from the *EPE PCB Service*. See page 627.

Cupboard Guard

For some reason the "micropower" op.amp, type ICL7611, called for in the Cupboard Guard project does not appear in many catalogues. However, it does seem to be appearing on more retailers shelves and is certainly listed by Cricklewood (081 452 0161). The ORP12 light dependent resistor is another device which seldom appears on retailers lists, but they can usually offer it, or an equivalent, from their current stocks.

The piezoelectric warning device used in the prototype is the "High Power buzzer" from Maplin, code FK84F. Other types may certainly be used in this circuit, but bear in mind that the Darlington transistor collector current should not exceed 300mA. Also, sounders which require external drive circuitry are NOT suitable and this point should be checked when ordering.

Finally, if the "Guard" is used to signal that a young child has tampered with the medicine cabinet, it must not be used in isolation. All the usual "safety code" procedures must be followed at all times.

Amstrad PCW 8-Channel A/D Converter

The only listings we have come across for the BAT85 Schottky diode used in the Amstrad PCW 8-Channel A/D Converter is from Electromail, code 300-978. Any readers finding it difficult to source the AD7828 high speed 8-bit analogue-to-digital chip will find it is also listed by the above company under order code 635-482. This CMOS device is not cheap, around the £20 mark, so take special care when handling it.

When ordering the LF356N i.c., make sure you emphasise the letter N as this designates the d.i.l package version. Most of the "computer connectors" are standard times now and should be readily available.

The double-sided printed circuit board for the Amstrad A/D Converter is obtainable from the *EPE PCB Service*, code 838.

Tri-State Thermometer

The miniature bead thermistor used in the *Tri-State Thermometer* is one from the more popular temperature range types and should

HC High-speed CMOS technology

- HCT High-power CMOS (LS replacement)
- F Fast (high-speed TTL replacement) BCT BiCMOS technology (high-speed, low-power)
- LVT Low voltage technology (3.3V supply)

The letter 'N' simply denotes the dual-inline version of the device (i.e., the chip is supplied in a standard d.i.l. package). Actually, I have never understood why the 'N' is included in the package marking. After all, isn't the package style obvious?

Next month: We describe a versatile power amplifier which can be configured for singleended and bridge operation. In the meantime, if you have any comments or suggestions for inclusion in *Circuit Surgery*, please drop me a line at: Faculty of Technology, Brooklands College, Heath Road, Weybridge, Surrey, KT13 8TT. Please note that I cannot undertake to reply to individual queries from readers however I will do my best to answer all questions from readers through the medjum of this column.

be generally stocked. The tri-colour, common cathode l.e.d. now appears in most component suppliers catalogues and should also be readily available.

The LM393 dual comparator i.c. may be a little difficult to track down locally, but it is currently on the Maplin, Cricklewood, Greenweld and Cirkit lists.

The fibreglass insulation wadding will have to be purchased from your local DIY store and the auto connectors and wire will mean a trip to a local garage or motorspares shop.

Teach-In '93 (Micro Lab)

Those readers who have been following the *Teach-In '93* series and undertaking the "practical experiments" have now reached the micro-processor stage and will, no doubt, have been highly impressed by the quality of the back-up *Micro Lab* project described last month. Although the price tag may, at first, seem high, when compared against a commercial counterpart it stands up very favourably and is excellent value.

There is one small but worthwhile update to the design, the battery life of the SRAM supply can be substantially improved with the following modification: To place the SRAM in minimum power consumption mode the CS pin 20 must be within 0-2 volts of the battery voltage on pin 28. To ensure that this happens an additional pull up resistor of 15k is recommended to be fitted between pin 20 and pin 28 of IC3. This can be soldered onto the back of the printed circuit board, attaching the resistor directly to the pins. Place some insulation onto the resistor leads to ensure they do not touch any other pins of IC3.

any other pins of IC3. Two companies have agreed to produce complete kits for the Micro Lab and are selling these for f149.95 per kit; which in view of the complexity of the project seems a very reasonable price. The two companies are: Greenweld Electronic Components, 27D Park Road, Southampton, Hants SO1 3TB and Magenta Electronics, 135 hunter Street, Burton-on-Trent, Staffs DE14 2ST. A ready built, inspected and tested version, with full back-up service, is also being offered by Magenta for the sum of £179.95.

Both the specially-programmed EPROM and PAL device are produced solely by Dytronics and are supplied with the *Micro Lab* p.c.b., from the *EPE PCB Service*. The "Micro Lab User Manual" is also supplied with the board, and contains useful information.

PLEASE TAKE NOTE

Barcode Battler (July 1993)

In last month's review on the latest handheld game entitled Barcode Battler, we gave a Tomy "Customer Careline" phone number.

The Tomy Customer Careline number has changed, and is now **1** 0703 872 267.

Constructional Project

CUPBOARD GUARD

T.R. de VAUX-BALBIRNIE

Audible protection for cupboards, drawers and filing cabinets.

A LOUD tone a few seconds after a door has been opened is provided by the Cupboard Guard. One possible application for this is to signal that a child has opened a "prohibited" drawer or cupboard. Another use is to provide a warning when a filing cabinet, desk drawer, drinks cupboard, etc. has been opened without permission.

Since the unit is very small, self-contained and battery-operated, it may be moved from place to place as the need arises. In use, Cupboard Guard is simply placed inside the drawer or cupboard to be protected and switched on. Carrying cases and bags may also be monitored providing they are dark inside when closed.

The on-off switch may be key-operated to prevent an unauthorised person from switching off the warning but for lowsecurity everyday applications an ordinary switch may be used as in the prototype unit (see photograph).

OPERATING TIME

While switched on in standby mode, the current requirement is less than $10\mu A$ ($6\mu A$ approximately in the prototype unit). This may be regarded as negligible and the internal battery will provide many months of service providing the device is not called upon to sound very often.

The delay time (that is, the time elapsing between opening the door and the alarm sounding) is adjustable between limits of two and 20 seconds approximately and this will be set for best effect at the end of construction. This time will be a compromise between being sufficient for legitimate access yet short enough to provide a timely warning.

Important Note: If this circuit is used to signal that a child has opened a medicine cabinet or similar application, it must not be used in isolation. All other safety procedures must be followed and the Cupboard Guard used only as additional "last resort" protection. Also, for any such serious application, the loudness of the sounding device must be checked to ensure that it is sufficient.

The Guard operates by sensing *light levels*. While a door remains closed there will be little light inside the cupboard. When opened, the light level will rise and it is this change which operates the alarm. In cases where there is no such change – or only a small one – this circuit is unsuitable. Note that fairly dim room lighting is sufficient to trigger the device.



The complete circuit diagram for the Cupboard Guard is shown in Fig. 1.

Fig. 1. Full circuit diagram for the Cupboard Guard.

The principle component is the micropower op-amp, IC1. This has been specially chosen for its extremely low quiescent current requirement. This is important because the unit draws current all the time it is switched on and so imposes a drain on the battery.

The op-amp IC1 is used in comparator mode and operated without feedback. Thus, if the voltage applied to pin 3 (the non-inverting or + input) exceeds that at pin 2 (the inverting or - input), the output at pin 6 will be *high* (positive battery voltage). In other cases it will be *low* - that is, switched off.

The voltage applied to the non-inverting input pin 3 is set to a value which depends on the adjustment of preset potentiometer, VR1. This operates in conjunction with fixed resistor, R1, to provide a voltage lying between 0V and 4.5V approximately.

The voltage applied to the inverting input pin 2 is derived from the potential divider consisting of fixed resistor R2, in the upper section, in conjunction with the light dependent resistor (LDR), R3, and fixed resistor R4 in the lower one. With the light-dependent resistor dimly illuminated, its resistance is very high but when extra light shines on it, as when a cupboard or drawer is opened, its resistance becomes much lower.

The effect of this is that the voltage applied to IC1 pin 2 will be high with the door closed and low with it open. With preset VR1 suitably adjusted and with the cupboard door closed, the voltage at pin 2 will exceed that at pin 3 so the output will be off.

Under *light* conditions (cupboard open), the conditions are reversed and the voltage. at pin 3 now exceeds that at pin 2. The opamp switches on and the output goes high.

amp switches on and the output goes high. Current now flows into the base of Darlington transistor, TR1, through preset potentiometer, VR2 (connected as a variable resistor), fixed resistor R5, and diode, D1. The transistor is thus turned on and collector current flows through the audible warning device, WD1. The purpose of capacitor C1 and fixed resistor R6 will be explained presently.

SOUND-OFF

The specified audible warning device was loud enough to be heard throughout a medium-sized room. However, as stated previously, the sounder should be chosen according to the application. Using a more powerful one will probably necessitate using a larger box so this must be borne in mind when ordering components. A pulsetone may be found more effective in

attracting attention than a steady-tone type although this was not thought worthwhile in the prototype unit.

The buzzer should be of the piezoelectric type and may draw up to 300mA – this being the maximum collector current allowed for the specified Darlington transistor, TR1. This allows a good choice of devices including some very loud minisirens. Note that sounders requiring *external* drive circuitry are unsuitable so this point must be checked before ordering

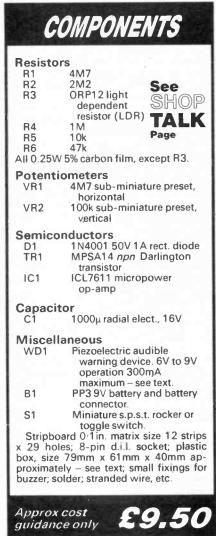
HOLD-OFF

To prevent the warning from sounding immediately the door is opened, capacitor Cl and associated components are called into play. When the circuit is triggered and ICl pin 6 is high, Cl charges through preset VR2 and resistor R5. The voltage across it then rises from zero at a rate determined by its value and the adjustment of VR2.

The base of the Darlington transistor TR1 requires 1.4V to turn on and diode D1 introduces a further voltage drop of 0.7V approximately (i.e. the forward voltage drop). The result is that TR1 is held off until C1 develops 2V approximately across its ends.

With the value of Cl specified and with VR2 set to minimum resistance, the holdoff time is two seconds approximately. At maximum resistance, it is around 20 seconds. Resistor, R6, provides a discharge path for Cl so that the circuit, having operated, will be ready for further use after a minute or two.

The light level at which the unit operates depends on the relative voltages at the



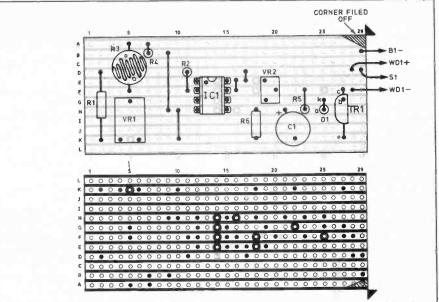


Fig. 2. Stripboard component layout and details of breaks required in the underside copper tracks.

two op-amp inputs and not their absolute values. This means, in practice, that the circuit will trigger at the same light intensity whatever the state of the battery. There will come a time, of course, where the battery is unable to operate the buzzer properly – the circuit should be tested frequently and the battery replaced when the buzzer is not as loud as it should be.

MINIMISING POWER

As mentioned previously, it is essential for the continuous current drain to be kept as low as practicable. It is therefore necessary to reduce the current flowing through the potential dividers consisting of R1 and VR1 on the one hand and R2, R3 and R4 on the other. This is done by keeping all resistor values in this section of the circuit high.

CONSTRUCTION

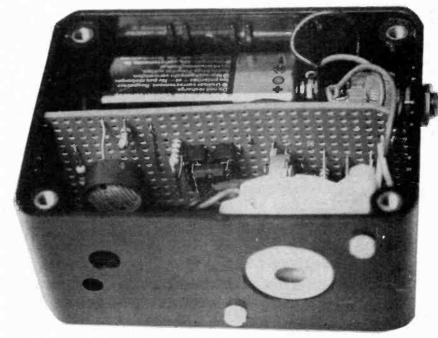
Construction of the Cupboard Guard is based on a circuit panel made from a piece of 0.1in. matrix stripboard, size 12 strips x 29 holes. Topside component layout and details of underside track breaks and interstrip links are shown in Fig. 2.

Begin by cutting the stripboard to size and filing it to fit the slots if using the specified box. Make all track breaks then solder the inter-track link wires in position as indicated. File off one corner, as shown, to allow clearance for connecting wires to pass over the top later.

After a careful check for errors – particularly that breaks in tracks are complete – the on-board components including IC1 socket may be soldered into position. The LDR should be soldered to provide approximately 10mm clearance from the circuit board.

Note that C1, D1 and WD1 are *polarized* components and, as such, must be connected into the circuit the correct way round. Do not insert IC1 into its socket at this stage.

Solder the following wires to the righthand edge of the circuit panel: the negative battery connector wire to strip B, the positive audible warning device wire (red wire)



and a short piece of stranded connecting wire to strip D and the negative audible warning device wire to strip F.

Complete construction of the circuit panel by adjusting preset VR1 fully anti-clockwise also VR2 fully clockwise – as viewed from IC1 position – to provide minimum hold-off time. This will simplify the setting-up procedure.

PREPARING THE BOX

Carefully measure the positions of the LDR and preset VR1 on the circuit panel. Drill holes of 6mm and 4mm diameter respectively in the side of the case to correspond with these positions (see photograph).

Slide the circuit panel into position temporarily to ensure that the holes are in the correct place. Check that VR1 may be adjusted using a small screwdriver through the 4mm hole. Remove the circuit panel again and drill holes for the audible warning device and on-off switch mounting.

Refering to Fig. 3, mount all remaining components and complete the interwiring. Insert IC1 into its socket observing the orientation. Slide the circuit panel into position again and check that the battery cannot cause short-circuits with the copper tracks when in position; if necessary make a cardboard or plastic shield. Secure the battery using an adhesive fixing pad.

Perform a rough check on the system by switching on – the buzzer should sound after a short delay – and rotating preset VR1 sliding contact slowly clockwise. A point should be reached when the sound stops.

If all is well, switch off and return VR1 fully anti-clockwise again. Pass the power supply wires through the space left by the filed-off corner of the circuit panel. Fit the lid before proceeding since this will affect the amount of light reaching the LDR. and hence the optimum setting for VR1.

SETTING-UP AND TESTING

Commence testing by switching on the unit – the audible warning device should sound in normal room lighting. Cover the LDR hole with a finger and adjust preset VR1 clockwise so that the buzzer stops sounding. Note that the response time for the LDR under these conditions is very slow and it will take several seconds for the buzzer to begin sounding or stop as the case may be.

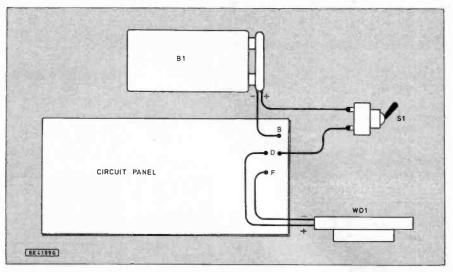
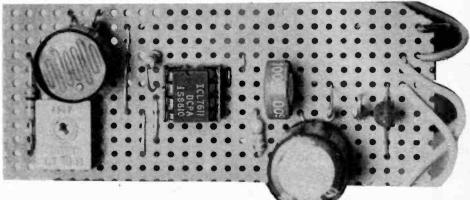


Fig. 3. Interwiring from circuit board to battery, warning switch and alarm buzzer.



Completed circuit board showing layout of components.

It is quite normal for the buzzer to take a few seconds to reach full volume. It may also change its pitch during this time. Correct adjustment to VR1 is therefore a rather tedious process but it is worth taking time over.

Remove the lid, slide out the circuit panel and adjust VR2 for the required holdoff time – anti-clockwise adjustment (as viewed from the i.c.) increases the timing. If it is adjusted to approximately mid-track position, it should provide a delay of 10 seconds approximately. Note that a minute or two must be allowed between operations for capacitor C1 to discharge otherwise the timing will be too short. Re-fit the circuit panel and check operation. Over the next few days, further small changes to VR1/VR2 settings may be made for best results.

ONGUARD

Since the LDR R3 is situated several millimetres below the hole in the box, the response is directional. Normally, the unit will be placed in the drawer or cupboard with the buzzer and LDR facing directly outwards or upwards. However, some experimentation may be needed to find the best position.

Your belongings will always be kept safe with the Cupboard Guard!



6 Church Street, Wimborne Dorset BH211JH Tel: (0202) 882299 Fax: (0202) 841692 Modem: (0202) 882270 DX: 45314 Wimborne

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Tape motor by EMI 2 speed & reversible, £2, Order Ref. 2P70.

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5" 4 ohm, 2 for £1, Order Ref. 136. 61/5" 4 ohm, with tweeter, £1, Order Ref. 895. 61/5" 6 ohm, £1, Order Ref. 896. 61/5" 8 ohm, ŵith tweeter, £1, Order Ref. 897. 6" x 4" 4 ohm, £1, Order Ref. 242. 5" x 5" 15 ohm, £1, Order Ref. 906. 5" x 3" 16 ohm, £1, Order Ref. 725. 7" x 4" 16 ohm, 2 for £1, Order Ref. 684. 8" 15 ohm audax £1. Order Ref. 684.

7" x 4" 16 ohm, 2 tor £1, Urder Ref. 104. 8" 15 ohm audax, £1, Order Ref. 504. 9" x 3" 8 ohm, 5W, £1, Order Ref. 138. 3" 4 ohm, tweeter, £1, Order Ref. 433. Sanyo 61/4" 4 ohm, 10W, £1.50, Order Ref. 1.5P11. Goodmans 61/4" 10W 4 ohm, £2, Order Ref. 2P27. 6" x 4" 15 ohm 10W, £2, Order Ref. 2P167. Horn engaker £3, Order Ref. 3P82.

Area To Solver, £2, Order Ref. 3P82.
20W 5" by Goodmans, £3, Order Ref. 3P145.
20W 4 ohm tweeter, £1.50, Order Ref. 1.5P9.
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blete with test prods, price £7.50, Order Ref. 7.5P8. LCD CLOCK MODULE. 1.5V battery operated, fits nicely into our 50p project box, Order Ref. 876. Only £2, Order Ref. 2P307.

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+ 12V 1.5A, - 12V 1.5A. £5, Order Ref. 5P199. Astec No. 12530. + 12V 1A. - 12V .1A. + 5V 3A.

uncased on pcb, size 160 x 100mm, £3, Order Ref. 3P141

Astec No. BM41001 110W 38V 2.5A 25.1V 3A part metal cased with instrument type main input socket & on/off dp rocker switch, size 354 x 118 x 84mm, £8.50, Order Ref. 8,5P2.

Astec Model No. BM135-3302 + 12V 4A, +5V 16A, - 12V 0.5A totally encased in plated steel with mains input plug, mains output socket & double pole on/off switch size 400 x 130 x 65mm. £9.50 Order Ref 9 5P4

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(all cased unless stated)

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2P112

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9V d.c. 150mA, £1, Order Ref. 762.

9V d.c. 500mA output in 13A case, £2, Order Ref. 2P113.

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MORE SPACE EXPERIMENTS

The space shuttle *Columbia* (STS-55), launched on April 26, had five licensed radio amateurs in its crew of seven. Once again SAREX, the Shuttle Amateur Radio Experiment, was a secondary payload with a programme of prearranged contacts with school radio stations during the mission.

SAREX operations included contacts by voice and packet on 2-metres and a second amateur station was installed in the German spacelab module on board. This was designated SAFEX, the Spacelab Amateurfunk-Experiment, and was operated by two German payload specialists who made a number of scheduled contacts with European schools with this equipment.

SAFEX had a 2-metre f.m. downlink and a 70cm f.m. uplink, using a dual band external antenna mounted on the German module, and this gave the SAREX team the opportunity to compare the performance of their window mounted antenna with the German externally mounted antenna. During orbit 61, they transmitted using the window antenna and on orbit 62 they used the external antenna, asking amateurs in the south-eastern United States to participate in the test by taking signal strength readings of the received signal during both orbit passes.

The externally mounted antenna - basically a quarter-wave whip with an airtight feedthrough - proved to be the better of the two, with a significant signal boost compared to the window antenna. Later, when the window antenna failed the shuttle crew moved their packet TNC and Motorola 2-meter hand-held into the German spacelab to continue operations using the external antenna. *(W5Y) Report).*

SWLs may like to note that special QSL cards are available for reception of the SAREX transmissions. Reports on the STS-55 mission should be sent to IBM Amateur Radio Club/1993, PO Box 1328, Boca Raton, FL 33429-1328, USA, marking the envelope STS-55 SWL, enclosing a self-addressed envelope, minimum 10cm x 24cm, and sufficient IRCs to cover return postage.

REMEMBER THE 1-V-1?

The first electronic project I ever tackled was a single valve 0-V-0 regenerative detector receiver which very quickly hooked me on shortwave radio. I was amazed at what I could hear from around the world on this little set but soon realized it could be improved.

The addition of two extra valves, one as an r.f stage, one as an audio amplifier, made it a 1-V-1 and this set provided me with my sole listening facility for a number of years.

Eventually, and as more funds became available, I moved on to modern, more sophisticated, equipment but I never forgot that exciting sense of involvement which comes from pulling stations in with a set that needs personal operating skill to get the best from it.

Such circuitry is considered vintage today and undoubtedly the performance obtainable at the time cannot compare with that of some of today's remarkable receivers. I have long held the belief, however, that abandoned techniques of the past could benefit from present-day knowledge, techniques and components to produce performance not far short of some of the ultra-sophisticated circuits that have replaced them.

RADIO BYGONES

An example of this is contained in recent three-part article in Radio Bygones by T.S. Christian. His research into the TRF (tuned radio frequency) techniques of the 1930s and developments of the 1940s, combined with the potential of last-generation frame-grid valves - plus help from a modern computer - has produced a 1-V-1 design claimed to be at least as useful as a solid-state direct conversion receiver. He claims it provides good a.m. performance for broadcast listening, better strong-signal performance, and gives the operator control over all performance characteristics.

He comments, "The comparative simplicity of the TRF ensured that it remained popular with shortwave enthusiasts after it had been displaced in professional and domestic applications. However, as so often happens, its very simplicity could have contributed to its downfall ... it may have been neglected rather than developed; skimped rather than elaborated."

In a review of the prototype, Tony Green GOOJJ comments "this radio had a performance I would not have believed possible from a set of such seemingly simple design. Its selectivity is more than adequate for serious CW listening, which its built-in 1kHz note filter brought up to communications receiver quality."

"With a little practice, resolving SSB became easy and careful use of the reaction control allowed many stations way down in the noise to become perfectly readable – allowing me to hear my first Japanese amateur station on 40m which was unreadable on my transceiver ... a very usable radio with a degree of performance comparable to many communications receivers I have used."

"It is not a 'turn on and tune in' radio but more a 'sit down and fiddle' receiver giving it an appeal which has all but disappeared in recent years ... it wouldn't impress the key-pad entry radio user, but could be a firm favourite with someone prepared to learn how to use it properly."

The first part of this article was in the February/March 1993 issue of *Radio Bygones*. This bimonthly magazine provides a fascinating mix covering domestic, amateur and professional radio "from yesterday backwards". Available by post only, it costs £17.00 a year, (Eire and overseas £18.00), sample copy £3.00, from G.C. Arnold Partners, 9 Wetherby Close, Broadstone, Dorset BH18 8JB.

REPEATER LICENSING CHANGES

As from April 1 st, licences for amateur repeater stations have been in the hands of individual repeater keepers. Previously, the Radio Society of Great Britain has been the licensee for all amateur repeater stations and there has been a general prohibition on individual amateurs operating a repeater.

The new arrangement lifts that prohibition but the repeater keeper will now be responsible for the correct operation of the repeater, for monitoring its use and taking steps to limit unauthorised transmissions. He/she must also provide effective close-down facilities when interference is caused to the repeater.

Amateur radio repeaters are stations located at advantageous sites throughout the country, intended to extend the range of communications between amateurs operating "mobile" by automatically receiving and re-transmitting their messages.

Unfortunately, they are frequently subject to interference, abuse, and unacceptable operating practices and there has been much debate on the best way to deal with the problem, sometimes culminating in the Radiocommunications Agency stepping in to close-down a repeater for a period of time. The RA is obviously hoping that by devolving the licences they are also devolving the problem.

SUMMER BROADCAST GUIDE

The International Short Wave League's *Guide to English Language Short Wave Broadcasts to Europe*, covering current summer schedules, is now available. This helpful booklet, published twice a year to reflect broadcasters' seasonal scheduling and other changes, gives details of transmissions receivable round the clock listed by time, frequency and nature of programme. A new feature is the inclusion of details of English language transmissions not intended for, but audible in, Europe.

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