

ECG MACHINES?/6v 10AH BATTS/24V 8A TX Ex

government ECG machinesi Measures 390X320X120mm, on the front are controls for scan speed, scan delay, scan mode, loads of connections on the rear including video out etc. On the front panel are two DIN sockets for connecting the body sensors to. Sensors not included, Inside 2 x 6v 10AH sealed lead acid batts (generally not in good condition), pcb's and a 8A? 24v torroidial transformer (mains in), sold as seen, may have one or two broken knobs etc due to poor storage, £15.99 ref VP2

HYDROPONICS DO YOU GROWYOUR OWN? We have a full colour hydroponics catalogue available containing nutrients, pumps, fittings, environmental control, light fittings, plants, test equipment etc Ring for your free copy.

PC COMBINED UPS AND PSU The unit has a total power leads for drives etc. Inside is 3 12v 7.2aH seated lead acid batteres Backup time is 8 mins at full load or 30 mins at half load Made in the UK by Magnum, 110 or 240vac input, +5v at 35A, -5v at 5A, +12v at 9A, -12v at 5A outputs 170/260v220mm, new and boxed £29 95 Ref. PCUPS2

Ref PCUPS2 WINDOWS 95 CD Assupplied with Hewlet Packard PC's these CD's have all the window files on them and were intended to be used to restore windows on a PC after a crash etc.£15 REF SX06

ALTERNATIVE ENERGY CD, PACKED WITH HUNDREDS OF ALTERNATIVE ENERGY RELATED ARTICLES, PLANS AND INFORMATION ETC £14.50 REF CD56

aerial photography kit This rocket comes with a built in cameral it files up to 500 feet (150 m) lums over, and takes an aerait photograph of the ground below. The rocket then returns afely with its film via its built in paracute. Takes standard 110 film Supplied complete with everything including a launch pad and 3 motors (no film) £29 98 ref astin.

SATELLITE MODULATOR MODULES prices from just 9p Surface mount modulators full of components. Fitted with an F type connector and a uhf type connector Pack of 100 £9 95 ref SS20 PROJECT BOXES Another bargain for you are these smart

ABS project boxes, smart two piece screw together case measuring approx 6%5%2° complete with panel mounted LED. Inside you will find loads of free bits, tape heads, motors, chips resistors, transistors etc Pack of 20 £19 95 ref MD2 **REMOTE HEATING CONTROLLERS with 30A**

MAINS RELAY from just 99p These units were designed to be plugged into a telephythone socket. You then called the phone and some how it turned the heating on Each box contains lots of bits including a mains 30A relay, pack of 20 £20 ref SS34

PIR CAMERABuilt in CCTV camera (composite output) IR strobe light, PIR detector and battery backup Designed to 'squirt' pictures down the 'phone line but works well as a standalone unit Bargain price £39 95 ref SS81J. These units are brand new modules designed to take 'pictures' of intruders and then transmit the pictures down the telephone line. The PIR detects the intruder, fires the strobe light this ensures a perfect picture even in total darkness. The picture is stored in memory inside the module and then sent by modern (not included) down the telephone line. The units also have a nicad battery pack included presumably to maintain operation in the event of mains power failure. Output from the camera is standard b/w composite 320x240 porels with a 90x65 degree field of view, the picture quality is excellent. Each PIR also contains a video capture and compression unit The infra red strobe has a range of 15m The oir has a range of 12m Power requirements are 12v dc 400mA. Power supplies avail at £5 ref SS80The units are supplied with connection details etc but we do not have any information on using the compression and capture unit or interfacing to modems etc The units do have operational PIR's, strobes and camera's (camera is 12vdc and gives out standard composite 1 v p-p video) how you adapt these to work together is entirely up to you/Retail price for the units was in excess of £200 each sale price £39 95 ref SS81JPower supplies £5 ref SS80

TELEPHONES Just in this week is a huge delivery of telephones. all brand new and boxed. Two piece construction with the following features- illuminated keypad, tone or pulse (switchable) recail; redial and pause, inpl/low and off ringer switch and quality construction finished in a smart off white colour and is supplied with a standard international lead (same as US or moderns) if you wish to have a BT lead supplied to convert the phones these are also available at £1 55 each ref BTLX Phones £4.99 each ref PH2 10 off £30 ref \$52

3HP MAINS MOTORS Single phase 240v. brand new, 2 pole: 340x180mm, 2850 pm, builtin automatice reset overload protector, keyed shaft (40x16mm)Made by Leeson £99 each ref LEEI BUILD YOU OWN WINDFARM FROM SCRAP

Newpublication gives step by step guide to building wind generators and propellors. Armed with this publication and a good local scrap yard could make you self sufficient in electricity! £12 ref LOT81

CHIEFTAN TANK DOUBLE LASERS 9 WATT+3 WATT+LASER OPTICS Could be adapted for laser instener

Iong range communications etc Double beam units designed to fit in the gun barrel of a tank, each unit has two semi conductor lasers and motor drive units for alignement 7 mile range, no circuit diagrams due to MOD, new proce £50,0007 us? £199. Each unit has two gallium Arsenide injection lasers, 1 x 9 watt, 1 x 3 watt, 900nm wavelength, 28vdc, 600hz pulse frequency. The units also contain an electronic receiver to detect reflected signals from targets £199. Ref LOT4 **MAGNETIC CREDIT CARD READERS AND**

ENCODING MANUAL £9.95 Cased with fiyleads, designed to read standard credit cards¹ complete with control elctronics PCB and manual covering everything you could want to know about whats hidden in that magnetic strip on your card¹ just £9 95 ref BAR31

Hi power 12v xenon strobe variable rate flasher modules and tubes £6Useful 12v PCB fitted with control electronics and a powerful Xenon tubel just apply 12v DC to the input and the tube will flash On the board is a small potentiometer which can be used to vary the flash rat! PCB measures just 70x 55mm and could be incorporated into many interesting projects! £6 ref FLS1 Pack of 10 is £49 ref FLS2

Hydrogen fuel cells now in stock

Our new Hydrogen fuel cells are 1v at up to 1A output, Hydrogen input, easily driven from a small electrolosis assembly or from a hydrogen source, our demo model uses a solar panel with the output leads in a glass of salt water to produce the hydrogen! Each cell is designed to be completely taken apart, putback together and expanded to what ever capacity you like, (up to 10watts and 12v per assembly. Cells cost £79 ref HFC11

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PHILIPS VP406 LASER DISC PLAYERS, SCART OUTPUT, RS232 CONTROLLED £24.95 REF VP406

SMOKE ALARMS Mains powered, made by the famous Gent company, easy fit next to light fittings, power point Pack of 5£15 ref SS23, pack of 12 £24 ref SS24

4AH D SIZE NICADS pack of 4 £10 ref 4AHPK

ELECTRIC FENCE KIT Everything you need to build a 12vdc electric fence, complete with 200m of fence wire £49 ref AR2 SENDER KIT Contains all components to build a A/V transmitter complete with case £35 ref VSXX2

10 WATT SOLAR PANEL Amorphous silicon panel fitted in a anodized aluminium frame. Panel measures 3' by 1' with screw terminals for easy connection... 3' x 1' solar panel £55 ref MAG45 Uniframed 4 pack (3'x1') £58.99 ref SOLX

12V SOLAR POWERED WATER PUMP Perfect for many 12v DC uses, ranging from solar fountains to hydroponical Small and compact yet powerful works direct from our 10 wett solar panel in bright sun. Max hd 17 ft Max flow = 8 Lpm, 1. 5A Ref AC8 £18.99

SOLAR ENERGY BANK KIT 50x 6"x12" 6v solar panels(amorphous)+50 diodes £99 ref EF112

PINHOLE CAMERA MODULE WITH AUDIO! Superb board camera with on board sound! extra small just 28mm square (including microphone) ideal for covert surveillance. Can be hidden inside anything , even a matchbox! Complete with 15 metre cable, psu and tv/vcr connectors. £49.95 ref CC6J

SOLAR MOTORS Tiny motors which run quite happity on voltages from3-12vdc. Works on our 6v amorphous 6" panels and you can run them from the sun! 32mm dia 20mm thick. £1 50 each WALKIE TALKIES 1 MILE RANGE £37/PAIR REF MAG30

LIQUID CRYSTAL DISPLAYS Bargain prices, 20 character 2 line, 83x19mm £3.99 ref SMC2024A 16 character 4 line, 62x25mm £5.99 ref SMC1640A 40 character 1 line 154x16mm £6.00 ref SMC4011A

YOUR HOME COULD BE SELF SUFFICENT

IN ELECTRICITY Comprehensive plans with loads of info on designing systems, panels, control electronics etc £7 ref PV1 LOW COST CORDLESS MIC 500 range, 90-105mhz, 115g, 193 x 26 x 39mm, 9V PP3 battery required. £17 ref MAG15P1 AUTO SUNCHARGER 155x300mm solar panel with diode and 3 metre lead fitted with a cigar plug. 12v 2watt. £12.99 REF AUG 10P3.

SOLAR POWER LAB SPECIAL 2x 6"x6" 6v 130mA cells, 4 LED's, wire, buzzer, switch + 1 relay or motor £7.99 REF SA27

SOLAR NICAD CHARGERS 4 x AA size £9 99 ref 6P476, 2 x C size £9 99 ref 6P477

5.25" FLOPPY DISKS, pack of 500 disks £25 ref FD;

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30 WATTS OF SOLAR POWER for just £69, 4 panels each one 3'x1' and producing 8w, 13v. PACK OF FOUR £69 ref SOLX

200 WATT INVERTERS plugs straight into your car cigarette lighter socket and is fitted with a 13A socket so you can run your mains operated devices from your car battery. £49.95 ref SS66 THE TRUTH MACHINE Tells if someone is lying by micro tremors in their voice, battery operated, works in general conversation and on the 'phone and TV as well £42.49 ref TD3

INFRARED FILM 6" square piece of flexible infra red film that will only allow IR light through Perfect for converting ordinary torches, lights, headlights etc to infra red output only using standard light bulbs Easily cut to shape 6" square £15 ref IRF2.

33 KILO LIFT MAGNETNeodynium,32mm diameter with a frong bott on the back for easy mounting. Each magnet will lift 33 folos, 4 magnets botted to a plate will lift an incredible 132 kilos! £15 ref MAG33

HYDROGEN FUEL CELL PLANSLoads of information on hydrogen storage and production. Practical plans to build a Hydrogen fuel cell (good workshop facilities required) £8 set ref FCP1

STIRLING ENGINE PLANS Interesting information pack covering all aspects of Stirling engines, pictures of home made engines made from an aerosol can running on a candlel £12 ref STIR2 ENERGY SAVER PLUGS Saves up to 15% electricity when used with findiges, motors up to 2A, light bulbs, soldering irons etc. 59 ear eff. 1077. 10 neck £69 ref. LOTZ2

12V OPERATED SMOKE BOMBS Type 3 is a 12v tragger and 3 smoke cannisters, each cannister will fill a room in a very short space of time! £14 99 ref SB3. Type 2 is 20 smaller cannisters (suidable for smulated equipment fires etc) and 1 tragger module for £29 ref SB2 Type 1 is a 12v tragger and 20 large cannisters £49 ref

HPOWERZENON VARIABLE STROBES Useful 12v PCB fitted with hi power strobe tube and control electronics and speed control potentiometer. Perfect for interesting projects etc 70:55mm 12vdc operation. £6 ea ref FLS1, pack of 10 £49 ref FLS2 NEW LASER POINTERS 4.5mw, 75 metre range, hand held unit runs on two AA batteries (supplied) 670mm. £29 ref DEC49J HOWTO PRODUCE 35 BOTTLES OF WHISKY FROM A SACK OF POTATOES Comprehensive 270

page book covers all aspects of spirit production from everyday materials. Includes construction details of simple stills. £12 ref MS3 **NEW HIGH POWER MINI BUG** with a range of up to 800 metres and a 3 days use from a PP3 this is our top selling bug! less than 1" square and a 10m voice pickup range. £28 Ref LOT102. **IR LAMP KIT** Suitable for cctv cameras, enables the camera to be used in total darkness! £6 ref EF138

INFRA RED POWERBEAM Handheld battery powered lamp, 4 inch reflector, gives out powerful pure infrared light! perfect for CCTV use, nightsights etc. £29 ref PB1.

SUPER WIDEBAND RADAR DETECTOR Detects both radar and laser, XK and KA bands, speed cameras, and all known speed detection systems. 360 degree coverage, front&r earwaveguides, 1,1%2,7%4 6° fts on visor or dash £149

LOPTX Made by Samsung for colour TV £3 each ref SS52 LAPTOP LCD SCREENS 240x175mm, £12 ref SS51

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give you information on setting up different businesses, you peruse these at your leisure using the text editor on your PC. Also included is the certificate enabling you to reproduce (and sell) the manuals as much as you like! £14 ref EP74

HIGH POWER DC MOTORS, PERMANENT

MAGNET 12 - 24v operation, probably about 1/4 horse power, body measures 100m x 75mm with a 60mm x 5mm output shaft with a machined flat on it. Foring is simple using the two threaded bolts protruding from the front. E22 ref MOT4

INFRA RED REMOTE CONTROLS made for TVs but may have other uses pack of 100 £39 ref IREM

Online web catalogue bull-electrical.com

ELECTRONIC SPEED CONTROLLER KIT For

the above motor is £19 ref MAG17. Save £5 if you buy them both together, 1 motor plus speed controller rrp is £41, offer price £36 ref MOT5A.

SONY STEREO TV CHASSIS assemblies comprising complete TV PCB excluding tube and scan coils. Nicam stereo, mains input. Appear to be unused but sold as seen 'Would probably be good for spares or as a nicam stereo TV sound receiver and amplifier. For KV29F1U and KV25F1U(BE3D) PCB no's 1-659-827-12 1-659-826-14 1-711-800-11 £20 ref STV1

RCB UNITS Inline IEC lead with fitted RC breaker. Installed in seconds. Pack of 3 £9.98 ref LOT5A

RADIO CONTROLLED CARS etc No remotes but good strippers for servo's motors and receivers. Sold as is, no returns, mixed types. £3 each ref RCC2

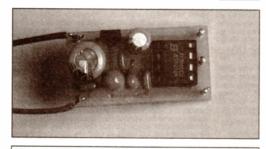
VOICE CHANGERS Hold one of these units over your phone mouth piece an you can adjust your voice using the controls on the unit! Battery operated £15 ref CC3

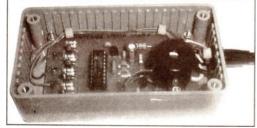




Cover illustration by Jonathan Robertson







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EPE TEACH-IN 2000 **Starts next month** - see page 707

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INTERIOR LAMP DELAY by Steve Challis Don't you just hate being plunged into darkness when the car door shuts? Here's a solution! Plus lights on alarm and battery saver
MAINS CABLE DETECTOR by Robert Penfold This simple Starter Project detects mains cables by sensing their "hum"
QWL LOUDSPEAKER SYSTEM by John Dix Enjoy superb hi-fi sound at a fraction of the cost of an equivalent

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The TELEBOX is an attractive fully cased mains powered unit, containing all electronics ready to plug into a host of video monitors or AV equipment which are fitted with a composite video or SCART input. The composite video cuput will also plug directly into most video recorders, allowing receivers (TELEBOX MB). Push button controls on the front panel allow reception of 8 fully tuneable off air UHF colour television channels. TELEBOX MB covers virtually al televi-sion frequencies VHF and UHF including the HYPERBAND as used by most cable TV operators, Ideal for desidop computer video systems & PIP (picture in picture) setups. For complete compatibility - even for monitors without sound - an integral 4 wett audio amplifier and low level Hi Fi audio output are provided as standard. Brand new - fully guaranteed. TELEBOX ST for composite video input type monitors \$236.95

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Good SH condition - from E299 - CALL for into PHILIPS HCS35 (same style as CM8833) attractively styled 14" colour monitor with <u>both</u> RGB and standard composite 15.625 Khz video inputs via SCART socket and separate phono jacks. Integral audio power amp and speaker for all audio visual uses. Will connect direct to Amiga and Atarl BBC computers. Ideal for all video monitoring / security applications with direct connection to most colour cameras. High quality with many features such as front concealed (lap controls, VCR correction button etc. Good used condition - fully tested - guaranteed **Only £99.00** (E)

PHILIPS HCS31 Ultra compact 9" colour video monitor with stan-dard composite 15.625 Khz video input via SCART socket. Ideal for all monitoring / security applications. High quality, ex-equipment fully tested & guaranteed (possible minor screen bums). In attrac-tive square black plastic case measuring W10" x H10" x 13%" D. 240 V AC mains powered. Only £79.00 (D) Only £79.00 (D)

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VISA

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32U - High Quality - All steel RakCab

Made by Eurocraft Enclosures Ltd to the highest possible spec, rack features all steel construction with removable side, front and back doors. Front and back doors are hinged for easy access and all are lockable with five secure 5 lever barrel locks. The front door

The sector's lever barrer locks. The form dool is constructed of double walled steel with a "designer style" smoked acrylic front panel to enable status indicators to be seen through the panel, yet remain unobtrusive. Internally the rack features fully slotted reinforced vertical fixing members to take the heaviest of 19" rack equipment. The two moveble vertical fixing struts equipment. The two moveole vertical hixing situats (extras available) are pre punched for standard 'cage nuts'. A mains distribution panel internal-ly mounted to the bottom rear, provides 8 x IEC 3 pin Euro sockets and 1 x 13 amp 3 pin switched utility socket. Overall ventilation is provided by fully louvered back door and double skinned top section



tury louvered back boot and boddle skillned top section with top and side louveres. The top panel may be removed for fitting of integral fans to the sub plate etc. Other features include: fitted castors and floor levelers, prepunched utility panel at lower rear for cable / connector access etc. Supplied in excellent, slightly used condition with keys. Colour Royal blue. External dimensions mm=1625H x 635D x 603 W. (64" H x 25" D x 23%" W)

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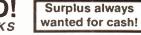
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19" RACK CABINETS Superb quality 6 foot 40U

2010



NEXT MONTH

EPE TEACH-IN 2000

★ During this 10-part series we shall lead you through the fascinating maze of what electronics is all about! ★ We shall assume that you know nothing about the subject!

★ We shall take individual components and concepts in simple steps and show you, with lots of examples. that using electronic components need not be a complex task and that you too can actually design and build something that works!

Much of electronics is about building blocks, and once you have understood what they can do and why they can do it, these blocks can be combined in many different ways to achieve increasingly more sophisticated goals. To assist you in getting to know about the various building blocks, a set of illustrative computer programs has been prepared. We believe these to be capable of running on any comparatively recent PC-compatible computer (from Windows 3.1 upwards). We stress, though, that it is not necessary to own a computer in order to gain benefit from following this Teach-In series.

The programs not only illustrate particular electronics concepts discussed in each Tutorlal, but also offer you interactive involvement, with the ability to specify your own component values and voltages. Self-test and experimental exercises are included. The programs also allow you to use your computer as an item of test equipment, letting you input data from both analogue and digital circuits, displaying It as meaningful screen data and/or waveforms.

STOPWATCH

A PIC-based I.c.d. stopwatch design giving Start, Stop and Lap functions and a maximum time of 10 hours in increments of hundredths of a second. Not only can the functions be triggered by pushbuttons or by remote infra-red beams via a radio frequency link, but the unit will also output serial data to feed a large, high brightness I.e.d. display.

Part 1 describes the design and construction of the Stopwatch and radio links, etc. Part 2, in the December issue, gives details of the large l.e.d. display - each digit measures approximately 200mm by 125mm.

This versatile unit could be used for timing - and displaying times to competitors and crowd – athletic. equestrian, sailing, cycling or motoring events, etc.



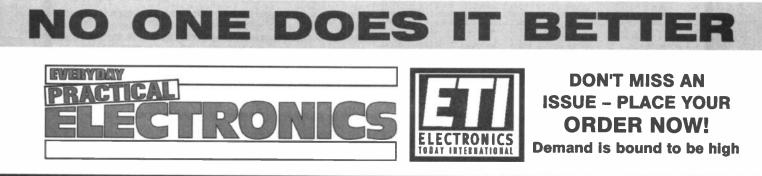
PLUS: FREE **48-PAGE BOOKLET: IDENTIFYING ELECTRONIC** COMPONENTS

ACOUSTIC PROBE

Starter Proje This project could be regarded as the audio equivalent of a telescope. Its basic function is to pick up sounds via a microphone, greatly amplify the resultant signal, and then feed it to a pair of headphones. This gives users a sort of "larger than life" version of what they would normally hear, permitting them to detect sounds that would otherwise be inaudible.

Apart from making sounds louder, it is often possible to place the microphone very close to the sound source, or even actually touching it, so that otherwise inaudible sounds can be monitored. When used in this way the unit acts as a sort of electronic stethoscope, and the barely audible sound from a watch can be made to sound more like a shipyard in full production. It is even possible to place the microphone underwater, perhaps to monitor the wildlife in a pond, provided the microphone is given adequate waterproofing.

PLUS: ALL THE REGULAR FEATURES



NOVEMBER ISSUE ON SALE FRIDAY, OCTOBER 1

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Powerful 250mW output providing excellent range and performance. Size 20mm x 40mm. 9V-12V operation. 3000m range..... £16.45

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Connects directly to 240V A.C. supply for long-term monitoring. Size 30mm x 35mm. 500m range...... £19.45

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Scrambled output from this transmitter cannot be monitored without the SCDM decoder connected to the receiver. Size 20mm x 67mm. 9V operation. 1000m range..... £22.95

SCLX Subcarrier Telephone Transmitter

Connects to telephone line anywhere, requires no batteries. Output scrambled so requires SCDM connected to receiver. Size 32mm x 37mm. 1000m range...... £23.95 SCDM Subcarrier Decoder Unit for SCRX

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Remote control anything around your home or garden, outside lights, alarms, paging system etc. System consists of a small VHF transmitter with digital encoder and receiver unit with decoder and relay output, momentary so alternate, 8-way d.l.l. switches on both boards set your own unique security code. TX size 45mm x 45mm. RX size 35mmx 90mm. Both 9V operation. Range up to 200m. -0.95

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rohn-Hite 5200 - Seese, Function Generator seder LDM-179 - Distortion Meter seder LDM-179 - Distortion Meter seder 216 - Damultiplexer and Frame Alignment Monitor (new) modulator (min) second 1068 - Damultiplexer and Frame Alignment Monitor (new) second 2018 - Distortion Meter second 2019 - Distortion Meter second 2017 - Data Comme Analyses second 2016 - Dower Meter & Sector second 6800 - Power Meter & Sector second 6800 - Power Meter & Sector second 6800 - Dower Meter & Sector second 5900 - L.F. Synthesised Signal Generator 520MHz sector Dame 8904 - Synthesised Signal Generator 520MHz sector 2007056/0770 - Sector Sector sector 2007056/0770 - Sector Sector sector 2007056/0770 - Sector 2007056/0770 - Sector 2007056/0770 - Meter Fragor Sector sector 2007056/0770 - Sector 2007056/0770 - Sector 2007056/0770 - Meter Sector sector 20070	 19	riet Packard 6180A - Deta Generator
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rohn-Hite 5200 - Seese, Function Generator seder LDM-179 - Distortion Meter seder LDM-179 - Distortion Meter seder 216 - Damultiplexer and Frame Alignment Monitor (new) modulator (min) second 1068 - Damultiplexer and Frame Alignment Monitor (new) second 2018 - Distortion Meter second 2019 - Distortion Meter second 2017 - Data Comme Analyses second 2016 - Dower Meter & Sector second 6800 - Power Meter & Sector second 6800 - Power Meter & Sector second 6800 - Dower Meter & Sector second 5900 - L.F. Synthesised Signal Generator 520MHz sector Dame 8904 - Synthesised Signal Generator 520MHz sector 2007056/0770 - Sector Sector sector 2007056/0770 - Sector Sector sector 2007056/0770 - Sector 2007056/0770 - Sector 2007056/0770 - Meter Fragor Sector sector 2007056/0770 - Sector 2007056/0770 - Sector 2007056/0770 - Meter Sector sector 20070		riett Packard 83508 - Sweep Oscillator Mainframe (various plug-in options available)
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IXIN-FLIGHT STEREO UNIT is a stereo amp. Has two most useful mini moving coil speakers. Made for BOAC pasengers. Order Ref: 29. 1 × 0-1mA PANEL METER. Full vision face 70mm

square, Scaled 0-100. Order Ref: 756. 2×LITHIUM BATTERIES, 2:5V penlight size. Order

Ref: 874

Ref: 874. 2x3m TELEPHONE LEADS. With BT flat plug, Ideal for phone extensions, fax, etc. Order Ref: 552. 1 × 12V SOLENOID. Has good ½in. pull or could push if modified. Order Ref: 232. 3 × IN-FLEX SWITCHES. With neon on/off lights, saves leaving things switched on. Order Ref: 7. 2 × 6V 1A MAINS TRANSFORMERS. Upright mounting with fixing clamps. Order Ref: 9. 1 × HUMIDITY SWITCHES. As the air becomes damper, the membrane stretches and operates a microswitch. Order Ref: 32.

microswitch, Order Ref: 32

4 × 13A ROCKER SWITCH. Three tags so on/off, or changeover with centre off. Order Ref: 42. 1 × SUCK OR BLOW-OPERATED PRESSURE

SWITCH. Or it can be operated by any low pressure variation, such as water level in tanks. Order Ref: 67.

Vanation, such as water level in tanks. Order Net. 57. 1 × 6V 750mA POWER SUPPLY. Nicely cased with mains input and 6V output lead. Order Ref: 103A. 12 VERY FINE DRILLS. For p.c.b. boards etc. Nor-mai cost about 80p each. Order Ref: 128. 5 × MOTORS FOR MODEL AEROPLANES. Spin to

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also act as speakers. Order Ref: 139. 6 × NEON INDICATORS. In panel mounting holders

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box and you need never be late. Order Ref: 211. 4×12V ALARMS. Makes a noise about as loud as a

car hom. All brand new. Order Ref: 221. 2×(6in.×4in.) SPEAKERS. 16 ohm 5 watts, so can

be joined in parallel to make a high wattage column. Order Ref: 243.

1 × PANOSTAT. Controls output of boiling ring from simmer up to boil. Order Ref: 252. 2 × OBLONG PUSHSWITCHES. For bell or chimes,

these can switch mains up to 5A so could be footswitch if fitted in pattress. Order Ref: 263. 50 × MIXED SILICON DIODES. Order Ref: 293. 1 × 6 DIGIT MAINS OPERATED COUNTER. Stan-

dard size but counts in even numbers. Order Ref: 28. 2×6V OPERATED REED RELAYS. One normally

n, other normality closed. Order Ref: 48. 1 × CABINET LOCK. With two keys. Order Ref: 55. 6½ 8Ω 5 WATT SPEAKER. Order Ref: 824. 1 × SHADED POLE MAINS MOTOR. ³/4in. stack, so

quite powerful. Order Ref: 85. $1 \times CASE$, $3\% \times 2\% \times 1\%$ with 13A socket pins. Order Ref: 845.

2×CASES. 21/2×21/4×13/4 with 13A pins. Order Ref:

565 4×LUMINOUS ROCKER SWITCHES. 10A mains. Order Ref: 793.

12V 8A POWER SUPPLY. Totally enclosed with its own cooling fan. Normal mains operation, 12V comes out on a 2m heavy duty lead. Price £11. Order Ref: 11P6.

EXCEPTIONALLY NICE PROJECT CASE. This is totally enclosed, well ventilated and the front is cut out to accept a 12V brushless fan size 80mm square. This is held in by 4 spring clips. The size of the case is 6x6x31/4in. deep. Price £4. Order Ref: 4P111. 12V BRUSHLESS FANS. Both square and black,

multi-bladed. The smaller is 80mm square, the larger one 90mm square. Both brand new. Price £3.50 each. Order Ref: 3.5P24 for the 80mm and 3.5P25 for the 90mm

VERY USEFUL POWER SUPPLY. German make by the Vastec company, model ref. no. EN660950. This is a very well made and neatly cased unit with its own 12 brushless cooling fan built in. The case size is approximately 6in. square by 4in. high. The maximum load of the unit is 200VA and split up as follows: +5V 20A, -5V 3A, +12V 8A, -12V 3A. Price £9. Order Ref: 9P22

SPECIAL PRICE FOR TWO MONTHS

A SUPER 12V YUASA BATTERY. It is 18AH Yuasa made jelly type so maintenance free and usable in any position. Brand new and guaranteed 12 months. The regular price of these is £40 but for the months of September and October you can buy at £15 includ-ing VAT, two or more also carriage free. Order Ref: 15P78.

VERY POWERFUL BATTERY MOTOR. Intended to operate portable screwdriver. It is 21/2in. long and 11/2in. diameter. Has a good length spindle. Will operate with considerable power off any voltage between 6V and 12V d.c. Price £2. Order Ref: 2P456.

D.C. MOTOR WITH GEARBOX. Size 60mm long, 30mm diameter. Very powerful, operates off any d.c. voltage between 6V and 24V. Speed at 6V is 200 rpm but higher with higher voltages of course. Price £3. Order Ref: 3P108.

MOTOR SPEED CONTROLLER. For d.c. motors up to 24V and any power up to 1/6 h.p. They reduce by intermittent full voltage pulses so there should be no loss of power. In kit form $\pounds12$. Order Ref: 12P34. Or

made up and tested, £20. Order Ref: 20P39. VERY THIN DRILLS. 12 assorted sizes vary between 0-6mm and 1-6mm, price £1. Order Ref: 128. EVEN THINNER DRILLS. 12 that vary between 0.1

and 0-5mm, price £1. Order Ref: 129. BT TELEPHONE EXTENSION WIRE. This is proper heavy-duty cable for running around the skirting board when you want to make a permanent extenson. 4 cores property colour coded, 25m length, only £1. Order Ref: 1067

A MUCH LARGER PROJECT BOX. Size 216mm × 130mm × 85mm with lid and 4 screws. This is an ABS box which normally retails at around £6. All

brand new, price £2.50. Order Ref: 2.5P28. LARGE TYPE MICROSWITCH with 2in. lever. changeover contacts rated at 15A at 250V, 2 for £1. Order Ref: 1/21R7

BALANCE ASSEMBLY KITS. Japanese made, when assembled ideal for chemical experiments, complete with tweezers and 6 weights 0.5 to 5 grams. Price £2. Order Ref: 2P444. CYCLE LAMP BARGAIN. You can have 100 6V 0-5A MES bulbs for just £2.50 or 1,000 for £20. They

are beautifully made, slightly larger than the standard 6:3V pilot bulb so they would be ideal for making displays for night lights and similar applications.

DOORBELL PSU. This has a.c. voltage output so is ideal for operating most doorbells. The unit is totally enclosed so perfectly safe and it plugs into a 13Å socket. Price only £1. Order Ref: 1/30R1.

FLASHING BEACON. Ideal for putting on a van, a tractor or any vehicle that should always be seen. Uses a Xenon tube and has an amber colour dome. Separate fixing base is included so unit can be put away if desired. Price £5. Order Ref. 5P267. MOST USEFUL POWER SUPPLY. Rated at 9V 1A,

this plugs into a 13A socket. Is really nicely boxed,

£2. Order Ref: 2P733. 1-5-6V MOTOR WITH GEARBOX. Motor is mounted on the gearbox which has interchangeable gears giving range of speeds and motor torques. Comes and with full instructions for

changing gears and calculating speeds, £7. Order Ref: 7P26.

4

SPECIAL 12V RECHARGEABLE BATTERY. This is the Jap made Yuasa. It is sealed so can be used in any position. £3.50 each or 5 for £15. Order Ref: 3.5P11. The batteries have a capacity of 2-3AH which may be a bit low for some jobs but remem-ber you can join them in parallel to give a high amperage.

FOR QUICK HOOK-UPS. You can't beat leads with a croc clip each end. You can have a set of 10 leads, 2 each of 5 assorted colours with insulated crocodile clips on each end. Lead length 36cm, £2 per set. Order



Ref: 2P459. BIG 12V TRANSFORMER. It is 55VA so over 4A

BIG 12V IMANSFORMER, it is 55VA so over 4A. Beautifully made and well insulated. Live parts are in a plastic frame so cannot be accidentally touched, £3.50. Order Ref: 3.5P20. TWIN 13A SWITCHED SOCKET. Good British make, white, quite standard size so suitable for flush mounting or in a surface box. £1.50. Order Ref: 1.5P61.

1.5P61. 1.SP61. 1mA PANEL METER. Approximately 80mm × 50mm, front engraved 0-100, price £1.50. Order Ref: 1/16R2. LIGHT ALARM. Or it could be used to warn when any cupboard door is opened. The light shining on the unit makes the bell ring. Completely built and neatly cased, requires only a battery, £3. Order Ref: 3P155.

WATER LEVEL ALARM. Be it bath, sink, cellar, sump or any other thing that could flood. This device will tell you when the water has risen to the present level. Adjustable over quite a useful range. Neatly

level. Adjustable over quite a userul range. Neally cased for wall mounting, ready to work when battery fitted. £3. Order Ref: 3P156. BIKE RADIO. In fact, it's more than a radio, it's an alarm and a spotlight. The radio is battery operated, of course, and needs 3 AA cells. Only one band but this is the FM band so will receive Radio 1 and 2.

this is the FM band so will receive Radio 1 and 2. Comes complete with handlebar fixing clips. Price £4. Order Ref: 4P72. PHILIPS 9in. MONITOR. Not cased, but it is in a frame for rack mounting. It is high resolution and was made to work with the IBM. 'One per disk' computer. Price £15. Order Ref: 15P1. METAL CASE FOR 9in. MONITOR. Supplied as a flat pack. Price £12. Order Ref: 12P3. TELEPHONE EXTENSION LEAD. Nicely made and BT approved. Has the plug into BT socket one end BT approved. Has the other end, total length 12m. £2. Order Ref: 2P338. INSULATION TESTER WITH MULTIMETER. In-ternally generates voltages which enable you to read

INSULATION TESTER WITH MULTIMETER. In-ternally generates voltages which enable you to read insulation directly in megohms. The multimeter has four ranges AC/DC volts, 3 ranges DC milliamps, 3 ranges resistance and 5 amp range. Ex-British Telecom but in very good condition, tested and guaranteed, probably cost at least 250 each, yours for only 27.50 with leads, carrying case £2 extra. Order Ref: 7.5P4.

REPAIRABLE METERS. We have some of the

REPAINABLE METERS. We have some or the above testers but faulty, not working on all ranges, should be repairable, we supply diagrams. £3. Order Ref: 3P176. CHARGER FOR YUASA BATTERY. This battery charger plugs into a 13A socket, charges at approxi-mately 1/2A so it would charge this battery overnight. Complete with croc clips, ready to go. £5. Order Ref: 5P260 5P269

TOROIDAL MAINS TRANSFORMERS

All with 220/240V primary winding 24V + 24V at 25VA would give 25V at 1A or 50V at 1/2 price, £3. Order Ref: 3P245. 1/2A,

price, £3. Order Ref: 3P245. 0-7V 40VA, has main wind-ing 7V at 5A and a secon-dary winding 12V 1A, price £3. Order Ref: 3P238. 0-110V + 0-110V at 120VA would give you 110V at just over 1A or 220V at ½A, price £8. Order Ref: 8P SV + 3EV ± 150VA would give 25V at 4A or 20V



35V + 35V at 150VA would give 35V at 4A or 70V at 2A, price £8. Order Ref: 8PG9.

35V + 35V at 220VA would give 35V at 61⁄2A or 70V at 33VA, price £10. Order Ref: 10PG4. 110V + 110V at 220VA would give 110V at 2A or 220V at

price £12. Order Ref: 12PG5. 110V + 110V at 500VA would give 110V at 5A or 220V at

nearly 3A, price £25. Order Ref: 25PG8.

SUPER WOOFERS.

A 10in. 4ohm with power rating of 250W music and normal 150W. Normal selling price for this is £55+VAT, you can buy at £29 including VAT and carriage. Order Ref.



29P7. The second one is an 8in. 4ohm, 200W music, 200W normal, again by Challenger, price £18. Order Ref: 18P9. Deduct 10% from these prices if you order in pairs or can collect. These are all brand new in maker's packing.

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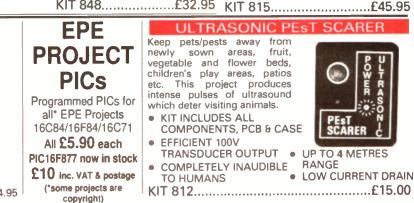
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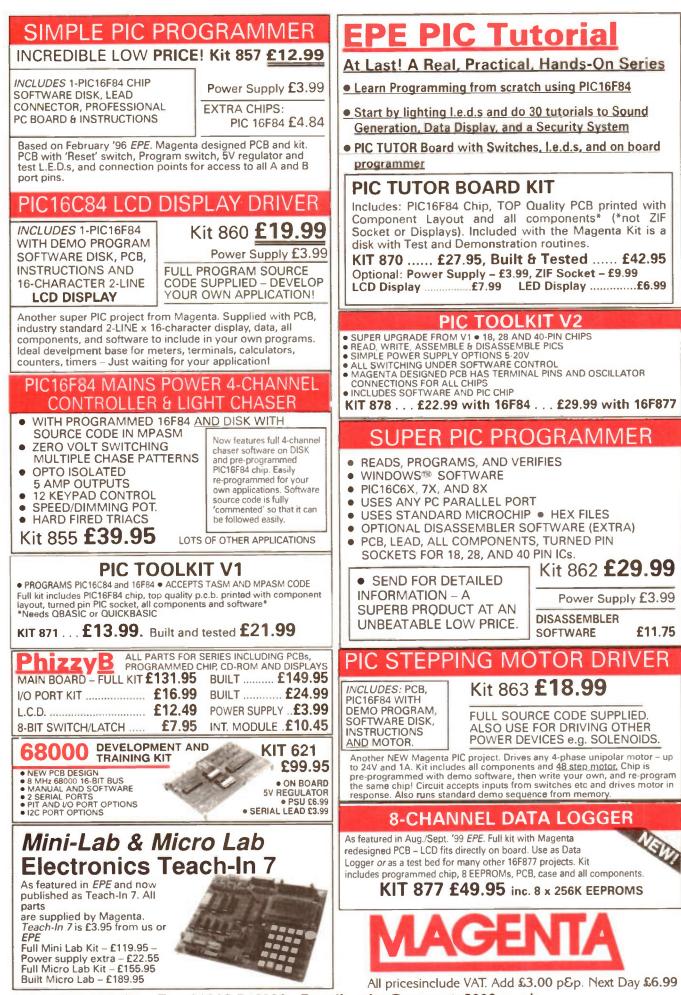
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VOL. 28 No. 10 OCTOBER 1999

WORRY

It's amazing what you have to consider in this job. For instance, we spent quite some time thinking, altering and worrying about this month's cover. Most people will wonder why but a few will see it straight away and, no doubt, take us to task; hopefully we have done enough to pacify t hem.

What is he babbling on about? Sexism, that's the problem.

You see, the concept of this month's cover was to illustrate the workings of a microprocessor. We thought that if we showed some "robots" pulling levers as the workings inside the chip it would be quite interesting. I think you will agree that Jonathan has come up with an unusual and artistic design. however, when we first saw it we were worried that all the "operators" were obviously of the female gender and some would see that as sexist, so we decided to make sure both male and female androids were shown. Personally, I know which I prefer - oh dear, now I'm treading on dodgy ground.

Someone also suggested they should be clothed but it's not the nudity of the androids that worried us, after all androids don't need clothes do they? Come to that they don't need gender either; perhaps they should have sexlessness?

INTELLIGENT SLAVES

We could, of course, have argued that female androids had been chosen for their higher level of intelligence, but some would say they depicted female androids as slaves - whatever argument we use it's still sexist. Best to steer well clear of such arguments, particularly in an electronics magazine. We always find it hard to win arguments with androids anyway, they're so logical.

Anyway, inside your microcontroller are thousands of intelligent "beings" (we only have room to show a handful) pulling levers to make decisions that are interpreted into binary data that's sent to the connection pins that connect to the outside world - or that's what we would have you believe. If you do want to understand more about using microcontrollers, please read John's Mini Tutorial on the new PIC16F87x this month.

All (non sexist) comments on our covers will be of interest.

AVAILABILITY

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Everyday Practical Electronics/ETI, October 1999



Add a touch of luxury and practicality to your vehicle. Also provides lights on warning and battery saving.

T'S a sad fact that you often don't appreciate something until you no longer have it! This was driven home recently to the author when he acquired a "new" car. The first dark evening he jumped in and shut the door, and was plunged into total darkness. The mere 12-second delay fitted to the previous vehicle was ample to settle in and drive away. It was then decided that some form of interior lamp delay was a must.

There have been plenty of past articles showing potentially usable circuits based around chips such as the ubiquitous 555. Whilst perfectly functional, these circuits are limited in additional features. In this design, full advantage is made of a PIC16x84 to provide features that would have required a large amount of logic not so long ago.

The basic operational features are as shown in Table 1.

CIRCUIT DESCRIPTION

The complete circuit diagram for the Interior Lamp Delay is shown in Fig.1. The vehicle 12V battery supply is fed through diode D1 to the voltage regulator IC1, which supplies +5V for PIC microcontroller IC2. The diode is included to provide protection

Table 1. Operational Features.			
Unlock the vehicle	Interior lights on for 30 seconds		
	then fade off.		
Lock the vehicle	Interior lamps directly off.		
Open the door	Interior lamps directly on.		
Close the door (ignition off)	Interior lamps fade off after 30 seconds.		
Close the door (ignition on)	Interior lamp delay function cancelled but lamps fade off.		
Side lamps on	If the ignition is turned from on to off with the lights on (or having been on within the last 10 seconds) then the interior lamp will fade on and remain illuminated for up to 30 seconds before fading off again. If the ignition is turned on during the 30-second period then the interi or lamps will fade off.		
Battery saver	If the doors are left open for more than five minutes the interior lamps are switched off to reduce battery drain.		
Lights on warning	If a door is opened with the side lamps on and the ignition off then an audible warning is activated for 10 seconds.		

against reverse supply connection. Capacitors C1, C2 and C3 provide supply decoupling for the regulator.

Because IC2 runs at 5V, each of its inputs needs to be conditioned prior to application. In the case of the door switch input this is effected by resistors R8 and R9, capacitor C6 and diode D3. Resistors R8 and R9 form a potential divider, which reduces the input voltage by half. For a supply voltage of 13.8V this would give 6.9V.

This is still too high for IC2, whose recommended operating voltage should not exceed 6V. Consequently, Zener diode

D3 is included to limit

the voltage to 5.1V.

Some of you might

should not be arranged

to give the correct

voltage without the

use of a Zener diode.

Well, the supply volt-

age in a motor vehicle

can vary over a large

range, from typically

9V whilst cranking, to

just over 14.5V when

charging with a mini-

why the

divider

wonder

potential

mal load.

Thus the potential divider configuration used here is arranged to give the correct logic level inputs under most conditions. Capacitor C6 is included to filter out any unwanted noise. The same circuit arrangement is used on the other three inputs.

The vehicle's interior lamp is controlled by the PIC through the network around transistors TR1 to TR3. To provide a dimming feature, pulse width modulation is employed. Power-f.e.t. TR1 is switched on/off at a high frequency. The on/off ratio affects the average power delivered to the lamp and hence its brightness, as illustrated graphically in Fig.2.

Since TR1 is either on or off it does not dissipate any appreciable heat. As a result it does not require a heatsink and runs cool in normal opera-

tion. Even when running a 100W test lamp, the case temperature remained within acceptable limits. It is expected that most interior lamps will 10W test 20W/

be around 10W to 20W!

In order to turn TR1 hard on, a voltage of around 12V is needed at its gate terminal. Transistors TR2 and TR3 convert a logic 1 output at IC2 pin RB7 from 5V to 12V. With RB7 at 0V, TR2 and TR3 are turned off. When RB7 goes high (5V) current flows via R7 into the base of TR3.

Transistor TR3 then conducts, drawing current via R2 and R3, the resulting voltage drop at their junction causes TR2 to turn on. The collector potential of TR2 then rises to 12V and this is applied via R6 to the gate of TR1 causing it to conduct. The drain to source resistance of TR1 falls to a low value and the interior lamp illuminates.

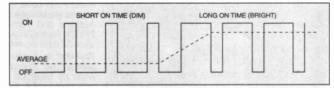
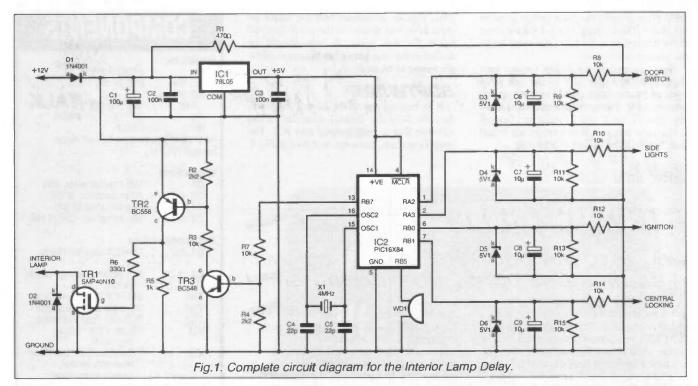


Fig.2. Pulse width modulation varies the average power/brightness of the lamp, as represented by the dotted line.





When the RB7 output of IC2 goes to 0V, TR3 switches off, as do TR2 and TR1, and so current no longer flows in the interior lamp.

Resistor R1 is fitted to provide a "pullup" voltage from the 12V power supply for the door switch input.

During periods of full brightness, the RB7 output of IC2 has to remain at 5V. When dimming is required a pulse-width modulated signal is generated and the On time is gradually reduced until the interior lamp is off. Diode D2 is included to prevent any reverse voltage spikes entering TR1.

Resonator X1, in conjunction with capacitors C4 and C5, forms the 4MHz oscillator needed by IC2.

An audible alarm facility has been included in the form of WD1. This is connected directly to IC2 pin RB5, which pulses the buzzer when the vehicle lights are inadvertently left on.

CONSTRUCTION AND TESTING

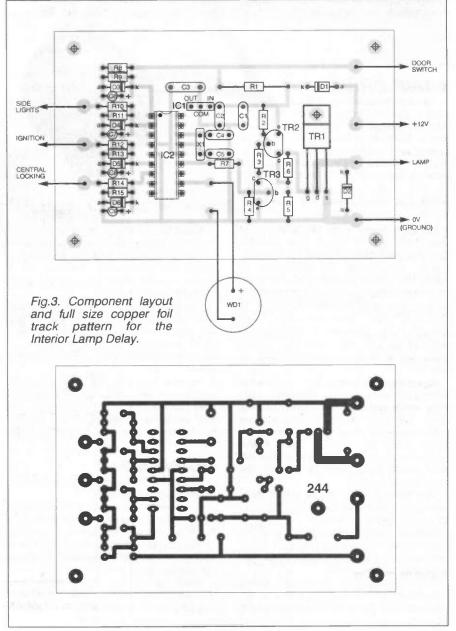
The components for the Interior Lamp Delay are mounted on a single-sided printed circuit board (p.c.b.), whose details are shown in Fig.3. This board is available from the *EPE PCB Service*, code 244.

Begin construction by using the board as a template to drill the equivalent mounting holes in the suggested case.

Solder the components into the board observing their correct orientation, where appropriate, and using a socket for IC2. Do not insert IC2 yet. Once all the components have been fitted, carefully check the board for solder splashes and other signs of bad soldering. A current-limited power supply (if available) set to 12V should now be connected to the board.

With a multimeter check that the 5V supply is correct at pin 14 of the socket for IC2. If not, switch off the supply and carefully check the orientation of IC1.

It is necessary to next check each of the used inputs to IC2, namely socket pins 1, 2, 6 and 7 (RA2, RA3, RB0 and RB1). The voltage reading at pin 1 should be about



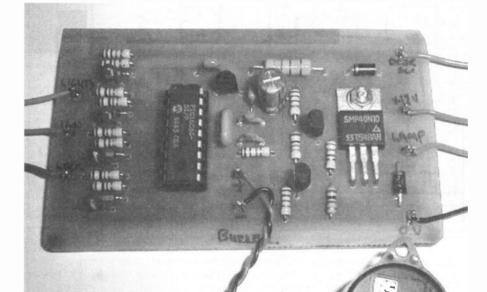
Everyday Practical Electronics/ETI, October 1999

 $5 \cdot 1V$. Now ground the door switch input to the board. The reading at pin 1 should drop to 0V and return to the previous value when the ground is removed.

Next move on to pin 2. The voltage here should be 0V. Apply 12V to the side lamp input and the voltage should rise to approximately 5.1V. The ignition and central locking inputs (pins 6 and 7) must be checked in the same manner. If any errors are found then rectify them before continuing. TR1, plus an additional bolt for which an extra hole was drilled in the p.c.b. (no pad exists for this hole). A hole should be drilled in the case above the buzzer to allow the sound to be emitted.

SOFTWARE

It is beyond the scope of this article to describe fully the internal operation of the software that is programmed into IC2. The main flow chart, however, is shown in Fig.4.



LAMP DRIVER

This concludes testing of the inputs and attention should now be directed to the lamp driver. Disconnect the bench supply and temporarily connect a suitable 12V lamp between the lamp output and the 12V supply. Use a lamp which your bench supply can handle.

Re-apply power to the board. The lamp should not light. If it does, then carefully check around TR1, TR2 and TR3 for any errors. Also check the orientation of diode D2. Using a small piece of wire, carefully bridge pins 13 and 14 of IC2's socket. The lamp should illuminate. If not, once again, check for errors. If all appears OK disconnect the supply, remove the link, and insert IC2 (which needs to have been pre-programmed, of course).

Again re-apply power. The lamp should remain off. Using another link wire, carefully apply power to the central locking input. The lamp should illuminate when power is applied and go off when removed. There should be no delay. Leave power applied to the lock input. Now ground the door switch input. After 30 seconds, the lamp should fade off.

The main features of the circuit have now been tested. Full functional tests can be carried out on the vehicle.

Remove power from the board and finish construction by attaching the board to the case and fitting suitable lengths of the correct gauge insulated wire.

The accompanying photograph shows how the author attached the buzzer to the p.c.b., making use of the mounting bolt for Basically, the PWM signal is generated by an interrupt routine. The value passed to it (BULB) controls whether the lamp is turned on, faded on/off, or turned off. A second variable (BUZZ) is used to control the lights-on buzzer WD1.

The source code was written in C and is fully commented. It was compiled using the excellent Custom Computer Services PCM compiler. Their web site is at http://www.ccsinfo.com and is well worth a visit.

All the files for the software, both source and hex code, are obtainable as stated on the *EPE PCB Service* page. The PIC should be initialised for HS oscillator, power-up timer on, watchdog timer on.

WARNING

Please read the following notes carefully before installing the unit.

A car battery can deliver a very high current. This will almost certainly result in a fire if not treated with caution. In addition to this, modern vehicles are fitted with a variety of electronic modules. A wrong connection can be costly!

When tapping into supply wires (permanent supply, ignition, side lamps, central locking) it is advisable to connect a

COMPONENTS

Resistors R1 R2, R4 R3, R7 to R15 R5 R6 All 0.25W 5% except R1.	470Ω 0.5W 2k2 (2 off) 10k (10 off) 1k 330Ω carbon film o	See SHOP TALK page
Capacitors	100µ radial	elect. 35v

C1 C2, C3 C4, C5 C6 to C9	100μ radial elect. 35v 100n ceramic (2 off) 22p ceramic (2 off) 10μ tantalum 10V (4 off)
Semiconduc	
D1, D2	1N4001 rectifier diode (2 off)
D3 to D6	5V1 400mW Zener diode (4 off)
TB1	SMP40N10 power f.e.t.
TR2	BC558B pnp transistor
TB3	BC548B npn transistor
IC1	78L05 +5V 100mA
101	regulator
IC2	PIC16C84/04P (or 'F84) microcontroller
	(preprogrammed)
Miscellaneou	IS
WD1	buzzer, low current, 3V to 16V
X1	4MHz ceramic resonator
Printed circu	it board, available from the
EPE PCB Sen	vice, code 244; 18-pin d.i.l.
socket; termin	al pins (9 off); plastic case
to suit; cable t	ies; cable connectors (see
	ng wire; solder, etc.

Approx. Cost Guidance Only £24

low value fuse as close as practical to the tapping point. This will protect your additional wiring should a fault occur. The author used 20mm 1A fuses in suitable holders. These are available from accessory shops.

Another item that requires respect is the Airbag. If your vehicle is fitted with one of these devices then it is advisable to disconnect the vehicle battery and wait for at least 10 minutes before attempting to fit any electrical accessory. Finally, if you have a coded radio please make sure you know the security code before disconnecting the supply!

INSTALLATION

Do not be tempted to use the "scotch lock" type of connectors. These are great for temporary use but could cause trouble at a later date. If possible solder all connections. If you are unsure at all then enlist the help of a competent friend. It's better to be safe than have to walk!

Choose a location for the unit where you will be able to hear the buzzer, but away from heat and moisture. Nylon cable ties are excellent for mounting the completed unit.

You will need to locate the wire that connects between the interior lamp and the door switch. Cut into this wire and connect the wires from the unit as indicated schematically in Fig.5. Connect the remaining wires and secure neatly. Ensure that the wires are not resting against any

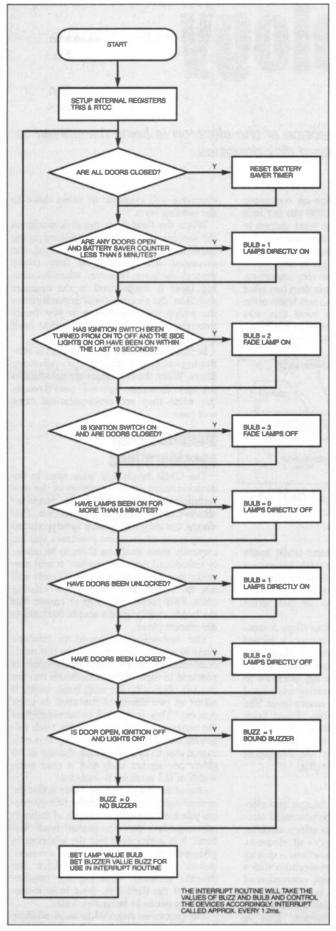


Fig.4. Software flow-chart.

Photo: Interior view of the assembled printed circuit board mounted inside its plastic case. Note that the buzzer is bolted to the board.

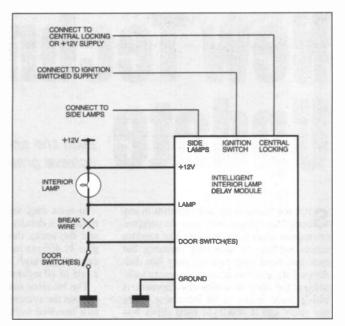


Fig.5. Schematic representation of the connections between the p.c.b. and the car's utilities.

sharp edges or in danger of becoming trapped in any moving object.

The central locking input must be connected to a wire that is at 12V when the doors are unlocked. If your vehicle is not fitted with central locking or a suitable wire cannot be found then connect this wire to the permanent supply. If it is grounded or left open circuit the unit will not function.

When completed, reconnect the vehicle battery and continue testing the unit.

FINAL TESTING

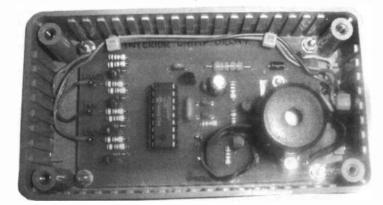
Sit in the vehicle and close the door. After 30 seconds the interior lamp should fade off. Open the door and the interior lamp should illuminate. The interior lamp will remain illuminated for as long as the door is open (up to five minutes). Now switch on the ignition. When the door is closed the interior lamp should fade off after a delay of about two seconds.

Switch off the ignition. The interior lamp should remain off. Turn on the side lights. Now switch the ignition on then off. The interior lamp should fade on. Turn on the ignition. The interior lamp (if still on) should fade off. Finally switch off the ignition and open the door. The lights-on buzzer should now activate for 10 seconds or until the door is closed or the lights turned off.

If you feel patient, the battery saving function can now be tested. Leave the door open. After about five minutes the interior lamps should extinguish. To re-enable the lamps close the door and re-open it.

Finally, if the central locking input has been connected, switch off the lights, ignition, and close the door. Lock the vehicle. The interior lamp should extinguish immediately. When the vehicle is unlocked the interior lamp should illuminate immediately and remain illuminated for up to 30 seconds.

Now wait for it to get dark and reap the benefits.



New Technology Update Even the spin direction of the electron is being harnessed to achieve greater hard disk densities.

STORAGE is one of the key elements in any computer system. Not only do computers require short term memory like random access memory or read only memory, but they also need long term memory like disk drives. Like all other areas of computer technology, the pace at which development is taking place seems to be increasing. Only ten years ago a 40Mbyte hard drive was large. Now most computers sold in the High Street today come with drives that have storage capacities of a few Gigabytes. The access time of the drives is also much shorter despite their greatly increased capacities.

To give an indication of the staggering improvements that have been made in the technology, industry reports indicate that in 1991 the average disk size was only 140Mbytes, whereas the average size in 1999 is just over 7Gbytes. The cost has also fallen dramatically from well over £1000 per Gbyte in 1991 to around £20 per Gbyte in 1999.

New record

In May of this year IBM announced they had set a new world record in hard disk data storage density with a figure of 20 billion bits per square inch. This is more than three times the density of any disk drive that is being manufactured today. It would enable every square inch of disc space to hold two TV quality films, four CD ROMs worth of data, or the text from about 2500 average sized novels.

To achieve this density milestone, significant advances have been made in the development of the heads that read and write the data. In 1999, IBM launched the first magneto-resistive (MR) heads, and since then data density has increased at greater than 60% per year. However, to maintain this trend IBM have further developed this technology in the form of a Giant Magneto-Resistive head (GMR).

One of the problems with increasing the data density is that the space occupied by a single bit becomes smaller and the output from the head is reduced when reading it. Using older technologies the output would fall to the point where noise would cause the data being read to be filled with errors.

The new GMR head is now the most sensitive type of head available for reading magnetic data from disk. In addition to the GMR head required for reading the data, a narrow track thin film inductive write head was used in combination with the associated disk electronics using a system known as Partial Response Maximum Likelihood (PRML) that is used in many drives today. The disk media itself was an integral part of the system and used an ultra-low noise cobalt alloy magnetic material. In tests data was written on concentric tracks at a density of 490,000 bits per inch and the tracks themselves were spaced to give 41,400 per inch. The data rate was also exceedingly high, being written and read at a rate of 10 million bytes per second.

The bit error rate is also very important. In tests the system gave less than one error in a hundred million bits, and when error correction circuitry was used this was reduced to less than one error in a trillion.

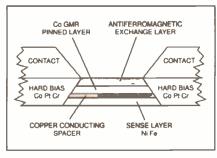


Fig.1. Representation of GMR head construction.

GMR Heads

The basic principle behind GMR heads was discovered at IBM in 1988, however it took another three years before the idea had been sufficiently developed to a form where it could be used in disk drive technology.

The sensors consist of four films: a sensing layer; a conducting spacer; a pinned layer; and finally an exchange layer. The sensing, spacer and pinned layers are all very thin and they allow the electrons to move freely between the sensing and pinned layers via the conductive spacer layer. The magnetic orientation of the pinned layer remains constant, held by the exchange layer that adjoins it. However, the orientation of the sensing layer changes in line with that of the magnetic field from the disk.

Spin Doctor

To tum these magnetic changes into electrical variations that can be processed electronically, the head exploits effects associated with the quantum physics of electrons. Electrons have two spin directions – spin up and spin down. Conduction electrons with a spin parallel to the magnetic orientation of the material move freely, but those with a spin in the opposite direction are hampered, experiencing more collisions. This is exhibited as a high resistance.

In a GMR head, electrons with one spin direction will pass through the pinned layer. Those with the opposite spin direction will not move freely and as a result fewer will pass through the layer. These electrons will continue to move down to the sensing layer.

When the field from the disk results in the sensing layer being magnetised in the same direction as the pinned layer, the electrons will be able to flow freely through the layer. However, when the sensing layer is magnetised in the opposite direction, the electrons will generally have the wrong spin orientation to pass freely through the layer. This will manifest itself as a high resistance.

In more familiar terms, this effect is similar to passing light through two polarising filters. When the two filters are polarised in the same direction, light will pass through, but when they are cross-polarised none will pass.

Manufacturing considerations

The GMR heads that were used in the demonstration of the capability of the new technique closely resemble the magnetoresistive (MR) heads in construction. This means that they will be able to be produced using many of the same processes that are currently used, enabling them to be quick-ly introduced onto the market. It will also mean that the costs of the new heads will not be significantly above the existing ones. This factor will help to ensure that the heads swiftly gain a secure foothold in the market place.

The technology required to produce these heads borrows much from the semiconductor industry. Photolithography is required in view of the dimensions that are needed. Typically the read track width is taken as two thirds of the track to track spacing. This is needed to accommodate the tolerances in the track width, track following and track to track spacing. It is estimated that to provide a data density of 40 Gbits per square inch that a read track width of 0.3 microns is required.

One of the major requirements is that the optical equipment used in the lithographical process has sufficient depth of focus to accommodate the non-planar head features. It is anticipated that the advances in photolithography required for semiconductor manufacturing will drive the process forward, enabling the requirements of the hard disk drive head manufacturers needs to be met as well.

The processes required to mass-produce heads capable of attaining the record densities is now well advanced, and it is expected that it will be possible to produce these heads by the year 2000. With densities of these values, this type of head is certainly to be the head of the future.

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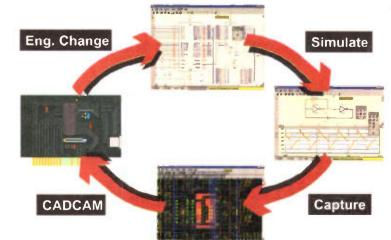
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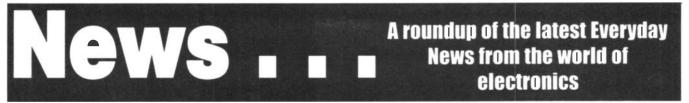
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Everyday Practical Electronics/ETI, October 1999



SOUNDLY DIGITAL Have analogue amps had their day? Barry Fox finds that Sharp think so.

APANESE electronics company Sharp JAPANESE electronics company thinks analogue audio amplifiers have had their day, because its new digital amplifier reproduces sound more faithfully, costs less, is smaller and consumes less power.

Conventional amplifiers are analogue, taking in a weak waveform and putting out a more powerful replica to drive loudspeakers. All add spurious noise, so designers have tried amplifiers which boost digital words of shape that describe the waveform. The circuitry is expensive and runs hot. Sharp's researchers in Hiroshima have worked with Professor Yoshio Yamazaki of Waseda Delta-Sigma 1-Bit University OD Modulation. This uses a very rapid stream of single bits instead of words.

Sharp chops or "samples" the analogue waveform at 64 times the speed at which a CD system samples sound (64 × 44.1kHz). The level of the wave at each sample point is measured, compared with previous samples (in a network of seven feedback loops) and a single digital bit switched between 1 and 0 to represent whether the wave is moving up or down. The stream of 2.8 million bits a second is so rapid that the steering description is very accurate.

The bit stream switches the level of a power signal to re-create a waveform that replicates the original, but much stronger.

Sharp has developed a chip set which reduces power consumption and heat generation to one half that of an analogue amplifier of similar audio performance. Bat-eared hi-fi buffs should be happy, because the high sampling rate provides an audio frequency range of 0-100kHz (which far exceeds human hearing).

Super Hi-Fi

The first product, a super hi-fi amplifier, is expected before the end of the year. The technology will then spin down into budget home audio systems, portables, car stereos and PCs.

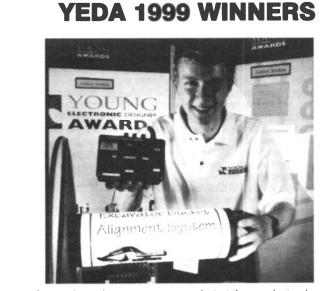
Sharp's team leader Yasutomi Katano acknowledges two practical problems. His amplifier performs best when connected direct to one of the new super hifi DVD players promised for Christmas (by several manufacturers including Sharp), because there is no need to convert an analogue waveform into digital code. But the record companies worry that consumers will connect their disc players to a recorder instead of an amplifier, and make digital clones. Sharp proposes a deliberately non-standard 13-pin connector which works intelligently, hand-shaking with a similar connector on a disc player so that it only sends high quality code to an amplifier.

Because the amplifier is switching a powerful signal at 2.8MHz, it is effectively a radio transmitter with the speaker cables working as an aerial in a band used for radio-location, maritime services etc.

Katano acknowledges that it is "a fundamental requirement" to trap all electromagnetic radiation with shielding and

filter coils. "We are confident we have solved the problem", he says. Twenty years ago Sony launched a digi-

tal amplifier which streamed digital words at 500kHz, the radio frequency allocated for international radio distress calls. The circuit leaked signals and Sony quickly withdrew the product.



E ARE always pleased to encourage people to take up electronics, especially the We are always pleased to encourage poople to take up of the second as we can young, and it is good to report on others who do so as well. For as long as we can the tribulation of the second as the remember, we have annually highlighted the Young Electronic Designer Awards (YEDA), both as a forthcoming event and as a concluded ceremony.

This year's YEDA had 20 national finalists and the winners received their awards from His Royal Highness The Duke of York. Ashley Jordan (17) of Ludlow College, Shropshire, with his Excavator Bucket-Alignment System won the Duke's own award for the most imaginative concept. The prize was £2000, shared by Ashley and his school.

lan Thomas (16) of Trent College, Nottingham, won the prize for the most commercially viable project with his Guitar Anti-tamper Device. The IEE Award for the best new entrant to YEDA was won by Simon Green (16) of Helena Romanes School, Dunmow, Essex - his design was an Electronic Metronome for musicians, for which he and his school also received £2000.

It's good to see that girls also featured as finalists - electronics is a subject that can be explored and enjoyed by either gender. For example, Laura Haughian (16) of St Mary's High School, Lurgan, Northern Ireland, was highly commended for her "Easy Find" sound-activated device for locating lost remote controls.

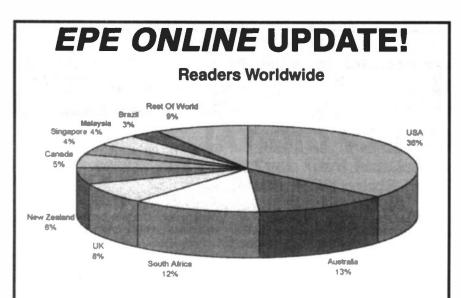
We were pleased to see that our friends at Radley College, Oxon, participated as well. Max Horsey's student Chris Shelmerdine (15) invented "Catomatic", a device for feeding a cat automatically for a five day period, for which he was "highly commended".

The annual YEDA competition was sponsored this year by CPC (Combined Precision Components), DTI (Department of Trade and Industry), EMTA (Engineering and Marine Training Authority) and the IEE (Institution of Electrical Engineers). The competition is open to students between the ages of 12-25 in secondary schools, colleges and universities. It challenges young designers to invent and produce a novel electronic device that meets an everyday need.

The overall objective is for contestants to have fun putting their ideas into practice and in doing so to discover the exciting opportunities which a career in the electronics, communications and IT industries can offer.

If your school or other educational establishment does not already participate in

YEDA, persuade your teacher/tutor to contact the organisers: The YEDA Trust, 60 Lower Street, Pulborough, W. Sussex RH20 2BW. Tel: 01798 875559. Fax: 01798 873550. Web: www.yeda.org.uk.



Shipping the printed copy of *EPE* to customers outside of the UK is slow and/or expensive. For example, express airmail delivery of a printed magazine can cost as much as \$83 US Dollars for a 12-month subscription! This is one of the reasons why we teamed with Maxfield & Montrose Interactive Inc (a polymedia design company based in the United States) to launch the online version of the magazine – *EPE Online* – in November 1998.

Since its inception, *EPE Online* has fulfilled its promise of providing the worldwide community with instant access to our state-of-the-art technical information and highquality constructional articles without delays or paying exorbitant shipping charges.

Online subscribers can purchase issues 24 hours a day, seven days a week, 365 days a year from our highly secure server in the USA, then download and read their online copies of the magazine anytime they wish. Furthermore, by cutting out printers, distributors, retail stores, and others who do not contribute to the knowledge and information contained in our magazines, we are able to pass on these savings to our international customers. In fact a 12-month subscription to *EPE Online* costs just \$9.99 US Dollars (about £6.25), which makes it highly accessible to those readers in less affluent countries who simply cannot afford the rates associated with print copy international magazines.

The pie-chart reveals that EPE Online has met its mandate to supply a worldwide audience. Subscribers cover the globe from North and South America, "Down Under" (Australia and New Zealand), South Africa, Western and Eastern Europe, Scandinavia, Asia, and the Pacific Rim.

Of course EPE Online contains many useful features in its own right, such as active links that can take readers directly to any web sites of the companies and suppliers featured in our articles, thereby augmenting the knowledge and information contained in our magazine. Readers can also quickly and easily create their own "sticky notes" and attach them anywhere in the machine-readable copy of the magazine to remind themselves of useful information, thoughts and ideas at a later time.

EPE Online originally used sophisticated locking technology, which was only available for use on the Windows 95, 98 and NT platforms. More recently we've moved to a new scheme, which is much easier to use, and which has the added advantage of making EPE Online available on Macs, UNIX, and indeed any computer for which an un-ZIP utility is available. Furthermore, readers can now download and read the magazine on multiple computers (for example a desktop at home and a notebook whilst travelling).

EPE continues to lead the magazine industry by using state-of-the-art technologies to deliver the world's first (and easiest to use) web-delivered electronics and computing hobbyist magazine!

RIGHT CONNECTIONS

ESR Electronic Components will be wellknown to many of you for their wide range of essential hardware components such as plugs, sockets, switches, cases etc. They have also built up a good reputation for the quality and low cost of their kits for many *EPE* constructional products, which they supply and advertise via their website.

We are pleased to learn from ESR that they have introduced a new range of popular cable hardware. This, they say, is the beginning of many new additions to the hardware side of their business.

The new range of cable hardware includes all the popular items required to

complete hobbyist or service engineer work to a professional standard. The range includes such basic products as cable ties and bases, up to cable clips and glands. A large (and colourful) selection of heatshrink is added as well, with sizes from 1.6mm to 25.4mm.

The new range is included in ESR's latest shortform datasheet. To obtain a copy, or for more information, contact:

ESR Electronic Components, Dept. EPE, Station Road, Cullercoats, Tyne & Wear NE30 4PQ. Tel: 0191 2514363. Fax: 0191 2522296. E-mail: sales@esr.co.uk. Web: http://www.esr.co.uk.

In Record Time By Barry Fox

SONY is going back to basics with a new VCR, soon to be launched in Europe. Smart Dial is for people who are baffled by the timers on modern VCRs, or have lost the remote control needed to set them.

The new VCR has a "rotary button" on the front. Press once and turn to set the start time on an l.c.d. Press again to set the stop time. Press a third time to set the channel. Push to confirm and go out for the evening, sure in the knowledge that the programme will be recorded to watch later.

B.A.E.C.

WE are pleased to see that the latest newsletter from the British Amateur Electronics Club shows that membership of the Club continues to grow, and that all new members quote "their magazine" as *EPE*! We do try to encourage readers to take an interest in this worthwhile Club and it's obviously paying-off!

It's difficult to summarise the aims of B.A.E.C., but in a nut-shell, it's a forum where electronics enthusiasts can share their enthusiasm and ideas with each other. Its quarterly newsletter is just one aspect of this, get-togethers at various venues around the UK are another (Mr Chairman-Editor, why not include a brief para on the Club's aims in each newsletter so that newcomers and newswriters have it spelt out for them?).

For more info on joining B.A.E.C. contact the Secretary, Martyn Moses, 5 Park View, Cwmaman, Aberdare, Mid Glam CF44 6PP. Tel: 01685 877808. E-mail: mpmoses@compuserve.com.

Radio Show

THIS year's Leicester Amateur Radio Show (the 28th) will once again be held at Donnington Park, Castle Donnington, between 24 and 25 September, from 9.30am to 5pm daily.

The show will feature 150 stands of amateur radio, computer, electronics and related equipment. There will be a Clubland area featuring both local and national Clubs and Societies, including the RSGB. There will also be (amongst many other things) many component stalls and computer stands which will be of interest to electronics enthusiasts.

Enquiries to Geoff Dover G4AFJ. Tel: 01455 823344. Fax: 01455 828273.

E-mail: g4afj@argonet.co.uk. Web: http://www.lars.org.uk.

DUE CREDIT

WCN Supplies of Southampton have sent us a copy of their 12-page catalogue together with a letter saying that, as a goodwill gesture to readers, they will redeem any credit notes issued by Greenweld before they went into liquidation, provided the credit notes are only used to pay for up to 50% of any order; i.e. if you have a £5 Greenweld credit note then you can use it as part payment for any order over £10.

Note that the company that took over the Greenweld name and customer list (see *News* last month) have not taken on responsibility for the original company's debts.

WCN Supplies are at 61 Millbrook Road East, Southampton SO15 1HN. Tel. 01703 226522.



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Starter Project

MAINS CABLE DETECTOR

A back-up mains cable locator for your DIY toolbox.

ROBERT PENFOLD

PROBABLY most keen do-it-yourself enthusiasts are aware of the dangers of drilling into the walls of practically any building, and use some form of pipe/cable detector to check that it is safe prior to doing any work of this type. Such precautions should ensure that there are no nasty surprises, but some types of cable can be difficult to detect.

Most pipe and cable locators are actually metal locators that are optimised for this application. They are quite good at finding things like nails in doors and plasterwork, locating metal pipes, and finding cables in metal conduits. However, they tend to be less effective at finding electric cables that are in plastic conduits.

The problem seems to be that there is simply not that much metal in an electric cable, especially a lighting type that is only designed to carry modest currents. This makes such cables difficult to detect unless they are close to the surface of a wall.

MAKING A HUM

The project featured here uses an alternative approach to finding cables, which is to pick up the 50-Hertz mains "hum" signal produced by the cable. This signal seems to be relatively easy to locate, even with a small cable that is buried deep in a wall.

One obvious drawback of this method is that it will not detect any form of pipe or small metal objects such as screws and nails. It is, therefore, best used as a backup to a conventional pipe and cable locator rather than as the sole method of detecting drilling hazards.

SYSTEM OPERATION

Units of this type sometimes use an inductive coupling from the cable to the detector. This requires a current to be flowing in the cable, which effectively becomes the primary winding of a transformer. An inductor at the input of the detector circuit acts as the secondary winding, and a small 50Hz "hum" signal is produced in this inductor if it is close enough to the cable.

Practical tests with this type of cable detector were not very encouraging, and the signal from the inductor was often so weak that it was virtually impossible to detect. The design finally evolved uses the slightly different arrangement shown in the block diagram of Fig.1. Rather than an inductor, the sensor is a small metal plate. The mains cable and the plate form a very low value capacitor, with the air, etc. between them acting as the dielectric.

This gives an extremely loose coupling from the cable to the input of the detector cir-

cuit, but the large signal level in the cable of around 650V peak-to-peak helps to give a reasonable signal level from the plate. A high impedance buffer amplifier at the input of the detector circuit also helps to minimise losses through the capacitive coupling.

This stage is followed by a voltage amplifier that further boosts sensitivity, but only a modest amount of amplification is needed here. The output signal is monitored via a crystal earphone, and it is due to the good sensitivity of this type of earphone that high gain is not needed in the detector circuit.

CIRCUIT OPERATION

The full circuit diagram for the Mains Cable Detector appears in Fig.2. In Fig.1 the unit is shown as having separate buffer

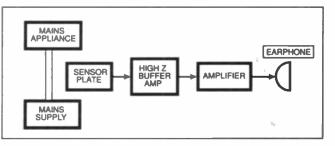


Fig.1. Block diagram for the Mains Cable Detector.

and voltage amplifier stages, but in the final circuit these have been merged into a single amplifier based on operational amplifier, IC1.

Having a very high input impedance plus some voltage gain in a single stage can cause problems with stray feedback and consequent instability. However, in this case the voltage gain of the circuit is quite low and no stability problems were encountered.

The circuit is basically just a non-inverting mode amplifier. The non-inverting input (pin 3) of IC1 is biased to half the supply voltage by resistors R1 to R4. IC1 is a bifet device that has a j.f.e.t. input stage and an extremely high input impedance. In fact, its input impedance at low frequencies is so high that it can be ignored. The input impedance of the circuit as a whole is

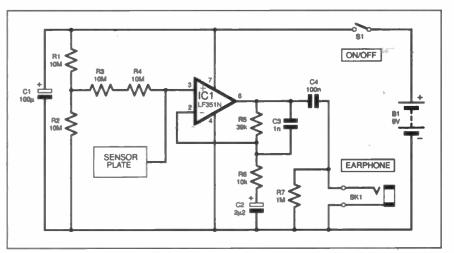


Fig.2. Complete circuit diagram for the Mains Cable Detector.

therefore equal to the parallel resistance of R1 and R2 in series with R3 and R4, or some 25 megohms.

LOOP GAIN

The closed loop voltage gain of IC1 is controlled by negative feedback resistors R5 and R6, and is equal to (R5 + R6)/R6, or approximately five times in other words. Capacitor C3 provides increased negative feedback at middle audio frequencies and above, and therefore provides a progressive roll-off in the gain of the circuit over this frequency range.

The "hum" signal is predominantly at low frequencies, and the high frequency roll-off does not reduce the sensitivity of the unit to this signal. It does help to reduce general noise and breakthrough of r.f. (radio frequency) signals. It also reduces the risk of instability due to stray feedback at high frequencies.

Capacitor C4 and resistor R7 couple the output signal to the crystal earphone and remove the d.c. component in the signal at the output of IC1. The unit is unlikely to work properly using any other type of earphone or headphones. A small 9V battery is adequate to power the circuit, which has a current consumption of less than two milliamps.

CONSTRUCTION

The Mains Cable Detector starter project is based on the EPE multi-project printed circuit board. This board is available from the EPE PCB Service, code 932. The component layout, together with the actual size foil master pattern and interwiring are shown in Fig.3.

The usual warning regarding this pc.b. therefore has to be given. Unlike a normal custom printed circuit board, the multi-project board has numerous holes and pads that are left unused. In order to avoid placement errors it is therefore essential to take slightly more care than normal when fitting the components, and to thoroughly check the finished board for errors.

Construction of the board follows along the normal lines with resistors and capacitors being added first, taking care to fit the electrolytic capacitors with the correct polarity. There is a single link-wire towards the top right hand corner of the board, which should also be fitted at this stage. This link can be made from a piece of wire trimmed from a resistor leadout.

Single-sided solder pins are fitted to the board at the points where connections to switch S1, socket SK1, the battery, and the sensor plate will be made. "Tin" the tops of the pins with a gener-

in a holder.

MAKING SENSE

Practically any small

to medium size plastic

case should accommo-

date this project. It is

best not to use a metal

case, as this would

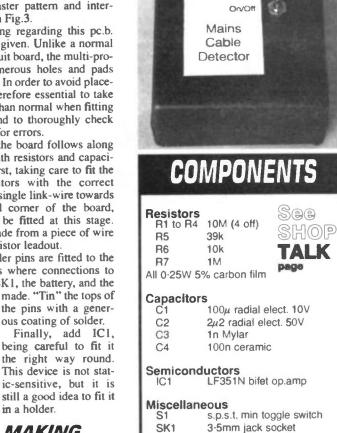
make it difficult to get the sensor plate work-

The general layout

of the unit is not too

ing effectively.

Components mounted on the Multi-project printed circuit board. Note the single link wire, top right.



Earphone

in Social in	000
S1	s.p.s.t. min toggle switch
SK1	3.5mm jack socket
B1	9V battery (PP3)
-	

Small plastic case, size to choice; multiproject printed circuit board available from the EPE PCB Service, code 932; copper clad board or aluminium plate for sensor. size 50mm x 50mm approx; crystal earphone; 8-pin d.i.l. holder; battery connector; wire; solder pins; solder, etc.



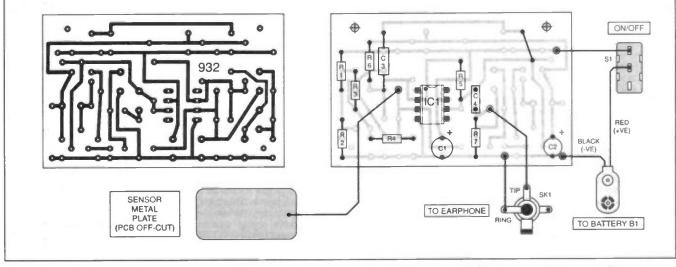


Fig.3. Multi-project printed circuit board component layout, underside full size copper foil master and wiring to off-board components. Not all holes/copper pads are used, so you must double-check component positioning before soldering in place.

important, but try to keep output socket SK1 and its wiring reasonably well separated from the sensor plate and the wire that connects it to the circuit board. The sensor plate can be a piece of copper laminate board (as used for do-it-yourself printed circuit boards) having an area of around five square centimetres or so.

As supplied, this type of board often has a rather dirty and corroded finish on the copper side. Scrape a small area

of the copper with the blade of a

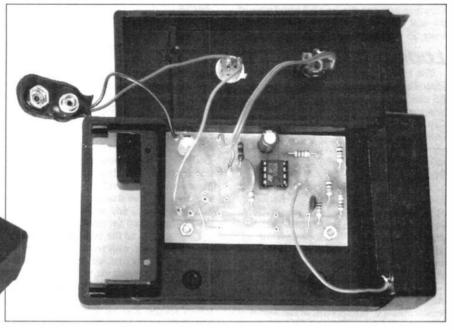
penknife

to produce a clean surface to which a reliable solder connection can be easily made. The sensor is glued in place at the front of the unit, inside the case.

A small piece of aluminium can be used instead of copper laminate board, but soldering to aluminium can be difficult even if the special solder is used. It is easier to bolt a solder tag to the piece of aluminium and then make the connection to the tag.

IN USE

When first switched on there will almost certainly be some "hum" and general noise



The completed detector, above left, and layout of components inside the prototype case. The sensor plate is glued in place at the front of the unit, inside the case.

on the output of the unit even if it is not placed close to a mains lead. The output level should be quite low though. Placing the unit near to the mains lead of any appliance that is plugged into the mains supply should produce a loud 50Hz "hum" signal from the earphone.

In practice, there is a fair amount of noise on the "hum" signal, which will consequently produce more of a "buzzing" sound from the earphone. It seems to be possible to detect cables whether or not they are actually passing a current, but detection is certainly easier if there is a current flow.

Walls, floorboards, etc. do not significantly hinder signal pickup, but there can be a general spreading of the apparent signal source. Even so, it is not usually too difficult to follow the path of power or lighting cables.

SHOP TALK with David Barrington

Interior Lamp Delay

A number of components called-up for the Interior Lamp Delay project could cause constructors local sourcing problems. Fortunately, most seem to be RS components and can be ordered through a local bona fide dealer or through Electromail (201536 204555 or RS http://rswww.com), their mail order outlet.

The SMP40N10 power f.e.t. (code 264-816); the 4MHz ceramic resonator (656-186); small plastic case (508-914) and the low current, 3V to 16V d.c., buzzer (2245-001) all came from the above source. Apart from the power f.e.t., most of our advertisers should be able to supply similar/identical devices.

For those readers who want a ready-programmed PIC16F84 (the C84 is no longer produced), one is available from Magenta Electronics (201283 565435 or http://magenta2000.co.uk) for the inclusive price of £5.90 (overseas readers add £1 for postage). For those who wish to program their own PICs, the software is available from the Editorial Offices on a 3-5in. PC-compatible disk, see EPE PCB Service page. If you are an Internet user, it can be downloaded Free from our FTP site:

ftp://ftp.epemag.wimborne.co.uk/pubs/PICS/interiorlamp.

The printed circuit board is available from the EPE PCB Service, code 244 (see page 773). Finally, you must read the *Warning* notes and installation advice *before* commencing this project.

QWL Loudspeaker System

As far as the woodworking requirements for the QWL Loudspeaker System are concerned, these will have to be obtained from your local timber merchant or large DIY superstore. Also, as indicated in the article, the square metre of Terylene damping material may be purchased from the dressmaking department of your local store.

Turning now to the loudspeakers and crossover units. These were chosen by the designer after a visit to **South Coast Speakers Ltd** of Southampton and are of Norwegian origin. Contacting them, they offered the following (VAT inclusive) pricing for the SEAS speakers and their own crossovers, in kit form or ready assembled:

Driver unit set (two P17REX polypropylene bass units and two

H457(25TFF) dome tweeters) £147.88 plus £5.49 post and packing, total £153.37. Crossover kit (pair) £39.08 plus £2.49 p&p = £41.57. Asssembled crossover £45.08 plus £2.49 p&p = £47.57. Full, all inclusive kit £192.45; assembled £198.45. They can also supply the ancilliary items, such as the black speaker fixing screws, damping material etc. To spread the cost, single drive units and crossovers can be purchased.

For further details and orders contact: South Coast Speakers Ltd, Dept EPE/ETI, 58 Wilton Road, Southampton, Hants, SO15 5SZ. Phone: 01703 703221 or Fax: 01703 778221. Payments should be made payable to South Coast Speakers Ltd.

Micro Power Supply

Not too many problems should be experienced when gathering together the parts for the *Micro Power Supply* project.

The LP2950 +5V voltage regulator was chosen, in preference to the standard 78L05, because it has improved regulation, draws much less supply current and will operate with a very low differential between input and output voltages; ideal for battery powered circuits. If your usual supplier is unable to supply the specified regulator, it is currently listed by Electromail (compared 204555 or RS http://rswww.com), code 648-567 or 411-826, and by Maplin (http://www.maplin.co.uk), code AV350.

Regarding the "switched capacitor" voltage converter, the one used in the prototype is marked as an IS7660 but after numerous catalogue searches only Maplin, code YY75S, had it listed. It would appear that the most popular version is the one with prefix letters ICL (7660) and should be available from most good component suppliers. It is certainly stocked by Electromail, code 651-490.

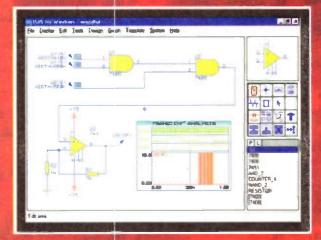
The small single-sided printed circuit board is available from the EPE PCB Service, code 243 (see page 773).

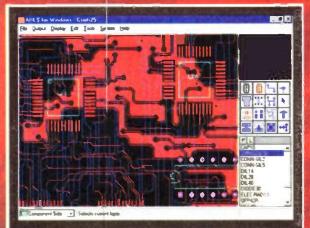
Mains Cable Detector

Apart from the case, all components required to build the Mains Cable Detector should be readily available, off-the-shelf items. Most of our components advertisers stock the high impedance crystal earpiece.

Unfortunately, the two-piece case, with battery compartment, used in the model would appear to be one originally purchased from Greenweld (code CS4330) and may no longer be available. You could try contacting the new owners by Fax on 01992 613020 or by E-mail: greenweld@aol.com. In the meantime, the original case measured roughly 120mm × 60mm × 32mm and, no doubt, advertisers should be able to offer a suitable alternative.

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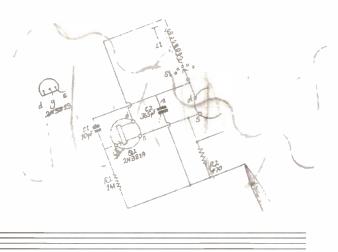
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PART FOUR – NEGATIVE RESISTANCE OSCILLATORS

HE PHENOMENON of negative resistance has been known and understood since the earliest days of radio. Indeed, its application to oscillatory circuits pre-dates the thermionic valve. Put simply, it occurs when the current flowing through a device or circuit is rising whilst the voltage across it is falling and Ohm's law appears to have been reversed.

Cavity magnetron oscillators, which depend on negative resistance, are used in many homes for generating the microwaves which heat food. They also power the RADAR (RAdio Direction And Ranging) systems which help to ensure the safety of travellers by air and sea.

Some rather unusual transistor circuits which exploit negative resistance, form the subjects of this article.

MAINTAINING OSCILLATIONS

The sudden application of a direct voltage to a tuned circuit formed by an inductor (coil) and capacitor shock-excites oscillations. Because of resistive losses (mainly in the coil), the oscillations gradually fade away, their duration and magnitude being directly related to the Q factor of the tuned circuit (the higher the Qthe lower the resistive losses).

Oscillations can be maintained by connecting an amplifier to the tuned circuit and feeding back energy in order to continually repeat the excitation. Earlier articles in the series have described a number of oscillators of this kind.

The feedback eliminates or cancels out the resistive losses. Viewed in this way, it can be said to create negative resistance.

It follows that devices and circuits which display negative resistance, even those which do not function as amplifiers, can also be used to maintain oscillations. An electric arc, Hull's Magnetron, the Esaki or tunnel diode, all have a characteristic which exhibits falling voltage with rising current; i.e. negative resistance.

They function by cancelling out the resistance in the tuned circuit so that the oscillations can continue. When circuits of this kind are analysed from the feedback standpoint, the positive feedback is said to exist within the device itself.

NEGATIVE RESISTANCE

Negative resistance is created when the current flowing through a circuit is rising whilst the voltage across it is falling, and Ohms law appears to have been reversed. Devices which display this characteristic can maintain oscillations in a tuned circuit by cancelling out its resistive losses.

The technique was used by early radio pioneers before the invention of the thermionic valve. Today, powerful microwave generators and devices on the frontiers of semiconductor technology exploit the phenomenon of negative resistance.

TUNNEL DIODE

A Japanese scientist. Leo Esaki (b Osaka, Japan 12 Mar 1925 now an American citizen), first described his tunnel diode in 1958. A two-terminal device, it represents a particularly vivid example of the phenomena of negative resistance, which enables it to function as an oscillator.

Tunnel diodes comprise a junction of p and n-type semiconductor material and, in this respect, resemble conventional junction rectifiers. Pure silicon and germanium have to be doped with impurities before they can function as semiconductors. Pentavalent impurities, such as antimony, phosphorous and arsenic, convert the

silicon or germanium into n-type material. If a trivalent impurity, such as boron, gallium or indium, is added, a *p*-type semiconductor is produced.

Normal junction diodes have impurity levels of about one part in 108. Esaki discovered that if the doping levels are increased to around 1 part in 103, the characteristics of the diode are completely changed.

The current/voltage characteristic of a typical tunnel diode is displayed in Fig.1b. Conduction is high in the reverse direction (*p* side of the junction negative with respect to the n side) and current rises with forward voltages until a peak is reached.

Beyond this point, increasing the forward voltage reduces the flow of current and the diode displays negative resistance. A

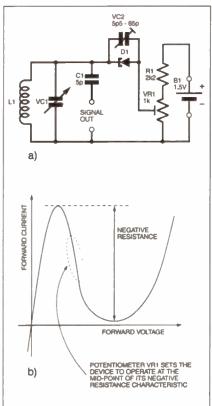


Fig.1a. Typical circuit diagram for Esaki's tunnel diode negative resistance oscillator and (b) tunnel diode further increase in current/voltage characteristic.

forward voltage restores the normal current/voltage relationship, and the biasing of the device is critical.

A typical circuit diagram of a negative resistance oscillator using a tunnel diode is given in Fig.1a. The tuned circuit formed by coil L1 and variable capacitor VC1 determines the frequency of oscillation, and the resistor network, R1 and VR1, reduces the voltage of a single cell (B1) to the few hundred millivolts needed to bias tunnel diode D1 to the correct operating point. Preset potentiometer VR1 enables the bias voltage to be adjusted to suit different devices.

When working at medium and high frequencies, shunting the diode with a trimmer capacitor, VC2, makes the circuit more willing to oscillate. As the frequency of operation increases into the v.h.f. and u.h.f. region, this component can be dispensed with.

Tunnel diodes are low-cost, low noise devices, capable of operating at very high speeds. When they were introduced in the late 50s it was widely anticipated that, because of these attributes, they would find wide application in the then new computer industry.

However, they have the disadvantage of low output voltage swing and, being a two-terminal device, there is no isolation between input and output and this causes circuit design problems. These drawbacks, combined with the dramatic developments in transistors, seem to have eased the tunnel diode into an early obsolescence.

MORE NEGATIVE

Following Esaki's discovery of the tunnel diode, more work was done during the 60s on the development of negative resistance devices of this kind. Significant discoveries were made, and much effort seems to have been expended on the invention of acronyms.

In 1964, American scientists, R. L. Johnston and B. C. de Loach, working at the Bell laboratories, discovered the IMPATT (IMPact, Avalanche, Transit Time) diode. The application of a d.c. voltage to these devices results in the direct generation of microwaves.

Three years later, Americans, Prager, Chang and Weisbrod, developed the TRAPPATT (TRAPped, Plasma, Avalanche, Transit Time) diode. Comparatively high efficiencies were claimed for these devices, but the maximum operating frequencies are somewhat lower than those achieved by the IMPATT diode.

In 1968, a British scientist, G. T. Wright, described a new negative resistance microwave device which he called the BARITT (BARier controlled Injection and Transit Time delay) diode. Capable of operating in the microwave region, the BARITT diode shares the low-noise, low-cost advantages offered by other semiconductor structures of this kind.

Although the physics of these devices is extremely complex, they all depend for their operation on the creation of negative resistance, a phenomena first exploited more than half a century earlier. They are not readily available to the home constructor, but any reference to negative resistance oscillators would have been incomplete without a brief mention being made of them.

Ordinary transistors can, however, be persuaded to operate in this way, and a number of circuits will now be considered.

50kHz OSCILLATOR

It is not too widely known that the emitter/collector structure of some small signal transistors can be made to display a pronounced negative resistance characteristic. A circuit which exploits this is given in Fig.2a, where the emitter/base/collector junctions of an *npn* bipolar transistor are connected across a power supply via load resistor R2 and a voltage divider chain comprising preset VR1 and resistor R1.

The characteristic curve of the semiconductor structure is shown in Fig.2b, and the device can be set around the mid-point by means of VR1. No connection is made to the base (b) of the transistor, and the polarity of the supply voltage has to be reversed (supply positive to the emitter (e) of an *npn* transistor).

The impedance of the semiconductor is low, and it is best suited to maintaining oscillations in a series tuned circuit which has a matching low impedance at resonance. Accordingly, the tuned circuit is formed by capacitor C1 in series with coil L1, and the components specified in Fig.2a can be made to resonate at 50kHz.

The output signal is taken from across the coil via coupling capacitor C2. Oscillation continues to be maintained when the output is fed into an impedance as low as 4.7 kilohms, but signal voltage is reduced and preset VR1 has to be set very precisely.

If possible, the input impedance of the accepting circuit should be at least 100 kilohms and, preferably, 470 kilohms. The value of capacitor C2 should be as low as possible consistent with the delivery of sufficient output.

Output and waveform quality depend, to a considerable extent, on the L/C ratio of the tuned circuit. Formulae relating inductance,

A 50kHz OSCILLATOR

The 50kHz negative resistance oscillator is the simplest and, at the same time, the most unusual oscillator in the series. Illustrated in Fig.2, and relying on the semiconductor junctions within a transistor to create negative resistance, it has something in common with Esaki's tunnel diode.

The circuit will oscillate vigorously from low audio frequencies to more than 100kHz and, when carefully adjusted, will produce a good output waveform. Its characteristic curve (Fig.2b) clearly displays falling voltage with rising current.

100kHz TO 10MHz OSCILLATOR

The two-transistor circuit shown in Fig.3 uses changing bias voltages on a directly-coupled pair of transistors to create the rising current/falling voltage characteristic associated with negative resistance. It will oscillate from low audio frequencies to around 10MHz, and is tolerant of high values of capacitance in the tuned circuit.

Waveform quality is reasonably good provided the controls are set to ensure that the output does not exceed 3V r.m.s. The internal capacitance of the circuit is relatively high, and this will slightly curtail the frequency coverages of the coil and capacitor combinations scheduled in Table 1.

capacitance and frequency were given in Part One (see also Part Three), and the tuning capacitor values suggested there would ensure good results with this circuit. Electrolytic components can be used to provide the comparatively high values of capacitance required for tuning below 100Hz without there being any perceptible deterioration in performance.

The circuit will oscillate vigorously from the lowest audio frequencies to around 150kHz, but 100kHz should be taken as the upper frequency limit for reliable operation. With a reasonable L/C ratio in the tuned circuit, careful adjustment of preset VR1 will produce a perfect sinewave output in the region of 5V r.m.s. Current flow through the device is a little more than 4mA when the circuit is oscillating.

All of the transistors listed in Fig.2a will oscillate reliably up to 100kHz, but the various specimens of BC237 and 2N3707 tried produced the greatest output. Readers who wish to experiment with this circuit will be assured of good results if they start by using one or other of these devices.

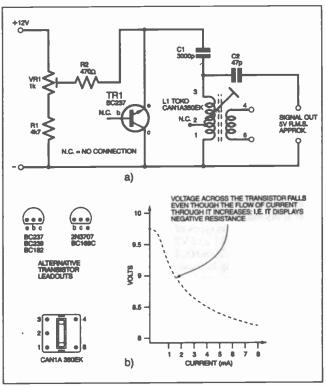


Fig.2a. Circuit diagram for a 50kHz Negative Resistance Oscillator. This circuit exploits the negative resistance characteristic within a transistor. Note that the emitters of npntransistors must be connected to supply positive for this circuit to function. The current/voltage characteristics of an npn transistor connected as shown is given in (b).

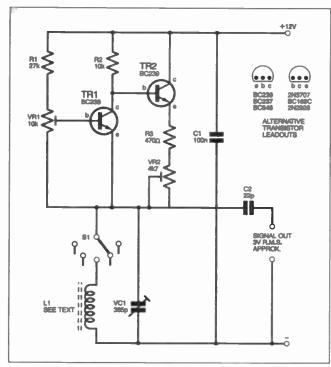


Fig.3. Circuit for a 100kHz to 10MHz negative resistance oscillator.

A number of *pnp* transistors were tried, with the supply polarity reversed, but they could not be persuaded to oscillate.

TWO TRANSISTOR OSCILLATOR

The two transistor combination depicted in Fig.3 is sometimes offered as a tunnel diode substitute. The profile of its characteristic curve is certainly very similar, but the actual operation of the circuit has nothing in common with Esaki's tunnel diode.

When the voltage across the circuit is low, most of the current flows through transistor TR2, and, as voltage increases, so does the flow of current. However, if the supply voltage continues to be increased, a point is reached when transistor TR1 begins to conduct. This transition point is, of course, determined by the setting of TR1's base bias potentiometer VR1.

As the current through TR1 increases, the voltage dropped across its collector load resistor R2 increases. The collector (c) of TR1 is directly coupled to the base (b) of TR2, and the falling voltage reduces the current drawn by the second transistor. The net result is a fall in the current flowing through the circuit, even though the voltage across it is rising; i.e. negative resistance is being created.

When placed in series with a parallel tuned circuit, this two transistor combination will maintain oscillation from audio frequencies up to 10MHz or so. The tuned circuit of Fig.3 is connected on the negative side so that the moving vanes and frame of tuning capacitor VC1 can be grounded. Switch S1 selects inductors, L1, and the output is taken from the "hot" end of the tuned circuit, via capacitor C2.

Earlier comments regarding the input impedance of the accepting circuit apply equally here. Bypass capacitor C1 ensures consistent operation with various types of power supply.

Potentiometers, VR1 and VR2, should be adjusted to get the circuit working at, say, 200kHz, before refining the settings to ensure oscillation across the highest frequency range. They can also be adjusted to produce a good waveform at the expense of output.

This arrangement thrives on relatively high ratios of capacitance to inductance in the tuned circuit (with parallel tuning, this lowers impedance at resonance and increases the Q factor). Capacitors of 2000pF can be wired in parallel with inductors as low as 50μ H and the circuit will still oscillate vigorously, with no reduction in output, and the quality of the waveform is improved. This should be kept in mind if the circuit is used as a spot frequency generator.

F.E.T. OSCILLATOR (100kHz to 30MHz)

Although it is claimed that valves possess distinct advantages as the maintaining devices in oscillatory circuits, there are some transistor configurations which valves cannot emulate. One of these is the combining of pairs of transistors of opposite polarity. An arrangement of this kind is shown in Fig.4, where a complimentary pair of field-effect transistors (f.e.t.s) has been used to create negative resistance. Close scrutiny reveals a variant of the Butler source (originally cathode) coupled oscillator discussed last month.

In Fig.4, f.e.t. TR1 is a source follower and TR2 a grounded gate stage, and the different polarity of the two devices (*npn* and *pnp*) enables the inputs and outputs to be directly connected. This combination results in a "two terminal" device which will maintain oscillation when placed in series with a parallel tuned circuit.

Preset potentiometer VR I enables the circuit to be set at the midpoint of its negative resistance characteristic. It should be adjusted to ensure that oscillation is maintained at the maximum capacity setting of VCI on the highest frequency range. The circuit will then perform satisfactorily on all switched ranges.

The internal resistance of the power supply has to be reasonably low or operation becomes erratic on the higher frequency ranges. The value suggested for VR1 should, therefore, be adhered to.

Current changes triggered by the circuit going in and out of oscillation induce voltage changes across VR1, making it difficult to adjust the operating point. Zener diode D1 holds the voltage across the potentiometer constant and makes the setting up process much easier.

If the power supply delivers less than 12V, the value of resistor R1 should be reduced accordingly. With low resistance supplies of 6V or less, R1 and D1 can be dispensed with.

The negative resistance generator (TR1/TR2) is placed in series with the tuned circuit formed by L1 and VC1. The output is taken from the "hot" end of the tuned circuit via capacitor C1, and, again, it should be fed into an impedance of at least 100 kilohms to avoid excessive damping. The power supply is bypassed by capacitor C2.

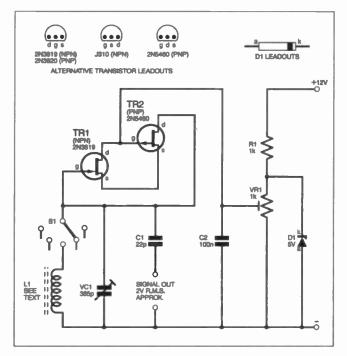


Fig.4. Circuit diagram for a 100kHz to 30MHz negative resistance oscillator using a complimentary pair of f.e.t.s.

TABLE 1: NEGATIVE RESISTANCE OSCILLATORS Frequency coverage with Toko coils and a 10pF to 365pF tuning capacitor. (See Fig.4 and Fig 5 for circuit diagrams).

Band	Toko coli L1	Base wiring	Min. Freq. (MHz)	Max.Freq. (MHz)
1	CAN1A350ER	В	0.1	0.25
2	CAN1A350ER	D	0.22	0.64
3	RWR331208N2	Α	0.52	1.73
4	154FN8A6438EK	Α	1.4	5.4
5	154FN8A6439EK	Α	3.8	15.5
6	KXNK3767EK	Α	7	30

Notes:

(1) See Fig.6 for details of coil base wiring.

(2) Coverage can be varied, between fairly wide limits, by adjusting the coil ferrite cores.

100kHz TO 30MHz OSCILLATORS

Directly-coupled, complimentary pairs of transistors are used to generate negative resistance in the 100kHz to 30MHz circuits given in Fig.4 and Fig.5.

Bipolar and f.e.t. versions oscillate vigorously, from the lowest audio frequencies to above 50MHz. Biasing arrangements are more complicated with the bipolar transistor circuit, but there are no setting up adjustments.

As with all of the negative resistance oscillators, a single winding, untapped coil will suffice, and this simplifies range switching. The frequency coverages obtained with Toko coils and the specified tuning capacitor are given in Table 1.

Waveform is reasonable, but both circuits display slightly more drift than the oscillators described earlier in the series. They are, however, perfectly suitable for use as simple signal generators.

IN OPERATION

Most combinations of *npn* and *pnp* field-effect transistors should work in this circuit, but only those listed in Fig.4 have been tried. The use of a J310 type ensures reliable oscillation, on the highest frequency range, with the tuning capacitor set at maximum.

Frequency coverage with a 10pF to 365pF tuning capacitor and a range of Toko coils is scheduled in Table 1. Details of the coil base connections are given in Fig.6. Readers who would prefer to wind their own coils should refer to Part One for details.

Signal output is reasonably constant over the various ranges and across the swing of the variable capacitor, but it falls to around 1V r.m.s. at the maximum setting of the capacitor on the highest frequency range. Oscillation is maintained from the lowest audio frequencies to above 60MHz.

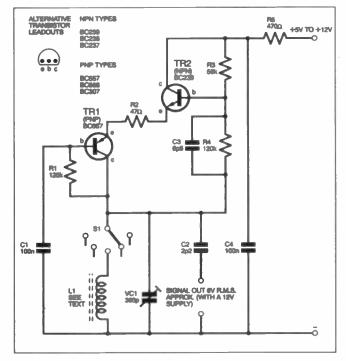


Fig.5. Circuit diagram for a 100kHz to 30MHz negative resistance oscillator using a complimentary pair of bipolar transistors.

If the circuit is to be used exclusively for the generation of low frequencies (100kHz and below), delete the Zener diode and increase VR1 to 4.7 kilohms. At low frequencies, a higher supply voltage and a higher ratio of capacitance to inductance in the tuned circuit ensure a better waveform, and the signal voltage is correspondingly increased.

Above 30MHz, the circuit becomes reluctant to oscillate with high tuning capacitor values, and VC1 should be reduced to 50pF or even 25pF. Leads must be kept as short as possible, and stray capacitance minimised, or the upper frequency limit will be curtailed.

BIPOLAR OSCILLATOR (100kHz to 30MHz)

A bipolar transistor version of the previous circuit is given in Fig.5. Biasing arrangements are more complicated, but the circuit oscillates vigorously and does not require setting up.

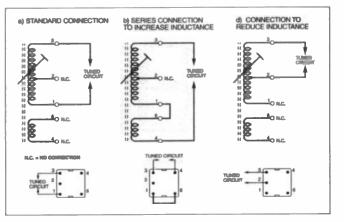


Fig.6. Base connection details for Toko coils.

Base bias is applied to *pnp* transistor TR1 via resistor R1, and voltage divider resistors R3 and R4 fix the bias on the base (b) of *npn* transistor TR2. Capacitor C3 is necessary to ensure adequate feedback at radio frequencies. Low value resistor R2 prevents the circuit behaving erratically. The base of TR1 is grounded by capacitor C1, whilst C4, together with resistor R5, decouples the oscillator from the power supply.

Again, the output is taken from across the tuned circuit, and its r.m.s. value is approximately equal to half the d.c. supply voltage. Output coupling capacitor C2 has been given a very low value in order to attenuate the rather high signal level, which is extremely constant over the swing of the tuning capacitor and the switched ranges.

The circuit has a slightly higher internal capacitance than the f.e.t. version, and this modifies coverage. However, the particulars given in Table 1 still approximate closely to the frequency ranges achievable with the specified variable capacitor. At low frequencies, below 100kHz, operation may become erratic if the ratio of capacitance to inductance in the tuned circuit is low.

Most small signal transistors will work well in this circuit. Those that are known to be suitable are listed in Fig.5.

PERFORMANCE

The field-effect and bipolar transistor versions of the wide range, negative resistance oscillator exhibit slightly greater drift than the conventional feedback oscillators described in earlier instalments of the series. The bipolar version also applies a rough audio modulation to the r.f. output when the circuit is oscillating at high frequencies. Modifying the time constants of the various circuit elements did not cure this.

Both versions of this circuit are vigorous oscillators, capable of delivering a constant output with a reasonably good waveform. They are certainly suitable for use as simple, wide-range signal generators, as audio modulators, or for generating spot frequencies.

MAINS POWER

Readers who lack experience of constructing and commissioning MAINS-POWERED equipment are reminded that the voltages involved can cause DEATH OR SERIOUS INJURY. Anyone who is uncertain about his or her ability to construct or work on equipment of this kind should seek the guidance of an experienced person, use batteries or purchase a commercially produced mains isolated power supply.

PROBLEMS WITH BATTERIES

The current consumed by the transistor circuits included in this series is extremely modest, and well within the capability of small dry batteries. However, the voltage of dry cells varies over quite wide limits, from when they are fresh to the point when they can only just power a circuit.

Voltage reduces imperceptibly, and the unwary can be left wondering why an oscillator won't spring into life when the problem is one of fading batteries. Ageing also effects the internal resistance of dry cells, and this can influence the performance of oscillators, even when bypass capacitors have been provided. There is, therefore, much to recommend a mains power supply unit to the serious experimenter.

LOW-VOLTAGE REGULATED SUPPLY

A circuit diagram for a mains driven Low-Voltage Power Supply, with alternative regulated outputs, is given in Fig.7.

The mains transformer, TR1, primary winding must, of course,

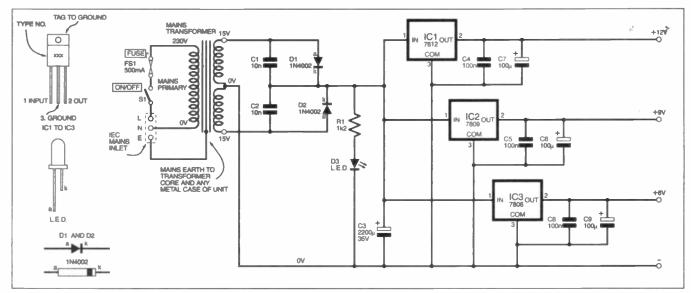


Fig.7. Suggested circuit for a low voltage regulated mains power supply giving outputs of 12V d.c., 9V d.c. and 6V d.c. (Please read warning note, under "Mains Power", given earlier.)

suit the local supply voltage, and the current rating of the secondary winding need be no more than 500mA if the unit is to be used solely for powering the oscillator circuits included in this series. A bench power supply of this kind will, however, be useful for other projects, and a transformer with a 2A or 3A secondary rating would be a sound investment.

The secondary winding output voltages should be 15V-0V-15V for the bi-phase, full-wave rectifier arrangement. This will produce a no-load d.c. voltage across reservoir capacitor C3 of around 21V. Under maximum load, this will fall to approximately 16V, leaving a safety margin above the 15V minimum input required for the 12V regulator, IC1.

An 18V-0V-18V secondary transformer can be used if one is to hand. This will produce an off-load voltage of around 25V, which can exceed the input rating of some 6V regulator i.c.s (this is usually 24V). If an 18V-0V-18V transformer is used, the input to the 6V regulator, IC3, should, therefore, be connected to the output of the 12V regulator, IC1 (i.e., the devices should be connected in tandem) in order to avoid exceeding its input rating.

SAFETY FIRST

The inclusion of a low-current mains fuse, an on-off switch and an IEC mains input chassis plug are good features on equipment of this kind. Mains Earth should be connected to the transformer core and the case of the unit, which should be made of metal. It is best not to connect the mains earth to the output negative rail: electrical interference can be carried into sensitive equipment via the earth wiring.

The specified rectifier diodes D1 and D2 are rated at 1A. If a more powerful unit is contemplated, 3A (1N5402) components must be fitted. Capacitors C1 and C2 are connected across the rectifiers in order to prevent any modulation hum.

The switching action of the diodes will modulate any r.f. current flowing in the circuit at a multiple of the mains frequency, usually 100Hz, and this interference can be picked up by a radio receiver powered by the unit. Modulation hum is tuneable; i.e. it only becomes audible when a signal is tuned in on the receiver. These capacitors should be ceramic components with a minimum working voltage of 50V.

An "on" indicator, comprising an l.e.d., D3, and its dropping resistor R1 is a useful, but not essential, feature.

SMOOTH TALK

The d.c. output from the rectifiers is smoothed by reservoir capacitor C3. An approximate formula for determining the value of power supply reservoir capacitors is:

Value of capacitor in μF = current drain in Amps × 2000.

This formula gives the lower limit of value, and a decent quality power supply should have twice as much reservoir capacitance as this. However, when the supply is regulated, the regulator i.c.s remove much of the ripple which develops under load, and the specified 2200μ F will be adequate provided the total current drain does not exceed 1A.

The voltage regulators, IC1 to IC3, are all 1A rated types chosen to deliver dry-battery related voltages. If difficulty is encountered obtaining the 6V and 9V types, 5V and 8V regulators can be substituted without detracting from the versatility of the unit.

If the power supply is to be used to deliver output currents close to the regulator's 1A maximum rating, the i.c.s should be fitted with small heatsinks, especially if an 18V-0V-18V transformer is installed.

Regulator i.c.s produce about $40\mu V$ of broadband noise which extends into the r.f. spectrum. The combination of ceramic and electrolytic capacitors connected across each output bypasses this interference to ground.

Outputs regulated in this way have a very low resistance (around 70 milliohms) and a high degree of isolation from one another. This last feature is particularly desirable when powering the different sections of an item of equipment; e.g., an oscillator stage and a buffer stage.

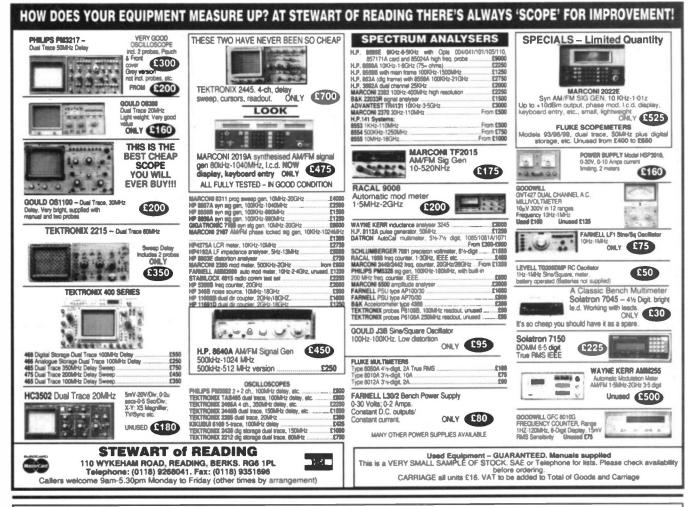
Next month: Crystal controlled oscillators will be considered.



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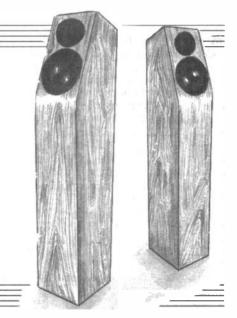


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Constructional Project

OWL LOUDSPEAKER SYSTEM JOHN DIX



An updated design that should appeal to all audiophiles

HIS design originally appeared in 1988 (*ETI*, Aug '88), and a modified version is presented here describing the incorporation of improved drivers and a matching crossover.

After many hours of measurement and listening, it is hoped that the design will appeal to existing owners of the QWL in terms of an upgrade, as well as to new constructors looking for a wide frequency range loudspeaker system occupying a reasonable floor space.

INTRODUCTION

The factors governing the size of a loudspeaker invariably involve considerations of overall performance, cost and domestic acceptability. Referring specifically to moving coil drive units and their enclosures, the larger the size of the enclosure the more extended is the bass response, which in turn adds to the scale of the sound by providing a certain low frequency ambience.

The full benefit of this improved ambience can only be achieved in a large room where, of course, the larger size of enclosure stands a better chance of being domestically acceptable. Unfortunately, the larger size loudspeaker system also tends to be more complex, because the use of a larger diameter bass unit requires the addition of a smaller unit, to cover the critical mid-frequency range adequately smoothly, before handing over finally to a high frequency unit.

Overall then, the design becomes more expensive arising from difficulties in integrating the responses of the individual drive units over the complete frequency range and constructing the cabinet to keep wall vibration to a minimum.

SYSTEM OPTIONS

From what has been outlined above, it is not surprising to find that the loudspeaker system manufacturer has concentrated on the smaller enclosure designs to achieve a cost reduction without an appreciable sacrifice in performance.

Although it is more difficult to maintain the low frequency response of the small enclosure speaker, there are also a number of important advantages associated with a reduction in the dimensions of the enclosure. Thus, it is not unusual to discover a so called budget design that represents really excellent value for money with a performance that can only be surpassed by designs costing a great deal more; sometimes by a factor of three or more.

The advantages arising from the smaller enclosure stem from the dimensions of both the cabinet and the loudspeaker drive units. The smaller cabinet dimensions provide not only a reduction in material costs but also a significant increase in structural stiffness, reducing unwanted radiation from the cabinet walls. The narrower frontal area also improves the sound distribution.

By using a smaller bass unit the complete frequency range can be covered simply by adding a high frequency unit or tweeter. The "seamless" coverage provided by the small bass/mid-range unit can then be made to extend smoothly just beyond the critical mid-frequency range easing the crossover design and producing a radiation pattern conducive to a natural spread of sound and a usefully wide stereo sound stage¹.

IN RESPONSE

As far as a reduced power handling at low frequencies is concerned, it does not represent a too serious disadvantage because in most types of music, including the organ, the power is distributed in such a way that, below about 200Hz, spectral levels fall off naturally at a rate of about 3dB/octave². However, if the small enclosure results in too abrupt a roll-off in bass level below 100Hz then the bass lightness becomes readily apparent, reducing the enjoyment of certain types of music. It follows that there is a limit to the economy feasible with the small diameter bass unit if it is to provide reasonably linear long-throw cone excursions necessary for adequately low frequency radiation.

When it comes to domestic acceptability, the small enclosure has an obvious advantage particularly if it could be tucked away inconspicuously on, say, a shelf. However, close proximity to a wall can give rise to interfering standing wave patterns causing some deterioration in the stereo image and, in the author's experience, an obvious audible improvement, in terms of depth and precision of stereo image, is obtained by spacing the



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loudspeaker units out from the wall by anything up to about one metre.

Continuing to follow the small enclosure approach, the additional requirement is for a pair of loudspeaker stands to locate the enclosures stably in space at a height such that the high frequency information reaches the ears of a seated listener without undue absorption by soft furnishings. Possibly the next logical step is to consider whether the space within and under the stands could be usefully utilised perhaps to enhance the low frequency performance.

Bearing all these points in mind, the author embarked on a design aimed at satisfying the requirements for low cost, a free-standing configuration, and the minimum of dimensions consistent with an adequate low frequency performance.

DESIGN CONSIDERATIONS

A free-standing loudspeaker cabinet with similar dimensions to that of a small enclosure on a stand, if of conventional design and construction, can present a difficult acoustic problem to the designer because of the long narrow parallel cabinet walls. The walls will tend to vibrate and resonate at similar frequencies giving rise to a resonant pipe-like colouration to the low frequency sound, which is difficult to control and eliminate.

An alternative approach, that is satisfying from an acoustic engineering point of view, is to deliberately use and exploit the characteristics of a resonant pipe in such a way that the loudspeaker unit is correctly loaded and terminated at the low frequencies, whilst adequately suppressing unwanted pipe resonant modes. The low frequency efficiency of such an arrangement is somewhere between that of a horn and a bass reflex enclosure and therefore reduces the demands made on the low frequency excursions of the small diaphragm bass speaker unit³.

The principle involved utilises the properties of a closed-at-one-end quarter wavelength pipe as originally proposed by Voigt' in his patent and subsequently adapted and described by R. West and R. Baldock in their designs. The design produced by R. West' was intended for a corner position with the speaker unit firing into the corner to spread the high frequency sound by reflection from the walls, and R. Baldock's designs were intended for either a semi-omnidirectional sound distribution⁵ or a wall reflected distribution⁶.

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Present day practice favours loudspeaker operation away from corners and walls, firing directly at the listeners. It is the author's aim to revise the previous design to cater for this latest approach, which is more closely matched to the improved stereo depth and imaging available from some of the current stereo sources.

OUARTER-WAVE LOADING

Having finally decided to keep to the original Quarter-Wave Loading (QWL) enclosure design, an overall view and cabinet dimensions (two required) can be gained by looking at Fig.10 and Fig.11. The finished appearance of the completed two-speaker "column" can be seen from the accompanying photographs.

The bass enclosure consists of a quar-

ter wavelength rectangular section pipe with a linear taper, resonant at about 50Hz. The bass loudspeaker unit is situated at approximately half way along the acoustic axis in the best position to suppress higher order resonant modes.

high at the tapered

end and is still reasonably high at the loudspeaker unit. This ensures that effective acoustic loading is presented to the loudspeaker cone and small excursions of the cone at high pressure are manifested as much larger low pressure movements of air out of the port at the bottom of the enclosure.

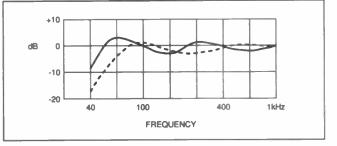
Such a process, similar to horn loading, contributes to efficient bass frequency operation with low distortion, up to a frequency of 200Hz, where direct radiation from the cone takes over. The enhanced bass response produced by this method of loading compared with that from the same unit in a 10 litre sealed enclosure is shown in Fig.1, where the curves were obtained under identical measurement conditions.

Bearing in mind this result, it can be seen that this enclosure not only satisfies the requirements of being free-standing, with the drive units at a convenient height, but also provides an enhanced bass response, using the space that would otherwise have been taken up by a stand. Furthermore, only small cone excursions are required in the bass loaded region and this places the minimum of demands on linearity of the cone suspension and the magnetic field in the voice coil gap, allowing reasonably low priced drive units to be employed.

The effect on power handling was quite dramatically demonstrated to the author when two nominally identical bass drive units were compared: one in a 10 litre completely sealed enclosure and the other in a quarter-wave loading enclosure. Switching from one to the other and slowly increasing the power level, the unit in the sealed enclosure was the first to show signs of distress.

Continuing the quest for a low price design, it is tempting to consider a wide range twin-cone unit for use in this enclosure. As can be seen in Fig.2, the high frequency response of a 165mm diameter paper cone bass unit used in this position showed considerable ripple in the response due to cone break-up modes. Also, when a small tweeter cone is added to the main cone to widen the frequency range, any improvement in frequency response is accompanied by main cone break-up ripple as shown in Fig.3.

A smoother performer was found to be a 165mm polypropylene cone bass unit with the frequency response as shown in



At resonance the Fig.1. Bass response comparison between quarter-wave acoustic pressure is design (solid) and 10 litre sealed enclosure (dashed).

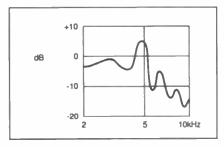


Fig.2. Frequency response for a 165mm paper cone bass unit.

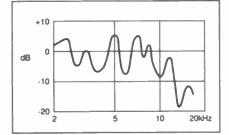


Fig.3. Response of a twin-cone unit.

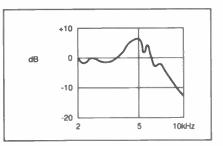


Fig.4. Frequency response of 165mm polypropylene bass unit.

Fig.4 and this type is recommended for use in this design. (Needless to say the latest polypropylene bass unit adopted for this updated design is even smoother!)

HIGH FREQUENCIES

Because of the unsatisfactory response of twin cone units, space is provided in the top of the quarter-wave enclosure (see Fig.10 and Fig.11) to house a suitable high frequency "tweeter". Referring again to Fig.10 and Fig.11, it can be seen that the speaker unit baffle board has been kept as small as possible both for the sake of rigidity and the need to keep the frontal area to a minimum for good stereo performance.

The top of the bass enclosure also serves to stiffen the unit baffle. The baffle is deliberately sloped to time-align the outputs from the two units and to improve the coupling of the bass unit to the air column in the enclosure.

The original speaker units used in this design were supplied by Tandy and a simple crossover arrangement, where the tweeter was fed from a capacitor, kept the overall cost to a minimum whilst yielding a very good performance for the money. However, as the original speaker units are no longer available, it provided an ideal opportunity to investigate the possibility of upgrading the choice of loudspeaker driver units.

The advantage of the quarter-wave loading is that it is not sensitive to the bass driver characteristics in the way that the bass reflex cabinet design is. All that

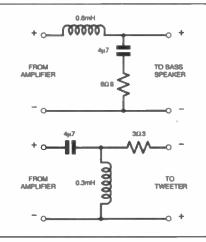


Fig.5. Crossover circuit diagram.

is asked for is a reasonably low bass resonant frequency.

One further consideration was that of the type of crossover to employ. The original capacitor feed to the tweeter and direct connection to the bass speaker meant that the bass unit worked up to 6kHz or so before its contribution was taken over by the tweeter. This is fine in some respects, such as maximum damping from the amplifier, but the directivity of the bass speaker increases with operating frequency and the optimum stereo listening position becomes quite restrictive and critical at the higher frequencies.

Also, the tweeter capacitor feed with its low rate of cut-off with frequency means that the drive voltage at tweeter resonance can be embarrassingly high. Thus an upgrade also meant the adoption of an improved crossover arrangement.

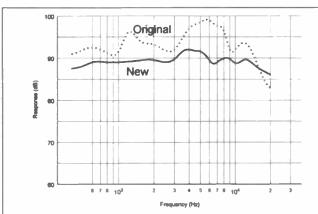
At this point, the author sought the assistance of South Coast Speakers of Southampton for help in the choice of loudspeaker units and a crossover design. After several visits, and a review and analysis of the existing design, speaker units from the Norwegian firm SEAS were considered for the upgrade.

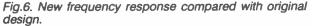
The units finally chosen were the P17REX polypropylene bass speaker and the 25TTF fabric dome tweeter (see *Shoptalk*). These were installed in the QWL enclosure and delivered to South Coast Speakers where a 2nd order Linkwitz-Riley crossover was designed and evaluated.

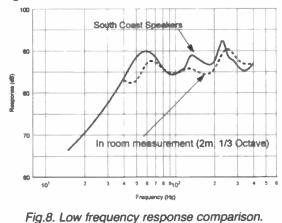
CROSSOVER

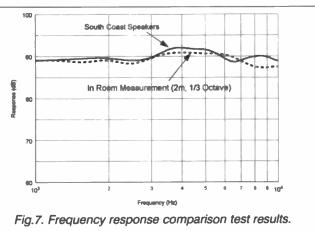
The Linkwitz-Riley crossover design originated as a measure to control the direction of the main lobe of the forward radiation of a two-unit speaker system. It is unusual in that, at the crossover frequency, each filter response is 6dB down (as opposed to 3dB) and the drivers are wired in *opposite* polarity to achieve an in-phase contribution to the overall radiated sound. As far as the phase response of the two sections of the crossover is concerned the Linkwitz concept is to aim at a constant phase difference between the two output signals to achieve a good group delay response.

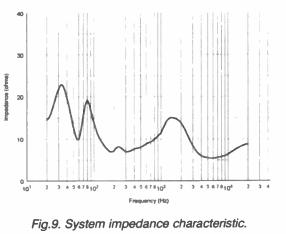
The final circuit of the QWL crossover is shown in Fig.5 and the measured loudspeaker frequency response from 500Hz to 20kHz is shown in Fig.6, where it is





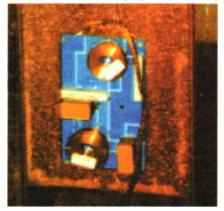






compared with the response of the original 1988 design. The advantage of the new drivers and a more complex crossover is readily apparent from the responses. There is also an improvement in power handling from 25W to 80W, which perhaps is not surprising considering that the weight of the magnet of the bass speaker, for example, represents an increase of over a factor of two.

The frequency response measurements, using 1/3 octave noise between 1kHz and 10kHz, have also been repeated in the author's own room with a capacitor microphone at two metres. The upper frequency was limited to 10kHz because of the response of the microphone available. The results are shown in Fig.7, where it can be seen that a comparison between the South Coast Speaker calibration and the in-room measurements is most encouraging.



The crossover board mounted on the rear panel, just above the rear port.

As far as the frequency response below 500Hz is concerned measurements become a little more difficult. At low frequencies the loudspeaker produces sound both from the cone of the bass unit and the rear port. It is the achievement of a true representation of the combined response that presents a problem.

A straightforward summation of the output of the cone and port from the South Coast Speaker's measurements is shown in Fig.8 and again in-room 1/3 octave noise measurements compare quite favourably. This time the microphone response cannot be trusted much below 40Hz.

Taking a view of the frequency response as a whole, it can be seen that the response extends from 40Hz to 20kHz with an SPL(1W/1m) of 89dB, deviating by ± 3 dB from 1kHz to 20kHz and by ± 3 dB/-6dB from 40Hz to 500Hz. The low frequency properties of the cabinet can be associated with an intriguing mixture of tuned pipe and horn loading, the advantages of which are evident in terms of an improved transient response from the bass unit.

The penalty for this improvement, is the increase in response deviation below 400Hz. However, these deviations are small compared with standing wave effects in the listening room at these frequencies and the overall



response compares very favourably with other speakers of, what might be termed, an audiophile status.

Finally, the impedance characteristic shown in Fig.9 illustrates that the loudspeaker will present a relatively easy load to most amplifiers, producing really loud results with amplifiers in the 50W range.

CONSTRUCTION

Now we have discussed the various merits and selected our speakers, it is time to undertake the construction of the QWL enclosure. This is made up from 12mm(1/2in.) thick chipboard and 6mm (1/4in.) thick plywood as shown in Fig.10.

The enclosure has been designed to make the construction as simple as possible, and if the various pieces shown in Fig.10 are cut accurately square then there should be no difficulties in assembly. Referring to Fig.10 and Fig.11 it can be seen that there are only two angle cuts to be made, those at the top of both the long front and back panels. All the rest are simple 90 degree butt joints and it is



The 25TTF (H457) tweeter from SEAS.

left to the individual constructor to decide whether to attempt the angle joints or simply butt the joints and fill the wedge shape gaps with whatever technique and material is convenient.

Dimensions quoted are not critical provided everything is checked to fit as shown in the diagrams so that airtight joints are obtained, particularly in the high acoustic pressure areas in the tapered wedge and around the speaker unit. (It should be noted that the baffle cut-out dimensions in Fig.10 are for the new loudspeaker units and existing cabinets will satisfactorily house the new units simply by opening up the cut-out for the bass unit by about 2mm (1/16in.) or so, and the vertical dimension of the right hand cut-out for the tweeter by 6mm. It is desirable to ensure that the surrounds for the two units almost touch on the vertical axis.)

The front, back, bottom, top, and internal partition members are all made of nominally 12mm (1/2in.) thick chipboard and should all be matched to the same width of 178mm (7in.). The two side panels are made of nominally 6mm

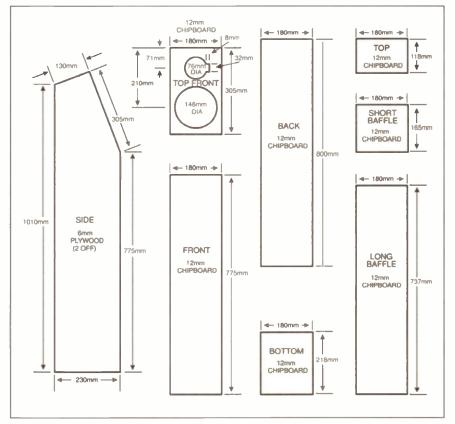


Fig.10. Cutting details and dimensions for a single enclosure (two for stereo).

(1/4in.) thick plywood and it is recommended that one of the panels is marked out to indicate where the 12mm thick panels are located. These can then be cut to size and checked for fit and the assembly pinned and glued together to form the structure drawn in Fig.11.

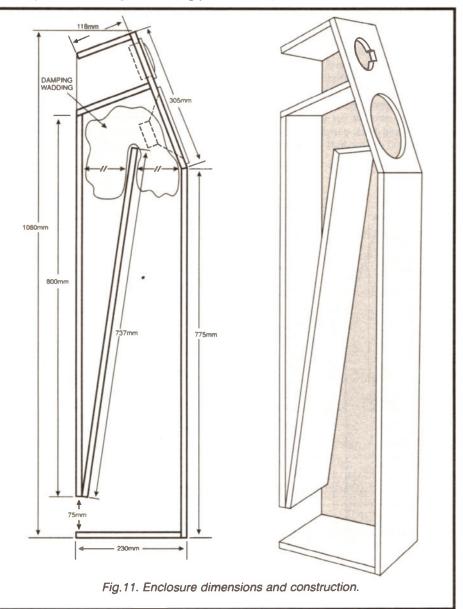
As the assembly progresses check it for squareness and, if necessary, secure one or two cross-pieces of plywood offcuts with pins driven a little way in to hold the assembly square while the glue sets. Being reasonably liberal with the glue should ensure airtight joints but pay particular attention to the pointed end of the wedge section, and, if necessary run a fillet of glue along this particular joint.

Finally, complete the assembly by glueing and pinning the second plywood panel into place. It will be noted that the enclosure is reasonably light and stiff and this minimizes the energy storage in the enclosure walls.

Tapping the sides of the enclosure produces different notes at different positions indicating that the internal bracing and asymmetry is working to minimize undesirable reflections and panel resonances. Finishing tasks involve punching the pins home, filling, and sanding prior



Once the wiring to the crossover is completed it's time to face the music.



to painting or covering with material or an iron-on veneer.

The enclosure was checked for any undesirable resonances by very slowly sweeping through the low frequencies with a sine wave generator as well as taking account of the impedance characteristic of Fig.9. The damping material is obtained by buying a square metre of Terylene wadding from the dressmaking department of your local store. It should weigh about 100 grams and is cut in two: a piece for each enclosure. Each piece is folded lengthwise in two and the resulting strip is folded again twice to form a 25cm square, ready for insertion as shown in Fig.11.

LOUDSPEAKER MOUNTING

One final task now remains, and that is to mount the speaker units and crossovers on each completed enclosure. The loudspeaker units are mounted from the outside of the enclosure and the bass unit needs a sealing gasket fabricated from, preferably a rubber based, draught sealing strip. It is also recommended that the tweeter is mounted on a sealing strip to avoid any extraneous noise set up between tweeter surround and the cabinet.

Use chipboard screws and do not over tighten. Matching black screws can be obtained and with their extra length it is advisable to glue small soft wood blocks behind each screw hole location.

The bass speaker signal lead is simply passed down through the bass enclosure and out through the rear port, whilst the screened tweeter lead is secured by clips down the back of the enclosure, see photograph. Connections can then be made to the crossover board fastened to the back of the cabinet and the board enclosed, if desired, in a plastic box. Readers may also wish to experiment with the provision of steel or plastic spikes in the base of the enclosure.

PERFORMANCE

The choice of loudspeaker is often a very personal decision and the present design is the result of many hours of measurement and listening. Over a period of time friends have been invited to audition these speakers and their performance received very favourable comments by even the most candid listener. It is felt that the majority of readers will not be disappointed with the final results of this project.

In summary then, these loudspeakers are relatively cheap for the performance obtained (as a useful rule of thumb they compare very favourably with commercial designs costing in the order of three times the cost of the speaker units and crossovers in this design) and are fairly simple to build. They occupy very little floor space, are easily moved to a desired position for serious listening, and are of the right height to preclude the need for stands.

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Alex- Animated Head



Taking a programmer's look at some of the PIC16F87x family's facilities.

T LONG last Microchip's new APIC16F87x family of microcontrollers is actually available through component distributors and retailers. Forecast in late 1998, production release commenced in July 1999.

The author has had engineering samples for some months and has examined them with considerable interest. A general look at the family was taken in the PIC16F87x Review (Apr '99). Now we can encourage you to explore the possibilities of these very welcome PICs, knowing that you can obtain the production devices.

The aim of this Mini Tutorial is to share with you some of the findings made by the author when designing the 8-Channel Analogue Data Logger, published in Aug/Sept '99. The findings relate to investigations using the PIC16F877 but apply in principle to the PIC16F873, '874, and '876 devices as well.

Readers should also study the 200-page data sheet that covers the devices, Microchip code DS30292A (see later).

The following EPE subject material is also referred to in this Mini Tutorial:

- PIC Tutorial (Mar-May '98)
- PlCtutor (CD-ROM version of the PIC Tutorial)
- PIC Toolkit Mk1 (Jul '98)
- PIC Toolkit Mk2 (May-Jun '99)
- Virtual Scope (Jan-Feb '98)

PROTOCOL

In the course of this article, reference is made to figures from Microchip data sheet DS30292A and the reference style used is, for example, DS-FIG.11-2 - meaning data sheet Figure 11-2.

Note also the use of the \$ (dollar) sign when referring to hexadecimal numbers, and the % (per cent) sign for binary numbers, e.g. \$9F and %11001111.

Where PIC source code examples are given, the dialect used is TASM (the differences between TASM and MPASM were discussed in Toolkit Mk1. PIC Tutorial and PICtutor).

All registers and bits referred to by name should have those names and their values "equated" at the head of any program that uses them.

PAGES O TO 3

The subject of Pages in relation to PIC16x84 devices was well covered in the

PCFG3: PCFG0	AN7 ⁽¹⁾ RE2	ANG ⁽¹⁾ RE1	AN5 ⁽¹⁾ RE0	AN4 RA5	AN3 RA3	AN2 RA2	AN1 RA1	ANO RAO	VREF+	VREF-	CHAN A
0000	A	A	A	A	A	A	A	A	VDD	VSS	8/0
0001	A	A	Α	A	VREF+	A	A	A	RA3	Vss	7/1
0010	D	D	D	A	A	A	A	A	VDD	Vss	5/0
0011	D	D	D	A	VREF+	A	A	A	RA3	Vss	4/1
0100	D	D	D	D	A	D	A	A	VDD	VS8	3/0
0101	D	D	D	D	VREF+	D	A	A	RA3	Vss	2/1
011x	D	D	D	D	D	D	D	D	VDD	Vss	0/0
1000	A	A	A	A	VREF+	VREF-	A	A	RA3	RA2	6/2
1001	D	D	A	A	A	A	A	A	VDD	VSS	6/0
1010	D	D	A	A	VREF+	A	A	A	RA3	Vss	5/1
1011	D	D	A	A	VREF+	VREF-	A	A	RA3	RA2	4/2
1100	D	D	Ð	A	VREF+	VREF-	A	A	RA3	RA2	3/2
1101	D	D	D	D	VREF+	VREF-	A	A	RA3	RA2	2/2
1110	D	D	D	D	D	D	D	A	VDD	Vss	1/0
1111	D	D	D	D	VREF+	VREF-	D	A	RA3	RA2	1/2
U-0	U-(W- 0	U-0	R/W-0	R/	W-0	R/W-0	R/W-I	0	
ADFN	A	and the second	* 7		PCFG	3 PC	FG2	PCFG1	PCFG	0	
bit7									h	itO	

Note 1: These channels are not available on the 28-pin devices.

Fig.1. Selectable functions of the ADCON1 register.

PIC Tutorial and PICtutor texts, and a lot of readers expressed gratitude for the explanation.

Whereas the 'x84 had only PAGE0 and PAGE1 to be set in relation to the use of some Special Registers, the 'F87x devices have four Pages (Microchip actually calls them Banks - but we'll stick with Pages).

In the STATUS register, two bits (instead of one) control the Page selection, bits 6 and 5 respectively. Consequently, the shorthand technique used with the x84 of defining the instruction PAGE0 or PAGE1 to mean clearing or setting of STATUS bit 5 accordingly, is less easy to use when two STATUS bits are affected. Therefore, in common with the Microchip data sheet, the STATUS bits will be referred to by their allocated names, of RP1 (bit 6) and RP0 (bit 5).

Their settings are as follows:

PAGE	RP1	RP0
PAGE0	0	0
PAGEI	0	1
PAGE2	1	0
PAGE3	1	L

You will make life easier for yourself if you "equate" these two bits at the head of your programs:

RP0: .EQU 5 RP1: .EQU 6

The Register File Maps in DS-FIG.2-3 and DS-FIG.2-4 show which registers are in which Pages (Banks).

PIC PORTA AND PORTE

PORTA is a 6-bit bi-directional I/O (input/output) port whose bits RA0 to RA3 and RA5 can alternatively be used for analogue input.

PORTE is a 3-bit bi-directional I/O port whose bits RE0 to RE2 can alternatively be used for analogue input.

The first matter of interest that came to light when experimenting with the PIC16F877 was that its PORTA did not behave as expected when used as a normal I/O data port.

The PIC16F877 was originally loaded with the TUTTEST program that allows users of the PIC Tutorial and PICtutor development boards (which are for use with PIC16x84 devices) to initially test their system. PORTB behaved as expected, but not so PORTA.

Examination of the data sheet revealed that PORTA's default mode is not for digital data I/O, but for analogue data input.

The register which controls PORTA's digital or analogue use is ADCON1, whose settings options are shown in Fig.1 (taken from data sheet DS-FIG.11-2). This same register controls PORTE as well.

Register ADCON1 is at address \$9F, which is in PAGE1. Fig.1 shows that PORTA and PORTE are jointly set for analogue use when ADCON1's bits 0 to 3 are set to 0000 (the default condition on powerup). To jointly use PORTA and PORTE for digital purposes, the bits are set to 0111 (or 0110 since bit 0 can be 0 or 1 in this instance), i.e. ADCON1 has to be loaded with %xxxx111x – where x can be 0 or 1.

You will already be familiar with having to set the data direction (TRISx) registers depending on whether a port's bits are to be used for input or output. So, for both PORTA and PORTE to be fully set for digital output, TRISA and TRISE have to be cleared, which also has to be done in PAGE1.

Thus, an example of initialising your code to use PORTA and PORTE for digital output is:

BCF STATUS, RP1	; clear
	; PAGE2/3 bit
BSF STATUS,RP0	; set PAGE1 : bit
MOVLW %00000111	; all-digital
	; I/O code
MOVWF ADCON1	; load it
	; into
	; ADCON1
CLRF TRISA	; all PORTA
	; for output
CLRF TRISE	; all PORTE
	; for output
BCF STATUS, RP0	; reset to
	; PAGE0

Conversely, an example of initialising your code to use PORTA and PORTE for digital input is:

• •	
BCF STATUS, RP1	; clear
	; PAGE2/3 bit
BSF STATUS, RP0	; set PAGE1
	; bit
MOVLW %00000111	; all-digital
	; I/O code
MOVWF ADCON1	; load it into
	ADCON1
MOVLW %00111111	
MOVWF TRISA	; all PORTA
	; for input
MOVWF TRISE	: all PORTE
	; for input
BCF STATUS.RP0	; reset to
201 011100,1110	: PAGE0
	, -

Note that TRISE ignores bits 3 to 7.

A-TO-D PREPARATION

From Fig.1, you will recognise that ADCON1 is set to xxxx0000 when PORTA and PORTE are to be used for analogue input. You also need to set the two associated TRIS registers for input (in the same way as setting them for digital input).

There is another option that can be chosen for ADCON1 as well – its bit 7 (ADFM) controls how the 10-bit digital value of the converted analogue signal is stored in the PIC's registers dedicated to this purpose, ADRESH (\$1E – PAGE0) and ADRESL (\$9E – PAGE1).

There is the choice of whether the result is justified to the left or right in these bytes. Take the example of a conversion result of 1023 (the maximum for a 10-bit conversion). The binary value of 1023 is, as two bytes, 00000011 11111111, stored as 00000011 in ADRESH and 11111111 stored in ADRESL.

This is the case when the ADFM bit (bit 7) is set to 1 (DS-FIG.11-8). However, if the ADFM bit is set to 0, the result is formatted as 1111111 11000000. This choice can be useful in some post-processing operations where it can save the use of multiplying (rotation commands).

As used in the *Data Logger*, a right-justified result was required, and so ADFM was set to 1. Since this design uses all of PORTA and PORTE for analogue input, the ADCON1 register was set with the commands:

BCF STATUS,RP1	; clear
BSF STATUS,RP0	; PAGE2/3 bit ; set PAGE1
bbr bini objai v	; bit
MOVLW %1000000	; all-analogue
	; input code
MOVWF ADCON1	; load it into
	; ADCON1
MOVLW %00111111	;
MOVWF TRISA	; all PORTA
	; for input
MOVWF TRISE	; all PORTE
	; for input
BCF STATUS, RP0	; reset to
	; PAGE0
	; PAGE0

ADFM ANOMALY

When the first tests were made using the PIC16F877 samples provided by Microchip, the above commands were duly given in the *Data Logger* software that was being developed. To the author's consternation, the ADFM bit had no affect on the justification, which remained doggedly to the left when the result was displayed on an alphanumeric l.c.d.

It was eventually found that bit 5, not bit 7, was being used as the ADFM bit, and using that bit produced the required justification. What led the author to the conclusion that it should be bit 5 rather than bit 7 is that DS-Table 2-1 showed the power-on-reset condition for ADCON1 as being -0-0000, even though ADFM was shown as bit 7 on the left of the table.

Taking it up with Microchip, it transpired that the samples provided were Microchip's *early* engineering samples (version A) in which bit 5 was indeed the ADFM. They went on to say that production devices (version B) do have bit 7 as ADFM. This was proved when they sent version B samples to the author.

However, Microchip have asked us to advise anyone who has early engineering samples that this situation exists. Readers buying the chips from distributors or retailers should automatically be supplied with the production devices where bit 7 is the ADFM.

The fact remains, though, that the ADCON1 reset values given in DS-Table 2-1 of the 1998 DS30292A data sheet are incorrect and should read as 0---0000. Other tables in the same data sheet are similarly incorrect (3-1, 3-9, 3-12, 11-2).

A-TO-D CONVERSION

We have seen that ADCON1 is the register that sets PORTA and PORTE bits for analogue or digital use, and that ADRESH and ADRESL hold the 10-bit conversion value. A fourth register, ADCON0 (\$1F – PAGE0) is responsible for the A-to-D control modes, as itemised in DS-FIG.11-1, but summarised as:

Bits 7-6 Bits 5-3	Conversion clock rate Channel selection
Bit 2	Conversion status
Bit 1	Not used
Bit 0	A/D facility on/off

Prior to an A-to-D conversion being made, all bits have to be set appropriately. An example of the conversion sequence is shown in Listing 1, with entry point at ADCGET.

LISTING 1

ADCORT.	BCF STATUS.C	:	clear carry flag
ADCGG1.	MOVE CHAN, W	2	get channel number
	MOVWF STORE	;	temporarily store it
	RLF STORE, F		multiply it by 8
	RLF STORE, F	:	to set it into correct bits
	RLF STORE, F		suited to ADCON0
	MOVIW \$10000001	2	set clock Fosc/32 with A/D on
	IORWF STORE	;	OR this with stored value
	MOVWF ADCONO		move new value into ADCON0
	CLRF DELAY	;	set delay counter to 0
WAIT1:	DECFSZ DELAY, F	;	dec delay 256 times
	GOTO WAIT1	;	-
SAMPLE:	BCF PIR1, ADIF	7	clear A/D interrupt flag bit
	BSF ADCONO, GO	;	start A-D conversion
WAIT2:	DECFSZ DELAY, F	;	
	GOTO WAIT2	i	
WAIT3:	BTFSC ADCON0, GO	;	is conversion complete?
	GOTO WAIT3		no
GETVAL:	MOVF ADRESH, W	;	get conversion high byte
	MOVWF MEMHI	;	
	BCF STATUS, RP1	;	clear Page 2/3
	BSF STATUS, RPO	;	
	MOVF ADRESL, W	;	
	BCF STATUS, RPO	;	
	MOVWF MEMLO	i	store low byte in MEMLO
	RETURN	i	

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The channel number is held in CHAN and could be any value between 0 and 7. It has to be set into ADCON0 bits 5-3, consequently the first six commands are concerned with taking a copy of the CHAN value and rotating it to fill bits 5-3 of the temporary STORE. If the Channel number is 7, for example, CHAN holds %00000111, and the rotation into STORE produces %00111000.

The conversion clock rate is determined from DS-Table 11-1. The *Data Logger's* crystal clock runs at 3.2768MHz and the nearest value to this, as shown in DS-Table 11-1, is 5MHz and that a conversion factor of 32Tosc is required, which is set into ADCON0 with the bit 7-6 code of binary 10 (DS-FIG.11-1).

The converter is obviously required to be On, so bit 0 is set to 1.

The values for bits 7, 6 and 0 are thus set into W as %10000001, which is then ORed with the channel bits 5-3 held in the STORE (%00111000 in this example).

This composite value (%10111001) is then MOVed into ADCON0. Notice that bit 2, the Start bit (quaintly called the GO/DONE bit by Microchip!), is still at zero. The conversion does not start until this bit is set to 1. Also note that ADCON0 bit 1 has no function.

SOPHISTICATION

At this point it is possible to get rather sophisticated – but we're not going to!

The sophisticated way would be to use interrupt routines and precise timings, and a lengthy section in the data sheet discusses the options. They include calculation of the time taken to acquire the sample (which is relative to temperature, line impedance and capacitance values), and then to wait for an interrupt to occur on completion of the conversion.

Since the *Data Logger* is only required to take samples at the maximum batch rate of eight (all eight channels) every 0.5 seconds, time is not at a premium and so two simple delay loops are used, shown in Listing 1 as WAIT1 and WAIT2.

When WAIT1 has ended, the conversion is started by first clearing the A/D interrupt flag bit (bit 6 of register PIR1 – \$8C, PAGE0), and then setting ADCON0 bit 2 (the GO bit).

The delay in WAIT2 then occurs, after which the GO bit is polled in WAIT3 until it is read as 0, signifying that the conversion is complete.

The values of the 2-byte conversion can now be read. The high byte (MSB) is held in ADRESH, which is in PAGE0 and so can be immediately read and stored in the user's own nominated location, in this case MEMHI. The low byte (LSB), however, is held in ADRESL, which is in PAGE1, which has to be set before the byte can be read. After which a reset to PAGE0 occurs and the byte is stored in MEMLO, so ending the conversion sequence.

As used in the Data Logger, the sequence is somewhat more padded than shown here since it is written to read a conversion for each of eight channels in turn, and then to store the 2-byte result in a chain of eight serial EEPROM memories, and to decimalise the result for showing on an alphanumeric l.c.d. screen. Apart from the ADFM problem mentioned earlier, no surprises occurred when using the A/D facilities on all eight PIC16F877 channels, and the program worked first time. Which is more than can be said for storing the result in the serial EEPROM memories, as we shall reveal in a moment, plus the solution, of course!

ADC REFERENCE VOLTAGES

Referring again to Fig.1, you will see that pins RA2 and RA3 can be used as the pins on which external ADC reference voltages can be set, depending on the code set into ADCON1 bits 0 to 3. The columns 10 and 11 show how the reference voltage selection takes effect.

Although not discussed in the Data Logger text, the p.c.b. for that unit has been designed so that preset potentiometers can be inserted on it to set desired reference voltages on pins RA2 and RA3 for other applications. (The Data Logger itself does not offer this option through its software – readers must write their own software to suit their personal needs in this respect.)

The circuit diagram and p.c.b. assembly details for the insertions are shown in Fig.2 and Fig.3. Note the additional two link wires that are needed in order to complete the circuit between the wipers of the presets and their respective RA2/RA3 pins.

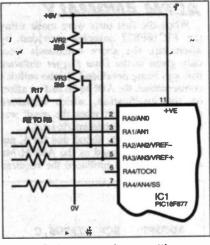
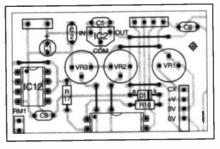


Fig.2. Reference voltage setting presets added to Data Logger circuit diagram Fig.1, and (below) Fig.3, their positioning on the Data Logger p.c.b.



Preset VR2 allows a reference voltage variation between approximately 2.5V and 5V on RA2, whilst VR3 allows variation between 2.5V and 0V on RA3. The use of a narrower range of reference voltage (which is normally 0V to 5V i.e. V_{ss} to V_{dd}) has the effect of providing amplification to the analogue input signal being converted.

By variously selecting different values for ADCON1 bits 0 to 3, different reference voltage combinations could be chosen for individual input channels.

It should be noted that when either RA2 or RA3 is selected as a reference voltage input, the selected pin cannot be used as an A-to-D input channel.

SERIAL MEMORY ACCESS

Once you get to know them, Microchip's 24LC256 serial EEPROM memories are really rather super little chips! It was their data sheet (DS21203D, 1998) that the author had difficulty with.

Attempts were made to write the routine which would store data at consecutive addresses in a 24LC256. Somehow, the logic of the data sheet's description and illustration eluded the author (it's not often he fails in such situations, but he failed this time!)

Running out of patience, he resorted to seeing if programs were available amongst Microchip's Applications Notes (on their CD-ROM and web-site).

There were several options available, of which the programs 2WDPOLL.ASM and 2WSEQR.ASM were selected as appearing to have the best options available (even though they were not written for PIC16F87x devices) and disk copies were made.

Being Microchip's own programs, they were naturally written in MPASM, whereas the author has a great preference for working in the TASM dialect. Consequently, the MPASM source codes were processed by the author's *PIC Toolkit Mk2* and converted to a TASM format.

Various modifications were then made to the programs to suit them to the *Data Logger's* needs, whereupon success was achieved! Data could now be written to the serial EEPROMs and, equally importantly, could be read back as well.

Those of you studying the Data Logger source code will find entry into the serial memory Write routine at label WRBYTE, and entry to the Read routine at READ. The routines are far too lengthy to list or describe here.

As a further plug for Microchip's Application Notes, they are well worth examining for all sorts of information and ideas, plus an awful lot of source code listings as well. Do have a good browse through them.

SERIAL OUTPUT

With the ability to write/read serial data assured, the next stage to be solved was that of instructing the PIC16F877 to output the data as a serial stream at a known baud rate. This turned out to be very straightforward.

The PIC16F87x family have a built-in structure which allows serial output through a dedicated pin, RC6, and for the rate of output to be selected according to the needs of the destination for that data (e.g. a PC) and in relation to the PIC's crystal controlled clock rate.

Additionally, these PICs can be instructed on such matters as synchronous or asynchronous transmission, parity, stop bits and byte size.

The 'F87x data sheet, once you have studied it for details of serial interfacing, is actually quite helpful. It was decided to use asynchronous serial transmission from the PIC to the PC. This requires only two lines to be connected between the two systems, one for data and one for the ground (0V).

It was further decided that the rate of transmission should be at the maximum likely to be found on the majority of readers' PCs, 9600 baud, with no parity, one stop bit and with 8-bit transmission.

The registers associated with serial transmission are:

- SPBRG (\$99 PAGE1) Baud rate generator
- TXSTA (\$98 PAGE1) Transmit status and control
- RCXTA (\$18 PAGE0) Receive status and control
- PIE1 (\$8C PAGE0) Peripheral interrupt control

Data sheet tables are provided for establishing the value to be set into register SPBRG in relation to several examples of clock rate: DS-Table 10-4 and DS-Table 10-5. Since these do not quote a value for a 3-2748MHz clock, as used in the Data Logger, the formula quoted in the data sheet's Example 10-1 was more useful on this occasion.

TIMING FORMULAE

In fact, two formulae can be derived from Example 10-1, depending on whether the PIC is to divide the clock rate by 64 or 16. The idea is to select a division rate to produce as large an SPBRG value as possible, up to a maximum of 255 (it's an 8-bit register).

In Listing 2 is shown a Basic program derived from the formula in Example 10-1. It calculates the SPBRG value in relation to the baud rate required, the clock rate available, and the two division factors of 64 and 16. Bit BRGH (bit 2) of the TXSTA register has to be set low if 64 is the divider, and high for a divider of 16.

Running the program in Listing 2 produces SPBRG answers of 4.333333(BRGH = 0) and 20.3333 (BRGH = 1). Since the latter is the higher, the BRGH bit has to be set to 1.

The bit which tells the PIC whether it is required to transmit synchronously or asynchronously is TXSTA register bit 4 (SYNC). DS-Table 10-1 shows that for asynchronous transmission the SYNC bit is set to 0.

Prior to commencing transmission, the basic transmission parameters are set as in Listing 3, with entry at label SETBAUD.

Since some of the affected registers are in PAGE1, this is set first, as in command lines 1 and 2. Then the integer of the chosen SPBRG value (20) is stored into the SPBRG register, register TXSTA is conditioned for SYNC = 0 and BRGH = 1, TRISC bit 6 is cleared for pin RC6 to be used as an output, and the transmission interrupt bit (PIE1,4) is cleared (interrupt not required). A reset to PAGE0 is made and the SPEN bit (bit 7) of register RCSTA is set.

This sequence is in accordance with the first three steps listed in the data sheet at Section 10.2.1. Step 4 (9-bit transmission) is not required. Step 5 (enable transmission) requires a reset to PAGE1, and the transmission bit (bit 5) of TXSTA is set,

```
LISTING 2

'calculate SPBRG for required baud rate

BAUD = 9600 ; replace this value with your own

FOSC = 3276800 ; replace this value with your own

X = (FOSC / (64 * BAUD)) - 1

Y = (FOSC / (16 * BAUD)) - 1

PRINT *BAUD =*; BAUD, *CLOCK =*; FOSC

PRINT *SPBRG at div 64 = *; X; *bit BRGH = 0*

PRINT *SPBRG at div 16 = *; Y; *bit BRGH = 1*
```

followed by a reset to PAGE0. The scene is now ready for data to be transmitted from PIC pin RC6, and the SETBAUD routine is exited.

SERIAL FORMAT

In the Data Logger the SETBAUD sequence is performed when power is first switched on and it remains in a state of readiness to send data to the PC until power is switched off again.

Consequently, having read the 2-byte sample data stored in the selected serial memory, it can be sent to the computer through the routine commencing at label SENDPC, shown in Listing 4.

However, before it can be sent, a slight readjustment of the data format is needed. The PC register which receives the serial data shifts it left by one place (multiplying it by two). This means that the PIC cannot send a data byte whose value is greater than 127. If it were to, bit 7 would be "lost" at the PC end.

LISTING 3

To over come this, the least significant byte of recalled data (held in MEMLO, as discussed earlier) has to be limited to a value of less than 128 and its eighth bit (bit 7) combined with the most significant byte (MEMHI), whose recalled value is never greater than three.

On entry into routine SENDPC in Listing 4, this rearrangement takes place in the first four lines. An example of what happens is as follows:

Suppose MEMHI holds a value of %00000011 and MEMLO holds %11111111. MEMLO is first rotated left into the W register (leaving MEMLO) itself untouched) so that its bit 7 "drops" into the Carry register. MEMHI is now rotated left and in doing so the Carry bit is rotated into it from the right. MEMLO's bit 7 can now be cleared, limiting MEMLO's value to less than 128.

The result is that MEMHI now holds %00000111 and MEMLO holds %01111111 and it is these values that are

; clear PAGE2/3 SETBAUD: BCF RP1 ; set PAGE1 BSF RPO ; BRG val for 9600 baud MOVLW 20 ; from 3.2768MHz, brgh=1 ; put into SPBRG reg MOVWF SPBRG MOVLW \$00000100 ; sync=0 (bit 4), brgh=1 ; (bit 2), clear other bits ; put into TXSTA MOVWF TXSTA ; set RC6 as output BCF TRISC, 6 ; clear interrupt bit (TXIE) BCF PIE1,4 set back to PAGE0 BCF RPO MOVLW \$10000000; set SPEN bit of RCSTA reg MOVWF RCSTA î ; set for PAGE1 BSF RPO BSF TXSTA, 5 ; enable transmission (TXEN) ; set for PAGE0 BCF RPO RETURN 2

LISTING 4		
SENDPC:	BCF STATUS, C	; clear Carry flag
	RLF MEMLO, W	; rotate MEMLO left into W,
		; bit 7 enters Carry
	RLF MEMHI, F	; rotate MEMHI left, Carry
		; enters bit 0
	BCF MEMLO, 7	; clear MEMLO bit 7
	MOVE MEMLO, W	; get MEMLO
	CALL SERIAL1	; send it to PC
	MOVE MEMHI, W	; get MEMHI
	CALL SERIAL1	; send it to PC
	RETURN	; end of routine

LISTING 5	
SERIALI: BTFSS PIF Goto Seri Nop Movwf TXF	; (showing TXREG empty) (AL1 ; ;
RETURN	; end of routine

transmitted to the PC via the called routine whose label is SERIAL1 (see Listing 5). How they are restored to their "true" values will be discussed shortly. Once that byte pair has been transmitted, the next pair can be read from one of the serial memories, and again transmitted via the SENDPC routine.

REGISTERS TXREG AND TSR

The routine that allows each byte to be transmitted, SERIAL1, has only three active commands. As soon as the PIC's serial transmission register (TXREG) is loaded with a byte of data, transmission is started by the PIC's own internal facilities.

To summarise the PIC data sheet, the heart of the transmitter is the Transmit Serial Register (TSR), which obtains its data from the read/write transmit buffer register TXREG. The user's software loads TXREG with the data byte to be transmitted, where it stays until the Stop bit from the previously loaded data has been sent.

As soon as the Stop bit has been transmitted, the TSR is automatically loaded with the new data from TXREG. Once this has occurred, TXREG is now empty and a flag bit is set in register PIR1 – its bit 4, named TXIF. When this flag is set, the software can load the next byte of data into TXREG, an action which clears the TXIF bit.

The routine entered at SERIAL1 first checks the status of PIR1 bit 4. If the bit is low, a previous transmission is still taking place. The routine loops continuously checking bit 4 until it is set. Prior to entry to SERIAL1, the W register was loaded with the data byte (as in Listing 4). When PIR1 bit 4 is found to be set, the W data is loaded into TXREG, to be automatically transmitted out by the PIC.

As soon as TXREG has been loaded, the SERIAL1 routine ends, and software can get the next byte, or do whatever it is told to do, such as end the full transmission sequence because all the bytes have been sent.

IRREGULAR TRANSMISSION

The Data Logger sends its serial data to the PC in consecutive blocks of data. However, in a another design on which the author is working, the need is for serial data to be output to the PC at irregular intervals, two bytes a time.

Whereas for consecutive data blocks, setting the Baud Rate factors at the head of the program proved satisfactory, in the random transmission design, the author found it necessary to send a byte of zero prior to each double-byte being sent.

What subtlety of difference between the two programs makes this action necessary

has not been established, although it is believed that it might be to do with PORTC being read between data words in the second design. PORTC is that which has to be used for serial input/output, and it seems possible that reading it (for switch status) between data words might affect the serial registers. It has yet to be more fully investigated.

PC RECEPTION

A program for use on a PC to receive serial data from a PIC is not included in Microchip's Applications Notes software listings library. A browse of the Internet for suitable software did not reveal anything that the author felt was suitable either. Consequently, he wrote his own routines specifically to import doublebyte serial data from the *Data Logger*.

The program is written in a mixture of Basic (suited to running from QBasic or QuickBASIC) and machine code. The Basic program loads and calls the machine code, which does the actual serial data importing, and then, formats the data for output to disk, in several different file styles, as discussed in the Data Logger text.

There are, in fact, several ways in which machine code can be accessed from Basic. The example shown in Listing 6 is the one on which the author has standardised for several years.

LISTING 6

```
LOADDATACODE:

OPEN FILE$ FOR BINARY AS #1

B = LOF(1)

SERIAL$ = INPUT$(B, #1)

CLOSE 1

A = 256

MA = VARSEG(MA$(0))

IF MA < 0 THEN MA = MA + 655361

MB = VARPTR(SERIAL$)

MC = PEEK(MB + 3) * A + PEEK(MB +

MD = INT(MA / 256)

MB = MA - (MD * 256)

POKE MC + 5, MB

POKE MC + 6, MD

RETURN
```

The majority of the Basic routines are self-explanatory to anyone who knows QBasic or QuickBASIC and will not be discussed here. However, the routines which access the machine code, and the machine code itself, deserve a bit of explanation.

On running the Basic program, integer variable array MA%(x) is first DIMmed for the maximum number of separate values (32766) as are required for access by

the machine code. All the values are in consecutive order within the PC's memory and their exact locations are accurately predictable.

Referring to Listing 6, the machine code whose file name is held in FILE\$ is then loaded as binary data into string variable SERIAL\$, at label LOADDATA-CODE. The address at which MA%(0) resides is then obtained, and POKED into the machine code at two predetermined consecutive addresses.

Note that in line 10 (MC =) attempting to multiply by 256 as a "live" value would result in an "overflow" error and so variable A is used, having been allocated that value in line 6. Also, because integer variables whose true values are greater than 32767 are returned as negative numbers, line 8 intercepts them if they occur, restoring them to their correct positive value. Additionally note that variable MB is used for two different purposes.

Once the routine in Listing 6 has been run, the machine code is accessed through the command:

CALL ABSOLUTE(SADD(SERIAL\$))

When the machine code routine has ended, the program reverts to Basic.

It is important to note that there is a slight difference between using QBasic and QuickBASIC in that QuickBASIC has to be loaded with the command QB/L, which automatically loads an additional library program (part of the QuickBASIC suite) which allows machine code to be run. QBasic does not require the additional library program and is simply loaded in the usual way with the command QB.

The machine code routine is shown in Listing 7. It is based on two PC interrupt calls to INT 14H, whose functions are documented in the PC Sourcebook – a publication which itemises the principal registers and interrupt calls for base-standard PCs (seemingly compatible with

```
' load machine code subroutine
' open file named in FILE$
' get length of file
' load file into SERIAL$
' close file
' define A as value 256
' get segment address of MA%(0)
' correct for negative value
' get pointer to SERIAL$
+ 2) ' get address of SERIAL$
' get MSB
' get LSB
' poke LSB into machine code
' poke MSB into machine code
' end of subroutine
```

processors from the 8086 upwards, including Pentiums).

On entry to the machine code, the address of MA%(0) is acquired from the value held at label SETSEGMENT, as previously POKED there from Basic.

Transmission format data passed from Basic (held in MA%(0)) is then read and loaded into the AX register. The data details the baud rate, parity, stop bit, and bit count configuration (assembled from the details in Table 1). The Basic software also passes details on which COM port (COM1 or COM2) is to be read (held in MA%(1)). This is loaded into the BX register to be then loaded into register DX, whereupon interrupt INT 14H is called, which passes the data to the PC's serial operating system.

Routine WAITDATASET is now called, in which INT 14H is polled until register AX returns with a value less than 256, signifying that a byte of serial data has been received and that it is now available in the low byte (AL) of register AX.

This data byte is an inversion of the byte presented to the PIC for transmission and is shifted left by one place. The inversion is corrected by subtracting the byte from 255.

Returning to the calling point, the byte is stored in register CL as the least significant byte of the double-byte required. WAITDATASET is again called, where the second of the two bytes needed is similarly acquired, and then stored in register CH. Note that CH and CL are the high and low bytes, respectively, of register CX.

Now a mixture of rotation and ANDing reconstitutes the double-byte data to its original value as stored in the *Data Logger's* serial memory. The value is then stored in the pre-determined location in the PC's memory relative to the integer array position back in Basic (from MA%(0) onwards).

The value is also checked to see if it is the end marker value transmitted by the PIC when a serial memory has been fully read. If it is not that value (two bytes each of value 127), the next double byte of transmitted data is acquired, and stored at the next consecutive PC memory location.

Back in Basic, the locations in which the machine code was stored (MA%(0))onwards) is then saved as a block whose length is the count value reached at the end of the machine code sequence.

KEYBOARD INTER-RUPT

A second serial input machine code routine is included with the *Data Logger* software. This includes the ability to press "Q" to quit from the machine code without waiting for the data transfer to be complete. This routine makes use of the INT 16H keyboard access interrupt to read which keys are pressed and to return a value accordingly.

LISTING 7	
JMP STARTALL	; entry point from Basic
SETSEGMENT: MOV DS,01111 RET	; value changed from Basic : for VARSEG(MA%(0))
STARTALL: PUSH DS CALL SETSEGMENT MOV SI,0 MOV AX, [SI] MOV BX, [SI+2] MOV DX, BX INT 14H	<pre>; store current data segment ; get MA%(0) segment address ; set source address count to zero ; get baud rate etc from MA%(0) ; get COM port number from MA%(1) ; load config data via ; interrupt call INT 14H</pre>
AND [SI+1][B],1 AND [SI+1][B],127 INC SI INC SI CMP [SI-2][B],255 JNE GETDATA CMP [SI-1][B],63 JNE GETDATA MOV [SI][W],0 MOV CX,SI ROR CX,1 MOV SI,0	<pre>; temporarily store it ; get second data byte ; rearrange bytes to orig format ; and store them ; rotate byte right ; rotate byte right ; clear bit 7 ; double-increment store address ; ; is this byte = 127? ; no, so get next sample</pre>
MOV AH, 2 MOV AL, 0 MOV DX, BX	; get data byte ; set interrupt factors ; ;
CMP AX,255 JA WAITDATASET MOV CL,255	<pre>; call interrupt INT 14H ; is returned value =< 255? ; no, repeat INT call ; yes, invert received value ; (subtract from 255) ; return to calling routine</pre>

A point of interest, however, is that although three of the author's computers would respond to the INT 16H call, a fourth (a "custom-built" machine used at EPE HQ) would not. This is puzzling since it was believed that the basic interrupt calls on PCs are upwards compatible

TABLE 1: INT 14H Com port parameter byte

7	6	5	4	3	2	1	0	Description	Allowable Values
~	~	•						Baud rate	000=110 baud 001=150 010=30 011=600 100=1200 (default) 101=2400 110=4800 111=9600
			~	~				Parity	00=No parity 01=Odd parity 10=No parity 11=Even parity
					V			Stop bits	0=1 stop bit, 1=2 stop bits
						•	~	Word length	10=7 bits 11=8 bits

- seemingly not in some instances. Can anyone throw light on this?

PIC EEPROM DATA MEMORY

The routines for writing to and reading from the PIC16F87x family's internal EEPROM data memory are worth highlighting. You will no doubt be familiar with the same routines as used with the PIC16x84 devices, and the 'F87x routines are similar, but not exactly the same since the 'F87x devices use different register locations to those used by the 'x84, as shown in Table 2.

The EEPROM data memory Write/Read routines are shown in full in Listing 7 and Listing 8. More information on them is on Microchip data sheet DS30292A page 43.

As with the 'x84 programming examples given in the *PIC Tutorial* and *PICtutor* texts (to which readers are also referred – either text source will do), the EEPROM Write routine at label SETPRM is entered with W holding the EEPROM byte address at which data is to be stored.

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The data to be stored is held in STORE1. Whereas the 'x84 routine was actioned in PAGE0 and PAGE1, the 'F87x routine is actioned in PAGE0, PAGE2 and PAGE3, hence the various RP0 and RP1 paging instructions.

It is worth noting that the 'F87x data sheet gives a programming example (page 43) in which a SLEEP command and interrupt are used to determine when the EEPROM Write function has been completed, rather than polling EECON1 bit 4. However, when the author tried the SLEEP method it failed to work. It is probable that another undocumented action has to be taken in addition to those shown, but this has not been investigated.

The EEPROM Read routine in Listing 8 is entered at label PRMGET with W holding the EEPROM byte address to be read. It is exited with W holding the data read from the EEPROM.

ASSEMBLER SOFTWARE

Readers who are interested in learning how to program in machine code suited to the 8086 and above processors are recommended to obtain the *excellent* shareware *A86/D86 Assembler/Disassembler* from the Public Domain Shareware Library (PDSL) whose details are given later.

The author has been using it for many years and for many PC-controlled *EPE* projects, including the *Virtual Scope* and *PIC Toolkit Mk1*. It is nearly as easy to learn as PIC programming, but has far more commands available. A useful associated book is Intel's 8086/8088 User's Manual.

OBTAINING BASIC

Until fairly recently, by far the vast majority of PCs will have been supplied with either QBasic or QuickBASIC installed. However, *EPE* does sometimes get questions from readers who do not have either and ask where they can obtain one or the other.

So far as is known, neither of the programs is actually supplied with PCs any longer – Microsoft wishing, perhaps, that users should acquire the more advanced VisualBASIC. The once-popular GW-Basic has long since been outdated and is not compatible with regard to using it with the QB machine code routines illustrated here.

LISTING 7	7	
SETPRM:	BSF STATUS, RP1 BCF STATUS, RP0	; set for PAGE2
	MOVWF EEADR	; copy W into EEADR to set ; EEPROM address
	BCF STATUS, RP1	; set for PAGE0
	MOVF STORE1, W	; get data value from STORE1
	BSF STATUS, RP1	; and hold in W ; set for PAGE2
	MOVWF EEDATA	; copy W into EEPROM data
		; byte register
	BSF STATUS, RPO BCF EECON1 EEPGD	; set for PAGE3 ; point to Data memory
	BSF EECON1, WREN	
MANUAL :	MOVLW \$55 MOVWF EECON2 MOVLW \$AA MOVWF EECON2 BSF EECON1,WR BCF STATUS,RP1 BCF STATUS,RP0	; these lines cause the action ; required by the EEPROM to ; store the data in EEDATA ; at the address held by EEADR. ; Set `perform write' flag ; set for PAGE0 ;
CHKWRT:	BTFSS PIR2,EEIF GOTO CHKWRT BCF PIR2,EEIF RETURN	; wait till bit 4 of PIR2 set ; ; clear bit 4 of PIR2 ;

LISTING 8	3	
PRMGET:	BSF STATUS, RP1 BCF STATUS, RP0 MOVWF EEADR BSF STATUS, RP0 BCF EECON1, EEPGD BSF EECON1, RD BCF STATUS, RP0 MOVF EEDATA, W BCF STATUS, RP1 RETURN	; set for PAGE2 ; ; copy W into EEADR to set ; EEPROM address ; set for PAGE3 ; point to data memory ; enable read flag ; set for PAGE2 ; read EEPROM data now in ; EEDATA into W ; set for PAGE0 ;

Readers who would like to get one or other of the QB versions are recommended to obtain it through the Internet. There are quite a lot of sites once you start looking. A general "search" call with the keyword QBASIC should begin to unravel the web of leads.

.

SOURCES

The full data sheets for the Microchip devices used in the Data Logger are available from Microchip: PIC16F87x family, code DS30292A, serial EEPROM memories: DS21203D (24AA256), DS21191C (24AA128), DS21189B (24AA64), DS21162C (24AA32). There are three ways to obtain them from Microchip: as downloads from their web site, from their fully inclusive CD-ROM (all products data and applications info), or as individual booklets.

Microchip Technology Ltd., Microchip House, 505 Eskdale Road, Winnersh Triangle, Woking, Berks RG41 5TU. Tel: 0118 921 5800. Fax: 0118 921 5835.

E-mail: techdesk@arizona.co.uk. Web: http://www.microchip.com.

Public Domain Shareware Library: PDSL, Dept EPE, Winscombe House, Beacon Road, Crowborough, East Sussex TN16 1UL. Tel: 01892 663298. Fax: 01892 667473.

Intel data books are available from Electromail, Tel: 01536 204555.

Microsoft Press publication, ISBN 1-55615-321-X.

TABLE 2: Comparison of EEPROM data memory registers

	PIC16F87x		PIC1	6x84 Equivale	nt
Register	Address	Page	Register	Address	Page
PIR2	\$0D	0	None	-	-
EEDATA	\$10C	2	EEDATA	\$08	0
EEADR	\$10D	2	EEADR	\$09	0
EECON1	\$18C	3	EECON1	\$88	1
EECON2	\$18D	3	EECON2	\$89	1
Register	Bit	Name/No	Register	Bit	Name/No
PIR2	EEIF	4	EECON1	EEIF	4
EECON1	RD	0	EECON1	RD	0
EECON1	WR	1	EECON1	WR	1
EECON1	WREN	2	EECON1	WREN	2
EECON1	EEPGD	7	None	-	-

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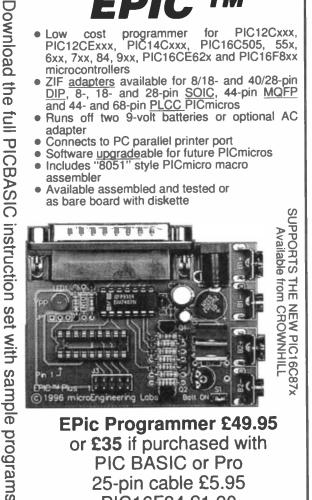
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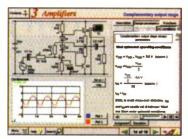
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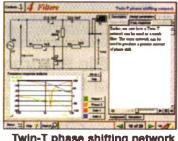
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-4 143 > 34

Virtual laboratory - sinusoids

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Circuit technology screen

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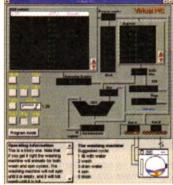
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A catalogue of commonly used IC schematics taken from the 74xx and 40xx series. Also includes photographs of common digital integrated circuits and circuit technology.

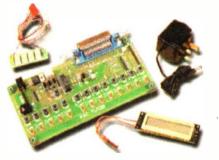
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Interested in programming PIC microcontrollers? Learn with PICtutor by John Becker



The Virtual PIC



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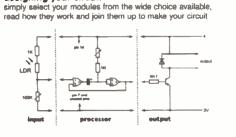
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Developed from Max Horsey's Teach-In series A Guide to Modular Circuit Design (EPE Nov '95 to Aug '96). This highly acclaimed series presented a range of tried and tested analogue and digital circuit modules, together with the knowledge to use and interface them. Thus allowing anyone with a basic understanding of circuit symbols to design and build their own projects.

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A Web Browser is required for Modular Circuit Design - one is provided on the EPE CD-ROM No. 1 (see below) but most modern computers are supplied with one.

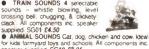
Minimum system requirements for these CD-ROMs: PC with 486/33MHz, VGA+256 colours, CD-ROM drive, 8MB RAM, 8MB hard disk space. Windows 3.1/95/98/NT, mouse, sound card (not required for *PlCtutor* or *Modular Circuit Design*).

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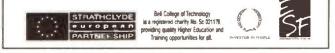
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John Becker addresses some of the general points readers have related. Have you anything Interesting to say? Drop us a line!

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★ LETTER OF THE MONTH ★

PICK OF THE PICS Dear EPE,

May I begin by thanking you for the PIC Tutorial series. After using it for only three months I have managed to connect a 16C84 to a 6402 UART and have effected MIDI program changes and made a sustain/soft pedal. I was able to do this latter item by "lifting" the two switch routines of TUT11 and adding bits of other Tutorials, adapting them to do what I want.

I suspect I am not on my own when I say that working through the Tutorials "parrotfashion" is boring. Many years ago I learned Basic by pinching routines out of one program and lifting bits (no pun intended) of another and I am learning PIC assembly language the same way, referring back to your Tutorials when I need to sort out exactly how a particular chunk of code works, usually in order to make it work a different way. With Basic it took me about three years before I was any good, but was able to adapt many games along the way. I'm sure PICs will take just as long.

The advent of the new PIC16F87x series is exciting as, with more memory and more pins, these devices seem even better for inexperienced would-be programmers such as myself. With that in mind, I am writing to ask if there is any chance of a PIC Tutorial Mk2. To provide a set of routines that can be mixed and matched to produce the result the aspiring programmer wants but is not able to do on their own without considerable help, which you provide in copious notes, as with the original series.

To date all of my needs are to do with MIDI. I would like to have routines which will: show me how to write to the 16F877 USART and how to configure it to add MIDI start/stop bits; how to write a first-in-first-out buffer with storage for 20 to 40 8-bit bytes and write to the USART or an 8-bit port for external serialising (preferably a "load it into the buffer and forget it" arrangement which leaves the processor free for number crunching elsewhere); how to read the USART: how to write a routine to clock MIDI data out of an ordinary 16C84 pin (a la MIDI Sustain Pedal, Feb '99), I can't sort out how it works; a neat routine calling data from STORE1, STORE2, STORE3 etc. would be much better for hackers like me - these routines should enable me to make a MIDI mixer, with the PIC running at 20MHz I should be able to do a lot of manipulation of input data bytes and exotic features such as MIDI mapping, layering and velocity changing should all become possible; how to read the ADCs, allowing me to add pitch blend and variable modulation.

When searching the Internet, countless programs can be found for the 16C5x processor, usually in MPASM, whilst your PIC Toolkit Mk2 will solve this latter problem for me some help with conversion between PICs would be welcome. Similarly, PIC BASIC compilers are now available as freebies and the one I've got (but cannot try until I build Toolkit Mk2) looks useful. I would welcome a discussion article, together with a few demo programs (to pinch the bits I like).

Doubtless other readers will have their own needs and ideas; can they be encouraged to submit them with the promise of the most popular being dealt with in a Mk2 Tutorial along the lines requested. Whilst I have listed a great number of wants, I don't think I'm on my own and suspect I speak for many who are strug-gling to write their own stuff and using your Tutorials as a bible and guide. Many thanks for a great mag, I don't care what you call it, just keep up the quality. Derek Johnson, via the Net

You are indeed using the Tutorials as I expected - selecting routines and modifying them to meet your own needs. One of the purposes of us publishing any project software, whether on page or on disk, is to let others share the code and select sections or just gain ideas for other designs. This too, of course, is what people do with the electronic circuits as well - select and modify for other functions. No-one is cheating by doing so, it's using common sense and cuts down on the wasted effort of "re-inventing wheels" each time!

Tutorial-wise, my PIC16F87x Mini Tutorial is in this current issue, of course. In it I highlight a fair number of points about using the new PIC family, with particular emphasis on the PIC16F877 (as used in the Data Logger of Aug/Sept '99). The article does not attempt to cover all aspects of using 'F87x devices, but gives practical advice based on my experience with them so far - the implementation of some functions proved to problematic, the data sheet not being the easiest of documents to follow in some instances. Hopefully you will be saved such puzzlement as I encountered from time to time!

Regarding MIDI routines, I can't offer help since I am not a MIDI user, but other readers may be able to, and I also plant the suggestion before Robert Penfold, who does know about MIDI (well, Robert?).

Nor am I familiar with PIC BASIC compilers - anyone care to comment on them?

courteous manner in which they have handled my correspondence.

What a change these days to find a magazine run by such a caring crew.

R. Tipping, Batley, W. Yorks

Thank you Mr Tipping. We do care and try to let it show.

VIRUS FEEDBACK

Dear EPE.

In Readout Aug '99, Pete Kelly of Australia said that the plural of virus is viruses or viri. Well, according to some VERY good Virii Creators it is Virii and not Viri/Viruses. Since virii exist because of them we should use the terminology they use. By the way, one viril creator created a virus that infects all .COM files in the current directory. The virii was only 27 bytes long!

Thanks for the great mag and keep up the good work. If you publish my letter please correct my spelling (except for Virii, Viri, Viruses). Ian Galpin, via the Net

You've got a point, lan, but I for one have absolutely no respect for those malicious vandals who do write viruses and shall not take any lessons in English or Latin spelling from them. They would appear to have brains that can achieve a working program, but it would be far better to use their brains in a constructive manner, not a destructive one. It is impossible to conceive what goes through their minds when they create a virus, some of which have caused untold distress to many, including patients relying for their survival on hospital computers.

There is no way in which we can condone this behaviour and should we somehow learn the name and address of a virus creator, we would have no hesitation in reporting it to the Police. The severest of penalties would not be too great for such criminal idiots.

As requested, we have corrected your spelling in non-viral matters (do we gather from your Email address that you come from Zambia?). Thanks for contacting us, my anger is not with you, but with virus writers.

DESCALING REVISITED Dear EPE.

Living in a hard water area where the lime factor in the water had got to the point of being ridiculous, I was prompted to search out some means of getting rid of or reducing the amount of

I contacted a company selling a commercial water descaler, but the price quoted was disappointing. I then remembered reading about a water descaler in one of my back issues of EPE (Oct '97) that worked the same way.

I obtained a kit from Magenta Electronics, finding it fairly easy to build and put together. The only difficult part being the winding of the coil around the copper pipe in a confined space. I lost a lot of skin from my knuckles but succeeded in completing the task.

I am glad to report that the device is working satisfactorily. My wife is especially delighted as to its effect, the periods between descaling are longer and the water feels softer. Magenta Electronics is an excellent company to deal with. A.J. Lewis, Harlow, Essex

A couple of years back we had quite a lot of correspondence in Readout concerning water descalers. Good to know that between us, Mark Stuart, Magenta and EPE have provided you with a working product. It is one of those areas in which no real scientific evidence appears to exist.

PhizzyBLY CARING

Dear EPE,

I would like to place on record my deep appreciation of the time and effort Alan Winstanley and "Max" Maxfield have put in trying to help me find the solution to a PhizzyBot problem I have. Also to your Editor and his secretary for the efficient and

PIC TOOLKIT V2.3

Last month in Readout, the introduction of PIC Toolkit Mk2 software version V2.2 was announced. Since then I have been extensively using the program in connection with probably my most complex PIC software program to-date, a Tide Machine (based on a PIC16F873), scheduled for Spring next year.

As a result, I have also made a few more minor improvements to Toolkit Mk2, making it even easier to use. The latest files, V2.3, were released onto our Toolkit Mk2 disk and the EPE FTP site on 6 Aug '99. Just follow the instructions in text files that accompany the main software. Version V2.3 incorporates all previous changes. The changes are stated near the head of each .BAS program listing, which can be read from DOS EDIT.

As was said last month, I shall no doubt make further improvements to Toolkit Mk2's software in the light of experience – make suggestions if you wish. (But please remember that I write such things only in my spare time!)

EDITING EDIT

Dear EPE,

I have been actively interested in radio/electronics for many years, and when microcontrollers became available, was very interested in these because of their flexibility. But, because of the prohibitive cost of computers to program them, I never used them at that time. Recently, though, I acquired an old 386SX computer running DR DOS V.6 plus Windows 3.1.

I have always read your magazine and always found something of interest in it, but when you published your *PIC Tutorial* series and *Toolkit Mk2*, I couldn't help myself, I had to have a go and get to grips with these marvellous little things. I constructed the hardware and obtained your software on disk and loaded it up on the 386. Hey presto it all worked straight away.

I followed your instructions in the use of *Toolkit Mk2* and programmed my first PIC using your test program – magic! Then on pressing the E key to enter the DOS Editor, the program leaves the main menu, pauses, then immediately returns to the main menu. During the short pause, the following appears on screen:

C:\PIC>tk2 C:\PIC>cd\pic C:\PIC>cd\pic command or filename not recognised C:\PIC>tk.bat C:\PIC>main02

and then returns to main menu.

Thinking that the command line C:\PIC>edit needed to be changed to C:\PIC\editor on my machine for the program to work and access the editor from the menu and back, I amended the .BAT file accordingly.

Then I entered the C:\PIC directory and entered TK. The main menu came on screen and on entering E on the keyboard to go to the editor, the message: Main Program Unforeseen MS DOS Error 62 Route came up. On pressing a key to restart the program as instructed, the screen flickered but the program refused to restart. Reloading the program, it all works, except the DOS editor, although I am no longer getting the Unforeseen DOS Error message.

Have you any suggestions how this can be overcome? To access the DOS editor, I have to quit the program, enter DOS, edit the file and then restart the Toolkit program each time. Apart from this I am delighted with *Toolkit's* ease of use and would like to thank you for all your hard work and efforts in producing the *PIC Tutorial* series and *Toolkit* so that us lessable enthusiasts can get to grips with these latest chip developments.

R.A. Hooper, Caterham, Surrey

Thank you for your kind words about PIC Tutorial and Toolkit Mk2, I am delighted you have taken to PICs. On the Edit situation, this command word is placed in the .BAT file from within TKMAIN02.EXE, which also updates the file name as necessary. In order to change from Edit to Editor you would need to possess and know how to use QuickBASIC. In this case you would amend the TKMAIN02.BAS so that the statement:

PRINT #2, "edit ";edit\$ becomes: PRINT #2, "editor ";edit\$.

You would then recompile the .BAS file to .EXE. I don't know any way round this, unless you obtain a version of (MS) DOS which will recognise EDIT as the operative command. I am not familiar with DR DOS.

NAMING PICS

Dear EPE.

Regarding *Readout* Sept '99 in which the question "what does PIC stand for" arose, as far as I am aware, PIC stands for Peripheral Interface Controller. I have heard people (understandably) calling them all sorts of things, like Programmable Integrated Circuits, Programmable In-Circuit Microcontrollers and Programmable Integrated Controllers.

The Peripheral Interface Controller definition can be found on some Arizona Microchip promotional literature, which is at www.jpixton.dircon.co.uk/pic/history.htmI.

This also raises another point: mostly in your mag PICs are referred to as PIC microcontrollers, which is a bit confusing ... Peripheral Interface Controller Microcontroller. It is a bit like when people say "my PIN number ..." which means "my personal identification number number"!!

Joseph Birr-Pixton, via the Net

Thanks Joseph! However, as a personal reaction, your preferred definition doesn't really call up in my mind the sophistication of what a PIC can actually do. The added term "microcontroller" is somehow far more evocative. We also feel obliged to include this word when referring to PICs in order to clarify to readers who may not be familiar with the type of device that they are. In itself the term PIC is only meaningful to those who already use them.

As surprising as it may seem to some readers, there are others who do not yet know about PICs. For the sake of those who are perhaps reading EPE for the first time, we have to spell out definitions in various ways somewhere near the beginning of an article. The abbreviation l.e.d. is an example, we usually spell it out at least once in an article as light emitting diode for sake of anyone who has not yet come across it. There is sometimes more to educational publishing than meets the casual eye!

IC MASTER

Dear EPE,

With regard to the June 1999 issue of *EPE*, in your *Readout* column you advise Alan J. Munday about a chip source, and you suggest he checks the *IC Master* at a library. It just so happens that the *IC Master* has become available online free of charge.

You previously had to be a subscriber to the rather expensive hard copy version in order to access the online version and/or the online updates, but recently they've made it free to anyone, and they have updated and improved the online search facilities. The URL is: http://icmaster.com.

Though the site is generally intended for commercial users, and you do have to fill out an online registration with a company name, they do not restrict access. Fortunately, the time difference between the US and UK should preclude any access conflicts as a result of excess user demand.

> Bob Schoonmaker, Woodside, New York, via the Net

Highly useful info Bob, many thanks.

MEASURING DOWN (AND UP)

Dear EPE,

Good news! I have now got the PIC and l.c.d. working together on the PIC Tape Measure. Richard Wilkinson, via the Net

That's actually the end of the story - let's go back some weeks:

Occasionally, readers run into difficulties when trying to get a PIC-controlled project to work for the first time. A not-uncommon situation manifests itself with projects that use an alphanumerical liquid crystal display as the readout device. All that the puzzled reader obtains is a single line of dark cells on the top line, and nothing but blanks on the second line.

In practically all cases, the answer has been that either the connections to the l.c.d. have not been satisfactorily made, or the reader has used a seemingly sub-standard l.c.d. whose timing requirements do not conform to full-spec devices. As a result, and particularly with the l.c.d. must be allowed to "stabilise" itself, the l.c.d. fails to accept the initialisation commands that set it into the 4-bit control mode.

I have run my PIC software routines on many different types of l.c.d. from several different manufacturers, and obtained "as full-spec" devices from major distributors/retailers. All have been equally responsive to the timings built into the PIC code.

Where timing has been the cause of a reader's problem, the recommendation to increase the length of the delay prior to initialisation, and that introduced during the transmitting of data to the l.c.d., has allowed the situation to be satisfactorily resolved.

Such was part of the advice I gave to Richard when he could not get his PIC Tape Measure (Nov '98) l.c.d. to start-up, and without it starting-up, of course, there wasn't a hope of him being able to use it!

As I recollect, I was in communication with Richard on several occasions, but still he could not get the device to work. Then, along came the above E-mail. Let Richard continue his story:

It turned out that the problem was with the programmer (a commercial kit), hence any changes to the code were not making any difference, even though something appeared to be programed into the PIC. This all came to light when I assembled a new programmer from another supplier (this one based on the *EPE* design).

The only problem I could find with the first programmer was that it could only muster 9V at the MCLR pin during programming from the PC's port.

The advice I would recommend to other readers who are programming PICs for the first time is obviously to check absolutely everything in the process. Thank you very much for all your help, it has been much appreciated.

Another satisfied reader! I heartily concur with Richard's sentiments that everything should be thoroughly checked. There are instances when some readers do not even take the most elementary steps to find out why they cannot get their project to work before contacting us (especially by E-mail, which seems to have become a medium people use all too spontaneously without first pondering the possible reasons for malfunction).

Whilst we and our contributors try to be helpful when people have problems with a project, we cannot judge what degree of competence the troubled reader has and what elementary things he might have done wrong. Years ago, a customer of mine returned a unit which he had assembled from a kit saying he could not get it work - when I turned over the board to examine his soldering, several components fell ou!!

As any experienced electronics constructor or software programmer will tell you, don't assume that all aspects are correct – check them one by one, even the most unlikely!

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Everyday Practical Electronics/ETI, October 1999



INTERFACE Robert Penfold A SERIAL APPROACH TO PC ADD-ONS

THERE seems to be a steady stream of letters from readers enquiring about using the PC serial ports with user addons. This is understandable, since most PCs have at least one serial port left unused.

With the increasing use of mouse port mice and internal modems it is not uncommon for both serial ports to be left unused. The single parallel port, on the other hand, is usually occupied by a printer, and a switching unit or parallel port card is therefore needed to use parallel interfacing. As pointed out in previous *Interface*

As pointed out in previous Interface articles, provided speed is not important it is possible to interface practically any add-on project to a PC serial port, but serial interfacing is never as easy as the parallel variety. A circuit based on a UART (Universal Asynchronous Receiver Transmitter chip) is needed to provide parallel to serial and (or) serial to parallel conversion. These days there is the alternative of basing the project on a microcontroller such as a PIC, which helps to keep down the cost and complexity.

Single File

Serial interfacing circuits have been covered in previous *Interface* articles, and this aspect of things will not be considered here. Unfortunately, getting the hardware successfully connected to the computer is only half the battle. Most programming languages have some form of support for the serial ports, but it is normally in a form that is of limited use with user add-ons.

With your own add-ons it is byte by byte communications that is usually needed, but the serial port support invariably seems to handle things on a one file at a time basis. It is really intended for swapping large amounts of information between the PC and another PC, a printer, a modem, etc.

One way around this is to make addons emulate computer terminals, so that they communicate with the PC using the correct protocols. Where relatively small amounts of data are involved this is a slightly clumsy way of doing things though, and is difficult to implement at all unless the peripheral device is based on a microcontroller.

Direct Approach

It is generally more practical to ignore any built-in support and control the port directly. At its most basic level this just involves writing transmission data to the appropriate address, with the baud rate and word format being set via the operating system. At the other extreme the baud rate and word format are set by writing to the control registers in the serial chip, and the status registers are used to regulate the flow of data into and out of the chip. The serial chip used in PCs is the 8250, or in recent PCs it will actually be a chip from the 16550 family. To be more precise, the serial interfaces will actually be handled by one of the support chips. This will include circuitry that is functionally the same as a device from the 16550 family. So do not bother looking on the motherboard to see which serial chip is used, because it will not be there.

The manual for the motherboard should indicate which chip is being mimicked by the support chips, but it does not really matter which "virtual" chip is used. The main control registers are exactly the same for 8250 and all versions of the 16550. It is only some of the more obscure functions that are different.

Serial ports one and two are at base addresses &H3F8 and &H2F8 respectively, and these are the addresses used when reading and writing data. The seven addresses above each base address are used for reading status information and writing to the control registers.

The following is a full list of the registers:

Address	R/W	Register	DLAB
Base	Read	Received data	0
Base	Write	Data for transmission	0
Base	R/W	Clock divider latch (LSB)	1
Base + 1	R/W	 Clock divider latch (MSB) 	1
Base + 1	Write	Interrupt enable	0
Base + 2	Read	Interrupt identification	
Base + 3	R/W	Line control	
Base + 4	R/W	Modem control	
Base + 5	Read	Line status	
Base + 6	Read	Modem status	
Base + 7	-	Reserved	

Baud Rate

Matters are complicated slightly by the base address having three functions and the address above this having two different functions. This is made possible by having a control bit that is used to switch these addresses between two modes.

This bit is called the Divisor Latch Access Bit (DLAB), and it is at bit seven of the line control register. With this bit set at 0, which seems to be its default state, the base address is used for reading and writing data and the one above is the interrupt enable register.

With DLAB set at 1 the base address and the one above are used for the clock divider latch. This works in the usual serial interface fashion, with a clock signal fed to the UART by way of a divide by "N" circuit. The two bytes in the divider latch together form a 16-bit value, and the clock signal is divided by this amount.

The baud rate can therefore be set by writing a suitable value to the divider latch, which offers a useful alternative to using the operating system. The clock frequency is at 1.8432MHz, but the UART has an internal divide-by-16 action.

This gives a maximum baud rate of 115200 with a value of 1 written to the divider latch. With some PCs it is supposedly possible to obtain a rate of 23400 baud with zero written to the divider latch, but in practice this does not normally seem to work.

It is possible to set any of the standard baud rates by using a suitable division rate, but it is not essential to use standard rates for you own add-ons. On the other hand, crystals intended for standard baud rate generation are readily available at low prices, so it is probably best to use a standard rate unless there is good reason not to.

The division rate is obtained by dividing 115200 by the required baud rate. Most user add-ons will operate at baud rates of 115200, 19200, or 9600 baud, which respectively need 1, 6, and 12 to be written to the divisor latch LSB, and zero to be written to the MSB. User addons will normally operate at high baud rates, so the required division rate is written to the LSB and the MSB is just set at zero.

Interrupts

The interrupt enable register is used to select the sources that will generate interrupts, and the easy way of handling the serial port is to simply disable all interrupts. It is not essential to use interrupts in order to utilize the serial port, and switching them off

should ensure that conflicts between the operating system and your own routines are avoided.

If you should need to use interrupts, the following table shows the function of each bit in the interrupt enable register. To enable an interrupt source set the appropriate bit of the interrupt enable register high:

Bit	Interrupt Source
0	Received data available
1	Transmitter holding register empty
2	Receiver line status
3	Modem status
4	Enables sleep mode (16750 UART only)
5	Enables low-power mode (16750 UART only)
6	Reserved
7	Reserved

To determine whether or not an interrupt is pending, and if so its source, the three least significant bits of the interrupt identification register are read. If bit 0 is at 0 an interrupt is pending, and the binary code in the other two bits indicates the source.

This table shows the code for each of the four possible sources:

Source	Bit 2	Bit 1	Priority
Receiver line status	1	1	1
Receiver data	1	0	2
Transmitter register	0	1	3
Modem status	0	0	4

Word Format

The word format can be controlled via the operating system, but direct control ensures that the serial port always works with the correct format for the add-on. The line control register is used to set the required format. The format for transmission and reception has to be the same incidentally, and any normal format can be set.

The functions of the line control register are as follows. There is always one start bit:

Bit 0	Bit 1	Data bits	
0	0	5	
0	1	6	
1	0	7	
1	1	8	
Bit 2 at 0 Bit 2 at 1		e stop bit o stop bits (1	•5 with 5 data bits)
Bit 3 at 0 Bit 3 at 1		No parity checking Parity checking enabled	
Bit 4 at 0 Bit 4 at 1	Od Eve	Odd parity (if parity enabled) Even parity (if parity enabled)	
Bit 5	Stu	ick parity bit	
Bit 6 at 0 Bit 6 at 1		rmal operati ces data out	on put terminal to logic 0
Bit 7	Div	risor Latch A	ccess Bit (DLAB)

Rationalisation

Suppose that we need to set up the serial port for operation at 9600 baud, and require the usual word format of eight data bits, one stop bit, and no parity. A value of 12 must be written to the divisor latch, but DLAB must be set to 1 to give access to these latches.

DLAB must then be set back to 0 and at the same time the other bits are set-up to give the required word format. Bits 0 and I must be set high, all the others have to be set low, giving a value of three to write to this register.

In GW BASIC (or Visual BASIC with the input and output functions added) these four instructions will set up serial port two correctly:

OUT	&H2FB,128
OUT	&H2F8,12
OUT	&H2F9,0
OUT	&H2FB,3

Bytes for transmission are then written to the base address, which is &H2F8 for serial port 2. If a value of 255 is written to the MSB of the divider latch at address &H2F9 the baud rate will be set at a very low value. In fact, it will be so low that an l.e.d. used to monitor the data output line (see Fig.1) will flash slowly enough for the binary output patterns to be observed.

Hold-Off

There is a potential problem when writing data to the serial port, and this is due to the relative slowness of a serial interface. With the speed of modern computers and programming languages it is easy to write data to the port at an excessive rate, causing bytes of data to be corrupted or lost.

There should be no problem if two bytes are written to the port in rapid succession, because the serial chip's buffering should store the second byte until it can be transmitted. Writing three or more bytes in rapid succession will almost certainly result in an error.

There are two ways of ensuring that data is not sent to the port at an excessive rate. The

time taken for each byte to be sent is easily calculated, and it is ten times the duration of each bit. At 9600 baud each bit lasts $104\mu s (1/9600 = 0.000104s)$. Ten bits therefore have a duration of 1.04ms (1040µs). Using a software-generated delay of at least this duration will therefore ensure that the serial chip is not overloaded.

The alternative is to hold-off the loading of data until the serial chip indicates that it is ready for the next byte. This can be achieved using interrupts, but the easier way is to monitor the appropriate status flag in the Line Status Register.

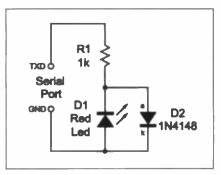


Fig.1. Simple I.e.d. serial port monitor circuit diagram.

A full list of the bit functions in the Line Status Register follows:

Bit	Function
0	Received data ready
1	Overrun (unread data overwritten)
2	Parity error
3	Framing error (no stop bit detected)
4	Break interrupt indicator
5	Transmitter buffer register empty
6	Transmitter register empty
7	Unused (reads logic 0)

When a byte of data is written to the chip it is first placed in the transmitter buffer register, but it is immediately transferred to the transmitter register if it is empty. Otherwise the data is transferred as soon as the transmitter register becomes empty. The data is then shifted out bit-by-bit, with any additional bits such as stop bits being added.

There are two transmitter status flags, and the one at bit 5 is set to 0 when the transmitter buffer register is full. The flag at bit 6 is set at 0 when there is data in either register.

A software loop can provide the required hold-off by monitoring either of these flags, and preventing further data being written to the port until the flag has returned to logic 1. This requires a loop along the lines of "repeat until (&H2FD AND 32) = 32"

Receiving data is trickier than sending it, and is something we will consider next time.

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Auto Supply Crowbar - A Sharp Drop

WHEN power is removed from CMOS logic, onds, causing unpredictable behaviour in the circuit. If the power has been temporarily removed, then in order to "reset" the circuit it is often necessary to count to ten before reapplying the power to ensure proper operation. A small power supply "crowbar" circuit,

A small power supply "crowbar" circuit, which has been used in a burglar alarm where it was necessary for the supply rail to drop sharply to zero when the power was removed, is shown in Fig.1. However, in order to save the standby battery if it is running during a mains failure, the crowbar must not present too high a load on the power rail.

During normal running, with +12V present on the rail, the Zener diode D1 conducts and feeds a small current into transistor TR1 base (b). This turns on TR1, which keeps TR2 switched off. The current consumption is approximately 0.1mA at this time.

If power is removed, as soon as the rail voltage drops below approximately 10V, the Zener diode ceases conduction, TR1 turns off and TR2 turns on. This effectively places resistor R3 across the rail. The resistor has a relatively low value, and the increased current will pull the rail down much sharper than allowing it to decay naturally, especially if the circuit contains filter capacitances.

Since most logic devices switch at around half the rail voltage, there should be no misbehaviour during the initial drift down to 10V. If a 5V rail is used, a 3.3V Zener diode can be substituted.

Barny Connell, Stoke Newington, London.

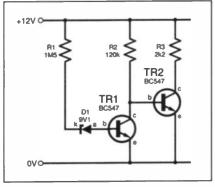


Fig.1. Auto Supply Crowbar circuit.

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Shoestring MW Radio - On A Budget

The Medium Wave Radio circuit diagram of Fig.2 uses relatively few components but delivers 500mW ($^{1}/_{2}W$) into an 8 ohm loudspeaker. In Cape Town, with high selectivity selected, the radio picks up the Voice of America clearly after dark.

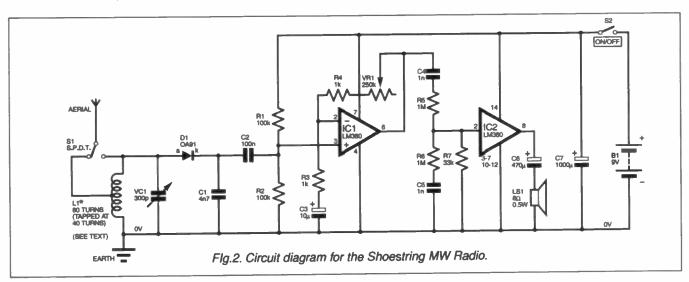
In the circuit, coil L1 is 80 turns of approx. 30s.w.g. enamelled copper wire, close-wound on a 5cm (2in.) diameter piece of p.v.c. piping. A centre tap at 40 turns provides greater selectivity and may be selected with switch S1. The value of tuning capacitor VC1 is not critical.

The Volume control VR1 controls the gain in preamplifier IC1. The output of IC1 is fed, via treble control/low pass filter capacitors C4 and C5, to a standard LM380 audio amplifier i.c.

A good aerial and earth are essential. The

aerial may be attached to a metal window frame or a long wire. The earth connection may be a metal spike sunk into the ground. Experimenters could try using a crystal earpiece wired between diode D1 cathode (k) and 0V which may give fair listening volume even with the battery (B1) disconnected.

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Everyday Practical Electronics/ETI, October 1999

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Constructional Project

MICRO POWER SUPPLY

A low-cost, low-power, mini board project that will provide a regulated supply of plus and minus 5V.

HIS little project first saw the light of day when it was realised that much time was being wasted in the workshop constructing regulated five-volt supplies on breadboards. With the everincreasing use of logic and microcontrollers such as the PIC many projects now require such a supply voltage.

ANDY FLIND

The introduction of new op.amps capable of high performance from low supply voltages means that analogue circuits can use it too, though an auxiliary negative supply is often still useful for these, especially where the negative rail of the primary supply is to be used as common or "ground". Where this is the case it is usually preferable to generate the extra supply rail electronically, particularly when the finished project is to be operated from a battery.

Also, with battery supplies in mind, it was decided that this project should use micropower components and the regulator should be a "low dropout" type. Finally, it was decided that the project should be miniaturised as far as possible so that it would take up minimum space on the breadboard or in a case used for testing a prototype. In fact, it could even be included in a finished project if desired.

REGULATIONS

The choice of active components for the project was straightforward. The regulator is the LP2950, which is very similar to the popular 78L05 3-terminal +5V regulator, even to the extent of having the same pinout connections.

It does have improved regulation however, draws a much lower supply current and can operate with a very low differential between input and output voltages, typically down to 100mV. This makes battery operation more economical since the supply voltage can be much lower before replacement is necessary.

ON THE FLY

The auxiliary negative supply is generated by an SI7660 voltage converter chip (see *Shoptalk* page) which is intended specifically for this function, especially with 5V supplies. These are sometimes known as "flying capacitor" devices, and a short description of the way in which they operate may be of interest. The basic principle, with capacitor C1 connected to the supply rails by a 2-pole two-way switch S1a and S1b, is shown in Fig.1. When in the position shown, the switch allows the capacitor to charge from the main supply voltage V_{IN} .

The switch is then set to the opposite position, so the voltage across capacitor C1 appears across the output and is partially transferred to capacitor C2. If the switch positions are alternated repeatedly, C2 will become charged to virtually the same voltage as the supply, but connected as shown it appears as an additional, *negative* output.

ELECTRONIC SWITCHING

In a practical circuit, having realistic capacitor values, the switches should be electronic of course, and they must operate at high speed, typically several kilohertz. They should have low "on" resistances and no "offset" voltages, such as the forward voltage drop of a diode, so CMOS devices are preferable. An oscillator is required to drive them, and problems of protecting them from reverse voltages must also be overcome.

Fortunately, all these issues are already overcome for users of the SI7660 which

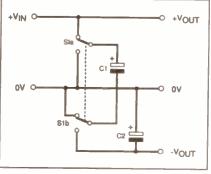


Fig.1. Principle of "flying capacitor" negative rail generator.

contains an oscillator, drive logic and the output switches. With a 5V input it requires only a pair of capacitors to complete the circuit.

For a zero load the output voltage is virtually the same as that of the input but it tends to fall with load. The output is often described as being similar to the input voltage in series with an 80 ohm resistor, so for a 10mA loading it would drop to -4.2V. Although often better in practice, this makes it unsuitable for use as a reference source, but as an auxiliary supply for op.amps it is ideal.

MICRO POWER SUPPLY

The full circuit diagram for the Micro Power Supply appears in Fig.2 and is fairly straightforward in nature. The input voltage is first reduced to a positive 5V supply by the LP2950 micropower regulator IC1. Capacitors C1 and C2 decouple the input to IC1 whilst C3 and C4 decouple its output.

The smaller value, non-electrolytic, capacitors C2 and C3 are often omitted by circuit designers, but the construction of electrolytic capacitors, using layers of foil wound into a cylindrical shape, gives them a fair degree of self-inductance which can make them ineffective at high frequency. The small ceramic capacitors by contrast work very well indeed at high frequencies so they are included to take care of these where necessary.

The negative supply is generated by the SI7660 negative supply generator chip IC2. The "flying capacitor" is C5, a 10μ F solid tantalum type. The output, taken from pin

CON	IPONENTS		
Capacitors C1 C2, C3, C6 C4 C5 C7	220 μ radial elect. 16V 100n resin-dipped ceramic (3 off) 100 μ radial elect. 10V 10 μ tantalum bead, 16V 47 μ tantalum bead, 16V		
Semiconduc IC1 IC2	tors LP2950 low power +5V regulator SI7660, "switched capacitor" voltage converter		
Miscellaneous Printed circuit board available from the EPE PCB Service, code 243; 8-pin d.i.l. socket; solder pins (5 off); solder, etc.			
Approx. Co Guidance C			

5, is decoupled by ceramic capacitor C6 and electrolytic C7, another tantalum component.

The reason for the use of tantalums is that these have a much lower self-inductance than ordinary electrolytics and should therefore cope with the 10kHz operating frequency of IC2 more efficiently. IC2 is another micropower component, so the overall quiescent supply current for this circuit from a 9V supply is just $140\mu A$.

CONSTRUCTION

All the components for the Micro Power Supply are accommodated on a small ($32mm \times 17mm$), single-sided printed circuit board (p.c.b.). This board is available from the *EPE PCB Service*, code 243. The topside component layout, together with the underside copper foil master, is shown in Fig.3. The maximum allowable depends to some extent on ambient temperature, but a practical limit of 200mW seems reasonably safe. It should be remembered that current supplied by the negative rail generator also comes from the regulator. For many small low-power applications such as signal processing with op.amps, CMOS logic or PIC microcontrollers dissipation will not be an issue.

NEGATIVE USE

The negative output should be regarded only as an auxiliary supply as it is not nearly so well regulated as the positive one. Its main purpose is to supply op.amps so that the common rail may be used as "ground" by allowing their inputs and outputs to operate below this value.

Because the output of the SI7660 behaves as though in series with an 80 ohm load, the output voltage will vary with load.

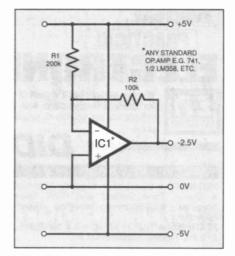


Fig.4. Circuit diagram for generating a negative reference voltage.

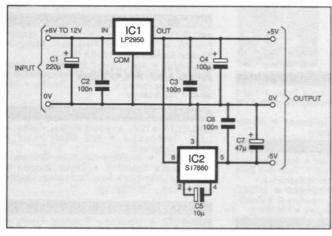


Fig.2. Complete circuit diagram for the Micro Power Supply.

Construction of the unit consists of fitting all the components except IC2, taking care to fit all the electrolytics with the positive side towards the top edge of the p.c.b., as viewed in the diagram. The markings on tantalum bead capacitors are sometimes less than models of clarity, so care may be needed to interpret these correctly. An 8pin d.i.l. socket should be provided for IC2.

TESTING

The first test is to apply an input voltage of between 6V and 12V and check for the presence of the positive 5V output. The current drawn should be tiny, just 140μ A or so, though there will be a small surge as the electrolytics, especially C1, take up their initial charge.

If the +5V output is present and correct, IC2 can be inserted into its socket and, when the unit is powered again, the *negative* output voltage should appear at the appropriate point. And that's it! Testing is now complete and the unit is ready for use.

Although the +5V output is capable of more than 100mA output a watch should be kept on its internal power dissipation to prevent overheating. This is calculated by subtracting the output voltage (5V) from the maximum input voltage (9V for a PP3 battery) and multiplying by the output current in milliamps (mA), for regulator dissipation in milliwatts (mW).

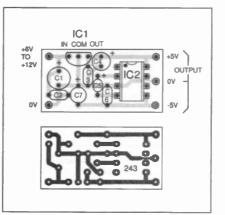


Fig.3. Printed circuit board component layout and full size copper foil master.

Measured results taken with the prototype were -5V for no load, -4.75V at 5mA, and -4.5V at 10mA.

If a reference voltage of some kind is needed, it should be taken from the positive supply. If it is essential to have a negative reference, one way to generate this would be with an inverting op.amp, as shown in Fig.4. The output voltage for this is given by 5(R2/R1) volts.

There will inevitably be some 10kHz ripple on the negative output, although this is very small. Measured values of this were 25mV at 5mA and 50mV at 10mA. In most cases this should not cause any difficulties, but some very sensitive circuits may need careful design or extra filtering to avoid its effects. Where the negative supply is not required IC2 can be removed, though in many cases it will be more convenient to just leave it in place.

IN USE

This little circuit should prove invaluable to those who like to experiment with their own circuits. These days it is often far simpler to regulate the supply to a circuit than to try to design it so that it will ignore supply voltage variations and the option of just dropping this little regulator board into designs with a blob of Blu-Tack or similar will save much valuable time, effort and space.

In fact, it could even be incorporated into complete projects where these are one-offs, perhaps constructed on stripboard, although if a p.c.b. is used it is more likely that it will be copied onto the board design in some form. The layout is not critical in any way save that the decoupling capacitors should be kept reasonably close to the i.c.s. Where it is to be used with the plug-in type of breadboard, short lengths of single-core insulated wire could be connected to the outputs and the other ends of these pushed into the board where needed.

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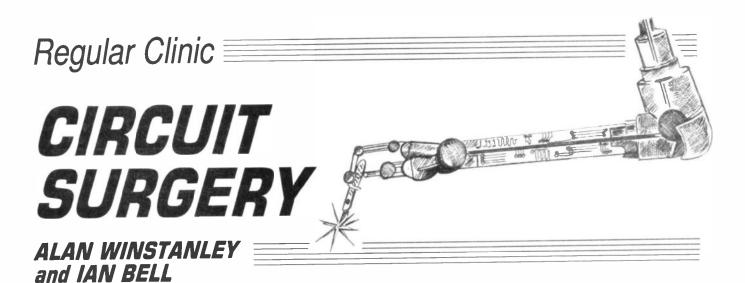
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A new variant of the 555 timer chip, also we return to the topic of digital panel meters, followed by a maths-intensive analysis of a 22-step logarithmic volume control.

ONE event in the integrated circuit world seems to have gone unnoticed so far in the hobbyist arena. Quite a long time ago we were tipped off by a reader about a new offering from the chip manufacturer Zetex, who introduced a new version of the good old 555 timer.

Now that the device has become readily available through some of the better mailorder channels, some of the features of this intriguing "new" device can be described.

Low Voltage 555

The Zetex ZSCT1555 low voltage timer looks like any other 555 timer but it has one very important difference – *it is guaranteed* to operate down to +0.9V supply voltage, even though it is a bipolar device! This opens up new applications for this veteran part number, especially using low-voltage power supplies (lithium coin cell batteries for example) for which the device is eminently suitable. Its current consumption is reduced too, drawing a meagre 75μ A at 1.5V, claims Zetex. The maximum supply rail is +9V d.c.

This third generation 555 timer has been co-produced by Zetex in association with Hans Camenzind, the designer responsible for the original Signetics 555 timer over 25 years ago. The new device is pin-compatible with older versions but there are slight changes to the formulae. In the monostable timer of Fig.1, the period is $1.63R_AC$, and in the astable of Fig.2, the frequency is 0.62 (R_A+2R_B) C.

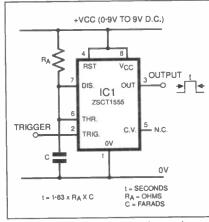


Fig.1. Monostable low-voltage timer.

My own tests proved that it did indeed operate as, say, a piezo disc sounder below 1.5V or so. But there is, of course, a severe restriction placed on the usefulness of the output signal at that sort of level, so don't expect to produce an ear-splitting siren powered by a coin cell! (It's about as noisy as a peeved bluebottle.) By tuning the oscillator frequency to match a piezo's resonant frequency, maximum output level will be obtained.

A full data sheet in Adobe PDF format and an application note for the ZSCT1555 are available from the Zetex web site at **www.zetex.com**. The chip is now available for purchase from Farnell Components (tel. 01132 636311), their part number is 698-155, price $\pounds1.25 + VAT$ for one-offs (minimum cash order $\pounds10$). A surface mount version is also produced. (ARW.)

Digital Panel Meters

I have a mechanical analogue project meter with a 64mV full-scale deflection (f.s.d.) and would like to convert it to a digital display. I noticed that most digital modules indicate 200mV. Can you tell me how to use one in my project? D. Lee, Birkenhead.

Most digital panel meters (DPM) tend to be 200mV types and are designed as a basic voltmeter. The end user must decide how to "scale" the DPM, whether as an ammeter (using the meter to read the voltage drop across a series resistor) or as a

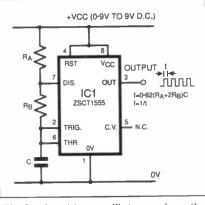


Fig.2. Astable oscillator using the ZSCT1555 will operate below 1V.

voltmeter for use at a higher voltage range, which is usually how they are utilised.

Such a module often uses a 7106 chip and will directly read 199.9mV (which is why they are called 3¹/₂ digit types – the leading "1" being the half digit). This topic was briefly described in *Circuit Surgery*, September 1998.

Ån external attenuator must be added to a 200mV digital panel meter's input to extend its range. You can assume that the module will have an extremely high input impedance (>100M Ω so the attenuator should be about one tenth of this resistance to avoid loading.

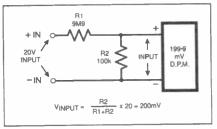


Fig.3. A voltage divider on a DPM input, produces a 20V f.s.d. meter.

To produce a 20V d.c. voltmeter a series input resistor of 9.9 megohm would be used, followed by a 100 kilohm ($100k\Omega$) resistor *across* the meter's input terminals, see Fig.3. Thus, most of the input voltage is dropped across the 9.9M Ω resistor and at an input of 20V only 200mV appears across the DPM input terminals. A 200V voltmeter would use 9.99M Ω and 10k Ω instead.

As regards fitting it into your project, due care is needed with 7106-based meter modules as they may require a completely independent d.c. supply voltage, which cannot necessarily be tapped from an existing supply. Problems arise when the DPM supply and the DPM signal input 0V rails are commoned together.

A good supplier of very competitivelypriced panel meters is Vann Draper (Tel. 0116 277 1400 or www.vanndraper.co.uk) who advertise regularly in our pages. Full instructions come with every meter and their l.c.d. model can be modified with a couple of track cuts ready for "commoning" of the 0V rails. Vann Draper tells me that they can also provide an l.e.d. type, subject to lead times.(ARW.)

Sound Levels and Decibels

Last month we discussed in some detail the rather involved mathematics behind Root Mean Square (r.m.s.) values, and we examined the meaning (or actually the total lack of any meaning whatsoever!) of audio p.m.p.o. ratings (Peak Music Power Output). This month, *Circuit Surgery* embarks on another mathematical extravaganza, courtesy of Ian Bell (whom as regular readers will know hails from the School of Engineering at the University of Hull).

The material that follows is maths-intensive and is written with the more advanced reader in mind. However, non-mathematicians shouldn't be disheartened as we are sure they will pick up plenty of snippets of valuable information along the way.

Unfortunately in the complex where I live, I have to keep the noise down! For my stereo system I would like to build a switched volume control for which purpose I have acquired a 22-position dual control rotary switch with gold-plated contacts. How could I calculate the resistor values? My source impedance is 50 kilohms.

I'm still keenly interested in electronics and always enjoy your column. Many thanks from Johnny Bruyns, Benoni, South Africa.

This is a good example of a simple question which in the event has a very involved reply: unfortunately it isn't always clear at the beginning just how tricky the solution really is, until it's actually been worked out!

One very simple solution (used on my trusty Marantz audio amplifier – ARW) is to use an ordinary logarithmic potentiometer with a "detent" mechanism on the shaft: the Volume control was calibrated in decibels and it clicks into one of 20 or 30 positions when rotated.

On The Threshold

Before we tackle the query directly though, we need to consider why it is not a straightforward case of using 21 or 22 equal value resistors. The quick answer is that volume controls usually have a logarithmic response, with the consequence that a given turn of the control at the "low" volume end results in a much *smaller* change in output voltage than the same turn at the "high" volume end.

This means the resistors will have to be scaled logarithmically. We need logarithmic volume controls because human hearing, or more specifically our perception of loudness, is logarithmic in nature. This is one of those everyday facts many of us know and take for granted, and which we decided to investigate in more detail.

The human ear is able to hear sounds of a very wide range of intensities (which is measured in Watts per square meter – Wm⁻²). The quietest sound which can be perceived is called *the threshold of hearing* and is about 1×10^{-12} Wm⁻². The *threshold* of pain is about 10,000,000,000,000 times more than this at about 10 Wm⁻² (these threshold figures are only approximate and vary with individuals and frequency). For those readers interested in loud audio – a rock concert can reach 0.1 Wm^{-2} or more if you are positioned close to the speakers!

An exponential increase in sound intensity, from the quietest audible to the loudest tolerable sound, is perceived basically as a linear increase in loudness. Whilst sound intensity in Watts per square meter is rigidly defined, loudness is a matter of human perception and will vary between individuals and with frequency, however, the general *exponential* nature of the relationship just described remains valid.

Exponential is the inverse of logarithmic – the intensity varies exponentially but our ears respond logarithmically, which is why we perceive a *linear* increase in loudness. Put another way: each ten-fold increase of sound intensity gives an equal step increase in loudness. We could also say each doubling of sound intensity gives an equal step increase in loudness – these would be smaller steps than for a ten-fold increase of course.

Going Decibels

Exponentially varying quantities can be difficult to deal with (try plotting the full range of sound intensities mentioned earlier on normal graph paper!) so a special scaling system is often used. First, we take a reference level (say the threshold of hearing) and then the sound intensity level we are interested in, and then we find the ratio between them - i.e. by what factor our intensity is larger than the reference.

Now, taking a logarithm of the ratio we get a value related to the perceived loudness. In fact this approach is the basis of the commonly used **decibel** notation.

The definition of a decibel (dB) is based on the logarithm of the power ratio of two signals P₁ and P₂, such that the power ratio in decibels is given by $10 \times \log_{10}(P_2/P_1)$ dB. If we are expressing *power gain* (e.g. of an amplifier) then P₁ would be the input power and P₂ the output power.

For measuring a power quantity relative

to a reference, P_1 would be the reference level and P_2 the value we are measuring. Note that because power ratios are used in the decibel definition it does not matter if P_1 and P_2 are expressed as peak or r.m.s. values as long as both are expressed in the same way.

As an example, if your personal stereo is delivering 2×10^{-2} Wm⁻² of sound power to your eardrums this would be 103dB relative to a threshold of

hearing reference at 1×10^{-12} Wm⁻². The calculation is: $10 \times \log_{10}(2 \times 10^{-2}/1 \times 10^{-12}) = 103$. Note if the measured value is equal to the reference, or if the gain of the system is 1 then we get OdB.

Reduced Power

If a circuit reduces power, i.e. it is an *attenuator*, then we actually get *negative* decibel values. For example, if the power output is 50 times smaller than the input

then the "gain" is -17dB (10 × log₁₀ × 1/50). If the power is reduced by one half then the output is at -3dB.

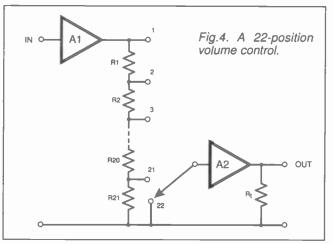
This is a figure that many readers may know as the *cut-off frequency* of a filter (or the bandwidth of an amplifier) which is usually quoted as the point when the gain falls to 3dB below the value in the passband. At this frequency the output power from the filter is half the value it is in the passband (for equal input power). It is worth noting at this point that a volume control is an attenuator and that a useful response to the reader's question would be to design a control with 22 attenuation ratios in equal decibel steps. Before we go on with this we need a few more comments about decibels though.

So far we have only discussed power ratios – this was obviously appropriate for sound intensities, but in circuits we often measure signals as voltages and currents. Power is related to the square of voltage or current. If we square something inside a logarithm, it is equivalent to multiplying the log by two (without the square). That is, $log(V^2) = 2log(V)$. So to express a voltage gain (V_1/V_2) in decibels we use $20 \times log_{10}(V_2/V_1)$. Note that we are multiplying by 20, not by 10 as we did with the power gain.

Strictly speaking this formula is only valid if the two voltages are across the same resistance value, but in many cases we are only interested in the voltage gain (not the power gain) and the $20 \times \log_{10}(V_2/V_1)$ formula is widely used for this purpose (V₂ being the output, V₁ is the input). Similarly current gains can be expressed in decibels using $20 \times \log_{10}(I_2/I_1)$.

Divided Volume

To design the volume control we will use the simplified situation shown in Fig.4. The volume control is a potential divider placed between two amplifiers, it therefore attenuates the output of the first amplifier, A1, before passing it to A2.



We will assume that the total value of the resistor chain in the potential divider is R_t and that the second amplifier drives a load of value R_t also, this means that voltage gain in dB will equal the power gain. We will also assume that the amplifiers have low output impedance and are not loaded heavily by R_t , and that the second amplifier has a high input impedance and does not load the potential divider. Let's also assume that both amplifiers have unity gain (1).

In a real circuit these assumptions may not be valid but the effect will generally be to shift the actual power output from A2 by a constant amount, or multiply it by a constant factor. Since it is the relative loudness produced by each step of the volume control which interests us this will not matter. It is still important, however, that the resistor chain does not heavily load, and is not loaded by, the amplifiers.

Referring to Fig.4, the switch S1 has 22 positions so there are 21 resistors. The first position (1) bypasses the divider and connects the amplifiers directly together. This is full volume (0dB attenuation). The 22nd position connects the input of the second amplifier to ground so no signal will be output (infinite attenuation): we could label this position "Mute". This leaves 20 remaining positions for different volume levels.

We can decide over what range in dB we would like to control the volume. 90dB might be reasonable – the range from a whisper to a loud personal stereo is of this order. If we chose 90dB each of the 20 switch positions increases the attenuation by 4-5dB.

Calculated Fun

Now for the fun bit – how do we calculate the resistor values? Recall that the formula for a standard two-resistor potential divider is $V_{out} = V_{in} \times R_B/(R_A+R_B)$, as shown in Fig.5. We can write R_A+R_B as R_t , the total value of the resistors in the potential divider. The "gain" of the potential divider is V_{out}/V_{in} which is simply R_B/R_t . Expressed in decibels this is 20 × $\log_{10}(R_B/R_t)$.

Our potential divider is a little more complex as it has many resistors, however it still has a total resistance that we will call R_t . For now we will not limit the number of resistors in the volume control divider, in fact we can assume there is an infinite number getting ever smaller as we move down the chain from the amp output to ground. We can label the *n*th resistor in the chain from the amp output as R_n .

If we consider taking an output from the volume control divider below the *n*th resistor and compare this with the basic potential divider, the equivalent to R_B will be the sum of the resistor values from R_{n+1} to infinity (the sigma symbol meaning "sum of"). This is a gain of:

$$20\log_{10}\left(\sum_{\frac{r=n+l}{R_{t}}}^{\infty}R_{r}\right)$$

If we move up the chain to just below the previous (i.e. (n-1)th) resistor the gain will be

$$20\log_{10}\left(\sum_{\frac{r=n}{R_{l}}}^{\infty}R_{r}\right)$$

The difference between these two adjacent steps in the volume control must be equal to whatever increment in gain we require for each switch position. We chose 4.5dB earlier, but we will keep things general here by using A to represent the constant step in dB attenuation for each switch position. Thus A =

$$20\log_{10}\left(\sum_{\frac{r=n}{R_{t}}}^{\infty}R_{r}\right) - 20\log_{10}\left(\sum_{\frac{r=n+1}{R_{t}}}^{\infty}R_{r}\right)$$

so A = 20log₁₀ $\left(\sum_{\frac{r=n}{\infty}}^{\infty}R_{r}\right)$

by dividing by 20, taking 10 to the power of each side, and taking the reciprocal we get

$$\left(\begin{array}{c}\sum_{r=n+I}^{\infty}R_{r}\\\sum_{r=n}^{\infty}R_{r}\end{array}\right) = 10^{(-A/20)} = k$$

In which it is convenient to write $10^{(-A/20)}$ as a constant k as shown. Rearranging, we get

$$\sum_{r=n+1}^{\infty} R_r = k \sum_{r=n}^{\infty} R_r$$

From which it can be inferred the resistor values for a geometric sequence with coefficient k and initial value R_1 . That is, the resistor values are R_1 , kR_1 , k^2R_1 , k^3R_1 ... and 0 < k < 1 so the series converges, that is the sum of all resistance values is finite. This should not be too surprising given the logarithmic nature of the response we are after.

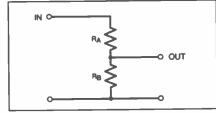


Fig.5. Basic potential divider.

From the properties of a geometric series we know that the sum of resistances to the *n*th resistor, S_n is:

$$n = R_1 \frac{(1-k^n)}{(1-k)}$$

S

Note that S_n is the equivalent of R_A in Fig.4, if we take the output from just below R_n . By taking the limit in the above equation, in which $n \rightarrow \infty$, we get the value of R_i (the total resistance of all the resistors).

$$R_t = \frac{\kappa_1}{(1-k)}$$

This is useful because it allows us to calculate the value of the first resistor in the volume control (R1 in Fig.4) once we have decided on the attenuation step and the total resistance value we want to use. From there we can use the fact that each resistor in the chain can be found by multiplying the previous value by k.

This is easy to do if you have a "constant" facility on your pocket calculator – just keep pressing "equals" after multiplying R1 by the constant k to get all resistor values. By using the equation for S_n for the total to two adjacent resistors (say R_{n-1} and

Table 1: Divider resistor chain

10010	T. DIVIN	101 10010101	
Resistor	Value	Total(Ω)	Gain (dB)
1	202kΩ	202169	-4-5
2	120kΩ	322593	-9
3	71-7kΩ	394326	-13-5
4	42·7kΩ	437054	-18
5	25-5kΩ	462505	-22.5
6	15·1kΩ	477666	-27
7	9•03kΩ	486696	-31.5
8	5-38kΩ	492076	-36
9	3·20kΩ	495280	-40-5
10	1-91kΩ	497188	-45
11	1-14kΩ	498325	-49-5
12	677Ω	499002	-54
13	403Ω	499406	-58-5
14	240Ω	499646	-63
15	143Ω	499789	-67-5
16	85·3Ω	499874	-72
17	50·8Ω	499925	-76.5
18	30 •2Ω	499955	-81
19	18·0Ω	499973	-85.2
20	10·7Ω	499984	-90
21 <i>(Re</i>)	15-8Ω	500000	off

 R_n) and taking the difference between them we can also get an expression for the value of the *n*th individual resistor as follows: $R_n = R_n (k^{n-1} - k^n)$

Remember 0 < k < 1 so k^{n-1} is larger than k^n so this will give a positive value for R_n as required. The only problem remaining is that we have assumed an infinite series of resistors, which is not very practical! For a practical circuit we need to terminate the sequence after a certain number of resistors (20 in our example).

Special Case

The last resistor must be treated as a special case. Its value must be equal to the sum of all the resistors left in the infinite series after we stop using it.

We shall call this resistor R_e (e for end). We will also use N to signify the number of resistors in the first part of the divider (N = 20 in our example). The value of R_e is obtained simply by subtracting the sum of the N resistors in the main part of the chain from the total, that is $R_e = R_I - S_N$, where S_N is obtained by putting n = N in the equation for S_n given above.

By substituting the formula for S_n and R_t given above into $R_t - S_N$ and simplifying we get: $R_e = R_t k^N$

As an example if we use A = 4.5dB (4.5 dB steps) we get $k = 10^{(-4.5/20)} = 0.5957$. If we choose a total resistance of 500k Ω (kilohms) and require 21 resistors (20 in the sequence, plus R_e) we get N = 20 and $R_t = 500000$, from which $R_I = 500000 \times (1 - 0.5957) = 202$ k Ω , and $R_e = 500000 \times (0.595720) = 15.8\Omega$. The other resistors can be found by multiplying successively by k, i.e. $R_2 = 0.5957 \times R_I = 120$ K Ω ; $R_3 = 0.5957 \times R_2 = 71.7$ k Ω ; and so on. It useful to add up all the resistor values (including R_e) to check that they equal R_t .

Finally, Table 1 gives all the values for our example, plus the gain at each point at the tap below the resistor concerned, and the accumulated resistance total. We do not know what the most appropriate values are for the reader's application so the values we have used are as an example only. However, it should be straightforward to apply the formula given to whatever attenuation step size, total resistance, and number of resistors are required. Phew! Well you did ask...! (*IMB*.)

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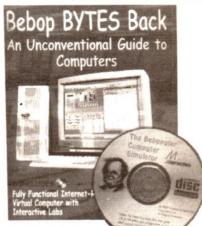
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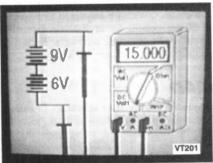
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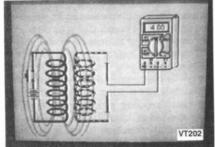
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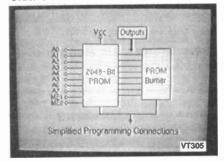


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SURFING THE INTERNET

REGULAR users of the *EPE* web site (**www.epemag.wimborne. our** home page. It uses the Excite engine and will hopefully help to point you in the right direction if you're searching for particular project details or keywords on the web site.

It's worth remembering that having arrived on the suggested web page, the quickest way of pin-pointing the precise keyword is to use the "Find" facility of your browser. In Microsoft Internet Explorer, this is located under the Edit menu ("Find on this Page") and it's an often underused but useful option when wading through pages of text.

EXPLORE NEW OPTIONS

This month, I'll suggest a few ways in which your Microsoft Internet Explorer browser can be improved. Version 5 is the one to go for, but don't bother trying to download it from the net as it will be found on most current cover disk CD ROMs!

There was a time when all web browsing had to be performed on-line, and if you wanted to view or print a copy for later reference or consumption you would have to do it on-line when the phone bill was ticking away. Alternatively, you could use one of the cache-reading programs such as Nearsite or Unmozify to read the pages later on. The ability to "work off-line" was a welcome introduction in MSIE V4, but it's a facility which is tucked away on a menu rather than being readily accessible on the button bar. This omission is quite tiresome and not a little annoying, as in my experience it's one of the browser's most used features.

The same is true of Internet Explorer Version 5. Just as I was cussing at this missed opportunity to improve this latest browser's functionality, I unearthed several browser enhancements on the Microsoft site (www.microsoft.com/windows/ie/webaccess) and I recommend that readers hop over and have a look.

The most useful one is (fanfare!) a toolbar button for enabling off-line working. This loads an "off-line" button shortly after the browser has booted. Unfortunately, the only way you can actually tell that you're in off-line mode is to check for a small icon in the browser's command bar underneath the main window.

More tools for MSIE 5.0 are also on offer. I especially like the optional extras for the right mouse-click menu, including (at last) the ability to highlight in yellow any text of interest. I find this a great idea when checking through long pages, or perhaps lengthy lists of software options for downloading: the highlighter enables me to scroll back up to the areas of interest and to focus on getting my work done.

IN CLOSE-UP

Web developers will enjoy the new View Partial Source and Images List functions which are also only a right mouse-click away. Now you can select portions of a web page which look interesting, and then View Partial Source will reveal the HTML just for that selection, rather than for the entire page.

The new Images List option will also be a tremendous boon for many developers; it displays the file size and dimensions in pixels of every image on a page, as well as the images themselves. More importantly, it totals up the image content per page and calculates the typical download time at a variety of speeds.

Other MSIE 5.0 "tweak" options are provided on the Microsoft site, including the ability to decorate the toolbar with a bitmap. The toolbar "wallpapers" supplied are unimaginably dull but you could always create your own bitmap using a graphics program. You will also discover that several third party developers now offer plug-in toolbars, including Alta Vista's search engine toolbar which integrates into MSIE 5.0. It's nice to see one or two worthwhile additions, not before time, which simplify the work of experienced users and developers for a change.

1984 AND ALL THAT

10

It isn't often that your scribe has the time or inclination to indulge in any navel-gazing, but it is sometimes stimulating to step back a little and maybe hazard a guess as to which way the "wired" world is going. The February 1984 issue of *Everyday Electronics* contained a small but very prophetic news item, the significance of which is only now starting to become apparent. In that issue, which featured a Sinclair ZX81 home computer on its cover, we reported on the advent of the world's first digital television in Japan, which we hailed as "marking a new generation that will serve as the focal point for the home information system of the future." (See the *history2.htm* page of our web site.)

Nowadays the whole world and his dog owns a mobile phone or pager and routinely uses the Internet for chatting, mailing and shopping. Rather than kitting everyone out with a complicated home computer, to my mind the increasingly widespread use of Web TV probably represents the final piece to fall into place in the home consumer information jigsaw. Web TV uses the TV screen as a monitor, and it can use a wireless QWERTY keyboard to communicate from the comfort of the couch. Some E-mails I receive already bear the "webtv.com" or "webtv.net" monikers. Both Sky and On-Digital promise an electronic mail service through digital set-top boxes soon.

LOOKING AHEAD

Looking ahead, as pressure on leisure time continues to soar, I think that consumers will routinely purchase more of their groceries via the Internet for doorstep delivery, and using their digital television which will become the family's main home computer "monitor". Regular standing orders could be placed or modified, relieving the customer of some of the chore and drudgery of shopping. This is already starting to happen via home computer Internet links.

With the arrival of Web TV, viewers will be able to browse through on-line holiday brochures complete with downloadable movie presentations of holiday destinations, reserve a flight or book a weekend break, send E-mails, and even download kids' homework resources, all simply menu driven through their digital TV. Need a plumber? Instead of thumbing through *Yellow Pages*, there will be an interactive web directory (and you can bet that AAA Plumbing will be there at the top of the search engine results).

You will be able to leave video messages for others in a "video box" using integrated cameras, messages being downloaded on command. However, the more sophisticated personal computer user will continue to use separate PC desktop or laptop systems allied to a suite of imaging and printing peripherals, connected to the Internet via wireless or cable moderns or eventually ADSL. But those unable to get to grips with a PC system should find Web TV a breeze.

Twenty years after prophesying the impact of the home information revolution, the use of Web TV as an interactive home information resource will gradually become as routine as using teletext. American users are already being exhorted to prepare for Web TV by buying a 28-inch or 30-inch television!

This is to compensate for the computer-screen orientated web material which is designed for PC monitors and is prevalent today: they invariably feature small graphics, tiny print and contain far too many navigation options. A Web TV window cannot scroll sideways, either.

Before long, web sites will have to be augmented or completely redesigned to allow for this new method of viewing them, eight feet away on a low resolution TV tube. One thing is for sure, there are interesting times and choices ahead for the consumer, and the way in which new generations will organise their lives is about to be transformed yet again.

PCB SERVICE

Printed circuit boards for certain EPE constructional projects are available from the PCB Service, see list. These are fabricated in glass fibre, and are fully drilled and roller tinned. All prices include VAT and postage and packing. Add £1 per board for airmail outside of Europe. Remittances should be sent to The PCB Service, *Everyday Practical Electronics*, Allen House, East Borough, Wimborne, Dorset BH21 1PF. Tel: 01202 881749; Fax 01202 841692 (NOTE, we cannot repty to orders or queries by Fax); E-mail: orders@epemag.wimborne.co.uk . Cheques should be crossed and made payable to *Everyday Practical Electronics* (Payment in £ sterling only).

NOTE: While 95% of our boards are held in stock and are dispatched within seven days of receipt of order, please allow a maximum of 28 days for delivery – overseas readers allow extra if ordered by surface mail.

Back numbers or photostats of articles are available if required - see the Back issues page for details.

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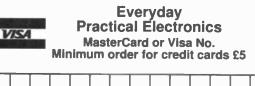
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Access the FTP site by typing the above into your web browser, or by setting up an FTP session using appropriate FTP software, then go into quoted sub-directories:

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PIC projects each have their own folder; navigate to the correct folder and open it, then fetch all the files contained within. Do not try to download the folder itself!

EPE text files: /pub/docs

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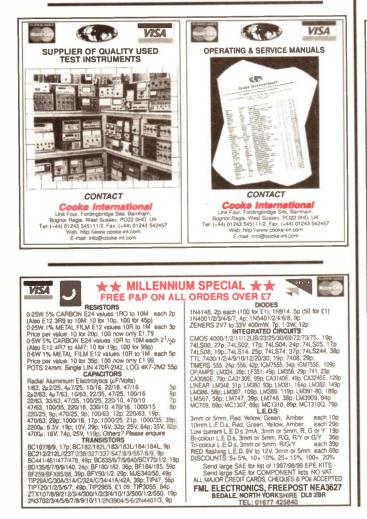
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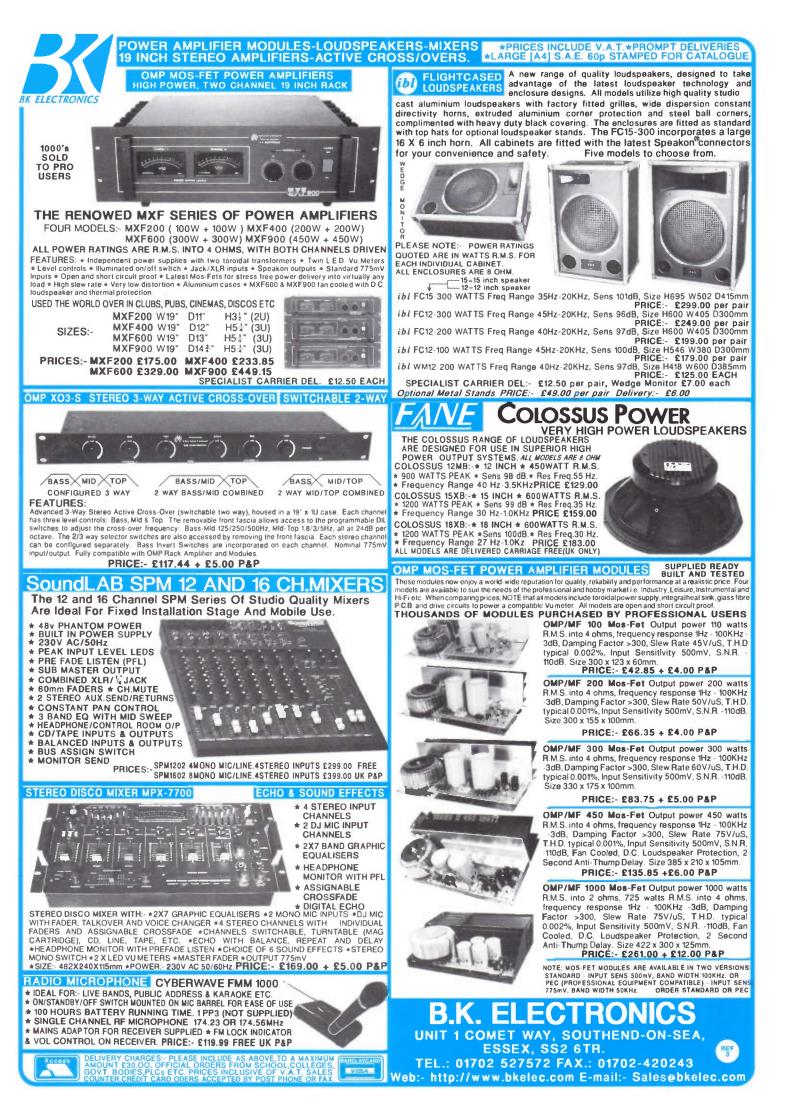
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