

With minor variation the above techniques have been applied to SSB rigs in an effort to simulate

Many CB aeralcs can be retuned to 28-29MHz with little difficulty and a host of reasonable test gear at very cheap prices is also available.

To date some 2 — 3000 conversions to 27MHz FM have been made to a variety of sets and basically the same technique applies to 29MHz.

Binary Arithmetic

The standard arrangement is as shown in the table across the page (below).

Power of 2	0	1	2	3	4	5	6	7	8	9	10	11	12	2 ⁿ
Binary No.	1	2	4	8	16	32	64	128	256	512	1024	2028	4056	Code sum
Sample	0	1	0	1	1	1	0	0	1	1	0	0	0	826

Codes Used in Various P.L.L. Devices

Binary Inputs	Binary in Internal ROM	BCD in BCD Code Convertor	Programme Inputs	Random Code
CC 13001	LC 7110	LC 7120	LC 7130	TC 9106P
CC 13002		UPD 861C	MSC 42502P	TC 9109P
LC 7113		UPD 2810C	UPD 858C	
MC 58472*		UPD 2812C		
MC 58473P		UPD 2814C	LC 7131	
MB 8719		UPD 2816C	LC 7135	
MB 8734		LC7137		
MC 145104				
MM 55104				
MM 55114				
MM 55124		Require Synthesiser Replacement		Require Synthesiser Replacement
MN 6040 A				

This system is known as straight binary. One other form is used in the systems discussed in the article ie. Binary Coded decimal or BCD. In this case only symbols up to 2^3 are used as number indicators, either two or three additional channels are used as decimal point indicators, and multipliers.

