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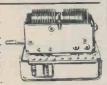


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		4004	6 <b>p</b>	IN5		20p	
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6-0-6 100MA (9-0-9 75MA, 12-0-12 50MA 75p each (15p) 0-4-6-9 150MA NO MOUNTING BRACKET, 65p (20p) 12-0-12 100MA 95p (15p), 12V 500MA 95p (22p), 12V 2 AMP £2.25 (45p), 12VOLT 4 AMP 2-75p (54p) 15-0-15V 1 AMP 2-10p (45p), 30-0-30V 1 AMP 2-75p (54p), 0-12-15-20-24-30V 1APPED AT 2AMP 4-50p (54p), 20-0-20V 2AMP 3-50p (54p), 25V 1-5AMP 1-45p (54p), 18V 1-5AMP RECTIFIED 2-00P (45p), 35V 2A, 2-5V 2A TOROID 2-95p (54p), 20VOLTS 2-5A 2-20p (54p).

I 1 XENON/TRIAC PULSE TRANSFORMERS 30p.

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SPECIAL OFFER: ZN414 RADIO CHIPS 75p. LM380 80p. LM381 95p. COLVERN 3K 5W wirebound pots 20p.

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50 VAC CAM UNITS, WITH 10 C/O MICRO SWITCHES, SUPPLIED WITH CAPACITOR FOR 240 VAC USE. EX EQUIPMENT. £1.95 (+35p p&p)

SWITCHES: MIN. TOGGLE SPST B x 5 x 7mm 45p, DPDT 8 x 7 x 7mm 50p, DPDT CENTRE OFF 12 x 11 x 9mm 75p, MIN. PUSH TO MAKE OR PUSH TO BREAK 16 x 16mm 15p EACH TYPE. SLIDER SWITCHES: DPDT MIN. 12p, DPDT C/OFF 20p, MICRO SWITCHES: STANDARD SIZE ROLLER ACTION 15p, MIN. 13 x 10 x 4mm 20p, PLESSEY WINKLER SWITCHES. 1 POLE 30 WAY 2 BANK ADJUSTABLE STOP 76p. 8-WAY RIBBON CABLE, MIN. SOLID CORE, 15p metre.

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Black, curly, 10' approx with stereo jack plug...50p 741S (wide bandwidth) 8-pin DIL..., 35p

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JAP 4 gang min. sealed tuning condensers

VALVE B	ASES	
Printed circuit B7G Chassis B7-B7G Shrouded Chassis B7G-B	11p	Car type panel lock and key 65p
B12A tube. Chassis B9A	13р	Transformer 9V 4A
Speaker 6" x 4" 5 ohm ide $4\frac{3}{4}$ " diam. 30 $\Omega$ $2\frac{1}{4}$ " diam. 32 or $8\Omega$ .	eal for car radio £1.55 £1.75	£3.78 Aluminium Knobs for ‡" shaft. Approx.
TAG STRIP-6 way 4p 9 way 6p Single 11p	5 x 50pF or 1000 x 300pF trimmers 35p	8" x 8" with indicator Pack of 5 95p
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self tapping screws 57p clear perspex sliding lid, 46 x 39 x 24 mm 15p.

ABS, ribbed inside 5mm centres for P.C.B., brass corner inserts, screw down lid, 50 x 100 x 25mm orange 65p; 80 x 150 x 50mm black 97p; 109 x 185 x 60mm black £1.52.

DIECAST ALI superior heavy gauge with sealing gasket, approx  $6\frac{1}{2}$ " x  $2\frac{3}{8}$ " x  $1\frac{3}{8}$ " £1.50;  $3\frac{3}{4}$ " x  $2\frac{3}{8}$ " £1.25.

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Up to 15w w/wound 10p 1 or 2% 5 times price

Cinch 8 way std 0.15

pitch edge connector

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12v d.p.c.o. heavy duty octal ... £1

700 ohm 11-31 volt minla-

185 ohm 6/12V miniature sealed 4 p.c.o. £1.45

POTS Wirewound

Log. or Lin., carbon rotary or slide. Single 30p With switch 40p Dual 45p Dual switch 55p 1.5m Edgetype 10 for 40p

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E299DDP120 / 218 / 224 / 338 / 340 / 350 / 352 · VF020 · E220ZZ002 ·

blue/fawn/green

KR150

Slider, horizontal or cal standard or submin 8p

.£1

ture sealed d.p.c.o.

1-1-1 watt 1 watt

		SWITCHES		
Pole	Way	Туре		
1	2	Slide	 	15p
6	2	Slide	 	24p
2	1	Rotary Mains	 	28p
2 2. 2	Alternating	Micro with roller	1	30p
2.	3	Miniature Slide		20p
	1	Toggle	 	32p
1	2	Sub-Min Toggle		75p
2	Alternating	2A Mains Push		- 1
		(¾" hole)	 	43p
CPCT	F 10 amm	240.		

S.P.S.T. 10 amp 240v. white rocker switch with neon. 1" square flush panel fitting 38p; Sidleen/AFA Very High Security barrel Key Switch, 2 tubular keys ...£2.95 Standard thumb-wheel switch 0-9 in 1248N or B or Comp. 1242 also 2p co...... £1.20 Standard Lever Key Switch D.P.D.T. locking plus D.P.D.T. and S.P.S.T. Heavy Duty non latching 82p

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n din to open end, 1 ½ yd, twin screened	45p
ole jack both ends 4ft	£1
ole jack plug to tag ends, 4ft	45p

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1k horizontal preset with knob 10 for 40p	3" Tape Spools 5p 1" Terry Clips 5p 12 Volt Solenoid 40p
CAINA A. A. C. C. C. C.	

ENM Ltd. cased 7-digit counter 21 x 11 x 11 approx. 12V d.c. (48 a.c.) or mains

		for	12v	Nicads,	ex-new
equipn	nent				£5.19

meter approx 7" diam	
Nicads, ex-new £5.19	YG150-S534 bead, KB13, E299 DHP230, 116-121 401 (TH7. VA1104, OD10)
) or mains £1.10	KR150 All 22p E23 glass bead 85p

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ELEC	TRO	LYT	ICS	Ma	ny .	other	s in	stock
Up to MFD	10V	25V	50V	63– 75V 1		200- 250V	300- 350 V	450- 500V
10	6р	7p		10p		15p	26p	32p
25	6р			10p			32p	
50	-6p		7p	12p	16p	23p	32p	
100	7p	8p	13p	15p	24p	26p		·-
250	12p	13p	15p	22p	36p		£1.10	£1.30
500	13p	15p	22p	30p	55p	_		£1.60
1000	16p	27p		60p		£1.05		_
2000	28p	47p		93p		_	_	

As total values are too numerous to list, use this price guide to work out your actual requirements 8/20, 10/20, 12/20, 22/50, 47/25. Tub. Tant 24p each 16-32/275V, 100/150V 100-100/275V 40p 50-50/385V, 2+2/200V non polar, 32-32-50/300V 20-20-20/350V 0.1+0.1/500V AC 80p 700mfd/ 200V, 100-200-60/300V £1:30 100-300-100-16/300V £1.85

RS 100-0-10	0 micro amp	null indicator
Approx. 2" x	₹" × ₹"	£1.85

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Bulgin D676 red, takes M.E.S. bulb	38p
12 volt, or Mains neon, red pushfit	
R.S. Scale Print, pressure transfer sheet.	12p

CAPACITOR GUIDE — maximum 500V Up to .01 ceramic 4½p. Up to .01 poly 6p .013 up to .1 poly etc. 7p. .12 up to .68 poly etc. 8p. Silver mica up to 360pF 10p, then to 2,200pF 13p; then to .01 mfd 21p. .1/750 13p. .01/1000, 8/20, .1/900, .22/900,

4/16, 25/250 AC (600v/DC). 15p. 5/150, 10/150, 40/150 50p. Many others and high voltage in stock.

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CONNECTOR STRIP Belling Lee L1469, 4 way polythene. 9p each

11 glass fuses 250 m/a or 3 amp (box of 12) Bulgin 5mm Jack plug and switched socket (pair	20p 40p
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GRUNDIG REGULATED TAPE MOTOR 6V nominal approx. 3 x 1½". Incl. shock absorbing

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Ferric Chloride, Anhydrous mil spec 1lb bag 97p RS neg. volt regulator 103, 306-099 (equiv. MPC900) 10A, 100 watt 4-30 volt. Adjustable short circuit protection. Normally £12.50+. £6.65

total relay with wer supply, r separately. sold sepa 4 steps of Mains p relay remote output. 2 Minitron, 7 segm 9 Counts unit. Digital count BCD), reed re Displays on 2 count

windows

for attack

cord-powered

9

Capacity

NICAD

rechargeable N a.h. at 10 hour

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Ex-new 450K.

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Battery Burglar doors,

£50 deduct 10% £100 deduct 20% Order E50 c Mail ( Over Over

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Crouzet 30-programmer, contacts 30-

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50 wafer 52 buzzer 10-way 8-12V L 10 cased "Makaswitch" Wood

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3 pc

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15.20 di	SEMICONDUCTORS Full spec. by Mu	ullard etc. Many others i		OTHER DIODES
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panels with 5A cermet pot. 4 copular transiste	BC186/7 BC213L/214B BC261B BC261B BC327/8 337/8 BC547/8+A/B/C BC556/7/7B/9 BCX32/36 BCY31 BCY30/1/2 BCY31/1 BCY30/1/2 BCX31/211 BCX32/36 BCX31/36 B	6C 8p OC45(ME2) 324 23p ON222 35p R2008B/2010B 31p TIP30 Mosfet£1.15 E.T 46p IS88A F.E.T. 47805 21p ZT1486 27X300/341	13p 23p 65N7/6K7 89p 65N7/6K7 89p 64T6 89p 50p 28p 1281 59p 1281 5	ture wire, 19/0.16, minus 55° to 105°C, 600V 3A, white, black or red. Half trade price at 54p 10M coll.  PVC QUALITY TAPE Lasso 10m x 15mm grey 38p 33m x 33mm green
Lamp cont turn trimpc indicator, 1 etc.	Amp   Volt   BRIDGE RE	20393 (MA393 2N456A 30p 73p 20p 20p 21.10 48p 58p	136p 57p 136p 50% £4.90 16p 45p £1.15 18p 35p 35p 35p 36p 50% £4.90	E1.13p  Trimmer: Post stamp type 3-30pF 16p 10-80pF 19p 30-140pF 23p  GARRARD GCS23T Crystal Stereo Cartridge £1.20 Mono (Stereo compatible)
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supple £14.	M1 1 68 5p 1 6/800 6p	BPX40 57p OCP71	The same of the same	IVRISTORS
power su £1	1N4007/BYX94 1 1250 8p BY103 1 1,500 21p	BPY10 92p BIG LI 2v 50r	n/A max. 1 400 BTX18-300	
	SR400 1.5 400 10p REC53A 1.5 1.250 16p	BPY68 BPY69 92P Wire en	d neons 6.5 500 BT107	R/SCR957/BRC4444. £1.14
d, regulated, andbook transformer	LT102 2 30 15p BYX22-200 1½ 300 25p BYX38-300R 2.5 300 48p	PHOTO SILICON CONTROLLE BPX66 PNPN 10 amp	D SWITCH 20 600 BTW92-600	R Pulse Modulated £8.75
rd 12-0-12V, 1.4A stabilized, regulox. 8½" x 4¾" x 3½" with handbook.  5A Double section bobbin transform in the section or flanded control or fla	BYX48-900 6 900 70p  BYX48-1200R 6 1,200 92p  BYX72-150R 10 150 42p  BYX72-300R 10 300 52p  BYX42-500R 10 500 65p  BYX42-300 10 300 36p  1 115401 3 100 16p	3" red 7 segment L.E.D. 14 D.I.L. 0-9+D.P. display 1.9 19m/a segment, common onde	PAPER BLOCK CONDENSER 0.25MFD 800 volt 87 1MFD 250 volt 56 1MFD 400 volt 88  TV KNOB Dark grey plastic for recessed shr (quarter inch) with free shaft extensis  CHASSIS SOCKETS Car Aerial 11p, Coax 8p, 5 pin 181 11p, 5 or 6 pin 240° din 8p, speak din switched 13p, 3.5 mm switched 1	TAA700 £2.40 TBA800 £1.24 741 dil. op amp uA702 op amp 53p (72) 709 52p SN76013N/ND £1.40 SN76228N £2.03 SN76131N £1.55 SN74107N 38p TAQ100 AMRF £1.22 CA3001 R.F. Amp 58p CA3132 £2.22 CD4013 CMOS 41p CD4069 TAA300 1 wt Amp£1.15
Approx	MR856 3 600 24p BYX42-900 10 900 92p BYX42-1200 10 1,200 £1.07 BYX46-300R* 15 300 £1.19 BYX46-400R* 15 400 £1.75 BYX46-500R* 15 500 £2.00	McMurdo PP108 8 way edge Multicore Solder ½kg. 16 or s.w.g. 60/40	18 or 20 £5.00 £1.15 2500 mfd. 40v : 56 0.1 mfd. 350/500v 10 for 50 10000 mfd. 15v 3 for £1.1 6800 mfd. 10v 3 for 90 32+32/275v 3 for 90	7400/4/10/20/30 16p 7402/4/10/20/30 16p 7414 64p 7438/74 27p 6 7414 64p 19 7483 79p 7493 41p
e. Cheques clearance: ce. Foreign 4-6 weeks	BYX52-1200 40 1,200 £2.90 RAS310AF* 1.25 1,250 48p	ASZ21 35p 2G302 2G401	6p 8+8 mfd. 375v4 for 90	P LM1303N £1.15 74154 £1.02
service. ensure cl service. id take 4	*Avalanche type  Amp Volt TRIACS	BCY30-34 24p 2N292 BCY70/1/2 10p 2N598	6 6p 25000 mfd. 25v 65 /9 8p 12000/12v 3 for £1.10	ZN414 Radio Chip £1.40
	8 400 Plastic RCA £1.40 25 900 BTX94-900 £4.50 25 1200 BTX49-1200 £6.75	HG1005 12p 2N130 HG5009 4P 1N190	12 10p capacitors 013, 17 £1.17 061, 066, 069, 075, 01	with secret fitting screws 11p  Belling Lee white plastic
# # O A	RS 2mm Terminals	HG5079 4P Germ. L78/9 4P 2N305 M3 12P Mot	diode 2p 10 for 65 5 Philips Head Cleaner Tape 32	P surface coax outlet box40p  Miniature Axial Lead Ferrite Chole formars 5 for 13p
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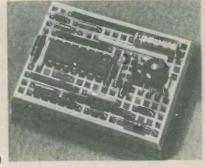
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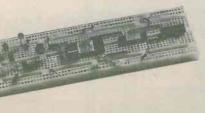
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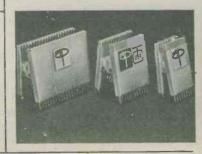


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	-	220				10p
			-	470		15p
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10V		220				5p_
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7428		35	32	7497	185		74162	92	130	74367		43	matching to a max	he
7430		17	24	74100	119	4	74163	92	7B	74368		49	of 6 diodes 25p per diode (1%)	
7432		25	24	74104	63		74164	104		74375		60	KV1210 275p	S
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7450		17		74122	46		74181		350	8629	divide	by 100	it is ready (about	-
7451		17	24	74123	48		74182			to 150		4.20p		1
7453	1	17		74124		1	74183		210				send 45p to reserve	
7454	1	17	24	74125	38	44	74184	135	1			n 7.80p	а сору.	
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# "EASI-BUILD" 100kHz CALIBRATOR

By M. V. Hastings

# Simple low-cost design takes advantage of BBC transmitter frequency accuracy

While not absolutely essential, an accurately calibrated tuning dial is a desirable asset for a communications receiver, and it can make the set easier to use by saving a lot of wasted time in searching for a particular band or station. In order to provide a home-constructed receiver with an accurate tuning dial it is necessary to have some form of calibration oscillator. The same is true if a set which is already calibrated undergoes realignment, and it is also possible for a receiver's calibration to become inaccurate due to mechanical or electrical drift.

The highest quality calibration oscillators are crystal controlled, have outputs at several fundamental frequencies and their harmonics, together with an a.f. modulating oscillator which can be switched in or out to make the signal more readily identifiable. The main problem with such a unit is that it is rather complex and expensive.

More than adequate results can be obtained from a very simple calibration oscillator, although it will be inevitably less accurate than a more sophisticated unit and will also be less easy to use. Nevertheless, a calibrator incorporating an LC oscillator instead of a crystal controlled oscillator can provide very accurate results if it is carefully set up, and there are few communications receivers, especially of the general coverage type, which can really make full use of the accuracy provided by a sophisticated crystal controlled unit.

# HARMONIC CALIBRATION

The calibrator described in this article provides a 100kHz signal which is rich in harmonics, these being available at good strength up to frequencies above 30MHz. It is therefore suitable for use over the entire medium and short wave bands. It is inexpensive and easy to construct, and is a practical accessory for even a very simple receiver.

For those unfamiliar with this type of equipment, it should perhaps be explained that it consists of an oscillator running at a basic frequency of 100kHz. Harmonics of this fundamental frequency are produced, these simply being signals at multiples of the fundamental frequency. Thus, a 100kHz calibrator will have outputs at 200kHz, 300kHz, 400kHz and so on. The circuit must be designed so that the harmonic output extends at good strength throughout the required wavebands, as it is the harmonics which provide the calibration

Obviously, the lower the fundamental frequency the greater will be the number of calibration points which will be produced. With a large number of calibration points it becomes more difficult to determine which is which, particularly at the higher short wave frequencies. More sophisticated calibrators may have two fundamental frequencies, one considerably higher than the other. For example, calibration points at 2MHz intervals may be provided, and these will be so far apart on the receiver dial that it is obvious which harmonic is being picked up. The 2MHz points can be marked on the dial, or noted, after which intervening points can be located by switching over to a lower fundamental frequency, say at 100kHz. Identification of these harmonics is then quite straightforward. Successive 100kHz harmonics above 6MHz, for instance, will be 6.1MHz, 6.2MHz, 6.3MHz, etc.
When using a single fundamental frequency, as

here, it is necessary to choose one which is a good compromise between an adequate number of calibration points and ease of identification. 100kHz is probably the best compromise. There may be some problem with identifying the harmonics, especially at high frequencies, but stations of known or approximately known frequency can be helpful in this respect, as will be explained more

fully later on.



The case specified has a compartment which accommodates a PP3 battery

# COMPONENTS

Resistors (All  $\frac{1}{4}$  watt 5%) R1 4.7k  $\Omega$ R2 560k Ω R3 1k  $\Omega$ 

Capacitors

C1 0.33µF ceramic plate C2 0.0047µF polystyrene

C3 390pF polystyrene or silvered mica C4 0.0068µF ceramic plate

C5 3.3pF ceramic plate or silvered mica

L1 7mH variable inductor type CAN1980 (Toko)

Semiconductors TR1 2N3819

TR2 BC108

Switch

S1 s.p.s.t. toggle, rotary

Sockets

SK1 wander plug socket, red SK2 Wander plug socket, black

Miscellaneous

Verobox type 65-2036H Veroboard, 0.15 in. matrix

Control knob

9 volt battery type PP3 (Ever Ready)

Battery connector Wire, solder etc.

### THE CIRCUIT

The circuit of the calibrator unit is given in Fig. 1, and it consists of a basic LC oscillator stage around TR1 followed by a buffer amplifier and

harmonic generator, TR2.

TR1 is connected in the common drain Colpitts configuration, with R1 as the source load resistor. Gate biasing is provided by the d.c. circuit through L1, and this coil also forms a 100kHz tuned circuit in conjunction with C3. L1 is an adjustable inductance and this enables the tuned circuit to be set up to the correct operating frequency. The source of TR1 is in phase with the gate and the necessary positive feedback for oscillation is provided from a tap in the coil via C2. The voltage gain from TR1 gate to its source is slightly less than unity; coupling the source to the tap in the coil causes L1 to provide sufficient voltage step-up to produce oscillation.

The output from TR1 is virtually a sine wave and is not sufficiently rich in harmonics for the present application. Also, although the impedance at TR1 source is fairly low since the transistor is used in the common drain mode, variations in output loading at this circuit point can still produce small but significant changes in the frequency of oscillation. Both of these problems are overcome by

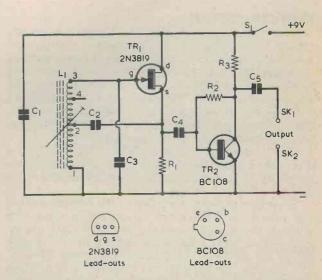
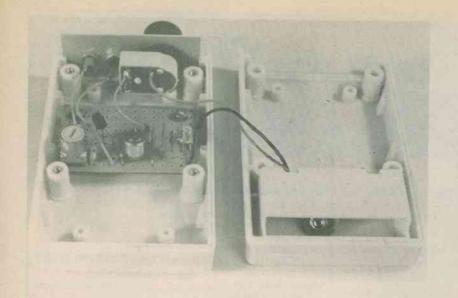
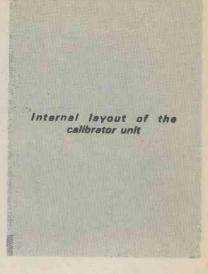


Fig. 1. The 100kHz calibrator has a very simple circuit. TR1 is a sine wave oscillator whilst TR2 functions as a buffer amplifier and harmonic generator





interposing a conventional common emitter amplifier, TR2 between TR1 source and the calibrator output.

TR2 produces a strong harmonic content and virtually eliminates any output loading effects. It also causes the mark-space ratio of the 100kHz fundamental it produces to be far removed from a one-to-one ratio. This is beneficial, as the even harmonics, at 200kHz, 400kHz and 600kHz, etc. would otherwise be very much weaker than the odd harmonics at 300kHz, 500kHz, 700kHz, and so on. Apart from the gradual falling off in strength of the higher frequency harmonics, there is still some difference in strength between even and odd harmonics, but this is of a comparatively minor nature and is of no real consequence.

C5 provides d.c. blocking at the output and C1 is a supply bypass capacitor. S1 is the on-off switch, and the current consumption from the 9 volt supp-

ly is approximately 3mA only.

### CONSTRUCTION

The prototype is built in a Verobox type 65-2036H, which has outside dimensions of about 110 by 68 by 33mm. This box has an integral battery compartment which will accommodate a 9 volt battery type PP3. As can be seen from the photographs, the on-off switch is mounted at the front panel in company with the output sockets, SK2 being nearer the switch. The PP3 battery fits in its compartment at the rear.

The remaining components are assembled on a Veroboard panel of 0.15in. matrix having 9 copper strips by 14 holes. The component layout and other

details are given in Fig. 2.

Start by cutting out a panel of the correct size using a hacksaw. Then clean up any rough edges with a small flat file and drill the two 6BA clear mounting holes. Next, the three breaks in the copper strips are made at the appropriate points. Finally, the various components are soldered into position on the board. This procedure is quite straightforward except in the case of L1.

L1 is a ready-wound component which can be obtained from Ambit International. Its five pins will fit straight into the component panel with pin 1 at hole A3, pin 2 at hole B3 and pin 3 at hole C3.

The two mounting lugs of its can do not, however, fit so readily. The lug corresponding to hole C2 is simply bent at right angles out of the way, or is removed with a pair of wire cutters. The other lug passes through hole A2, which is enlarged for it by being drilled with a bit of about 1.5mm diameter. The lug is soldered to the strip at hole A2, thereby connecting the screening can to the negative supply

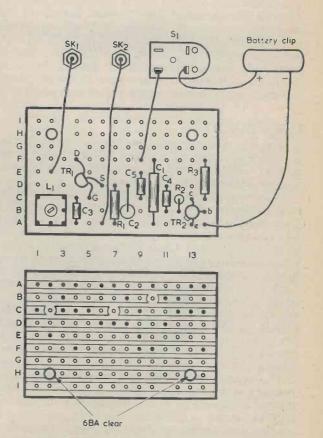
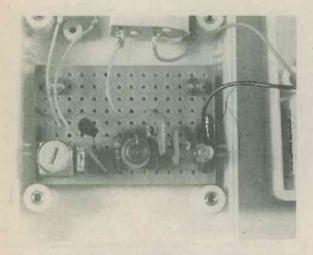


Fig. 2. The calibrator components may be conveniently assembled on a small Veroboard panel

The component panel is mounted in the bottom of the case, in the position shown in the photographs, by means of two 6BA bolts and nuts. Spacing washers on the bolts provide clearance between the Veroboard underside and the case. The board is not finally fitted in position until the connections to the output sockets, the on-off switch and the battery clip have been made.



A Veroboard module takes the components which make up the calibrator circuit

# ADJUSTMENT AND USE

Before the calibrator is ready for use it is necessary to adjust the core of L1 to bring the output frequency to 100kHz. This is achieved by beating the second harmonic with the BBC long wave transmission on 200kHz (1,500 metres). An ordinary transistor radio is tuned to this transmission and a short lead from socket SK1 is placed close to it. This may produce an audible beat note from the receiver as soon as the calibrator is switched on; if not, the core of L1 should be adjusted until the beat note is heard.

switched on; if not, the core of L1 should be adjusted until the beat note is heard.

A black insulated socket is used for the earthy output connection and a red insulated socket for the harmonic output

L1 is then carefully adjusted for the lowest beat note frequency possible, and there should be no problem in obtaining a beat as low as a few Hertz or even less. The calibrator oscillator will then be accurately set up to 100kHz.

The circuit does not have the same long term stability as is given by a crystal controlled oscillator, and it is therefore advisable to initially set

up the unit in this way each time it is used. For easy adjustment, a hole can be drilled in the lid of the case above L1, allowing access to its core without having to remove the lid.

When calibrating a receiver it will not normally be necessary to connect the output of the calibrator direct to the aerial input of the receiver. One short insulated lead may be connected to output socket SK1 and another to the receiver aerial terminal, whereupon satisfactory signal transfer should be obtained by placing the two leads close together. At high frequencies it may be necessary to twist the two leads together or even make a direct connection between SK1 and the receiver aerial input. The degree of coupling required depends on receiver sensitivity at these frequencies. It is advisable not to have a level of signal injection which is any higher than is necessary to produce a moderately strong signal at the receiver. Most receivers have spurious responses and an excessively strong signal from the calibrator could therefore produce confusing results. There should be sufficient signal transfer to make the calibration signal stand out from any other signals which may be picked up. The aerial must, of course, be disconnected from the receiver to minimise such signals. If there is any doubt as to whether or not a received signal is generated by the calibrator, the matter can be settled by switching the calibrator off and then on again. In many instances there will be no need to make any connection to the earthy output socket, SK2. If, however, this socket is connected to earth or to the chassis of the receiver being calibrated it will be found that the coupling to the receiver aerial input is of a more consistent nature.

As was pointed out earlier, it can sometimes be difficult to determine which harmonic is being received. One way around this problem is to locate a broadcast station of known frequency. Should the station be operating on, for example, 6.25MHz, the harmonics above this will be 6.3MHz, 6.4MHz, 6.5MHz, and so on. Those below it will be 6.2MHz, 6.1MHz, 6.0MHz, etc.

The amateur bands can also be helpful in this respect. Under reasonably good conditions these



are normally crammed from end to end with stations and so the approximate band limits are quite well defined. The calibrator can then be used to indicate the precise band limits and calibration points within the band. Once these calibration frequencies have been identified, the frequencies of the calibration points adjacent to them become obvious.

NEWS . . AND

# THE PET — A COMPUTER THAT CAN TEACH

Now available from Commodore is a new tutorial program in-troducing the newcomer to the Basic Computer Language. The program, which has been written by two experienced college professors, is one of a number of new software cassette releases by Commodore Systems designed to be used in conjunction with their £695 PET computer. "BASIC BASIC" is thoroughly interactive and enables the PET computer to teach you how to operate it. The newcomer to com-puting can now actually learn BASIC in several hours and then begin his own programming. The topics include line numbers, variables, strings, arrays and the use of the various commands such as LIST, RUN, and SAVE. Also basic keywords will be explained and used such as PRINT, READ/DATA, INPUT, IF/ THEN, GOTO, and FOR/NEXT. fifteen chatpers, six sample programs ... and homework assignments.

The price of the cassette is £9.00 and will undoubtedly further increase the enormous appeal of the PET Personal Computer within the educational field and to the newcomer to computing.

This is the first example of the considerable potential for teaching



G. R. Electronics add Video Board and Pocket Terminal to Commodore's KIM1 Microprocessor, using a domestic TV as visual display unit

where a student interacts with a computer. The student is able to proceed just as fast or slowly as his own ability allows, while the computer becomes effectively a textbook in which information is displayed on the screen — an exercise book and pen where information is written in via the keyboard and finally the intelligence of the computer enables it to ask questions of and talk to the user. Although used to teach in this first instance about

computers, the technique can be potentially expanded to many areas of education. It is considered to be a very powerful method of maximising an individual's learning capacity.

Around 21% of Commodore's overall sales of PET computers in the UK have been in educational establishments, colleges and universities, and a big expansion is expected with the start of the new academic year.

# COMING SHORTLY-PROGRAMMABLE CALCULATORS

**Exclusive New Series** 



# A WATCH FOR THE ACTIVE

Casio's new F-100 digital wristwatch is designed primarily to appeal to active people — sportsmen and youngsters particularly.

Listed at a recommended retail price (including VAT) of only £29.95, it features a super lightweight construction, coupled with dramatic and rugged styling in keeping with its intended environment.

In standard mode, Casio F-100 continuously displays accurate time in hours, minutes and running seconds, together with a clear indication of am/pm. Press a button to show date, month and day of week. Another button converts it from an ordinary watch into a one-hundredth of a second stopwatch able to record lap times as well as normal start/stop. The same button restores the watch to standard time-keeping mode, and a third button provides backlighting of the display for use in the dark.

Casing is an extremely tough, durable non-metallic black material. Figures in the display are large, clear and easy to read and the buttons are colour coded to make for simple operation. A lightweight sports strap completes the outfit.

For those who like the technical specification of F-100 but prefer a stainless finish case and bracelet, Casio offer the same watch with a stylish stainless steel outer case and bracelet at £44.95.

Prices quoted above are "recommended", and should be interpreted as the maximum anyone should expect to pay.

# COMMENT

# TECHNOLOGY EQUALS REDUNDANCY?

There is much concern being expressed that the rapid advances being made in technology are going to lead to massive unemployment. As people who are interested in electronics we must be aware of the wider implications of our interest.

At a press conference to mark the 50th anniversary of Motorola Inc., founded as Galvin Manufacturing Corporation in 1928, the President, Mr. William-J. Weisz, in the course of his address had

these reassuring words to say:

"Throughout history there has always been fear that technology is eliminating jobs. This is a misguided notion. Technology is changing the complexion of work. It is eliminating the routine and mundane in favour of the more economically productive and socially satisfying. As an example, the development of the microprocessor has spawned all types of new businesses making new products that were impossible before. This both improves life quality and provides new jobs. Electronics is permitting us to respond in innumerable ways to people's needs to communicate, to measure, to control and to compute. By so doing, we are free to find more meaningful work and more productive uses of human time, talent and energy."

# **BBC WAVELENGTH CHANGES**

We have, dating back to our February issues, given information and background to the changes which will have been operating for some days by the time these notes are read. In next month's issue we will be publishing an article — RADIO 4 CONVERTER, which converts 1,500 metres to medium waves. The converter radiates a local signal which is picked up inductively by the receiver on its own ferrite aerial.

It is of interest that, by mid-November coinciding with the new wavelengths, Marconi Communications Systems will have supplied eleven, out of a total of 24, of their new B6034 50kW transmitters, plus change-over units, diplexers and

triplexers.

The transmitters are being used to update existing BBC transmitting stations, some of which were installed by Marconi in the early 1930s, and when the re-equipment programme is complete the BBC's medium frequency UK network will be capable of fully automatic and unattended operation

The new B6034 is one of the world's most compact, efficient and reliable 50kW broadcast transmitters, Measuring 12ft x 5ft x 6ft 9in, the B6034 is less than half the size of its immediate 50kW predecessor — the Marconi type B6031. The advanced solid-state design incorporates only four vacuum tubes for the high power stages. It also employs an improved version of the Doherty type modulation system. Suitable for both local and remote operation, the equipment's operating frequency range is 525kHz to 1605kHz.

# TOOLS TO SPEED ELECTRICAL INSTALLATION REPAIRS

One of the problems associated with the maintenance and repair of electrical installation lies in making a secure connection from the wire to a replacement terminal. Now AB Engineering Company has introduced a new range of neat, lightweight crimping tools to provide a solution to cramped spaces and difficult access situations.

The new AB Crimpex range of crimping tools includes five different models for insulation and tube terminals or splices. All feature a fixed and moveable jaw, with a step-down rate leverage system between the jaw and handle which starts low — so the handle can be moved freely, then "accelerates" towards the end of the crimping process when the handles are close together and the operations natural grip can be used to complete a perfect crimp.



The models available offer twin and triple aperture jaws for 0.75-6mm<sup>2</sup> (SWG 18-10) terminals and splices with symmetrical crimping nests—terminals can be inserted from either side for right hand or left handed operation, and the handles are brightly coloured in red, blue, yellow and green for instant recognition of the model required.

instant recognition of the model required.

Full details of the Crimpex range may be obtained from AB Engineering Company, Apem Works, St. Albans Road, Watford, Herts. WD2

4AN.

# CONSTRUCTOR KITS

Lektrokit are taking the opportunity at Breadboard 78 to launch a major new range of products for retailers of hobbyist and home constructor kits.

The company are showing a comprehensive selection of new simple-to-use solderless breadboards, terminal and distribution strips, connectors, pins, sockets, jumpers, i.e. test clips, heat sinks, cabling etc. All these will be available for sale off the stand as individual items or in kits.

Their freely-available illustrated catalogue, entitled "The Faster and Easier Book", lists full details of their new product range, examples of which will be shown in circuit configurations ranging from single-chip simplicity to multiple-chip complexity.



# CUNNING LIGHT ALARM By Ian Sinclair

This circuit sounds an alarm when light first falls on a photocell and is then taken away.

This is the second project in the 10-part series of "Double Deccers", which may be assembled on two S-DECs positioned side by side. It is for a light-operated alarm, but with the rather unusual feature that light falling on the sensor arms the circuit, and the alarm is not raised until the light ceases to strike the photocell. The alarm is then switched on until it can be reset, after which the circuit remains dormant until armed by another light flash. An action of this sort is a useful feature of a burlgar alarm, since an intruder is lulled into a false sense of security when no alarm is sounded by a light beam being switched on. For the sake of demonstration, the "alarm" here is a light bulb, but methods of sounding a full-scale alarm are discussed later.

# CIRCUIT ACTION

The light detector is a photoconductive cell, or light dependent resistor, type ORP12. This is wired in series with the  $10k\,\Omega$  variable resistor VR1, which controls the sensitivity of the circuit, so that light falling on the cell, either continually or in the form of a flash, causes the voltage at point 27 in the circuit to go sufficiently positive to switch on TR1. TR1 and TR2 are connected as a Schmitt trigger circuit, with R2 ensuring that TR2 conducts when point 27 is at a low voltage, which is when the photoconductive cell is in darkness. With TR2 conducting, the current in R3 keeps point 15 at a low voltage (less than a volt), and the same current flowing in R4 takes the emitter voltage of TR1 at

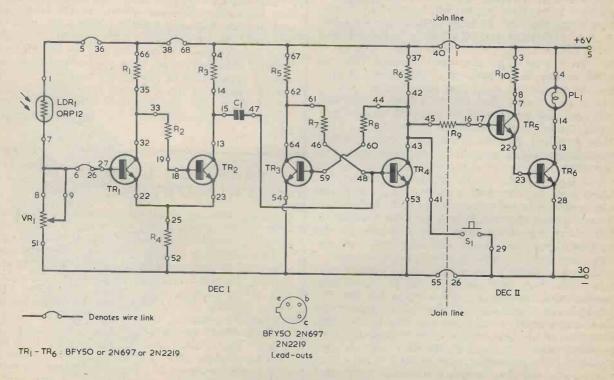


Fig. 1. Circuit of the alarm. This is armed when light falls on the photoconductive cell, the alarm being triggered when the light is taken away

A single S-DeC. Two of these, mounted side by side to form a single long DeC, can be used for all the projects in this series



point 22 high enough to keep TR1 switched off even when there is about one volt at point 27.

The second stage of the circuit is the bistable formed by TR3 and TR4. The effect of pressing the reset switch S1 is to make the collector voltage of TR4 equal to zero, so that no bias current can flow in R8, and TR3 is turned off. With no current flowing in TR3, the current through R5 flows through R7 into the base of TR4, keeping this transistor switched on and ensuring that the collector voltage of TR4 is low when the reset switch is released.

of TR4 is low when the reset switch is released. With a low voltage at the collector of TR4 no current flows through R9, so that TR5 is turned off. When no current flows in TR5 there is, in turn, no bias current into the base of TR6, so that PL1 is not lit. In our project, the alarm is indicated by PL1

lighting.

When the cell, LDR1, is illuminated, its resistance drops so that the voltage at point 27 rises. If the rise in voltage is sufficient, as set by VR1, then TR1 will turn on, and its collector current flowing in R1 will cause the voltage at point 32 to drop to a low value. There will now be no current flowing through R2, so that TR2 switches off, allowing the voltage at point 15 to rise to nearly

6 volts positive.

The capacitor C1 now comes into action. When the voltage at point 15 rises, the voltage at the other plate of the capacitor, connected to point 47, will also rise, and the rise in voltage causes current to flow between the base and emitter of TR4. C1 therefore charges, so that the final state is that point 15 is at about 6 volts positive with point 47 at about 0.6 volt positive, limited by the voltage across the base-emitter junction of TR4. The charging current flowing into the base of TR4 does not cause any change in the bistable, because TR4 is already conducting.

# COMPONENTS

Resistors

(All fixed values 1 watt 5%)

R1 1.8kΩ

R2 22k Ω

R3 1.8kΩ

 $R4 150\Omega$ 

 $R5 1.8k\Omega$ 

R6 1.8kΩ

R7 22k Ω

R8 22k Ω

R9 56kΩ

R10 1.8kΩ

VR1 10k Ω potentiometer, linear

Capacitor

C1 0.1µF polyester

Semiconductors

TR1-TR6 BFY50 or 2N697 or 2N2219

D1 1N4001 (see text)

Photoconductive Cell LDR1 0RP12

Switch

S1 push-button, press to make

Lamp

PL1 6V, 60mA, m.e.s.

Miscellaneous

2-off S-DEC

6V battery

Lampholder, m.e.s.

Relay (see text)

Control knob

When the light ceases to strike the photocell its resistance increases, so that the voltage at the base of TR1 decreases again. The Schmitt trigger switches over to its previous state, with TR2 conducting. The point of using a Schmitt trigger circuit is that these changeovers of voltage are rapid, much more rapid than the changes of voltage at point 27. This time round, the effect is that the collector voltage of TR2 drops suddenly.

The voltage drop at point 15 causes an equal voltage drop at point 47, due to the coupling action of the capacitor. This voltage drop, at point 48, is large enough to switch off the base current of TR4, so that its collector voltage rises. Current through R6 now flows through R8 into the base of TR3, turning this transistor on. The collector current of TR3, flowing in its load resistor R5, keeps the collector voltage of TR3 low, so that the current in R7 ceases and TR4 remains cut off after capacitor C1 recharges.

Since TR4 is not now conducting, current through R6 flows through R9 into the base of TR5, causing this transistor to conduct and turn on TR6. R9 and R10 act to prevent excessive dissipation in TR5. PL1 will now shine until the reset switch is pressed or the whole circuit is switched off.

For more serious purposes, the light PL1 can be replaced with a 6 volt relay whose coil is wired between points 4 and 14, as shown in Fig. 2. It can have an operating current between 12 and 70mA, and a good choice would be the "6 volt Open Relay" with 410 \Omega coil which is available from Relay" with  $410\,\Omega$  coil which is available from Maplin Electronic Supplies. A diode, such as the 1N4001, must be connected across the relay coil so that point 14 cannot be taken to a voltage much above 6 volts. This protects TR6 against excessive collector voltages when it turns off, because a sudden cessation of current through the coil of the relay causes a large pulse voltage to be generated. The contacts of the relay should be rated for whatever voltages and currents will be present, and can be used to switch sirens, bells, buzzers or any other form of alarm.

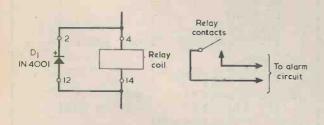


Fig. 2. For serious applications, PL1 is replaced by the coil of a relay, the contacts of which complete the alarm circuit. The latter is powered by a separate battery

S-DEC CONSTRUCTION

For construction as a Double-Deccer, join two S-DeCs end-on to form one long DeC. The dashed line in the circuit diagram shows how much of the circuit is built on each of the DeCs.

Start by inserting the wire links, five in all, which connect sections of the DeCs together. Insert also the resistor R9 which links the signal from one DeC to the other. The transistors can now be plugged in place, following the pinout diagram for the silicon n.p.n. transistors used for all these Double Deccer projects. The reset switch, the sensitivity control VR1, and the lamp PL1 can be mounted on the panels of the DeCs. The light detecting cell can be plugged directly into the DeC for testing, or used remotely if need be. Remember that stranded wire should not be plugged into the DeCs, so that if stranded wire is used for remotely located com-ponents the ends should be tinned lightly and smoothly to prevent the strands from separating before being plugged into the DeC holes.

Place C1 into holes 15 and 47 of DeC 1, and then insert the leads from VR1 into the appropriate points. Since the control is mounted on one of the panels it should be connected to the S-DeC by single strand insulated wires. Finally insert the remaining resistors and the leads for the battery

and the circuit is ready for testing.

### **TESTING**

Check all the connections, place the unit in a dimly lit room with its light turned off and set VR1 to its minimum resistance setting, i.e. with the slider at the end of the track which connects to point 51. Press and release S1 so that the circuit is reset. If PL1 lit up when the battery was connected it should now be extinguished. Now switch the room light on and off again — this should cause no change with VR1 at its least sensitive setting. Advance the setting of VR1 until switching the room light on and off again causes PL1 to light up when the room light turns off. This is now the correct setting of VR1 in the conditions of room lighting being used. If VR1 is too far advanced, the dim light of the room may arm the circuit and there will not be a sufficient drop in light intensity to trigger the

An important point is that VR1 should not be set to its minimum resistance point if LDR1 is in very strong light, such as strong sunlight. The photocell may then have a low enough resistance to cause excessive current to flow both in itself and in the potentiometer. This precaution does not apply when VR1 has been set up as just described under artificial light conditions.

The current drawn from the 6 volt supply is approximately 7mA when the circuit is not triggered to the alarm condition. When the circuit is so triggered the current increases by the amount taken by the bulb or relay coil, as applicable.

With regard to the first article in the series, published last month, Blob Board type ZB-6-D was shown as an alternative to using S-DeCs. We are given to understand that the boards are not at present being manufactured. If readers wish to continue with these alternative boards they should check the supply position with their component supplier before ordering.

# Trade News...

# SIGTRONIC ELECTRONICS ADD TO THEIR RANGE

Amongst the products available from Sigtronic Electronics, 27 Malvern Street, Stapenhill, Burton-on-Trent, Staffordshire DE15 9DY, are two new items namely, AVDEL BOND, cyanoacrylate, adhesive, which is a high strength, rapid cure adhesive, and although this product comes in a small 2GM/phial, it packs a mighty punch, in securing plastics, rubbers, transistors, components, etc. as it bonds quickly and permanently.

Another good feature of the adhesive is that only a small drop is needed to make a high strength bond, which exhibits a good resistance to stress, and it is also 'non toxic', and will if used sparingly and carefully make it a valuable addition to the 'constructor's tool box', at 75p plus 10p p&p, per phial.

The second product is a range of 'High Quality'



British made, ferric oxide CASSETTE TAPES, which are made under the name of ACADEMY, by a well known manufacturer and exhibit low noise and high output at high quality. All of which helps to produce excellent recordings, from the classics to pop, and are available in C60-C90 sizes at 62p and 72p (VAT included) plus 12p p&p, and are guaranteed for six months.

# DIGITAL THERMOMETERS WITH AUDIBLE ALARMS

The latest addition to Jenway's 8000 Series of digital thermometers is fitted with an audible alarm to warn of deviation from a given temperature.

A feature of the instruments is the use of solid state modular circuitry in their design, which provides the 8000 Series with its characteristic accuracy, reliability and economy of use. Fitted with HP9 dry cells as standard they provide up to 10 hours of continuous use, while the manganese alkaline MN 1500 cells offered as an option provide as much as 35 continuous or 80 hours intermittent

The unique feature of the 8000 Series, however, is the ability to detect and warn of deviations in the monitored temperature. When this facility is in

use, implying relatively long-term continuous monitoring of a given temperature, the instrument's LED display can be switched off to further improve battery power conservation. The temperature level about which deviations are to be noted, is set by means of a precision, multi-turn potentiometer conveniently located in the instrument's case. The control is adjusted while the display is observed, to obtain the desired setting. When the relevant temperature is reached or exceeded, the audible alarm is activated.

Jenway are the designers and manufactuers of what has become one of the widest ranges of handheld temperature and pH measurement instruments available in the U.K.

# Pye Electro-Devices franchise for Sasco

SASCO, the Crawley-based distributor of electronic components, has been appointed as a franchised distributor for the Controls Division of Pye Electro-Devices Ltd. Products initially stocked by SASCO include a wide range of relays. Pye Electro-Devices, formed in 1977 as the result of a merger between Pye TMC Components Ltd. and Magnetic Devices Ltd., has experienced in electromechanical components dating back more than 60 years, and manufactures components at six plants in East Anglia and Kent.

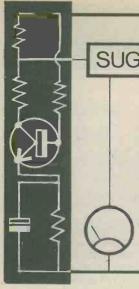
Products from the Pye Electro-Devices range handled by SASCO include the Series 120 (a.c.) and 125 (d.c.) compact, general purpose 2-pole changeover relays, the Series 130 and 135 3-pole versions.

# DARTRON D10 DUAL TRACE OSCILLOSCOPE

Dartron Instruments Ltd., of 280-282 Wood Street, London E17, makers of precision oscilloscopes and electronic instruments, have recently announced their latest oscilloscope model, the D10 Dual Trace Oscilloscope.

This latest model has an excellent specification and will be priced at under £200. Details may be obtained from the company at the address above.





# SUGGESTED CIRCUIT

# DISCRETE NAND GATES

By G. A. French

From the home constructor's point of view, CMOS logic has several advantages over the older t.t.l. technology. CMOS devices do not require a stabilized supply, and their current consumption is frequently negligibly low. So far as interfacing with external discrete components is concerned, CMOS gates will directly drive individual light-emitting diodes and 7-segment displays with reasonable brightness. However, an external transistor is required if it is desired to have a CMOS gate energise a relay, and this article describes means by which external transistor circuits can enhance the logic capability of CMOS and relay combinations.

# RELAY DRIVE

Fig. 1 shows one method by means of which a CMOS output, when high (i.e. at or near the potential of the positive rail), can cause a relay to be energised. The high output allows base current to pass via R1 into TR1, which thereby turns on and operates the relay. When the CMOS output is low (at or near the negative rail) the transistor is turned off and the relay is de-energised. The circuit is suitable for supply rail voltages of the order of 9 volts when the relay coil has a resistance of  $400\Omega$  or more and a suitable operating voltage. The voltage dropped across the transistor when it is turned on is, in practice, around 0.15 volt. R1 may be a  $\frac{1}{4}$  watt 10% resistor.

The output of the CMOS gate is not affected to any significant degree by the added resistor and transistor. It was found, for instance that when the output of a CD4011 NAND gate was connected to the resistor and transistor of Fig. 1, its high output voltage was only 0.1 volt negative of the positive rail.

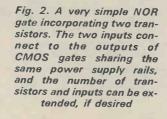
Fig. 1. Employing an external transistor to enable a CMOS gate output to drive a relay

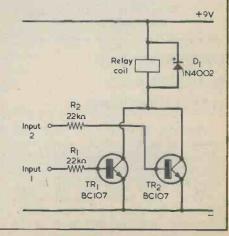
TRI BCIO7

In consequence the gate output can be connected to other gate inputs as well as to the transistor circuit.

Since it is necessary in any case to use a discrete transitor between the CMOS gate and the relay coil, the inevitable question arises whether simple transistor circuitry can be employed to augment logic performance in CMOS gate and relay applications. The answer to that question is that transistors can quite definitely augment the performance, and that they may be used to replace one or more CMOS gates.

Fig. 2 shows a transistor NOR





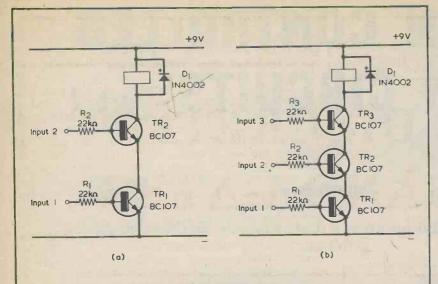


Fig. 3(a). A 2-input NAND gate. The relay energises only when both inputs are high
(b). With this circuit, all three inputs have to be high if the relay is to operate

gate. Its operation is virtually obvious at first sight, and we do not need to stay long with this particular configuration. The two transistor inputs are connected to CMOS gate outputs, and the relay

energises when either input, or both, is high. The relay is deenergised only when both inputs are low. If three NOR inputs are required a further transistor and series base resistor can be added,

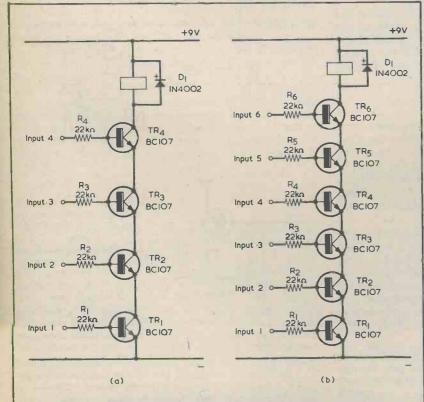


Fig. 4(a): A 4-input NAND gate employing bipolar transistors (b). Transistor 6-input NAND gate. It is possible to add even more transistors and inputs to the basic NAND configuration

the transistor emitter being connected to the negative rail and the collector to the lower terminal of the relay coil. The system can be extended to any number, within reason, of transistors.

### NAND GATES

The provision of transistor NAND gates is not so apparent, but here we can borrow a trick from internal CMOS circuitry itself, where NAND operation is accomplished partly by field-effect transistors connected in series. It is possible to similarly connect bipolar transistors in series.

A 2-input NAND gate appears in Fig. 3(a). When both Input 1 and Input 2 are low, both transistors are off and the relay is de-energised. If Input 1 is taken high TR2 remains turned off, and no collector current can flow in TR1. Similarly, if only Input 2 is taken high, the turned off TR1 prevents emitter current flowing in TR2. The two transistors can only be made conductive, and thereby operate the relay, when both inputs are high. The voltage dropped across the two transistors in series in then approximately 0.3 volt.

Fig. 3(b) shows a 3-input NAND gate incorporating discrete bipolar transistors. Again, the three inputs are connected to CMOS gate outputs All three transistors have to be turned on if the relay is to energise, and this can only happen when all the three inputs are high.

As with the NOR gates, the system can be expanded to take in more transistors and more inputs, but a limiting factor here is the voltage dropped across the transistors when they are turned on. Fig. 4(a) shows a practical circuit with four transistors and four inputs. All the inputs have to be high to energise the relay, and the voltage dropped across the series-connected transistors is then 0.6 volt. The 6-input NAND gate of Fig. 4(b) is also quite practical. Here, when all the inputs are high and all the transistors are conducting, the voltage dropped across them is of the order of 0.9 volt.

The author has checked out all the circuits accompanying this article, the relay he employed being the "Open Relay" with 410 Ω coil resistance which is available from Maplin Electronic Supplies. This particular relay is well suited to circuits of this nature since its minimum coil operating voltage is only 4.8 volts. It could, in consequence, be used in a transistor NAND gate array having even more transistors and inputs than appear in the 6-input gate of Fig. 4(b). In all the circuits it is assumed that the CMOS gates providing the inputs share the same power supply as that used for the relay coil and bipolar transistors.

# SILICON CONTROLLED SWITCH CIRCUITS Part 1

By

# John Baker

Working projects incorporating a device which deserves far more publicity

Although the silicon controlled switch has been available to the amateur for some time now, it has not really been featured to any great extent in the amateur electronics magazines. In fact, the author was unable to find a single constructional project employing one of these devices after an extensive search!

These devices, which are often given the abbreviated name 's.c.s.', are very versatile and are quite inexpensive, and so the reason for their lack of use is presumably merely that there are better known alternatives for the applications in which they can be employed. Thus they have often been ignored, even though they can have distinct advantages over alternative devices.

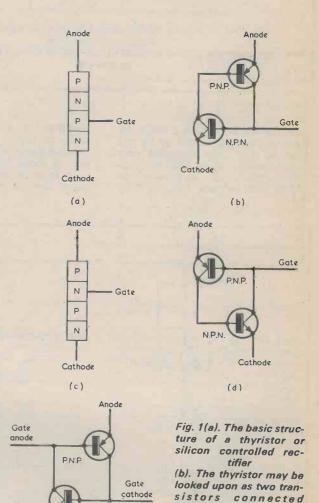
The purpose of this 2-part series is to outline the way in which an s.c.s. operates and some of the ways in which it can be used. Some practical examples of the type of circuit in which the device can be utilised will be given in the form of a few simple working projects.

### WHAT IS AN S.C.S.?

A silicon controlled switch is a development from two semiconductor devices which will be familiar to many readers. and it can be made to emulate either of these. The two devices are the thyristor, or silicon controlled rectifier (s.c.r.) as it is sometimes known, and the programmable unijunction transistor. It will be helpful to briefly discuss these two devices before considering the s.c.s.

A thyristor is a four layer device which has the basic semiconductor structure shown in Fig. 1(a). It is analogous to two transistors of different polarity connected as shown in Fig. 1(b). A thyristor is normally used to control a load in its anode circuit, the load connecting to the positive supply rail and the thyristor cathode to the negative supply rail. A control voltage is applied, when required, to the gate terminal.

With no gate voltage applied the device will be turned off since neither transistor has any significant base current. If the gate is taken about 0.6 volt positive of the cathode the lower n.p.n. transistor will begin to turn on and, in doing so, its collector will draw a base current from the upper p.n.p. transistor. In turn the collector of the p.n.p. transistor will apply a base current to the n.p.n. transistor,



N.P.N

Cathode

together in the manner

shown here

(c). The programmable

unijunction transistor has

its gate at the layer next to the anode

(d). Transistor equivalent

of the programmable unijunction transistor

(e). The silicon controlled

switch is a four layer

device with gate terminals at both of its in-

ternal layers

causing it to conduct more heavily. There is an overall regenerative action which continues until both transistors are turned fully on. It should be noted that the transition in the thyristor from being turned off to being turned on is very rapid, and that it can be initiated, if desired, by a very short

positive pulse at the gate.

Once a thyristor has been switched on it cannot be turned off again by reducing the gate voltage, and it can only be turned off by momentarily reducing the current through the device below a certain threshold level which is usually in the region of 20mA. The current only needs to be reduced below this level for a few tens of microseconds in order to switch the device off. Of course, the load current flowing through the device, once it has been switched on, should be above this threshold level in order to obtain correct operation. A thyristor is essentially a power device, and so this would normally be the case. A gate current of about 20mA is needed in order to switch a thyristor on. This may seem to be rather insensitive, but again it must be remembered that the device is normally used to control currents of 1 amp or more.

A programmable unijunction transitor does not really have much in common with an ordinary unijunction transistor apart from the fact that both may be used in fairly similar relaxation oscillator circuits. On the other hand, the p.u.t. is very similar to the thyristor and it is a four-layer device having the basic semiconductor structure shown in Fig. 1(c). The gate now connects to the n. section nearest the anode rather than to the p. section nearest the cathode. It operates in a similar manner to the thyristor, but the gate must now be taken negative of the anode to cause it to be turned on. The negative-going gate causes base current to flow in the upper p.n.p. transistor and initiates the regenerative action between the two transistors

which turns on the device as a whole.

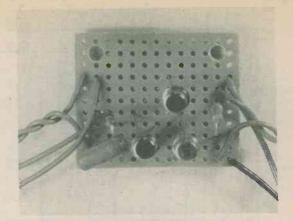
A silicon controlled switch is another four-layer device with one gate terminal connected to the p. layer nearest the cathode and a second gate terminal connected to the n. layer nearest the anode. If represented as two transistors, it has the configuration of Fig. 1(e). By ignoring the gate anode terminal it is obviously possible to obtain a form of thyristor operation and, similarly, by ignoring the gate cathode terminal it is possible to obtain a p.u.t.

action.

## TOUCH SWITCH

An s.c.s. can be used as a direct replacement for a p.u.t., but it is not suitable for use in place of a thyristor in most cases. An s.c.s. is not a power device and the BRY39 s.c.s. employed in the circuits to be described has a maximum dissipation figure of 275mW and a maximum "on" current rating of 175mA. However, the whole point of using the device in the thyristor mode is that it can be employed in low and medium power applications where a thyristor would not operate properly.

For example, the touch plate switching circuit shown in Fig. 2 would not function successfully if a thyristor were to be substituted for the s.c.s. Here, the relay is energised when the "on" touch plates are bridged by the skin resistance of the operator's finger. Obviously, only a very small current will flow into the gate cathode of the s.c.s., this being nowhere near the 20mA gate current which would



The Veroboard assembly for the touch plate switch

# COMPONENTS

### TOUCH SWITCH

Resistors R1  $100k \Omega$   $\frac{1}{4}$  watt 5% R2  $2.7k \Omega$   $\frac{1}{4}$  watt 5%

Capacitor C1 0.1µF type C280 (Mullard)

Semiconductors S.C.S. BRY39 TR1 BC108 TR2 BC179 D1 1N4148 (if required)

Relay See text

Miscellaneous Veroboard, 0.1in. matrix Materials for touch plates

## LOW POWER PILOT LIGHT

Resistors (All + watt 5%) R1 120kΩ R2 390 Ω R3 56kΩ R4 56kΩ

Capacitors C1 10µF electrolytic, 16v. Wkg. C2 10µF electrolytic, 16V. Wkg.

Semiconductors S.C.S. BRY39 D1 TIL209 or similar

Miscellaneous L.E.D. panel holder Veroboard, 0.1in. matrix

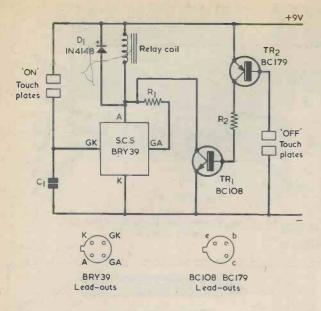


Fig. 2. A touch plate switching circuit incorporating a silicon controlled switch

be required by a thyristor. Less than 1µA gate current is required by the s.c.s. to turn on and the circuit has more than adequate sensitivity in this respect. In fact, spurious triggering to the "on" state can be more of a problem than lack of sensitivity, and R1 and C1 are included to improve reliability by preventing self-triggering.

After it has been turned on, the s.c.s. is turned off by bridging a finger across the "off" touch plates. The consequent small base current in TR2 causes a much larger base current to flow in TR1, R2 limiting this to a safe value. When the s.c.s. is turned on, the voltage drop between its anode and cathode is typically a little under 1 volt. When TR1 is turned on, by touching the "off" plates, the voltage between its collector and emitter is much lower, being only a few tens of millivolts. As a result the load current which flowed through the s.c.s. is diverted through TR1, and the s.c.s. becomes turned off. When the finger is taken away from the "off" plates the load is switched off as TR1 ceases to conduct, and it will only be switched on again when the "on" plates are touched.

The relay with its protective diode are only required if it is desired to control the circuit in which there is a high voltage or current, or where there is an a.c. supply. Control is then exerted by way of the relay contacts. Small d.c. loads such as transistor radios can be operated direct from the circuit and are simply connected in place of the relay coil and D1. The small voltage drop across the s.c.s. should not cause any problems here. A load current of only a few hundred microamps is sufficient to hold the s.c.s. in the "on" state.

If a relay is used in the circuit it can be any type having a coil resistance of about  $200\,\Omega$  or more and which is capable of energising at a nominal 6 yolts. The prototype was tested using an "Open Relay" having a 6 volt  $410\,\Omega$  coil, this relay being available from Maplin Electronic Supplies.

### CONSTRUCTION

The touch switch is assembled on a 0.1in. pitch Verboard having 13 holes by 11 copper strips, and details of the component layout are shown in Fig. 3. There are no breaks in the copper strips. D1 is mounted on the relay when these two components are used, its cathode (indicated by the plus sign in Fig. 2) connecting to the wire from hole D2.

Touch contacts can be made from copper laminate board using printed circuit techniques, or pieces of stripboard can be employed. There are other possibilities, and it is a matter of using one's initiative here. In the interests of good sensitivity the two touch plates should not be too small in area, and they must be kept free from excessive grease, dirt and corrosion.

The prototype touch switch is very sensitive and can be readily triggered from one state to the other, although the "off" function is a little less sensitive than the "on" function. A small improvement here could be obtained if desired, by fitting a high gain BC108C in the TR1 position.

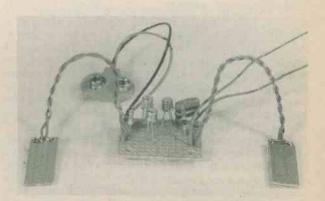
When the switch is in the "off" state it consumes no significant power as only minute leakage currents flow in the circuit. In the "on" state with the  $410\,\Omega$  relay it consumes about 20mA. The only current which flows in the "on" condition is, of course, that taken by the s.c.s. anode load.

### USE AS A P.U.T

A programmable unijunction transistor is not usually employed to switch a load but, like an ordinary unijunction transistor, is used in a relaxation oscillator circuit. Although there are superficial similarities between p.u.t. and u.j.t. relaxation oscillators, the devices themselves are completely different, as will already be apparent to readers who are familiar with u.j.t. operation.

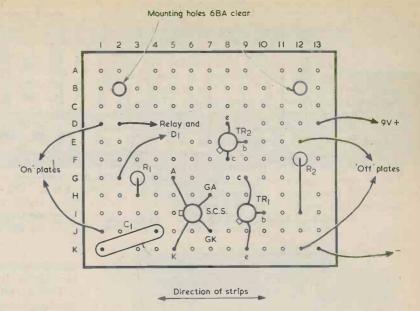
A relaxation oscillator is one in which a capacitor is charged via a resistor until the potential across the capacitor reaches a certain threshold level. The capacitor is then rapidly discharged or partially discharged, whereupon the capacitor charges up until the threshold voltage is developed across it once again. It then discharges or partially discharges once again, and the circuit continuously oscillates in this fashion.

The circuit diagram of a conventional p.u.t. relaxation oscillator using an s.c.s. in the p.u.t.



The complete touch plate switch unit. In this version the touch plates consist of pieces of stripboard

Fig. 3. Veroboard assembly for the touch plate switch



mode is given in Fig. 4. The gate anode terminal is held at approximately half the supply rail potential by the potential divider formed by R3 and R4. The cathode terminal is connected to the negative supply rail by way of R2, and this component must have a fairly low value.

When the supply is initially connected, C2 will have no charge and the anode terminal of the s.c.s, will be at the negative supply rail potential. With the gate anode held at half the supply rail potential the device is obviously strongly reverse biased and will not conduct between anode and cathode terminals. C2 is therefore free to charge up via R1.

When the voltage across C2 is about 0.6 volts positive of that at the gate anode, the s.c.s. is switched on and will continue to conduct until the current flowing through it falls below the holding level. Thus C2 is rapidly discharged through the s.c.s. and R2 until the voltage across it is too low to maintain the holding current, and the s.c.s. turns off. C2 then begins to charge again, and the whole cycle of events is repeated for as long as the supply voltage is present.

R1 must be low enough in value to provide sufficient current to turn the s.c.s. on, but it must not be so low as to hold the device on continuously. R2 must not be too high in value as it would then introduce negative feedback to the s.c.s. cathode circuit; this would hinder its regenerative action and

prevent it triggering properly.

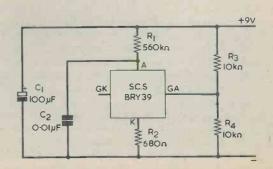
The circuit produces two output waveforms: a high impedance non-linear sawtooth across C2 and a low impedance pulse across R2 as C2 is discharged. The frequency of oscillation is set by the values of R1 and C2, and is largely independent of variations in supply voltage.

In a relaxation oscillator employing an ordinary unijunction transistor the voltage at which the timing capacitor discharges is set by the characteristics of the device used, and for practical u.j.t's is subject to quite wide variations between individual devices of the same type number. For example, with the 2N2646 the voltage can be anything from 0.56 to 0.75 of the supply voltage. This makes it impossible to obtain highly predictable results with regard to operating frequency even if the timing components have close tolerances.

With a programmable unijunction transistor, or an s.c.s. acting as a programmable unijunction transistor, the capacitor discharge level is set by an external potential divider (R3 and R4 in Fig. 4.) and, if necessary, highly predictable results can be obtained by using close tolerance components here and in the timing network.

Fig. 4 shows a conventional form of programmable unijunction transistor relaxation oscillator, but when an s.c.s. is employed as a p.u.t. it can be

Fig. 4. Using a silicon controlled switch in a conventional programmable unijunction oscillator circuit



used in the alternative configuration shown in Fig. 5. This is very much the same as Fig. 4, but the anode and cathode functions have been transposed and the reference voltage connects to the gate cathode instead of to the gate anode. The circuit works in basically the same way as that of Fig. 4, with C2 charging via R2 until the cathode is about 0.6 volt negative of the gate cathode. The s.c.s. is then turned on and discharges C2. The output signals are negative-going now, of course, whereas they were positive-going in Fig. 4.

At first sight neither circuit has any advantage over the other, but in practice the configuration of Fig. 5 seems to be considerably less critical with regard to component values than is that of Fig. 4,

and for this reason is to be preferred.

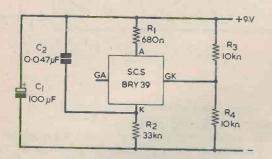
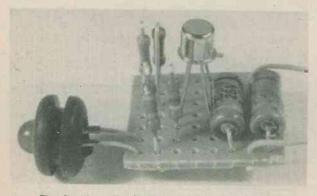


Fig. 5. An alternative form of the p.u.t. oscillator circuit which offers advantages in practice

# FLASHING PILOT LIGHT

A very simple practical project using an s.c.s. as a p.u.t. is shown in Fig. 6, and this gives the circuit of a low power pilot light. The idea behind this project is that it causes an l.e.d. to flash briefly at intervals of about 1 second, whereupon a very noticeable pilot light having a very low current consumption is given. Such a unit is particularly useful with small battery powered equipment where other types of pilot light would be unsuitable due to excessive current consumption and consequent reduction of battery life.

The circuit of Fig. 6 is simply that of an s.c.s. relaxation oscillator operating in the p.u.t. mode, with an l.e.d. included in the anode circuit so that it is pulsed on by the discharge current of the timing capacitor. The values of C1 and R1 have been



The flashing pilot light project. The Veroboard assembly is very small and may be secured by way of the I.e.d. panel mounting

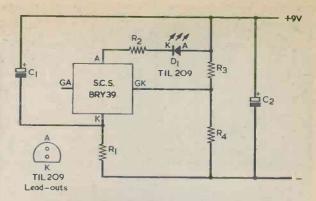


Fig. 6. Employing the p.u.t. style oscillator in a flashing pilot lamp circuit. Current consumption from the 9 volt supply is very low

chosen to produce a frequency of oscillation which

is slightly greater than 1Hz.

Ordinary unijunction transistors can be, and are, used in flashing pilot light circuits of this type, but they have the disadvantage of drawing a standing current of about 1 to 2mA apart from the capacitor charge current, which makes them rather inefficient. In Fig. 6 the values of R3 and R4 have been made fairly high so that only a fairly small standing current flows in addition to the charge current. The standing current is approximately 85µA, and the total current consumption of the circuit is only about 130µA.

A suitable component layout for the unit is shown in Fig. 7, and this is based on a 0.1in. matrix Veroboard panel having 8 holes by 7 copper strips. There are no breaks in the strips. The panel is very small and light. If the l.e.d. is mounted in a panel holder, an adequate mounting for the Veroboard will be provided as well. Due to its small size it should be possible to find space for the pilot light unit in nearly all items of battery operated equip-

ment.

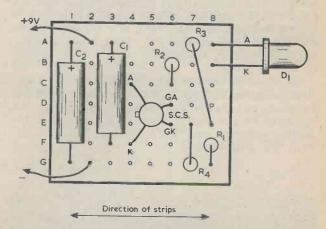


Fig. 7. Constructional details for the low power flashing pilot light

### **NEXT MONTH**

In next month's issue we shall examine further circuits incorporating the silicon controlled switch including, in particular, a versatile metronome.

(To be concluded)

RADIO AND ELECTRONICS CONSTRUCTOR

# RADIO & ELECTRONICS CONSTRUCTOR

# IN OUR NEXT ISSUE — PHASE LOCKED A.M. RECEIVER

Part 1 (2 parts)

Can a phase locked loop be used for a.m. detection?

It can, and very successfully with this practical medium wave superhet receiver.

This fairly simple receiver employs the NE567 phase locked loop i.c. to provide the second i.f. amplifier and detector functions. The receiver is powered from a PP6 9 volt battery and provides a good quality audio output of a few hundred milliwatts to an internal loudspeaker.



# CMOS RESISTANCE EVALUATOR

The Unit described is suitable for evaluating resistance values from less than 5  $\Omega$  to 10M $\Omega$  with eleven overlapping ranges.

The circuit is designed around 2 CMOS operational amplifiers, resulting in a low component count and ease of intitial setting up without the drawback of a dual power supply.

# 9 VOLT ELIMINATOR SPEAKER UNIT

Stabilized 9 volt output Current limit at 100mA

Originally designed and built for use with the "3 Band Short Wave Superhet" it can be employed with other short wave receivers requiring a 9 volt supply and an external speaker.

# **RADIO 4 CONVERTER**

This article describes a simple and inexpensive converter, which is particularly appropriate now that Radio 4 has been moved to the long wave band on 1,500 metres (200kHz) and many a.m. sound receivers, both home constructed and commercially manufactured, do not have a long wave band.

There is no need to make a direct connection between the converter and the medium wave radio as the converter radiates a local signal which is picked up inductively by the receiver on its own ferrite aerial.

# MANY OTHER ARTICLES

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# **BACK NUMBERS**

For the benefit of new readers we would draw attention to our back number service.

We retain past issues for a period of two years and we can, occasionally, supply copies more than two years old. The cost is 58p, inclusive of postage and packing.

Before undertaking any constructional project described in a beck issue, it must be borne in mind that components readily available at the time of publication may no longer be so.

# SHORT WAVE NEWS

FOR DX LISTENERS



By Frank A. Baldwin

Times = GMT

Frequencies = kHz

From my early days in radio, I have never ceased to enter the shack without a sense of wonder at this world of the short waves and a hope — often forlorn — that this time conditions will really be good, the QRM absent and the Dx rolling in. This constant hope, coupled with my own optimistic nature, are the spurs which jolt me awake at the most unearthly hours — and have done so for almost fifty years now! Just now and then my efforts are rewarded but that of course applies to all of us long-suffering Dxers. Someone, I've forgotten just who, once remarked that when the bug of short wave listening bites, it bites deep. I am covered with bites!

• SRI LANKA

Colombo on 11800 at 0055, YL with songs in the Hindi programme of the All Asia Service.

• NETHERLANDS ANTILLES

Bonaire (Trans World Radio) on 11925 at 0105, OM with a newscast in the English programme to the Far East, scheduled from 0035 to 0135.

PAKISTAN

Azad Kashmir (Free Kashmir) on **3860** at 1750, OM with song, local music, sign-off at 1816. The schedule is from 0045 to 0200 and from 1200 to 1816, the power is 10kW and the transmitter is located at Trarkhel.

SURINAM

SRS Paramaribo on **4850** at 0323, programme of pops on records, OM announcer in Dutch. The schedule is from 0815 to 0330 and the power is 10kW.

BRAZIL

Radio Itatiata, Belo Horizonte, on 4805 at 0225, OM with sports commentary in Portuguese. The schedule is around the clock and the power is 25kW.

AUSTRALIA

Radio Australia on 15240 at 0525, OM with a talk in English about prison life in Australia.

Radio Australia on 17795 at 0340, YL with a talk in English about local affairs. Also logged in parallel on 17870.

• U.S.S.R.

Radio Moscow on 7390 at 1725, piano music in the Russian 5th Programme "Atlantica" for seamen in the Atlantic, scheduled here from 1630 to 1730 and in parallel on 7340, 9685, 11705, 11790, 11810, 11830, 11870 and on 15455.

11790, 11810, 11830, 11870 and on 15455. Radio Moscow on 12000 at 1735, OM with the Hausa programme to Africa, scheduled from 1730

to1800.

Radio Moscow on 12055 at 1740, OM with the Zulu programme to Africa, scheduled from 1730 to 1800.

Radio Moscow on **11830** at 0610, YL with the English programme to Africa, scheduled from 0600 to 0700. Also logged in parallel on **11965**.

Radio Moscow on 11705 at 0732, OM with a talk in Russian in the programme "International Observers at the Round Table" — a rebroadcast of the Domestic Service 5th Programme — scheduled here on Sundays only from 0730 to 0800.

Radio Moscow on 11805 at 0738, piano music in a relay of the Domestic Service 2nd Progamme "Mayak".

WEST GERMANY

"Deutsche Welle — the Voice of Germany", Cologne, on 11765 at 0625, OM with the English programme directed to West Africa, scheduled from 0600 to 0630. Also logged in parallel on 11905.

SPAIN

Madrid on 11920 at 1807, OM with a newscast in the Spanish programme for Europe, scheduled here from 1300 to 1900. For those interested, the English programme to Europe is currently radiated from 2030 to 2230 on 6100, 7155 and on 9505.

INDIA

AIR (All India Radio) Delhi on 11620 at 1835, local songs and music in the English programme (General Overseas Service) to East Africa, the U.K. and Western Europe, scheduled from 1745 to 1945.

VATICAN

Vatican City on 11715 at 0615, Latin Mass to Europe, scheduled from 0715 to 0820. Also logged in parallel on 11740.

• EGYPT

Cairo (one of my old stamping grounds!) on 11785 at 0705, OM with a newscast in Arabic in the Domestic Service General Programme, scheduled here from 0700 to 1300.

• SWITZERLAND

Berne on 11780 at 0720, OM with a talk about the Red Cross and war prisoners in the English programme directed to Australasia, the Far East South and South-East Asia and Europe, scheduled from 0700 to 0730.

• ECUADOR

HCJB Quito on 11835 at 0800, OM with station identification, time-check then a religious programme in the English transmission to Europe and the Pacific, scheduled from 0700 to 0830 here and in parallel on 9525, 9665 and on 11900.

FINLAND

Helsinki on 11755 at 1445, OM's with a discussion about the Finnish Constitution in the English programme for Europe and North America, scheduled from 1325 to 1455 (Sundays only).

ALBANIA

Tirana on 11985 at 1454, OM with the Indonesian programme to South-East Asia, scheduled from 1430 to 1500.

ROMANIA

Bucharest on 11940 at 1500, OM with the opening of the English programme for Asia, scheduled from 1500 to 1530, followed by YL with a news bulletin.

ITALY

Rome on 11800 at 1948, YL with comment on current affairs in the English programme intended for the U.K., scheduled from 1935 to 1955.

• CUBA

Havana on 11930 at 0120, YL and OM alternate with news comment in the English programme for the Americas, scheduled from 0100 to 0450.

CANADA

Montreal on 11940 at 0124, OM with a talk about French separatism in the English programme for the Americas, scheduled from 0100 to 0130.

CZECHOSLAVAKIA

Prague on 11990 at 1440, OM with a talk about the atom bomb in the English programme directed to Africa, South Asia, the Far East and and the Pacific, scheduled from 1430 to 1500.

### CHINA

PLA Fukien Front on 3200 at 1850, YL in Chinese until sign-off at 1859. This is the Network 2 programme in Amoy and Standard Chinese to

Taiwan and other Offshore Islands, scheduled here from 1000 to 1900.

Radio Peking on 7503.5 measured at 2025, Chinese songs by YL in the Domestic Service 1, scheduled here from 2000 to 1735. Radio Peking on 11000 at 0007, YL with songs

Radio Peking on 11000 at 0007, YL with songs in Tibetan in the Domestic Service for Minority Groups to Tibet, YL in Tibetan, the programme being scheduled here from 2330 to 0025.

Radio Peking on 11290 at 0019, YL with songs in Chinese in the Domestic Service 1 programme, scheduled here from 2000 to 1735.

Radio Peking on 15510 at 0030, military music, YL and OM alternate with the Uigher programme in the Domestic Service for Minority Groups, scheduled here from 0030 to 0125.

Urumchi, Sinkiang, on 5060 at 1900, 'East is Red' theme on chimes, identification "Govorit Peking", YL with the programme in Russian to U.S.S.R., scheduled from 1800 to 2100.

Radio Peking on 11650 at 1535, OM with news comment in the English programme for South Asia, scheduled here from 1500 to 1600.

Radio Peking on 9080 at 2013, OM and YL in Chinese, local-type music in the Domestic Service 1, scheduled from 2000 to 2300, 2303 to 1400 and from 1503 to 1735.

Radio Peking on 9940 at 1745, OM with the Cantonese programme to South East Africa and South East Asia, scheduled from 1700 to 1800.

Radio Peking on 9880 at 1750, YL and OM in the Standard Chinese programme for Europe, North Africa and West Asia, scheduled from 1730 to 1830.

### COLOMBIA

Radio Bucaramanga on 4845 at 0402, OM with station identification followed by sign-off with the National Anthem. The schedule is from 1000 to 0400 and the power is 1kW.

### GUINEA

Conakry on 4910 at 0405, OM with a talk in French about local affairs. The schedule of this one is from 1230 to 0730 and the power is 18kW.

### NIGERIA

Lagos on 4990 at 0408, OM with a newscast in English followed by U.K. pop records at 0411. This is the National Programme which is in English and vernaculars and is scheduled here from 0430 to 1000 and from 1700 to 2305, the power being 20kW.

### ANGOLA

Radio Nacional, Luanda, on 3375 at 2001, OM with a newscast in Portuguese followed by identification and a musical programme at 2003. This is the 'A' Programme in Portuguese from 0400 (Sundays from 0430) to 0800, from 1530 to 2400. Programmes in Kikongo, a local language, are inserted in the above schedule from 2100 to 2130. The power is 10kW.

# • NOW HEAR THIS

Clandestine "Radio Freedom from South Yemen" on 9960 at 1415, Arabic songs and music, readings from the Holy Qur'an, political harangue, rousing music and male chorus and off at 1430. This is an anti-South Yemen Communist Government programme, calling on the armed forces to rebel against the Moscow-backed regime.



by P. R. Arthur

# AN UNUSUAL AND INEXPENSIVE APPROACH TO PROPERTY SURVEILLANCE

- \* During daylight will detect someone moving around a room
- \* After dark can be triggered off by just a torchlight
- \* Will not cause false alarms by responding to natural changes in ambient light level

A large number of light sensitive electronic designs of various types have been published in the past, and most of these come under the broad heading of light level detectors. In other words, they activate some piece of ancillary equipment when the amount of light received by the photosensitive device passes through a certain threshold level. Common examples of this type of project are automatic parking lights and automatic porch lights.

There is an alternative type of light activated switch which is better suited to certain applications, and this is a circuit that responds to fairly rapid changes in light level rather than to the detection of a particular light level. Probably the most obvious application for a device of this nature

is a burglar alarm system.

If such a unit has adequate sensitivity it is quite capable of detecting a person moving around a room during the hours of daylight. It will be particularly effective if the photocell is placed on the opposite side of the room to a window. Anyone moving around the room will, of course, produce shadows, but the light coming in through the window will almost certainly be well diffused and a large number of shadows will be produced. The walls and ceiling will further tend to diffuse the light and increase the number of shadows. As shadows cross the photocell they will produce small changes in light level which will trigger the unit.

After dark the system will probably not function in this manner due to a lack of ambient light, but it will still be triggered by an intruder switching on the room lights, or even by indirect light from an

intruder's torch.

Natural changes in the ambient light level will occur relatively slowly, and the unit must be designed not to respond to these so that false

alarms are not produced.

Light detector circuits of this type are not quite as effective as other forms of movement detectors, such as those employing microwave techniques or ultrasonic sound doppler shift techniques, but they are very much less complicated and comparatively inexpensive.

The unit which forms the subject of this article is a sensitive light change detector which is based on an operational amplifier i.c. and a silicon controlled switch (a form of highly sensitive thyristor). In the untriggered state the circuit consumes typically less than 1mA from a 9 volt supply and can therefore be battery powered if necessary.

### **OPERATING PRINCIPLE**

Fig. 1 shows the basic arrangement of the alarm unit in block diagram form. A photocell circuit produces a voltage which varies with light intensity in conventional manner, and the output from this is fed to a ground referenced CA3130 operational amplifier circuit via a coupling capacitor. The latter blocks the d.c. component in the output from the photocell circuit, and its value is chosen so that in conjunction with the input impedance of the amplifier it forms a high pass filter which also blocks any slow voltage changes. Reasonably fast changes in potential are, however coupled through to the amplifier.

The ground referenced amplifier is much the same as an ordinary operational amplifier, but it

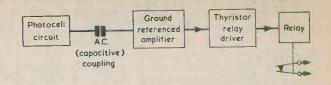


Fig. 1. Block diagram illustrating the operation of the light change alarm. This detects fast changes in illumination of the photocell, as could be caused by the movements of an intruder

has a single positive supply rather than the normal dual balanced positive and negative supplies. The fact that the negative supply is absent means that negative-going output half-cycles must also be absent, whereupon the amplifier output is effectively half-wave rectified. This is not of importance as only positive-going signals are needed in order to trigger the following thyristor stage. Also, the CA3130 operational amplifier has a low current consumption of a few hundred microamps only when used in this manner.

The thyristor is driven from the output of the amplifier, the quiescent output voltage of which will be little more than zero. Under normal conditions the thyristor is therefore switched off. If the photocell circuit is activated, the output of the amplifier will momentarily swing positive and switch on the thyristor. Once the thyristor has been triggered it latches in the on state, and continues turned on until it is reset. The thyristor is used to operate a relay whose coil forms its load, and the relay contacts can either be connected into an alarm system or can directly control an audible alarm generator of some kind.

The thyristor employed in this circuit is a silicon controlled switch (s.c.s.) which is connected to act as a highly sensitive thyristor. The performance of the circuit would not be as good if an ordinary thyristor, or silicon controlled rectifier, were to be used, as the output current from the amplifier is only just sufficient to trigger many of these. The s.c.s. places little loading on the amplifier output and will trigger when the amplifier output goes

more than about 0.5 volt positive.



The front panel of the light change alarm carries the Set-Reset switch and jack sockets for connection to the photoconductive cell and the external alarm circuitry

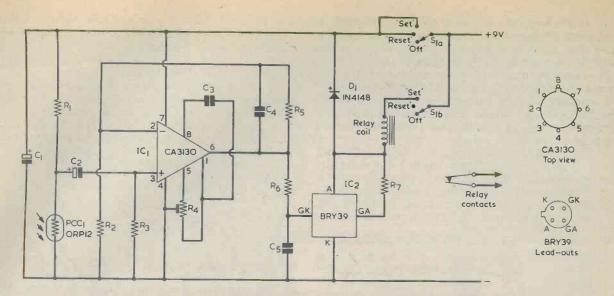


Fig. 2. The circuit of the alarm unit. The relay contacts may alternatively be normally open if it is intended to couple the unit to a simple warning circuit, such as a battery and bell in series

### THE CIRCUIT

The complete circuit diagram of the light change alarm appears in Fig. 2. The photocell is an ORP12 cadmium sulphide cell, which is inexpensive but gives excellent sensitivity. R1 is its load resistor and the optimum value for this component depends upon the ambient light level. It is therefore given a value which gives a good compromise over a wide range of light levels, and which also incurs a reasonably small current consumption.

IC1 is used in the non-inverting mode and the output from the photocell circuit is coupled to the non-inverting input by way of C2. R3 is the bias resistor for this input and, in conjunction with C2,

it also forms the high pass filter.

The voltage gain of the amplifier is set by the ratio of R5 to R2 at approximately 1,000 times, and this high level of gain gives the circuit excellent sensitivity. C4 rolls off the high frequency response and aids the stability of the circuit whilst C3 provides compensation for the i.c.

Although the quiescent output voltage of the i.c. is theoretically zero, practical operational amplifiers have an offset voltage between the inputs. This voltage will be small, but when amplified by the voltage gain of the i.c. it could easily become quite large, producing a quiescent output voltage of several volts. It is also possible that leakage current through C2 could result in a very small quiescent voltage appearing at the noninverting input of the amplifier; after amplification, this could also produce quite a high quiescent output voltage which would prevent the unit from functioning. Offset null correction is therefore included in the circuit, and R4 can be adjusted to produce zero quiescent output voltage.

The output from IC1 is fed to the input terminal of the s.c.s. (IC2) via R6. An s.c.s. is a very sensitive device and C5 and R7 are needed to prevent

spurious triggering of this component.

S1 is the on-off and reset switch. In the "Off" position of this switch the power is disconnected and the unit is switched off. In the next "Reset" position power is applied to the main circuit, but

not to the relay coil. This is necessary because when power is initially applied a voltage spike is produced by the photocell circuit and C2, and the spike could trigger the s.c.s. and cause it to latch in the on state. The triggering voltage is, indeed, passed to the s.c.s. but, without a load supply, it cannot latch on. Thus, if S1 is taken from "Off" to "Reset" and left in the second position momentarily, sufficient time is allowed for the circuit to settle down and become ready for normal operation. The switch is then put to "Set". The relay will not be actuated as the initial spike voltage from C2 will have

Once triggered by a change in light level, the circuit can be reset by momentarily putting S1 to the "Reset" position. The current flowing through IC2 then falls to zero and it turns off. Diode D1 ensures that there is no risk of high back-e.m.f. voltages from the relay coil being applied to the output of

IC2.

Some remarks need to be made concerning components. C2 is specified in the Components List as having a working voltage of 10 volts but it will be quite in order to employ a capacitor having a



The alarm unit is housed in a small plastic case which can be easily concealed. The photocell then couples to the input jack socket via screened cable

higher working voltage; as high, even, as 60 to 70 volts. S1 is a miniature 2-pole 3-way rotary switch, and that used in the prototype is a 3-pole 4-way switch with an adjustable end stop set to give 3-way operation. No connections are made to one of the poles. The relay is a "Sub-Miniature Relay" with 2 changeover contact sets and a  $185\,\Omega$  coil, and is available from Maplin Electronic Supplies. It draws a current of around 45mA when the alarm is triggered. The recommended battery is made up of

six HP7 cells fitted in a suitable battery holder. This will have contacts which take a battery connector of the PP3 type.

### CONSTRUCTION

Most of the components are assembled on a Veroboard panel of 0.1in matrix having 19 copper strips by 29 holes. Details of this panel are given in Fig. 3, which also shows the wiring to S1. The relay is secured to the board by means of two bare tinned

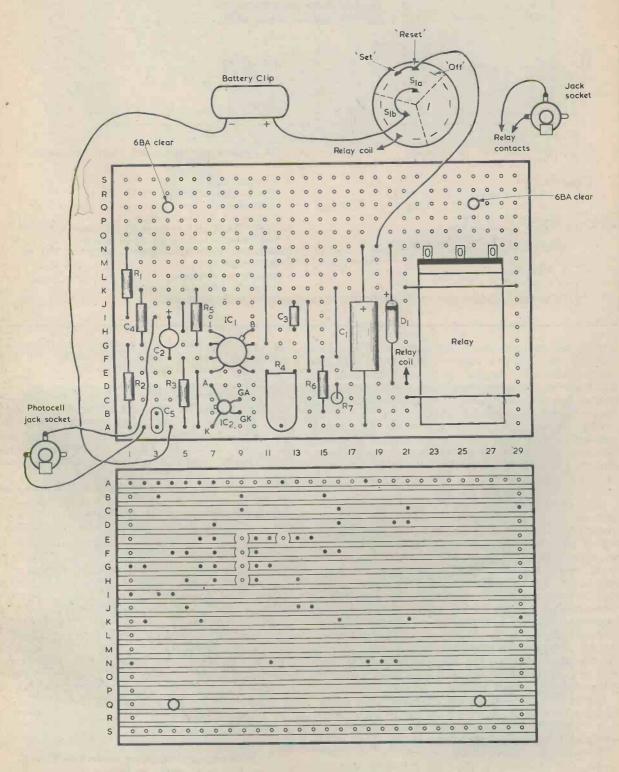


Fig. 3. Details of the component assembly on the Vero stripboard. The relay is held in position by two tinned copper wires soldered to the strips at the points indicated

copper wires passed over its body and soldered to the Veroboard strips, as illustrated. The relay will be held quite firmly if each of the wires is pulled tight on the copper side of the board when the second joint to the copper strip is made. The excess

wire on the copper side can then be cut off. If desired, additional stability will be given by first applying adhesive between the relay body and the board. The relay has its coil tags nearest the board.

Since IC1 is a CMOS device it is advisable to

### **COMPONENTS**

Resistors

(All fixed values 1 watt. 5%)

R1 10kΩ

R2 100Ω

R3 100kΩ

R4 100kΩ pre-set potentiometer, 0.1 watt, hori-

zontal R5 100kΩ

R6 1kΩ R7 100kΩ

Capacitors

C1 100µF electrolytic, 10 V. Wkg. C2 1µF electrolytic, 10 V. Wkg. C3 82pF ceramic plate C4 220pF ceramic plate C5 0.1µF type C280 (Mullard)

Semiconductors

IC1 CA3130T or CA3130S

IC2 BRY39 D1 1N4148

Photocell

PCC1 ORP12

Relay

 $185\Omega$  relay (see text)

S1 2-pole 3-way miniature rotary (see text)

Miscellaneous

Plastic case (see text)

Control knob

Veroboard, 0.1in. matrix

2-off 3.5mm jack sockets (if required)

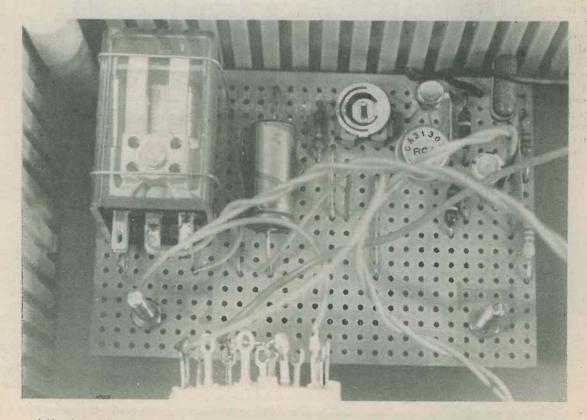
2-off 3.5mm jack plugs (if required) 6-off cells type HP7 (Ever Ready)

Battery holder

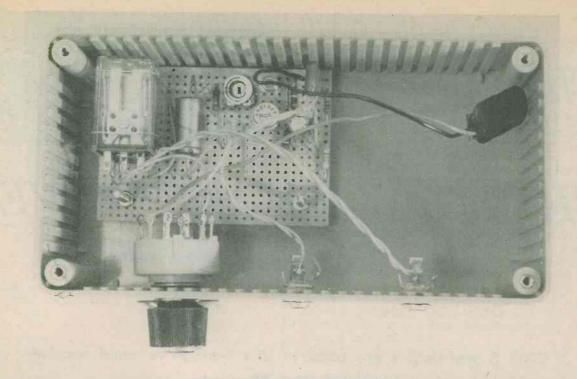
Battery connector

Screened cable (if required)

Nuts, holts, wire, etc.



A Veroboard panel provides a neat and readily assembled basis for the main wiring of the alarm unit



The Veroboard assembly is bolted to the bottom of the case behind the rotary switch. Adequate space remains for the battery holder

make this component the last to be soldered to the board. A soldering iron with a reliably earthed bit must be used.

The method of coupling the photocell to the unit depends upon the requirements for the particular installation in which the unit is to be used. The cell could be mounted on the case which houses the main circuitry but better security will be given in most instances if it is positioned remotely, whereupon it can be connected to the unit by way of up to 2 metres of screened cable terminated in a 3.5mm jack plug. The centre conductor of the cable carries one connection to the cell and the braiding provides the other connection. The braiding should be common with the negative supply rail and this will be achieved if it connects to the "sleeve" contact of the plug.

The manner in which the relay contacts conect to the external alarm or audible warning system can also vary according to individual requirements, but a good plan here is to connect the contacts to a second 3.5mm jack socket into which a plug connecting to the external circuit can be inserted. The prototype employed jack sockets in both the photocell and relay contact circuits, and these are shown in Fig. 3. Only one of the two relay contact sets is used and, for most alarm systems, the connection will be to a pair of normally closed contacts rather than to

a pair of normally open contacts.

The unit can be assembled in any plastic case capable of taking the components and the battery holder, and the prototype is housed in a box measuring approximately 150 by 80 by 50mm. One of the 150 by 50mm sides forms a front panel and has S1 mounted towards its left hand side. The

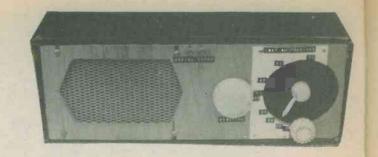
socket for the photocell is mounted in the centre and that for the relay contacts to the right, following the symmetrical layout shown in the photographs. The Veroboard assembly is mounted inside the case behind S1, and there is space for the battery holder in the remaining part of the case. The Veroboard panel is secured by means of two 6BA bolts and nuts, spacing washers keeping the soldered connections under the board clear of the case bottom and thereby preventing strain on the board.

#### ADJUSTMENT

After completion it is necessary to set up the offset null potentiometer R4. This requires a multimeter switched to a 10 volt range, or to a range which allows a clear reading of 9 volts. The meter is connected to the negative rail and to pin 6 of IC1, a suitable connection point for the latter being at the link wire between points K16 and F16 of the Veroboard. The photocell should be positioned where it will not be subjected to any rapid changes in light intensity, and the slider of R4 turned almost fully in the clockwise direction. S1 is switched to "Reset", whereupon the meter should give an indication of approximately 9 volts. R4 should then be adjusted in an anti-clockwise direction just far enough to reduce the voltage reading to zero. After switching off and removing the multimeter the unit is then ready for use.

It will be found that the alarm unit exhibits excellent sensitivity. Indeed, it may be considered excessively sensitive by some constructors. If considered necessary the sensitivity can be lowered somewhat by reducing the value of R5.

# The "6S3T"



# SHORT WAVE RECEIVER

by Sir Douglas Hall, Bt, K.C.M.G.

Only 3 transistors are used in this 6-stage reflexed receiver covering 19 to 75 metres.

The simple receiver to be described is a modernised version of a very successful design of the writer's which was published in the August 1964 issue of this magazine. Described as a "6-Stage 3-Transistor Short Wave Reflex Receiver", the first two reflexed stages employed p.n.p. silicon transistors which were amongst the first to appear on the home-constructor market. The receiver was built in large numbers and one reader stated that "in his opinion no circuit has been published anywhere which is comparable".

The receiver now to be described employs a similar circuit to that earlier set, and uses current silicon transistors. It will be found sensitive and selective, with a modest current requirement of only 12mA from a new 9 volt battery. The prototype tunes from just below the 19 metre band to just above the 75 metre band.

### RECEIVER CIRCUIT

As can be seen from the circuit diagram of Fig. 1 the aerial signal is applied via C1 to the emitter of TR1, which acts as a common base r.f. amplifier. The amplified signal at the collector is applied to L2, the primary of an r.f. transformer, and is coupled thence to the tuned secondary winding, L1, the tuning capacitor being VC1. It passes next to the base of TR2 which, acting as an emitter follower, provides current amplification. Diode D1 gives signal rectification, or detection, whereupon TR2, functioning as a common base audio amplifier, gives a.f. amplification.

Since the output at TR2 collector is at high impedance and the collector current is only about 600uA, the collector load consists of the large winding of an audio frequency transformer. This provides a high impedance without the loss of direct voltage which would occur if a high value resistor were used. The audio signal is passed via

C3 to the base of TR1, which acts as an emitter follower at a.f., the base offering a high input impedance which does not damp the incoming signal. The output from TR1 passes through the r.f. stopper R2 to the base of TR3, a high gain output transistor.

It will be seen that the positive end of R5 is taken

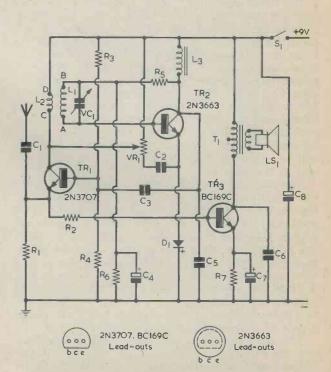


Fig. 1. The circuit of the short wave receiver. The stages are reflexed, with TR1 and TR2 both functioning as r.f. and as a.f. amplifiers

to the collector of TR2. When connected in this manner (in the earlier circuit it was returned to the positive rail) the resistor offers negative feedback of direct current for TR2. A further advantage is that R5 provides a small amount of damping across L3 and thereby removes the possibility of threshold howl at critical reaction settings.

Reaction is controlled by VR1, which is in an r.f. positive feedback path from the emitter to the base of TR2. Reaction feedback increases as the slider of VR1 approaches the end of the track connecting to C2. When the slider is close to the track end connecting to the positive rail it partially or fully shortcircuits L2. VR1 therefore functions also as a true volume control.

### COMPONENTS

Resistors

(All fixed values 4watt 10%)

R1 2.2kΩ R2 470 Ω

R3 470kΩ

R4 220kΩ

 $R5 47k\Omega$ 

R6 see text

 $R7 100\Omega$ 

VR1 5kΩ potentiometer, linear, with switch S1

Capacitors

C1 33pF silvered mica or ceramic

C2 0.01µF polyester
C3 0.047µF polyester
C4 47µF electrolytic, 3V. Wkg.
C5 1,000pF silvered mica or ceramic

C6 0.1µF polyester C7 47µF electrolytic, 3V. Wkg. VC1 500pF air spaced variable

### Inductors

L1,L2 see text

L3 see text

T1 LT700, LT712 or LT730 (see text)

#### Semiconductors

TR1 2N3707

TR2 2N3663

TR3 BC169C

D1 0A81

### Switch

S1 s.p.s.t. toggle, part of VR1

### Speaker

LS1  $3\Omega$  or  $8\Omega$ ,  $6 \times 4$ in. or  $7 \times 4$ in. (see text)

#### Miscellaneous

Control knob

Tuning drive (see text)

2 terminals

9-volt battery

10-way tagstrip (see text)

Plywood

Screws, wire, etc.

#### COMPONENTS

VC1, a 500pF tuning capacitor, is available from Home Radio Components in Jackson type E1. This was a popular value with valve radios and many older readers may have the component already on hand. A 365pF variable capacitor can alternatively be used, but it will not then be possible to receive the 60 and 75 metre bands. Whilst on the subject of component availability, the 2N3663 specified for TR2 can be obtained from Electrovalue and the OA81 diode from Bi-Pak Semiconductors.

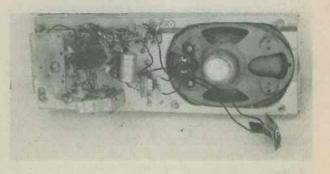
An excellent choice for L3 is the secondary winding of an R.S. Components intervalve transformer. This can be the midget or standard size with a 1:3 or 1:5 ratio. These transformers have not been produced for some years but the author mentions them in case a reader may still find one on dealers' shelves or in his spares box. Another good choice is the primary of an LT44 transformer made by Rex, a common component some 10 or more years ago. It is distinguished by a high primary winding resistance of  $2k \Omega$  and a green plastic covering. With either the intervalve transformer or the Rex transformer, R6 should have a value of  $10k \Omega$ .

Very nearly as good results, but with some loss of bass, will be given by the primary of a standard Eagle LT44 transformer. With this transformer, R6 should be 7.5k a. Whatever transformer winding is employed for L3, no connections are made

to the unused winding. If a 3  $\Omega$  speaker is used, the Eagle LT700 is suitable for T1. If an 8  $\Omega$  speaker is employed, the transformer can be an Eagle LT712. Another alternative is the Eagle LT730, which has a tapped secondary and may be used with either 3  $\Omega$  or 8  $\Omega$ speakers. These last two transformers are not quite as common as the ubiquitous LT700, but they are stocked by Maplin Electronic Supplies, amongst others. No connection is made to the primary centre-tap of any of these transformers.

### CONSTRUCTION

The components, including the speaker, are assembled on a in. plywood panel. This should have dimensions of about 4 by 12 or 13in., the final size being that which accommodates the parts comfortably. Measure the speaker first before cutting out the panel; it may be over 4in. wide! Components are arranged as shown in Fig. 2. The mounting of L3 and T1 depends on the particular transformers employed. If these have printed circuit mounting lugs, the latter may be bent out at



The rear of the front panel, as viewed from the tuning capacitor end

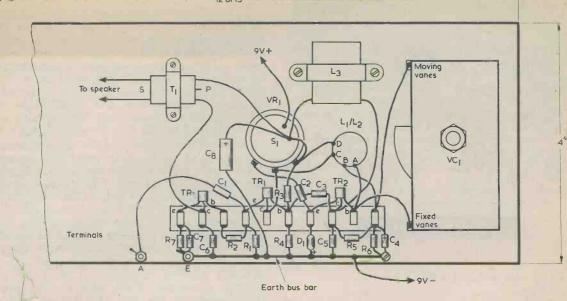


Fig. 2. Layout of components and wiring behind the front panel

right angles and have small tags soldered to them. Short woodscrews then pass through the holes in the tags. A 10-way tagstrip is required and this can be cut out from a "Standard" 18-way group panel retailed by Doram Electronics. It is secured to the plywood by small woodscrews passing through the holes in the centres of the two end tags. Any other 10-way tagstrip having a length of about  $3\frac{1}{2}$ in. will be suitable.

The coil, consisting of L1 and L2, is home-made, and is wound on a \(^3\)-in. diameter former. The points marked "B" and "D" in Fig. 1 are the earthy ends of the windings. L1 has 12 turns of 24 s.w.g. enamelled wire, close-wound. The winding is covered by a layer or two of Sellotape and L2 is wound immediately over the earthy end of L1. It consists of 5 turns of 32 s.w.g. enamelled wire, also close-wound and covered with Sellotape. It is essential that L2 be wound over L1 and not alongside it. Points "A" and "C" are now the live ends of the windings and should be physically their top ends. No reaction will be given if either winding is con-

nected into circuit out of phase. Small components are arranged as shown in Fig. 2. Note that a heavy gauge tinned copper bus wire runs parallel to the tagstrip. It is secured at one end to the earth terminal and by a woodscrew at the other end. A slow-motion drive is necessary for VC1 and this can consist of a manufactured item suitable for panel mounting. However, a perfectly sound drive may be made easily and inexpensively in the following manner. Obtain an ordinary small knob of the slightly tapered variety fitted with a grub screw. Cut a piece of s.r.b.p. to make a disc 3in. in diameter. Cut a hole at the centre of the disc which is marginally smaller in diameter than the base of the knob. A fret saw is the tool for this. First apply adhesive around the base of the knob then drive the knob into this hole so that the base is tight in the s.r.b.p. disc. Take an inch or so of in. rod (which could be, say, the excess cut off from a potentiometer spindle) and push a tight fitting grommet over it. Fit a in. bush to the panel such

that, when the knob with the disc is passed over VC1 spindle and the rod with the grommet is passed into the bush, the edge of the disc marries into the groove in the grommet. The idea is shown in Fig. 3 and the result is a smooth drive offering a ratio of about 7 to 1.

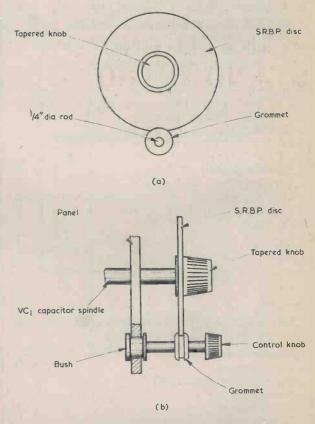
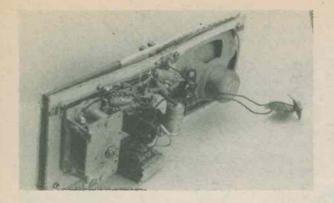


Fig. 3(a). Front view of a home-constructed slow-motion tuning drive. The control knob is removed

(b) Side view of the tuning drive



All the components are assembled on the rear of the front panel

### **USING THE SET**

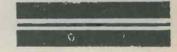
There are no setting up operations and the receiver is ready for use as soon as it has been completed. A few yards of wire slung across the ceiling will be needed for an aerial. If a long outside wire aerial is employed it may be necessary to reduce the value of C1 fairly drastically to preserve selectivity. Alternatively, a lower value capacitor could be inserted in series externally between the long aerial and the aerial terminal of the set. An earth is not essential, but a connection to the earth line of a mains socket may be tried.

Connect up a 9 volt battery, such as a PP7, switch on and advance VR1 until a hiss denotes the

onset of oscillation. Adjust VC1 until a station is heard. For amplitude modulation, keep VR1 backed down just below the oscillation point. The receiver should be oscillating gently for reception of c.w. or s.s.b. signals.

Constructors will want to tidy up the exposed front, possibly by adding a second panel. If this is to be done, bear it in mind before cutting down either of the control spindles. If the home-made tuning drive of Fig. 3 is used the s.r.b.p. disc may have an arrow marked on it, and the panel can be fitted with a scale showing the various bands. A suitable case for the receiver can be made very

# New Product



# PRINTED CIRCUIT DRILL STAND INTRODUCED

A printed circuit drill stand, specially designed for drilling small quantities of P.C. Boards, prototypes, one-off production specials, missed production holes and modifications, is now available from Technomark, Church Road, Maidstone, Kent.

The motor body is supported on a cantilever spring system which, when depressed, switches the motor on, and off, when released. If the drill motor body is adjusted so that the motor switches on with the drill just touching the board surface, drill wander can be eliminated to enable accurate drilling of plain copper surfaces.

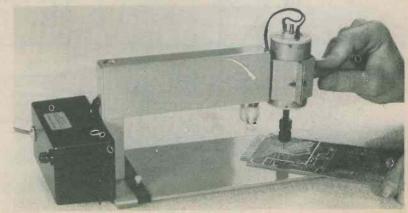
The 315mm long x 115 wide x 150 high 2.5kg drill stand has an integral 12V D.C. power supply, fused and switched, a motor clamp for drill height adjustment, a large throat depth (168mm), low voltage lighting and a reliable high speed motor attached to the unique parallel spring suspension.

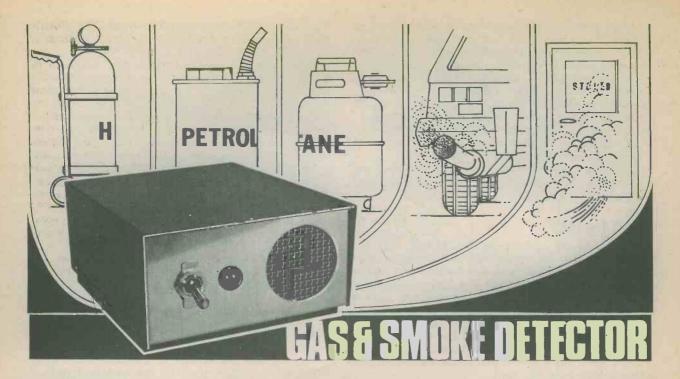
Full operative's instructions are supplied with each unit which comes complete with chuck,

collets, light and x-y locating jigs.

Recommended retail price of the Technomark P.C. Drill Stand is £61 plus VAT.

Accurate PCB drilling is assured with the new Technomark combination drill stand and positioning





# Part 2

By R. A. Penfold

### Concluding details of this sensitive and easily constructed unit

In last month's issue we described the manner in which the gas and smoke detector functions, then proceeded to constructional details of the main component panel and the fitting of components to the case. We deal next with the mains power supply section.

### MAINS POWER SUPPLY

The wiring of the mains power supply is shown in Fig. 5. T1 is mounted on the case bottom behind the loudspeaker and a solder tag is fixed under one of its

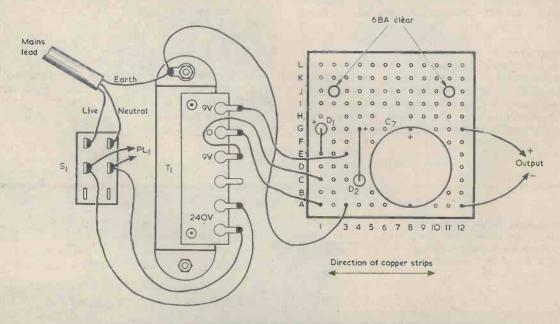


Fig. 5. The wiring of the power supply section. The transformer tag layout is that for the particular component employed in the prototype; other transformers may have their tags positioned differently. There are no breaks in the Veroboard copper strips

mounting nuts. The mains earth lead connects to this and thus earths the metal case. When the unit is powered from the mains it is essential that the case be properly earthed in this way for reasons of safety.

D1, D2 and C7 are wired up on a 0.1in. matrix Veroboard panel which has 12 copper strips by 12 holes. The component layout of this panel is provided in Fig. 5. There are no breaks in any of the copper strips. The completed panel is wired to the rest of the unit before it is finally secured on the base of the cabinet to the rear of \$1. It is mounted using a couple of 6BA bolts and nuts in the same way as the main component panel, and spacers are used to hold the panel underside clear of the metal of the case.

It is important to remember that the live and neutral mains connections are accessible at the tags of both T1 and S1 when the case lid is removed, whereupon all precautions against accidental shock must be observed. If there is any risk of the transformer primary tags making contact with the inside of the lid a piece of thin s.r.b.p. should be glued to the lid underside at the appropriate position, employing a powerful adhesive such as an epoxy resin.

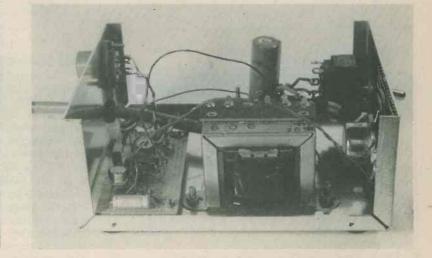
Any outstanding point-to-point wiring is next completed, after which the unit is ready for testing.

then returning to a fairly high level again. The effect is particularly pronounced after the gas sensor has been in storage for a long period.

Give the sensor several minutes after switch-on to achieve a stable operating condition and then adjust R3 in an anti-clockwise direction. At some point the alarm will sound again, and R3 should be backed off slightly from this point. The closer R3 is taken to the threshold of oscillation the more sensitive the circuit becomes. However, it is not desirable to set R3 for the highest possible sensitivity as the unit will then become oversensitive, and a small amount of tobacco smoke or even just breathing near the sensor will be sufficient to set off the alarm! The result is likely to be false alarms in a practical situation, and it is advisable to back off R3 about 30 degrees from the alarm threshold setting. The sensitivity of the unit will still be more than adequate when this is done.

It should be pointed out that the 812 gas sensor is a symmetrical device and can be plugged into its holder either way round. The sensor will not work indefinitely and will become slow to operate and insensitive after about two years of continuous use. It must then be replaced, a process which simply entails unplugging the old sensor and inserting a new one. It is advisable to

Side view of the interior of the gas and smoke detector. The main component panel is to the left of the mains transformer and the speaker is to the right



### **ADJUSTMENT**

Before switching on for the first time, adjust R3 fully clockwise. The alarm should sound almost immediately after switching on; if it does not, switch off and recheck the wiring for errors.

After a short while the alarm will turn off. This triggering at switch-on is caused by the resistance of the gas sensor starting at a high level, falling as it warms up and

periodically check that the unit is functioning properly by purposely releasing some gas or smoke near the sensor. This will ensure that the sensor is replaced before it becomes too ineffective.

(Concluded)

Copies of the November issue of Radio & Electronics Constructor featuring Part 1 of this attractive project are available from Data Publications Ltd., 57 Maida Vale, London W9 1SN. Price 58p inclusive of postage.

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# TRANSFORMER RATIO MATCHING By D. Snaith

We examine a mildly puzzling transformer property

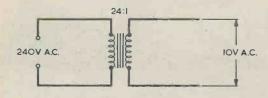


Fig. 1. A common application for an iron-cored transformer. Here, the transformer steps down the a.c. mains voltage to a secondary voltage of 10 volts

Iron-cored transformers are, basically, pretty simple components. If as in Fig. 1 we want to obtain an r.m.s. alternating voltage of 10 volts from the 240 volt a.c. mains supply we use an iron-cored transformer having a step-down ratio, from primary to secondary, of 24:1. In other words the primary has 24 times more turns than the secondary. If the current drawn from the secondary is 1 amp, the current flowing in the primary is one twenty-fourth of an amp. This is because the same power (voltage times current) must theoretically pass into the primary as is drawn from the secondary.

(In practice the current flowing in the primary will be a little higher than one twenty-fourth of an amp because the transformer is not a "perfect" component: it possesses "losses" due to resistance in the windings and other effects which result in the primary power being slightly higher than the secondary power. The small extra power which is lost is then dissipated in the transformer as heat. However, it is not impossible to design an iron-cored transformer so that it has very small "losses" and is thereby nearly "perfect", and we shall assume from now on that the transformers we are discussing are, indeed, of a "perfect" nature.)

### IMPEDANCE MATCHING

The use of transformers for changing alternating voltages and currents is easy to understand, and the voltage and current changes are directly related to the turns ratio of the transformer. We employ transformers also for impedance matching. For example, we may have a transistor audio output stage which will give of its best when coupled to an  $800\,\Omega$  load. We can't get  $800\,\Omega$  speakers but we can get  $8\Omega$  ones, and so we use a step-down transformer, as in Fig. 2, between the transistor output stage and the speaker. This process is known as matching the speaker to the output stage.

What step-down ratio does the transformer require to present what is effectively an  $800\Omega$  load to the transistor output stage? The answer is not 100:1, as it would have been with our voltage and current examples, but is 10:1 instead. The impedance change provided by the transformer is equal to the square of its turns ratio. The square of 10 is, of course, 100, and the 10:1 transformer presents an  $800\Omega$  impedance at its primary when the secondary connects to an  $8\Omega$  load. The same applies with any other impedance changing circuit using a transformer: the impedance change is proportional to the square of the transformer turns ratio.

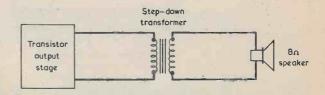


Fig. 2. Here, a transformer matches an 8  $\Omega$  speaker to a transistor a.f. output stage for which the optimum load is 800  $\Omega$ 

To see how this, at first sight, peculiar relationship holds true, let us look at a simple example in which the secondary load is a  $5\Omega$  resistor. as illustrated in Fig. 3. The transformer has a 2:1 ratio and, when the circuit is set up to operate, causes a voltage of 10 volts to appear across the 50 resistor. The current flowing in the resistor, is, in consequence, 2 amps. The voltage at the primary must be 20 volts, and the current in the primary must be 1 amp. Now resistance in ohms is equal to volts divided by amps, and so the primary winding is behaving like a resistor whose value, in ohms, is equal to 20 divided by 1, or 200. Our 2:1 transformer has, therefore, provided an effective resistance transformation of 2 squared or 4 times.

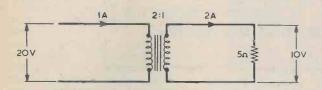


Fig. 3. As is explained in the text, this circuit causes the transformer primary to present an effective resistance of  $20\Omega$ 

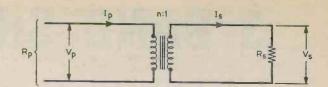


Fig. 4. A general case of transformer matching. Rp is the effective resistance presented by the transformer primary

In the last step but one, note how the second n nips smartly above the fraction bar (as it has to do because Is is below the bar).

Now, Vs divided by Is is Rs, so Rp is Rs multiplied by n squared.

We have been using resistances in this simple calculation but the same reasoning applies with impedances. However, it is only necessary to remember the basic calculation with resistances to understand how the turns ratio has to be squared when employing a transformer for impedance matching.

### ANY LOAD

Repeat the exercise of Fig. 3 with any numbers and you will find that the effective resistance transformation is always equal to the turns ratio squared. Let's try it with a resistor connected across the secondary whose value is called Rs and say that the effective resistance at the primary is Rp. The transformer has a ratio of n:1, n being any number. As voltage and current come into the picture, we'll say that the secondary voltage and current are Vs and Is, and that the primary voltage and current are Vp and Ip. Fig. 4 shows these

Let us now list some facts. First, Rs is equal to Vs divided by Is. Second, Vp is equal to Vs multiplied by n and, third, Ip is equal to Is divided by n. and, fourth,

$$Rp = \frac{Vp}{Ip}$$

Since Vp is equal to Vs multiplied by n,

$$Rp = \frac{Vs.n}{Ip}$$

And, as Ip is equal to Is divided by n.

$$Rp = \frac{Vs.n.n}{Is}$$
$$= \frac{Vs.n^2}{Is}$$

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# 3 BAND SHORT WAVE

# **SUPERHET** Part 4

By R. A. Penfold

Alignment and operation of the receiver

In last month's issue we completed the construction of this short wave superhet receiver. We now carry on to a description of the manner in which it is aligned, together with notes on its operation.

### "S" METER ADJUSTMENT

Before making any tuned circuit alignment it is necessary to adjust the "S" meter circuit for correct operation. The "S" meter may then be used, during alignment, as a signal strength indicator.

VR4 is adjusted as far as possible in an anti-clockwise direction before any significant deflection of the meter needle takes place. If, later, the "S" meter should seem to be a little over-sensitive, this effect can be corrected by shunting a resistor of a few kilohms across the meter, the required value being found by trial and error. On the other hand, should the "S" meter appear to lack sensitivity, then a reduction in the value of R15 should halp. help. Again, the required value is found by trial and error.

VR4 may require re-adjustment from time to time due to variations in the supply voltage caused by the

battery aging.

The alignment of the receiver will next be described. It will be helpful here to initially disable the Q multiplier by turning VR3 fully anti-clockwise and breaking the connec-tion between C33 and Tr1 drain.

### R. F. ALIGNMENT

Although in the various parts of the circuit there are some thirteen cores to be adjusted, the alignment procedure is not too complicated and can be carried out satisfactorily even if no test equipment is available.

As supplied, the Denco dual-purpose coils have their cores

screwed right in for packing purposes. Initially these are adjusted so that about 10mm of metal thread is visible at the top of each coil. With the set switched on and connected to an aerial and a speaker or headphones, it should be possible to receive a number of stations on each tuning range, except perhaps on Range 5, where propagation con-ditions can often result in there being few strong signals. If a calibrated r.f. signal generator is available it can be used to assist in obtaining the correct adjustment of the oscillator coil cores. These are set up to provide the following frequency ranges, either exactly or as near as possible: Range 3, 1.6 to 4.6MHz; Range 4, 4.5 to 12 MHz; Range 5, 10 to 25 MHz.

If a suitable signal generator is

not available, then the cores of the oscillator coils are simply left with 10mm protrusion of thread. The frequency coverage will then be approximately correct, and no signifi-

cant gaps in the coverage will occur.
The aerial coil cores are given any setting which enables the aerial trimmer control to peak signals at all positions of the tuning control. One way of achieving the correct settings is to tune the set to a signal at about the centre of one tuning range. With VC1 set at about half maximum capacitance the core of the appropriate aerial coil is adjusted to peak the signal. The "S" meter makes a useful tuning indicator. This procedure is then repeated for the other two tuning ranges.

### I. F. ALIGNMENT

The i.f. stages are aligned by first tuning to a reasonably strong sta-tion of consistent strength on Range 3 or Range 4 and then adjusting the cores of IFT2, IFT1, the i.f.

transformer associated with the mechanical filter, and then the mechanical filter itself, in that order. All cores are adjusted for maximum "S" meter reading, and S2 is set up for "A.M." reception. Use a proper trimming tool, such as the Denco TT5, for IFT2 and IFT1, as a miniature screwdriver or a similar implement can damage the cores. The mechanical filter and its matching i.f. transformer have a different construction, and their cores can be adjusted with a screwdriver provided adequate care is taken

### **B.F.O. ADJUSTMENT**

First tune as accurately as possible to an a.m. station and then switch S2 to the upper sideband mode. This should cause a beat note between the carrier of the a.m. station and the b.f.o. signal. The core of L3 is adjusted to zero-beat the b.f.o. with the carrier signal. The set is then switched to the lower side-band mode, and this should again produce an audio beat note. Adjust the core of L3 to reduce the pitch of this note slightly.

There should then be an audio

beat note with the set switched to either the upper or lower sideband mode. Ideally the core of L3 should be adjusted by trial and error so that almost precisely the same note is produced at each position of S2 but in practice good results will be obtained provided the two notes are

not greatly different.

Due to the positioning of the b.f.o. coil it may be necessary to use a cut-down trimming tool to adjust its core. As with all the Denco transformers the trimming tool should be of a type which engages correctly with the core. An alternative approach is to remove the Q multiplier panel from the front panel when the b.f.o. is adjusted.

### Q MULTIPLIER ADJUSTMENT

The Q multiplier may now be brought into circuit, with the link between C33 and TR1 drain being re-connected. Adjustments are still carried out with the same a.m. signal which was used for alignment of the i.f. and b.f.o. stages. S2 is set to the "A.M." position.

to the "A.M." position.

VR3 is now slowly advanced. As it approaches the point at which oscillation occurs the selectivity of the receiver will noticeably increase, but because IFT3 is not yet properly aligned with the i.f. amplifier the Q multiplier will have a similar effect to very slightly altering the tuning control.

The cores of IFT3 are adjusted to peak the received signal at the correct tuning control position, employing the "S" meter as a tuning indicator. VR3 should be kept just below the point at which oscillation occurs while these adjustments are made, and since the positions of the cores of IFT3 affect the feedback level, this control will need constant re-adjustment.

With both cores correctly adjusted and VR3 set to just below the threshold of oscillation it should be found that most of the modulation on the a.m. signal is outside the receiver's passband, and that only the very low notes are reproduced by the speaker or headphones.

Alignment of the Q multiplier may be very difficult if IFT3 is initially well out of alignment. Should this prove to be the case it will be necessary to remove IFT3 from its component panel and connect each winding, in turn, between TR1 drain and chassis via a 0.047µF d.c. blocking capacitor in series with the connection to TR1 drain. It is then possible to approximately align each winding by adjusting its core to peak the test signal. After both windings have been roughly aligned in this manner it should be a simple matter to set them up in the Q multiplier when the i.f. transformer has been reconnected into circuit.

As with the b.f.o. core, there is limited access to the lower core of

IFT3. Again, a cut-down trimming tool may be used. Or, the product detector and b.f.o. panel may be temporarily removed from the front panel when the Q multiplier is being adjusted.

### NOTES ON USE

The receiver is intended for use with an ordinary long wire antenna, and an earth connection is also very helpful on the lower frequency bands. Most of the controls are quite conventional. VC1 is the aerial trimmer control and this is adjusted to peak received signals. At high frequencies it will probably be found that there are two peaks with this control. The lower frequency one (VC1 vanes more fully meshed) is the correct one, and the other setting is for the image signal.

For s.s.b. reception on the low frequency amateur bands S2 is set to the l.s.b. mode as this is the s.s.b. mode usually employed (160, 80, and 40 metres). For s.s.b. reception on the high frequency amateur bands (20 and 15 metres) S2 is set to the u.s.b. mode. It is usually fairly obvious if this switch is in the wrong position since the signal disappears as the tuning control is adjusted towards the correct point.

For the reception of a.m. signals the i.f. bandwidth provided by the mechanical filter is just about the optimum, and the selectivity control, VR3, can be fully backed off. It is beneficial if it is advanced somewhat for s.s.b. reception, but do not advance it too far. This will result in the signal being increased in strength in comparison with any background signals, but the rather peaky i.f. response that results provides a very poor audio frequency response, and in consequence a loss of intelligibility. For c.w. reception the selectivity control is advanced to just below the threshold of oscillation.

Although the use of a product detector provides the set with quite a wide dynamic range on s.s.b. and c.w. reception, it is still possible for strong signals to cause an overload. Obviously the set does not have an infinite dynamic range on a.m.

either, and very strong signals could conceivably overload the detector. If this should occur then VR1 should be backed off as necessary to provide satisfactory results. It can sometimes be beneficial to back off this control to some degree in order to reduce cross-modulation noise, particularly on the low frequency bands when a long aerial is used.

The Jackson dial has provision for three tuning scales, and so one of these can be marked for each range covered using legends from "Panel Signs" Set No. 4. The dial can either be calibrated with a proper frequency scale or a simple alternative is to merely mark the positions of the amateur and broadcast bands.

### IMPROVED A.G.C.

It was pointed out in the first article in this series that the a.g.c. system, although satisfactory with a.m. signals, is less than 100% efficient with s.s.b. and c.w. signals.

The reason for this is that the a.g.c. system has a fast attack time and also a fast decay time. Thus on a strong s.s.b. or c.w. signal the a.g.c. circuit provides the necessary reduction in gain in the presence of the signal, but s.s.b. and c.w. signals are both of an intermittent nature and the gain of the set returns to maximum during the inevitable breaks in these signals. This often results in quite a high background noise level on even the strongest signals.

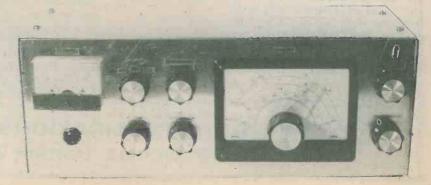
The "S" meter circuit also has a

The "S" meter circuit also has a fast attack and decay time since it is operated from the a.g.c. voltage. This results in the meter being rather difficult to read on s.s.b. and c.w. signals since the meter's needle is constantly moving, and usually moving very quickly.

moving very quickly.

Ideally both the a.g.c. and "S" meter circuits should have fast attack and slow decay times. With a decay time of about 2 seconds the a.g.c. voltage would be maintained during short breaks in the signal, and as a result there would be a lower background noise level on strong signals, and "S" meter readings would be very much

A general look at the front panel of the superhat demonstrates the large number of control functions which are available



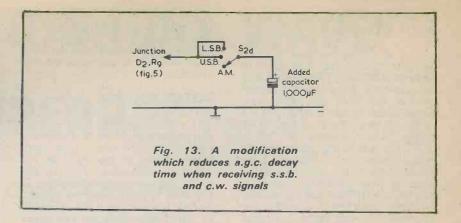
steadier.

A simple way of modifying the a.g.c. circuit to provide a slow decay time in the u.s.b. and l.s.b. modes is shown in Fig. 13. S2(d) is the previously unused section of S2, and it shunts a 1,000 µF electrolytic capacitor across R9 and R10 when "U.S.B." and "L.S.B." are selected. Because of the high value of the added capacitor, the charge that develops across it in the presence of a signal takes an appreciable time to leak away through R9 and R10 when the signal is absent. This provides the required slow decay

Unfortunately, the high value of the capacitor also increases the attack time, but the increase here is considerably less than the increase in decay time. Although theoretically less than perfect, the arrange-ment works quite well in practice.

The working voltage of the additional capacitor is not important since the voltage across it is only a fraction of a volt. In fact, a reverse polarity can be developed across it with strong signals. This reverse voltage, which is only about 250mV at most, does not cause any practical problems. It is not, of course, feasible to use a non-electrolytic capacitor because of the high value of capacitance that is required.

The additional capacitor can conveniently be wired between the ap-



propriate tag of S2 and the chassis solder tag on the product detector panel. A wire then connects the requisite contacts of S2 to the junction of D2 and R9.

### MAINS UNIT

The short wave superhet has now been fully described and it may be employed as a working unit in its own right. The author has, also however, developed two optional extras which may be added to the receiver. The first of these is a mains supply unit complete with speaker; this provides a 9 volt supply and thereby reduces the running costs since the 9 volt battery is not then required. It also offers a convenient housing for a speaker coupling to the receiver output. This item will be described in next month's issue. The following issue will give details of another optional extra, this being a fully tuned preselector which may be inserted between the aerial and the aerial input of the receiver.

### (Concluded)

In Fig. 4 of part 2 of this article, published in the October 1978 issue, the centre of switch S1 should be 18mm. from the left hand edge of the coilpack metal bracket, and not 18mm. from the right hand edge.



# UNDERSTAND DATA PROCESSING

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# MINIMISING A.M. INTERFERENCE

By Ivor N. Nathan

A neat aerial dodge for a.m. Dx listeners

Much is written and spoken about interference to television reception but little regard is given to radio reception. Mainly with television reception in mind, manufacturers of electrical equipment do all that is economically possible to ensure that their products are fitted with interference suppressors, and amateur radio operators go to great lengths to make certain that their transmitters do not spoil pleasure of people who may be watching television while they themselves are enjoying their hobby. It seems rather ironic, therefore, that during peak television viewing time some radio enthusiasts have their pleasure denied them because of interference the caused by radiation, from domestic television receivers, of various harmonics which fall within the medium and long wave bands of a.m. radio receivers. The Post Office is obliged only to ensure that reception of U.K. radio stations is free of interference, and it could well be argued that v.h.f. reception is available to the majority of listeners so there is no great need for a.m. to be used in any receiver incorporates aerial and earth terminals, a useful and efficient method of keeping interference to a minimum is to take use of screened coaxial cable as a down lead from a long wire antenna, with the screening connected to the receiver chassis and earth. The author was unable to easily install an outside long wire antenna so use was made of a disused 405 line (Band 1) television aerial fitted in the loft space. The antenna, consisting of a centre-fed 75  $\Omega$  dipole, approximately 10 ft. 6 in. from end to end and without a reflector or directors, was firstly remounted in the horizontal plane. Its coaxial cable feeder was re-routed to the radio receiver and a switch was connected in series with the screening at the receiver end of the feeder so that the screening could be either connected to the radio chassis and earth or not. The switch was mounted on the receiver cabinet. The accompanying diagram shows the details.

### HIGH QUALITY AUDIO

For high quality audio reception and negligible interference (assuming that an efficient antenna is included in the installation) v.h.f. is the unchallenged answer. It also provides the option of stereo radio reception for those who prefer it. However, there are quite a few people who still require the alternative of a.m. reception because they do not want to be restricted to the local station reception which v.h.f. provides. Some are music enthusiasts who are not content to listen to the programmes being transmitted at any given moment and prefer to listen to Continental stations such as Hilversum, Brussels, and various German and French stations. Others are foreign language students or people of foreign origin who prefer to listen at times to non-English programmes for reasons of language or cultural background. A third group consists of those who are interested in Dx (long distance) radio reception on long, medium and short waves primarily from a technical point of view

For the more serious a.m. listener, whose DECEMBER 1978

Band I dipole mounted in loft

750 coaxial cable

Aerial
terminal
Earth
terminal
Receiver

Dual-operation a.m. antenna. When the switch is open the coaxial cable screening couples capacitively to the Inner conductor, giving effectively a long wire antenna. Closing the switch gives a screened down lead and freedom from interference by adjacent television receivers.

If the receiver is an old mains model with a live chassis connected to one side of the mains a direct connection to its chassis must on no account be made. Should such a receiver be fitted with an earth terminal, that terminal will be connected to the receiver chassis via an internal isolating capacitor but, even so, the earth terminal must also connect to a reliable earth such as the mains earth. These precautions are essential to reduce the risk of accidental shock.

During non-peak television viewing time (mostly during the day) the switch can be in the open position so that the cable screening is not connected to earth; in this mode the inner conductor adds to the length of the antenna and the entire system functions as an efficient all-wave a.m. antenna. When adjacent television receivers come into use in the

evening, as will be evidenced by interfering harmonics on medium and long waves, the switch can be closed so that the down lead becomes screened and only the dipole section of the antenna is being effectively used. The result is reduced but interference-free reception on long waves, less reduced and still interference-free reception on medium waves, and nearly normal reception on short waves.

With the switch in the open position and using the system as a long wire antenna, the author, living in the Southgate area of North London, obtained daytime reception of Radio Orwell, which is the Ipswich radio station on 257 metres. The antenna system also provides very strong interference-free signals from BBC Radio Medway on 290 metres and BBC Radio Solent on 301 metres.

# IBA DEVELOPMENTS

### SURROUND SOUND

An advanced system of 'surround sound' broad-casting — believed by IBA engineers to be superior to any system previously broadcast in Europe — is to be field tested on Independent Local Radio shortly. This system, based on the "Ambisonic' technology of the National Research Development Corporation, uses information transmitted in a third channel to improve the limited surround sound quality obtainable with two channel systems.

First over-air tests are expected to be broadcast in the London area on Capital Radio (93.8 MHz VHF/FM). The programme material will be specially recorded by IBA engineers working in collaboration with Capital Radio and using a new surround mobile recording unit of IBA design.

These tests are the first in a series due to be transmitted during late 1978 and early 1979 from a number of ILR stations in various parts of the country.

Dates and times of the first broadcasts will be announced on Capital Radio. The radio trade will be kept informed of all broadcasts on "IBA Engineering Announcements" (Tuesdays at 9.10 a.m. on all ITV stations).

Listeners will be invited to participate by reporting the degree of stereo and monophonic compatibility on the receivers currently in general use. A reply-paid questionnaire will be available from the IBA Engineering Information Service (01-584 7011).

These tests are in support of studies being made by the European Broadcasting Union. The IBA are also investigating other current proposals for surround sound.

### TECHNICAL NOTES

The system uses information in a third channel together with an encoding matrix chosen to achieve optimum compromise between surround sound, stereo and mono reproduction. Because the system encodes all the significant information required for correct surround sound decoding in a fully recoverable way, the inherent advantages of linear decoding can be realised. For this reason, IBA engineers are convinced that three-channel radio receivers incorporating such linear decoders, as well as providing superior results, could be

manufactured for the consumer market more cheaply than two-channel receivers using the complex variable matrix decoders commonly required for many of the alternative surround sound systems that have been proposed. The third channel is bandwidth restricted and quadrature modulated on to the 38 KHz sub-carrier already present for stereo transmission. Because of the restriction of the third channel bandwidth this form of transmission is often called "2½-channel" transmission.

The domestic decoder needed for  $2\frac{1}{2}$ -channel surround sound reception (together with the necessary audio amplifiers and loudspeakers) would also be capable of correctly decoding suitable encoded disc and tape recordings. The IBA is planning further studies in these fields because of their importance in the daily programming of the ILR stations.

For listeners interested in surround sound reception, a technical leaflet is being prepared which will give full information on the form of decoder required. Full technical information and the leaflet will be made available to the public and technical press shortly.

### "IBA ENGINEERING PROGRESS"

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In an introduction, Mr. Tom Robson, IBA's Director of Engineering, points out that during the decade 1968-78 the number of IBA transmitting installations has increased from 40 to over 400.

The booklet is available on request to engineers, technicians and students from the Independent Broadcasting Authority, 70 Brompton Road, London SW3 1EY.







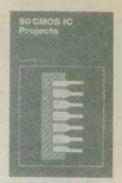


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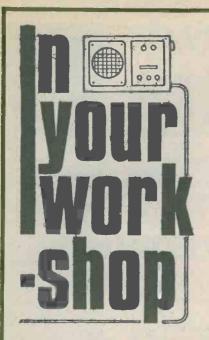
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# COMPUTER SUBTRACTION

**Ones Complement** Calculations

"Computers, computers," sighed Dick, "everything seems to be com-

puters these days!"
"That's the result," said Smithy cheerfully, "of what is known as the

exponential growth of technology."

"The trouble is," wailed Dick,
"that everything electronic is get-

ting so complicated."
"True, true," agreed Smithy consolingly. "An electronics engineer is nowadays the same as the Red Queen. He has to keep running all the time just to stay in the same

Dick looked at the Workshop clock morosely. He and Smithy had cleared up the last job for the day some five minutes earlier but there was still half an hour to go before their working day officially came to and end. After some desultory conversation with Smithy, Dick had now embarked on a subject which was plainly troubling him.

### COMPUTER SUBTRACTION

"I picked up a book about computers the other day," he went on aggrievedly, "and I just had to put it down again."
"Why was that?"

"It happened when I got to the chapter which dealt with computer calculations. I'll admit that the addition part in the chapter was easy enough to understand. A computer adds up in binary notation in just the same way that we add up in decimal notation. And, of course, there are only two figures in binary, these being 0 and 1."

"Fair enough," stated Smithy. "Each 0 or 1 is a binary digit, or a bit for short. You can't have a number in binary higher than 1, so that if you add binary 1 and 1 together the answer comes to binary

"That's right," said Dick. "Even I can follow that. But then the chapter went on to computer subtraction. And, blow me, the computer doesn't subtract one binary number from another like a human being does, it flaming well adds them!"

Smithy chuckled.
"What the computer does basically," he said, "is to carry out a subtraction by means of the 'ones complement' system. This allows the same circuit which does addi-

tion to do subtraction as well."
"It all sounda crazy to me," complained Dick. "Is this complement thing something special that you can only do with binary numbers?"

"Oh no," replied Smithy. "You can do it with decimal numbers, too. But with decimal numbers you use a nines complement instead of a ones complement.'

Dick frowned.
"Perhaps," he said slowly, "if I understood how the system worked with decimals I could understand it

more easily with binary."

This time it was Smithy who looked up at the clock. He paused thoughtfully for a moment, and

then came to a decision.
"Very well then," he said briskly. "There's still quite a bit of time to go before we pack up for the day, so if you come over here I'll do my best to gen you up on this number

complement business."

Dick, delighted at this turn of events, moved with commendable alacrity and it was a matter of moments only before he was perched on his stool alongside the Serviceman. He waited expectant-

"We'll start with some decimal subtractions first," said Smithy, pulling out a ball-point pen, "so I'll need my note-pad."

He drew the pad towards him. "Now, the word 'complement'," he continued, "means something which makes up a whole. From this it follows that the nines complement of any single number in decimal notation is the number which has to be added to it to make 9. So, the nines complement of 6 is

3. 3, and the nines complement of 2 is 7. Got it?"

"I think so," said Dick slowly.
"Let's try a few other figures.
Would the nines complement of 8

"It would."

"Blimey, this is easier than I thought! Let's go through the remaining numbers. The nines complement of 7 will be 2 and the nines complement of 5 will be 4. Figure 4 will have a nines complement of 5, figure 3 one of 6 and

figure 1 a complement of 8."

"You've got the idea," commended Smithy, "but you've left out two

numbers.

"Have I? Which ones are those?"
"9 and 0," replied Smithy. "The
nines complement of 9 is 0, and the nines complement of 0 is 9."

"Oh yes, of course. Where do we go from here, Smithy?"

### COMPLEMENT CALCULATION

"We get down to doing an actual nines complement subtraction," said Smithy. "And we'll choose a very simple one to begin with. We'll subtract decimal 3 from decimal 7. In the normal way of doing things, we would carry out the subtraction by putting the 3 under the 7 and then writing down the answer un-derneath. Which is, of course, 4."

Smithy jotted down the calcula-

tion. (Fig. 1(a).)

7 -3 -4	$\frac{7}{+6}$ $\frac{13}{13}$	$   \begin{array}{r}     7 \\     +6 \\     \hline     13 \\     +1 \\     \hline     4   \end{array} $
(a)	(b)	(c)

Fig. 1(a). A simple decimal subtraction carried out in conventional manner. 3 is subtracted from 7 (b). Here, the nines complement of 3 is added to the 7 (c). The 1 in the most significant position is next carried to the least significant position and added to the 3 to give 4

"How do we do it the nines com-plement way?"

"We write down the figure 7, and then add to it the nines complement of 3, which is 6. This gives us the figure 13." (Fig. 1(b).) "But," protested Dick, "that's

nowhere near 4."

"There's another step to take yet," said Smithy. "We next take the 1 off the left hand end of the 13, put it under the 3 and add these together. This gives us the correct answer, 4, and as you can see it's all done by addition." (Fig. 1(c).)

Dick looked down unhappily at

Smithy's note-pad.

"I just can't fathom this," he stated, scratching his head. "I'll agree that what you've just done

gives the right answer, though."

"Try one yourself."

"All right. I'll try subtracting—
let me see now—4 from 9. I'll put the 9 at the top and I'll put the nines complement of 4, which is 5, below it. Next I'll add them together. This gives me 14. What comes next?" (Fig. 2(a).)

"You do the same as I did," said Smithy. "A figure 1 has once again popped up at the left hand side of the sum number. You take this away from the left and add it to the right. The process, incidentally, is called 'end-around carry'. What you're doing is making a carry from the most significant position of the sum number, which is any digit apart from O at the extreme left hand end, to the least significant position. which is at the extreme right."

"I'll take your word for it," grinned Dick. "Okay then, I'll carry the 1 over and add it. Which gives

me 5." (Fig. 2(b).)
"Satisfied?"

"I'm satisfied that the system works," said Dick. "But I'm blowed

if I know why it works!"

"Think about it. With the calculations we've carried out up to now, adding the nines complement of the number we're supposed to be subtracting means that the answer is always too high by 9. If, for instance, we want to subtract 1 from 9, we first add to 9 the nines complement of 1, which is 8. The answer at this stage must be too large by 9, because we're adding 8 instead of subtracting 1. The same applies with any other single digit decimal calculations, where a smaller number is subtracted from a larger number."

"If," said Dick thoughtfully, "you subtracted 0 from any

number, you'd actually add 9, which is the nines complement of 0, to that number. So far, what you

say seems to make sense."
"Good," responded Smithy. "So, after arriving at an answer which is too great by 9, we next take 10 from it by removing the left hand 1, and add 1 to it at the right hand end. The final result is the correct solution."

An idea suddenly occurred to

"What happens," he asked quickly, "if we subtract the same number from itself. Say, for in-stance, we subtract 7 from 7." "The system," conceded Smithy,

"begins to creak a bit then. We'd

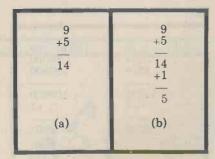


Fig. 2(a). First step in subtracting 4 from 9. The nines complement, 5, is added to

(b). An end-around carry then takes the 1 under the 4, the resultant addition giving 5

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add the nines complement of 7 to 7, giving us 9, but there'd be no extra 1 coming up at the left. But if we then add 1 to the 9 we'd get 10, whereupon the correct answer of 0 is given when we lop off the 1 at the

left."
"Humph," grunted Dick. "It looks as though I've uncovered a

weakness here.

"Not entirely," replied Smithy. "Assume that you've got a register in a computer which can only store a single number in decimal. I admit it would be a pretty funny thing to have such a register in a practical computer, but just assume it for a moment. If we did any of our nines complement calculations we would carry out our complement addition and then add 1. The 1 which appears at the left would simply slip out of the single digit register, and it just wouldn't see the light of day. This would happen even when you use the system to subtract the same number from itself."

### LARGER NUMBERS

"All right," said Dick. "Can we use this nines complement business

with larger numbers?"
"Oh yes," stated Smithy, taking up his pen again. "We could for instance subtract 5,723 from 8,457. Now the nines complement of 5,723 is 4,276, and you arrive at this by writing down the nines complement of each digit in turn. We next add the numbers as before whereupon, once again, a 1 sprouts up at the left hand end. We do an end-around carry by taking this 1 to the least significant position and add 1, giving us an answer of 2,734." (Fig.

3(a).)
"You said just now," commented Dick, "that the answer given by the first addition of single decimal numbers gives the answer which is too great by 9. What will it be in this

case?"

"Too great by 9,999," replied Smithy promptly. "Here, give me two more figures."

8457 - 5723	10000 - 99
8457	10000
+4276	+99900
12733	109900
+1	+1
2734	9901
(a)	(b)

Fig. 3(a). Subtractions of larger decimal numbers may also be carried out with the nines complement system (b). Another nines complement calculation

"All right," responded Dick. "Subtract 99 from 10,000."

"Fair enough," said Smithy. "In this case we have to carry out an important initial step. This consists of filling out the 99 so that it has the same number of digits as the 10,000. It is done by simply putting noughts to the left of the 99, giving us 00,099. The nines complement of this is 99,900 and that's what I'll add to the 10,000. The result works out as 109,900. We do the endaround carry, and the final answer comes out as 9,901." (Fig. 3(b).)
"Gosh," said Dick, impressed.

"It's easier doing the subtraction this way than by the ordinary method!"

Smithy chuckled.

"I'm glad you've noticed that with some numbers it can be easier. Well now, that's enough messing around with decimal numbers, so let's get down to binary numbers."

Dick groaned. "Dear, oh dear," he complained. "No sooner do I get myself clued up on one thing than you start pressing

on to the next!"
"Don't get too upset about it," said Smithy comfortingly. "Once you've grasped the complement principle with decimal numbers, you'll find that it's a piece of cake with binary numbers. Now, the highest single digit in decimal notation is nine, and so we subtract by working with the nines complement. The highest single digit in binary is one, and so with binary we work to the ones complement.

"This, commented Dick, "needs a little bit of thought. For starters, there are only two numbers in binary, these being 0 and 1."
"So?"

"So," continued Dick slowly, "I suppose that the ones complement of 0 must be 1, and the ones complement of 1 must be 0. Is that right?"
"It is," confirmed Smithy. "And

this is where the advantages of ones complement working shows up. You can change any binary number to it ones complement simply by changing all the 0's to 1's and all the 1's to 0's. Beautiful, isn't it?"
"Gosh," breathed Dick, as the

simplicity of the scheme gradually dawned on him, "I'll say it is. Let's try a binary subtraction, Smithy.'

#### **BINARY SUBTRACTION**

"Okeydoke," said Smithy equably. "I'll subtract binary 4 from binary 8. The figure 8 in binary is 1000 and the figure 4 is 100. I have to write this down as 0100, by adding a nought at the left, to give it the same number of digits as the 1000. Its ones complement then becomes 1011. Here we go!

Smithy quickly carried out the

calculation. (Fig. 4.)

"As you will observe," he said, "an extra 1 popped up again at the left hand end, just as with the Fig. 4. For binary subtractions, the ones complement is used

1000(8) 100(4) 1000 +101110011 +1 0100(4)

decimal figures, and we end-around carry this to the least significant position. The answer, of course, is 0100, or 4."

"Try some other numbers."
"All right," said Smithy. "I'll subtract 9 from 25, using binary notation and following the same rules as before."

He busied himself with the

calculation.
"Ah," he remarked with satisfaction, "this comes out quite tidily. The solution is the binary equivalent of 16." (Fig. 5.) cing up again at the clock, "has also had the advantage of taking us

painlessly up to knocking-off time."
"Right," said Dick, taking off his
overall jacket. "One must now make one's preparations for one's journey home."

Smithy's eyebrows rose. "Here," he said, "you're talking very posh all of a sudden, aren't you?"

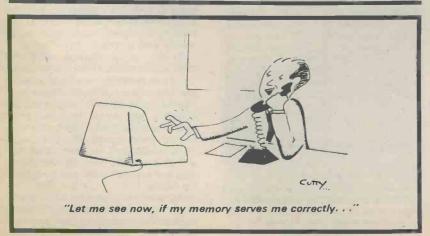
"One does it," grinned Dick, "in order that one can present one's compliments to you, one's old mate!"

Fig. 5. Another binary subtraction. The ones complement of a binary number is given by simply changing the 0's to 1's and the 1's to O's

11001(25) 1001(9) 11001 +10110101111 +1 10000 (16)

"One must say," said Dick enthusiastically, "that this ones complement business has turned out to be a lot easier than one would have thought."

"And our little gen-sesh," remarked Smithy cheerfully, glanAnd so, our ears resounding to the latest of Dick's appalling gags, we take our leave of the pair as they walk serenely out into the wintry evening air.



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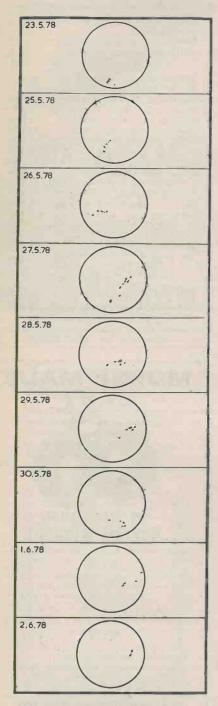
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# **SUNSPOTS**

### By Arthur C. Gee



Sunspot observations drawn by the author between 23rd May and the 2nd June showing the passage of a group of sunspots across the face of the sun. The 10 metre band was "wideopen" around the 27th May, when the group was facing the earth

MUCH interest is being shown in sunspots in general and the sunspot cycle in particular by the amateur radio fraternity, because we are in a transition period between one sunspot cycle and the next. As the intensity of sunspot activity determines radio conditions, there is much speculation, albeit scientific speculation, as, as to what the next sunspot cycle is going to be like and how intense its activity may be.

Sunspot cycles average more or less eleven years in duration, but considerable variation can, and does, take place. Periods as short as nine years or as long as thirteen years have been recorded. And their intensity varies greatly too. In the late 1950's, the cycle in progress then peaked to over two hundred on the Zurich Sunspot Number Scale. Around 1907, one of the lowest peak activities was recorded of only sixty. Great variation in activity takes place within the general trend. The last minimum of the sun's cycle was in mid-1976 and the increase in sunspot and other solar activity since then has been rather more rapid than expected. The frequency of these solar events has been greater than at any time since the end of 1970. There appears to be a correlation between the rate of increase in solar activity and the final level of the ensuing sunspot maximum. If this is so, the forthcoming cycle - number 21 should be an intense one.

A very interesting report entitled "Sunspot Cycle 21 — The Peak, How Much and When", was recently circulated as a "Special Report" for "H.R. Report", Greenville, New Hampshire, U.S.A., by O. Okleshen, W9RX, Propagation Editor, H.R. Report. He writes:-

"Of the many methods and authors of the 'how much and when' of a solar cycle maximum, the most recent and likely the most accurate method has been devised by A. I. Ohl, a Soviet scientist whose first publication was reported in 'Solnechnaya Dannyye'. H. H. Sargent, of the Space Environment Services Centre, Boulder, Colorado, modified the Ohl theory, thus enhancing the accuracy of the basic method developed by Ohl. The theory is based on the Regression of recurrent Geomagnetic activity recorded from the previous cycle to predict the sunspot maximum of the following cycle.

Mr. Sargent modified the Ohl theory by taking into account finer time resolution and more accurate data than was available to Ohl. The Ohl theory, as modified, provided accuracies of maximum sunspot cycle peaks when tested against observed data, within an accuracy of 1% in some cases. Whilst not

so in every case, accuracies within 5% of maximum sunspot level predictions appear to be common. Compared to previously known methods, the Ohl/Sargent method would be, by far, a major breakthrough in the prediction of sunspot cycle maximum levels, when accuracy and other advantages are considered. The data used to establish and test the original Ohl theory was extracted from the last 110 years. The data before 1848 is dubious because of the information and techniques available. For that reason, some of the other theories used at that time to predict sunspot maximum levels may be erroneous

Many other methods have been used to achieve individual and combined success to determine maximum sunspot peak levels. The IGY peak of 1957 led to anticipation of a peak level, certainly of a magnitude that would stimulate IGY research. One often used method is a 'slope' technique that observes the rate upward change at the beginning of a new cycle. This observed rate of change is then projected to a peak value. The Ohl method has two most important advantages over all other theories and methods of establishing the sunspot peak level. First, it appears to be superbly accurate, and second and probably most important, the Ohl method gives a year's earlier lead time. It is only necessary to use data from the prior decaying cycle.

The exact mechanism between the prior cycle recurrent geomagnetic behaviour and the succeeding cycle sunspot maximum is not clear. However, it is likely that one is directly related to the other in solar physics whereby it is even possible that a new definition of a solar cycle may have to be established. Conjecture may place some possible validity in the theory that coronal holes that relate to recurrent geomagnetic disturbances. may be the birthplaces for the succeeding cycle sunspot regions. This aspect of course, would have to be studied and proved.

There is no doubt that the Ohl/Sargent method has great potential, as those needing accurate sunspot predictions are and have always been confronted with widely ranging opinions. We know that cycle 21 predicted maximums of 50 to 60 have already been exceeded; the February 1978 smoothed sunspot level has afready reached at least 90, with the cycle peak approximately two years into the future!

As for specific figures of what may be in store for amateur radio, the following highlights and numbers have been extracted from the work of Mr. Sargent:

# What will Sunspot Cycle No. 21 be like?

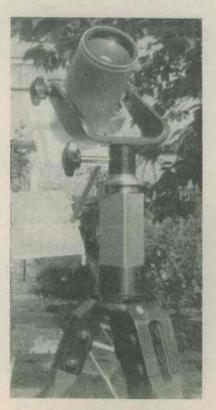
Monthly Smoothed Predicted sunspot numbers using the modified Ohl method for Cycle 21

Predicted Smoot	h Sunspot Maxi-	Approximate a	rrival date of
mum for Cycle 21	153.6	Maximum	early 1980
Jan 78. 58.6 Feb 78. 64.4 Mar 78. 69.6 Apr 78. 75.0 May 78. 80.6 Jun 78. 85.1 Jul 78. 89.5 Aug 78. 93.6 Sep 78. 97.6 Oct 78. 99.7 Nov 78. 103.3 Dec 78. 107.1	Jan 79 110.8 Feb 79 114.6 Mar 79 116.8 Apr 79 120.3 May 79 124.5 Jun 79 127.8 Jul 79 131.1 Aug 79 136.1 Sep 79 138.2 Oct 79 140.8 Nov 79 145.0 Dec 79 148.1	Jan 80 151.5 Feb 80 153.4 Mar 80 151.4 Apr 80 152.0 May 80 153.6 Jun 80 152.2 Jul 80 150.9 Aug 80 149.8 Sep 80 146.2 Oct 80 145.4 Nov 80 143.7 Dec 80 141.2	Jan 81 139.0 Feb 81 135.8 Mar 81 133.7 Apr 81 134.8 May 81 127.8 Jun 81 126.2 Jul 81 126.2 Aug 81 125.6 Sep 81 123.4 Oct 81 122.3 Nov 81 121.2 Dec 81 120.6

It is clear that if the predictions hold true, Cycle 21 will be a whopper and will likely parallel Cycle 18 and have an impact almost as severe as Cycle 19. During periods of high solar activity, many services will be detrimentally affected. Satellite damage from solar radiation is one example. Communications and power supplies will be interrupted. VLF Omega navigation alert used on overseas aircraft would likely be affected rendering the service at times, next to useless. Worse may be the impossibility of warning users that the system is failing. Space travel and related health hazards will become a significant factor to take into account. Magnetometer survey work for natural resources will be affected to such an extent that at times, the investigations would have to be curtailed with a severe cost impact on such resources even before production. CB radio on HF will likely be sheer havoc with skip interference. And now there is new evidence to support the fact that the weather is directly affected by solar sunspot behaviour and geomagnetic occurrences previously thought to be unrelated."

Solar radiation effects radio propagation primarily in two ways, viz, by untraviolet radiation and by streams of charged particles discharged in the direction of the earth, which effect the ionosphere. Ultraviolet light travels at 300,000,000 metres per second, so effects from this source happen eight minutes or so after observed effects on

the sun. Particle radiation takes longer to reach the earth; it may take up to 36 hours or longer. The effects produced in-



The author's equipment

clude the production of aurora, and high, absorption of radio waves.

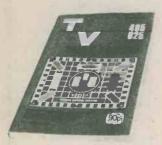
The sun \*akes approximately 27 days to complete a full rotation, so if sunspots are sufficiently intense to persist for a longer period than this, they will reappear as they come round on to the earth-facing 'face' of the sun again. They may persist for periods of four to five solar rotations. In these circumstances, periodic propagation changes will be noted every 27 days.

One of the easiest ways of assessing what's happening on the sun, and thus forecasting to some degree, possible ionospheric conditions, is to observe the sunspots on the sun. This can be done with quite a small telescope. Here we must repeat the oft'-repeated warning that one NEVER looks through a telescope directly at the sun. This would simply burn one's eyes out! What is done, is to project the sun's image on to a white screen, suitably placed at a distance from the eye-piece. With suitable equipment, a circular image of the sun, about 3 inches diameter. can be projected on to the projection screen, when sunspots can be clearly seen, if present. The equipment used by the author is shown in the accompanying photo. By tracing the size and position of the observed sunspots on a sheet of paper placed on the projection screen, records can be kept of the movement and change in size of the spots. A set of such charts drawn by the author is shown at the beginning of this article.

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# Radio Topics

By Recorder

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I wonder what arguments will be indulged in by the high fidelity enthusiasts at the turn of the cen-

The outsider, on surveying the present hi-fi scene, could well be excused for wondering what on earth the hi-fi squabbles are all about. High fidelity amplifiers have become virtually distortionless so far as laboratory tests are concerned, and yet some equipment reviewers will slam one amplifier and praise another to the skies. Objective measurements are put to one side, and amplifiers seem to be judged by subjective listening tests.

### MUSICALITY

To my mind, the current furore is to some extent inevitable, because true electrical reproduction of musical performances is impossible! An orchestra speaks with many voices, each consisting of, say, the vibration of a reed or the resonance of a string coupled to a sound-box, and these are all spaced out over a relatively wide area. To reproduce them electrically by means of a mechanically driven loudspeaker cone or cones canresult in an excellent copy of the original, but it can never be exactly the same as the original. If you want to hear the actual sound in the concert hall then you have to go to the concert hall itself.

The dividing line between the experiences of hearing music live and music reproduced is tenuous, and many sensible people are perfectly happy to virtually ignore its existence and enjoy the rightful pleasure of listening contentedly and in comfort to a good high fidelity system. In some instances the dividing line can very nearly dis-appear altogether. Imagine, for in-stance, the reproduction of a pop group whose instruments are all electronic and whose sounds are themselves generated by

loudspeakers.

But the dividing line cannot, with existing audio techniques, be completely eradicated. Between the live sound and the reproduced sound is the high fidelity system (including where applicable the recording or broadcasting process) and the high fidelity system therefore becomes a musical instrument in itself, with the result that subjective assessments of its performance become inevitable. Indeed, references are made to the "musicality" of a system. Since no two persons' subjective evaluations can entirely coincide there must always be some disagreement over the performance of different makes and designs of hi-fi equipment, and such disagreement will continue so long as the existing methods of audio reproduction remain in being.

My opening rhetorical question concerned the possibilities of hi-fi arguments continuing at the turn of the century. There are more than twenty years to go yet, but twenty years is a long time so far as high fidelity development is concerned. New audio techniques and quite startling advances are more than possible, but I would be very sur-prised indeed if these did not give rise to just as many arguments about performance as are currently

the fashion.

#### WONDERBOARD

I have just received a sample of "Wonderboard", a new circuit board originating from Orcus International of U.S.A. The board, which is about 0.15in. thick, is perforated with 6 rows of 31 holes having a Spacing of 0.1in. along the row. Spacing between rows across the board is 0.3in., 0.1in., 0.3in. and 0.3in. The board can therefore take all d.i.l. integrated circuits from 4 to 60 pins.

What makes "Wonderboard" special is that each hole is filled with a conductive elastomer which, after polymerisation, has a rubber-

like consistency but is electrically conductive. Wires, component leads and i.c. pins are simply inserted in the holes, whereupon electrical contact at each hole is provided by the elastomer. No soldering is required and there are no contact springs. The elastic conductive material forms a seal around the wire or pin passing through it, and the contact resistance between two conductors at any hole is typically of the order of 10 milliohms. The wires or pins are also held securely by the elastomer; about 3 kilograms of extraction force is required to remove

a 14 pin d.i.l. package. It is intended that components and i.c.'s be mounted on one side of the board and interconnecting wires on the other side. A typical circuit array can be seen in the photograph. Each of the holes is good for about 150 insertions and extractions, and the holes are identified on both sides of the board by letters for the rows, and numbers for the holes in the rows. For best results it is recommended that wires with a diameter of 0.25 to 0.5mm. (33 s.w.g. to 25 s.w.g.) be used, the 0.5mm. wire being easiest to insert. Wire ends and component leads should be cut at an angle so as to produce a point, thereby easing their insertion in a hole. I.C. pins do not need to be cut, but they should be vertical with respect to the con-

The U.K. representative for Orcus is Charcroft Electronics, Charcroft House, Sturmer (Haverhill),

Suffolk.

tact holes.

### PURE METAL TAPE

Development in recording tape continues unabated and a further step from chromium dioxide tape is now upon us. Pure metal, or iron metallic particle, tape offers even better tape performance, and permits further slowing of cassette tape speeds for longer playing time.

Tandbergs Radiofabrikk A/S, the Norwegian-based electronics group, state that they are already prepared to produce equipment capable of accommodating the new tape technology. In revealing their closely guarded monitoring of this substantially new tape technology over the past two years, Tandberg announce their own new systems breakthrough, referred to as "Actilinear'

It is claimed that the introduction of the new Tandberg Actilinear Recording System (for which world patents are pending) makes Tandberg the first electronics manufacturer able to exploit fully the capabilities of iron metallic particle tapes. Tandberg's new Actilinear system replaces their well established Cross-Field Recording System, and the first two products to be equipped with Actilinear, a 10½in. reel-to-reel deck type TD 20A, and the TCD 340A stereo

cassette deck, were both demonstrated earlier this year. A wider new product range incorporating Actilinear and its full iron metallic particle tape capability will be marketed when the tapes are available to the general public.

Tandberg Actilinear is a completely new recording system and employs a transconductance converter. This reduces amplifier slew rate effect and thus improves transient signal handling. Additionally, it reduces intermodulation interference from the bias oscillator and results in a dramatic 20dB improvement in signal handling capacity compared with conventional recording systems. Signal-to-noise ratio is improved because Actilinear gives stronger recording at the same or less distortion level than do previous systems.

The new metallic tapes give a possible 5 to 10dB improvement in maximum output level, a highly significant development which will also enable tape speeds to be reduced without loss of sound quality. This prospect is only feasible if the tape recording and replay system has been designed specifically to take these tapes. The Tandberg Actilinear recording system is stated to be the first to do so, and its introduction has wide implications for all recording, whether the application be educational, scientific, professional or entertainment. One

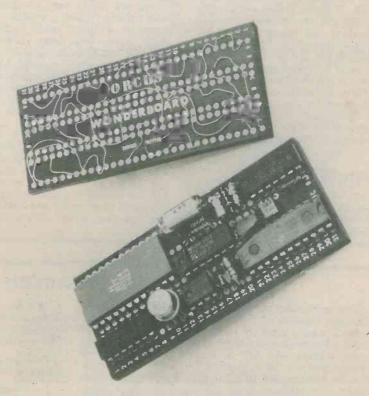
of the limiting factors (especially with cassette recorders) has been the inability to achieve good sound quality and sustain this while slowing down tape speed, to allow for long duration recordings. The com-bination of Tandberg Actilinear and the development of pure metal tapes will enable recording speeds to be reduced without loss of audio quality.

### INTERFERENCE RECEIVER

I see that Eddystone Radio are now in the process of meeting a major contract to manufacture and supply the Post Office with receivers for use by its Radio Interference Service. The contract, won incidentally in the face of fierce competition, calls for the production of over 200 receivers Type 40A.

The Type 40A is a portable single conversion superhet receiver covering the frequency range of 130kHz to 32MHz. It can be operated from either mains or battery supplies. Special design features which have been incorporated enable the receiver to measure impulsive and quasi-impulsive noise in accordance with the provision of British Standard 727. It is therefore suitable for measurements in connection with E.E.C. directives relating to radio interference.

With the introduction of new regulatory powers governing the acceptable level of radio frequency



A circuit built on Orcus "Wonderboard". Each hole in the board is filled with a conductive elastomer incorporating a silicone sealant, so that interconnections are automatically provided at each hole without soldering

interference which may be generated by all electrical appliances, the receiver Type 40A will provide the Post Office Radio Interference Service with a specialist equipment to trace and measure man-made electrical noise.

It is pleasant for the more venerable amongst us to see the revered name "Eddystone" still featuring in the news releases. Eddystone Radio Ltd. is nowadays a member company of Marconi Communications Systems.

### 9 WATT CHIP

New from Fairchild is the introduction of an audio power amplifier i.c. intended for high voltage applications driving  $8\Omega$  and  $16\Omega$  loads. This, the  $\mu$ A783, is encapsulated in the standard 12 pin power package and is designed for use as a low frequency Class B power amplifier. It can typically provide 9 watts into an  $8\Omega$  load. The i.c. can operate from a wide supply voltage range, with a maximum rating of 30 volts. Repetitive output current capability is 2.5 amps.

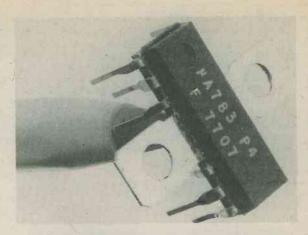
There is an integral thermal limiting circuit which offers the designer two advantages: an overload on the output, even if permanent, or an above-limit ambient temperature can be easily handled; second, the heatsink employed can have a smaller factor of safety compared with that for a conventional circuit. If junction temperature is too high the power output, power dissipation and supply current all decrease, thereby giving automatic protection for the device.

Typical applications include TV

Typical applications include TV audio circuits and inexpensive radio receivers. A further indirect advantage offered by the i.c. amplifier is that the use of a high operating voltage simplifies power supply filtering requirements.

### PAINTING

One of the more aesthetic pleasures of radio construction is the designing and subsequent



New Fairchild audio power amplifier i.c. intended for high supply voltage applications. It is capable of providing output currents and powers of 2.5 amps and 9 watts respectively, and has built-in thermal limiting

building of any electronic project in which one is engaged. An enormous amount of satisfaction can be obtained from merely surveying the completed work and observing the neatness of wiring, the logical positioning of components and the excellence of the solder joints. Indeed, very often the whole thing looks so darned good that it seems a shame to have to put it in a box!

But into a box the project has to go if it is not to collect dust or have the more fragile parts damaged. There are many very presentable ready-made plastic, plastic and metal or all-metal cases available these days. Sometimes, however, the case has to be home-made and this is where, until recently, I have been falling short of optimum so far as final appearance is concerned.

If the home-made housing uses aluminium sheet, or wood and aluminium sheet, it has to be painted. And painting is not one of my strong points. First of all I'm too impatient to get the job finished, whereupon the paint gets applied too thickly in places; and secondly I'm too impatient again to wait for the paint to set really hard before I start messing around with the case,

with the result that I get fingermarks and scratches all over it.

The solution to the problem is so obvious that I could kick myself for not having thought of it before. The answer is quick drying car touch-up paint. You can get this in small aerosols from places like Halfords, and the range of colours available is exceptionally wide. Since the aerosols are intended to match particular car make colours you can always be certain of getting exactly the same shade again if an aerosol runs out on you in the middle of a job

It is important to faithfully follow the instructions on the aerosol can, particularly with regard to shaking before use, and my favourite approach is to lay the case to be painted out on the garden path with some sheets of newspaper under it. As with all human endeavour there are penalties and risks. Working in the garden requires a fine day, which in the U.K. is a problem in itself. And it is necessary to be careful with the spray from the aerosol; otherwise you may end up with battleshipgrey chrysanthemums or an applegreen pussy-cat.

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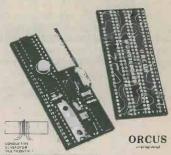
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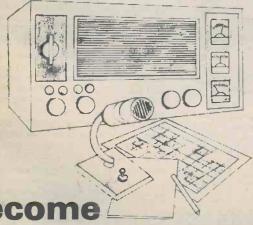
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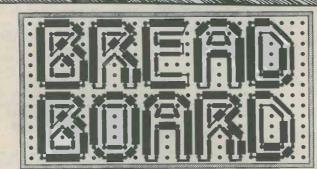
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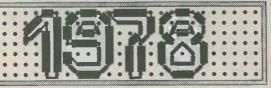
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(Continued on page 262)

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ASY66   90   *ZTX602   12   1 mag ohm   9 *0.01/25 Disc   5	ACY20	34	*ZTX202				
BC107	ASY66	90				9	
BC108	BQ107				0.25W		
**BC147/A** 8 2N39055 50 7400 13 **0.68/100 Radial 8 **2N3705 8 74L00 24 **2.2/100 Radial 8 **2N3705 8 74L00 13 **0.68/100 Radial 8 **2N3705 8 74L00 13 **0.68/100 Radial 8 **2N3705 8 74L00 13 **0.68/100 Radial 8 **0.68/100 Radial 9 **0.68/100 Radial 10 **0.68/100 R	BC108 .						*0.1/200 Radial B
**EC148/C 8 2N3703 8 74L00 24 *2.2/100 Radial 8 *2N3703 8 74L00 24 *2.2/100 Radial 8 *2N3705 8 74L01 13 *6.8/6 Tant in 10 *8.6/158	*BC147/A	8	2N3003		TTI		
***BC148/C			2N3055			13	
**BC158   9		8					
**BC2070		9	-2N3705	- 8			
**BC2078							*100/250 Axial 32
BC336B         11         *BA119         6         7405         14         W/W RESISTORS           BC351         10         IN4001/2         4         7408         17         2.5W Axial         10         0.2 to 2K2         10         0.47 to 10K         9         0.47 to 10K         9         3.3 to 2K2         3.3 t			DIOD				*680/25 Axial 18
BC351							10
BCY70         14         IN4001/2         4         7409         17         2.5W Axial         10         2 to 2K2         10         0.2 to 2K2         10         0.4 to 2K2         10         0.4 to 10         10         0.4 to 10         10         0.4 to 10         10         0.4 to 10         0.4 to 10         <							W/W RESISTORS
BCY71		14		4			2.5W Axial 10
BC   1772							
BD131   33   15940   4 7427   27   10W Axial   9	BCV72						
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BD139   33			*IS941	4			
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(Continued on page 263)

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