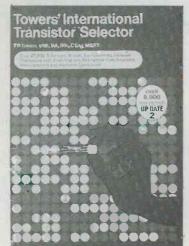


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OUR NEXT ISSUE WILL BE PUBLISHED IN LATTER PART OF NOVEMBER

Published in Great Britain by the Proprietors and Publishers, Data Publications Ltd, 57 Maida Vale, London W9 1SN.

**NOVEMBER 1980** Volume 34 No. 3

**Published Monthly** First published in 1947 Incorporating The Radio Amateur

Editorial and Advertising Offices 57 MAIDA VALE LONDON W9 1SN

Telephone 01-286 6141 Telegrams Databux, London

c Data Publications Ltd., 1980. Contents

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Annual Subscription: £9.50, Eire and Overseas £10.50 (U.S.A. and Canada

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Technical Queries. We regret that we are unable to answer queries other than those arising from articles appearing in this magazine nor can we advise on modifica-tions to equipment described. We regret that queries cannot be answered over the telephone, they must be submitted in writing and accompanied by a stamped addressed envelope for reply

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	66 Case	100 THY 16A/100	£0.62 £0.67
3 amp TO Volts No.	Price £0.32	200 THY16A/200 400 THY16A/400	£0.71 £0.89
Volts No. 50 THY3A/50 100 THY3A/100 200 THY3A/200 400 THY3A/400 600 THY3A/600 800 THY3A/800	£0.32 £0.35	600 THY 16A/600	£1.04
200 THY3A/200	£0.38	30 amp TO	£1,60 94 Case
600 THY3A/600	£0.58	Volts No.	
800 THY3A/800	£0.75	50 THY30A/50 100 THY30A/100	£1.38 £1.64
5 amp TO	SE Case	200 THY30A/200	£1.87
Volts No.	66 Case Price £0.41	50 THY30A/50 100 THY30A/50 200 THY30A/200 400 THY30A/400 600 THY30A/600	£2.06 £4.03
50 THY5A/50	£0.41 £0.52	No.	Price
Volta No. 50 THY5A/50 100 THY5A/100 200 THY5A/200 400 THY5A/400 600 THY5A/600 800 THY5A/800	£0.58	BT101/500B	£0.92
600 THY5A/400	£0.66 £0.79	BT102/500R BT106	£0.92 £1.44 £1.07
800 THY5A/800	£0.93	BT107	£1.07 £1.13
		2N3228	£1.13 £0.81
5 amp TO: Volts No.	220 Case Price		£0.89 £0.38
400 THY5A/400P	£0.86	BTX30/400L	£0.53
Volte No. 400 THY5A/400P 600 THY5A/600P 800 THY5A/800P	£0.79 £0.93	C106/4 BT116	£0.69 £1.73
Manager and State		DIODES	
A-	_		ance of
voltages availabi 5.1v, 5.6v, 6.2v, 6 15v, 16v, 18v, 20	e. 1.3v, 2.2 .8v, 7.5v, 8 v, 22v, 24	lass encapsulated r. v, 2.7v, 3.3v, 3.9v, 4.3 3.2v, 9.1v, 10v, 11v, 1 v, 27v, 30v, 33v, 39v,	3v, 4.7v, 2v, 13v,
1w-1.5w Plastic	and Met	al encapsulated. Ra	Z4 10p ange of
5.1v, 5.6v, 6.2v, 6	e. 1.3v, 2.2 .8v, 7.5v, 8	v, 2.7v, 3.3v, 3.9v, 4. 3.2v, 9.1v, 10v, 11v, 1	3v, 4.7v, 2v, 13v
15v, 16v, 18v, 20v 68v 72v 75v 82v	, 22v. 24v.	27v, 30v, 33v, 43v, 4	7v, 51v,
10w Metal stud	type SO1	0 case. Range of v	oltages
available. 1.3v, 2 5.6v, 6.2v, 6.8v, 7	2.2v, 2.7v,	3.3v, 3.9v, 4.3v, 4.7	V. 5.1V.
16v, 18v, 20v, 22v	, 24v, 27v	No. a) encapsulated. Rt. v. 2.7v, 3.3v, 3.9v, 4.3 3.2v, 9.1v, 10v, 11v, 1 2.7v, 30v, 33v, 43v, 4 No. 0 case. Range of v 3.3v, 3.9v, 4.3v, 4.7 9.1v, 10v, 11v, 12v, 1 3.3v, 33v, 43v, 47v, 5 No.	1v, 68v,
72v, 75v, 82v, 91v	, 100v.	No.	Z10 44p
BR	IDGE R	ECTIFIERS	00102
SILICON 1 amp Type No.	Price	SILICON 2 amp Type No.	Price

SILICON 1	mp	1000	SILICON 2 a	mp	
Туре	No.	Price	Туре	No.	Price
50v RMS	R1/50	£0.23	50v RMS	BR2/50	£0.52
100v RMS	BR1/100	£0.25	100v RMS	BR2/100	£0.55
200V RMS	BR1/200	£0.29	200v RMS	BR2/200	£0.60
400v RMS	BR1/400	£0.41	400v RMS	BR2/400	£0.67
			1000v RMS	BR2/1000	£0.78
SILICON 10	amp		SILICON 25	mp	
Туре	No.	Price	Туре	No.	Price
50V RMS	BR10/50	£1.73	50v RMS	BR25/50	£2.19
200V BMS	BB10/200	£1.96	200v BMS	BR25/200	£2.53

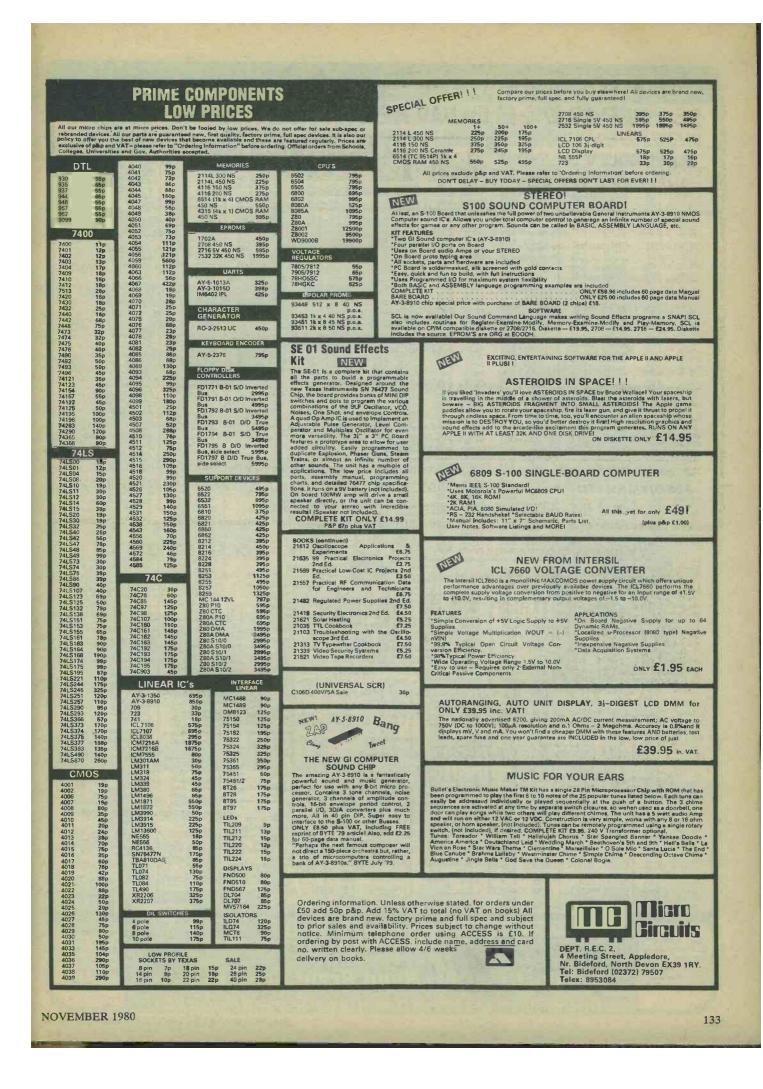
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**NOVEMBER 1980** 

VISA





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Please send an SAE with all enquiries. Access/Barclaycd (min 25 please) Callers welcome       PRICES EXCLUDE VAT - PLEASE ADD 15%* Postage 35p per order. CWO please. (*UK only)       Compatibility Callers Compatibility Compatibility       CWO PLEASE : Commercial MA terms on application. Goods are offered subject to availability, prices subject to change + so please phone and check if in doubt.       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Pa           TTL. Nand LSN 74030         7443N 7445N         1.15 7402N 7445N         7443N 7445N         1.15 7445N           7400N         0.13 7445N         0.94 741301         7445N         0.94 741301           7401N         0.13 741501         0.20 741530         74154N         0.94 741503           7402N         0.14 7402N         74154N         0.17 741503         74154N         0.17 741500           7402N         0.14 7405N         741555         0.24 7405N         0.14 741553         0.24 741573         0.33 741573           7402N         0.28 741500         741573         0.32 741573         0.33 74157         0.33 74157         0.33 74157         0.33 74167           7412N         0.17         7475N         0.38 74157         741576         0.38 74157         0.38 74157         741576         0.38 74157         0.38 74157         741576         0.38 74157         741576         0.38 74157         741576         0.38 74157         0.37 741578         0.38 74157         741576         0.38 74158         0.49 74157         0.37 741578         0.38 74157         0.37 741578         0.38 74158         0.49 74158         0.49 74158         0.49 74158         0.49 74158         0.49 74158         0.49 74158         0.49 74158 <t< th=""><th>74L5112 0.38       74L5169 2.00         74L5114 0.38       74L5170 2.00         74L5114 0.38       74L5174 1.20         74L5114 0.38       74L5175 1.10         74L21N 0.42       74L5175 1.10         74L21N 0.42       74L5175 1.10         74L21N 0.42       74L5175 1.10         74L2N 0.44       74L5181 3.50         74L2N 0.44       74L5181 3.50         74L2N 0.73       74L5181 3.50         74L2N 0.73       74L5181 3.50         74L2N 0.74       74L5182 2.10         74L2N 0.74       74L5182 2.10         74L512 0.78       74L5190 0.92         74L512 0.78       74L5190 1.25         74L512 0.78       74L5190 1.25         74L513 0.60       74L5190 1.25         74L513 0.60       74L5190 1.25         74L514 0.77       74198N 1.50         74L4N 1.29       74L5190 1.25         74L4N 1.29       74L5190 1.25         74L4N 1.29       74L5190 1.53         74L4N 1.29       74L5190 1.53         74L4N 1.29       74L5190 1.53         74L4N 1.29       74L5190 1.55         74L510 0.40       74L5280 1.55         74L510 0.55       74L5280 1.55         74L510 0.40       <td< th=""><th>VARICAP TUNING DIODES         TRANSISTORS BAL21 0.30           BAL21 0.30         BC337 0.08           BAL21 0.30         BC338 0.08           BBL058 0.36         BC307 0.08           BBL05 0.36         BC308 0.08           BBL05 0.35         BC413 0.10           BB212 1.95         BC414 0.11           KV1220 2.45         BC414 0.11           KV1225 2.75         BC56 0.12           KV1225 2.75         BC56 0.12           SWITCHING AND         BC640 0.23           SHOTKY DIODES         SCMAL DIODES           SMOTKY DIODES         SCMAR 0.40           BA162 0.19         SCMAR 0.40           BA379 0.25         SCMAR 0.40           SIGNAL DIODES         SCMAR 0.40           BRIDCES:         SCMAR 0.40           IN4148 0.06         SCMAR 0.40           IN4002 0.07         SA1085 0.32           SNA02 0.15         SCMAR 0.40           BRIDCES:         SCMAR 0.40           IN4002 0.07         SA1085 0.32           SNA02 0.15         SCMAR</th><th>Es         CAPACITORS All Smm or less spacing           CENAMIC 500 202, 203, 477, 649 862, 109, 159, 188. 0.04 229, 279, 339, 477 569, 689, 829, 1009. 0.05 1509, 2209, 2709 1309, 3909, 4709 0.05 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.05 22, 47% 0.09 MCMOLITHIC CERAMIC 100, 100 0.16 FEEDTHRU ING SOLDER IN 0.09 POLYESTER (SIDENES) 10m LEAD SPACING 100, 120, 203, 33N 0.17 477, 6601, 1000 0.18 200, 470N 0.29 POLYESTER (GENERAL) 10m LEAD SPACING 100, 130N, 470N 0.18 200m LEAD SPACING 100, 130N, 220, 33N 0.06 470, 6601, 9207 0.11 200m LEAD SPACING 100, 100, 220, 33N 0.08 100N 0.09 20m LEAD SPACING 100, 109, 220, 33N. 0.08 100N, 0.09 20m LEAD SPACING 200M, 470N 0.17 POLYSTYRENE 100, 159, 189, 229, 207, 470, 500, 6809. 0.08 1000, 100, 220, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.17 POLYSTYRENE 100, 130, 210, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.17 POLYSTYRENE 100, 130, 210, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.12 477, 560, 6809. 0.01 100, 122, 100, 100 220, 470, 500, 600, 101 100, 122, 207, 100, 103 47, 100, 210, 100 220, 47, 100, 103 47, 100, 100, 220, 30, 00 47, 100, 100, 220, 30, 00 47, 100, 100, 220, 30, 00 47, 100, 100, 100 220, 200, 100 100, 22, 207, 100, 100 100, 20, 200, 100 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.00 100, 0.</th></td<></th></t<>	74L5112 0.38       74L5169 2.00         74L5114 0.38       74L5170 2.00         74L5114 0.38       74L5174 1.20         74L5114 0.38       74L5175 1.10         74L21N 0.42       74L5175 1.10         74L21N 0.42       74L5175 1.10         74L21N 0.42       74L5175 1.10         74L2N 0.44       74L5181 3.50         74L2N 0.44       74L5181 3.50         74L2N 0.73       74L5181 3.50         74L2N 0.73       74L5181 3.50         74L2N 0.74       74L5182 2.10         74L2N 0.74       74L5182 2.10         74L512 0.78       74L5190 0.92         74L512 0.78       74L5190 1.25         74L512 0.78       74L5190 1.25         74L513 0.60       74L5190 1.25         74L513 0.60       74L5190 1.25         74L514 0.77       74198N 1.50         74L4N 1.29       74L5190 1.25         74L4N 1.29       74L5190 1.25         74L4N 1.29       74L5190 1.53         74L4N 1.29       74L5190 1.53         74L4N 1.29       74L5190 1.53         74L4N 1.29       74L5190 1.55         74L510 0.40       74L5280 1.55         74L510 0.55       74L5280 1.55         74L510 0.40 <td< th=""><th>VARICAP TUNING DIODES         TRANSISTORS BAL21 0.30           BAL21 0.30         BC337 0.08           BAL21 0.30         BC338 0.08           BBL058 0.36         BC307 0.08           BBL05 0.36         BC308 0.08           BBL05 0.35         BC413 0.10           BB212 1.95         BC414 0.11           KV1220 2.45         BC414 0.11           KV1225 2.75         BC56 0.12           KV1225 2.75         BC56 0.12           SWITCHING AND         BC640 0.23           SHOTKY DIODES         SCMAL DIODES           SMOTKY DIODES         SCMAR 0.40           BA162 0.19         SCMAR 0.40           BA379 0.25         SCMAR 0.40           SIGNAL DIODES         SCMAR 0.40           BRIDCES:         SCMAR 0.40           IN4148 0.06         SCMAR 0.40           IN4002 0.07         SA1085 0.32           SNA02 0.15         SCMAR 0.40           BRIDCES:         SCMAR 0.40           IN4002 0.07         SA1085 0.32           SNA02 0.15         SCMAR</th><th>Es         CAPACITORS All Smm or less spacing           CENAMIC 500 202, 203, 477, 649 862, 109, 159, 188. 0.04 229, 279, 339, 477 569, 689, 829, 1009. 0.05 1509, 2209, 2709 1309, 3909, 4709 0.05 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.05 22, 47% 0.09 MCMOLITHIC CERAMIC 100, 100 0.16 FEEDTHRU ING SOLDER IN 0.09 POLYESTER (SIDENES) 10m LEAD SPACING 100, 120, 203, 33N 0.17 477, 6601, 1000 0.18 200, 470N 0.29 POLYESTER (GENERAL) 10m LEAD SPACING 100, 130N, 470N 0.18 200m LEAD SPACING 100, 130N, 220, 33N 0.06 470, 6601, 9207 0.11 200m LEAD SPACING 100, 100, 220, 33N 0.08 100N 0.09 20m LEAD SPACING 100, 109, 220, 33N. 0.08 100N, 0.09 20m LEAD SPACING 200M, 470N 0.17 POLYSTYRENE 100, 159, 189, 229, 207, 470, 500, 6809. 0.08 1000, 100, 220, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.17 POLYSTYRENE 100, 130, 210, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.17 POLYSTYRENE 100, 130, 210, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.12 477, 560, 6809. 0.01 100, 122, 100, 100 220, 470, 500, 600, 101 100, 122, 207, 100, 103 47, 100, 210, 100 220, 47, 100, 103 47, 100, 100, 220, 30, 00 47, 100, 100, 220, 30, 00 47, 100, 100, 220, 30, 00 47, 100, 100, 100 220, 200, 100 100, 22, 207, 100, 100 100, 20, 200, 100 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.00 100, 0.</th></td<>	VARICAP TUNING DIODES         TRANSISTORS BAL21 0.30           BAL21 0.30         BC337 0.08           BAL21 0.30         BC338 0.08           BBL058 0.36         BC307 0.08           BBL05 0.36         BC308 0.08           BBL05 0.35         BC413 0.10           BB212 1.95         BC414 0.11           KV1220 2.45         BC414 0.11           KV1225 2.75         BC56 0.12           KV1225 2.75         BC56 0.12           SWITCHING AND         BC640 0.23           SHOTKY DIODES         SCMAL DIODES           SMOTKY DIODES         SCMAR 0.40           BA162 0.19         SCMAR 0.40           BA379 0.25         SCMAR 0.40           SIGNAL DIODES         SCMAR 0.40           BRIDCES:         SCMAR 0.40           IN4148 0.06         SCMAR 0.40           IN4002 0.07         SA1085 0.32           SNA02 0.15         SCMAR 0.40           BRIDCES:         SCMAR 0.40           IN4002 0.07         SA1085 0.32           SNA02 0.15         SCMAR	Es         CAPACITORS All Smm or less spacing           CENAMIC 500 202, 203, 477, 649 862, 109, 159, 188. 0.04 229, 279, 339, 477 569, 689, 829, 1009. 0.05 1509, 2209, 2709 1309, 3909, 4709 0.05 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.06 100, 202, 203, 407. 0.05 22, 47% 0.09 MCMOLITHIC CERAMIC 100, 100 0.16 FEEDTHRU ING SOLDER IN 0.09 POLYESTER (SIDENES) 10m LEAD SPACING 100, 120, 203, 33N 0.17 477, 6601, 1000 0.18 200, 470N 0.29 POLYESTER (GENERAL) 10m LEAD SPACING 100, 130N, 470N 0.18 200m LEAD SPACING 100, 130N, 220, 33N 0.06 470, 6601, 9207 0.11 200m LEAD SPACING 100, 100, 220, 33N 0.08 100N 0.09 20m LEAD SPACING 100, 109, 220, 33N. 0.08 100N, 0.09 20m LEAD SPACING 200M, 470N 0.17 POLYSTYRENE 100, 159, 189, 229, 207, 470, 500, 6809. 0.08 1000, 100, 220, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.17 POLYSTYRENE 100, 130, 210, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.17 POLYSTYRENE 100, 130, 210, 33N. 0.12 477, 560, 6809. 0.09 100, 100, 220, 30, 0.12 477, 560, 6809. 0.01 100, 122, 100, 100 220, 470, 500, 600, 101 100, 122, 207, 100, 103 47, 100, 210, 100 220, 47, 100, 103 47, 100, 100, 220, 30, 00 47, 100, 100, 220, 30, 00 47, 100, 100, 220, 30, 00 47, 100, 100, 100 220, 200, 100 100, 22, 207, 100, 100 100, 20, 200, 100 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.09 100, 100, 0.00 100, 0.
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**NOVEMBER 1980** 

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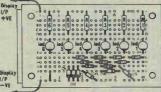
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TRANSISTORS AC126 16p	BD238 25p MJE2371 80 BD239C 29p MJE2901T (Higai BD240 30p 2955) 50	n 2N2412 27p p 2N2483 28p	ELECTROLYTIC CAPACITORS (Wine ended) 3-4 Volt	1
AC127 16p AC128 5p AC153 11p AC176 11p AC188 10p	BD242         30p         MJE2955 70v 90w           BD243/8 65wT         30p         45j           BD246         32p         M n 15 Russian 25           BD253         44p         Mo41TRussian 25           BD375         35p         Mp131 Dual	p 2N2614 4p p 2N2887 £2 2N2894 11p	25 69 160 129 2000 289 HAT SUIT	
ACY20 30p ACY21 20p ACY28 22p AD161/162 match pair 70p	BD437 350 MOSFET 15 BD438 28p MOSFET 15 BD537 60v 55w 36p BD677 60v 40w 50p BD678 Pwr. Darl. 50p MRF502 Improved	p 2N2906 9p 2N2907/A 9p	4         6p         100         7p         1500         71p           8         6p         250         12p         3300         24p         Summary           35         6p         470         8p         10000         70p         1500         71p           35         6p         470         8p         10000         70p         10000         70p           80         7p         1000         15p         Mail         M	
AF119 35p AF124 27p AF126 27p AF126 27p AF127 27p	BDX34B Pwr. Dail         n 16A Russian         25           BDX34B Pwr. Dail         n 16A Russian         25           BDX77         50p         NKT A49         23           DDX70         92p         NKTA52         20	P 2N3020 25P P 2N3053 16P P 2N3054 35P P 2N3055RCA 68P P 2N3132 240	50         6p         680         12p         XIIII           80         7p         1000         15p         XIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	
AF139 23p AF178 35p AF180 35p AF181 33p AF239 35p	BF115         18p         NKT152         38           BF137         11p         NKT153         24           BF167         18p         NKT153         24           BF173         18p         NKT154         26           BF173         18p         NKT251         18	P 2N3283 25p P 2N3418 15p P 2N3553 56p P 2N3583 18p	0.47         6p         150         16p         100         6p         100         6p           22         5p         220         6p         1600         28p         300         100	
ASY60 35p ASY63 4p ASY73 35p ASZ21 4p AU110 £1.43	BF179         23p         NK1775         10           BF180         12p         OC41         4           BF181         8p         OC41         4           BF182         18p         OC42         21	P 2N3645 3P P 2N3703 3p P 2N3704 4p P 2N3704 4p P 2N3707 3p	125 7p 600 16p 12 Volt 8 7p 50 7p 550 27p 10 8p 100 8p 1000 17p	
AU113 £1:03 AUY10 70p BC107/A/B 6p BC108/A/B/C 6p BC109/B 6p	BF183         18p         0C44         4           BF184         18p         0C45         13           BF185         18p         0C45         13           BF194/A         5p         0C71         1XK1201           BF195/C/D         5p         0C76         15	P 2N3711 3P P 2N3714 80v 150w P 2N3794 54p P 2N3794 14p 2N3799 18p	26 7p 200 13p 30 7p 260 15p	
BC125B 3p BC140 15p BC141 11p BC147/A/B/C 5p	BF196         5p         OC77         46           BF197         5p         OC81 (XK122)         44           BF198         5p         OC84 (XK122)         44           BF200         13p         OC80 (XK122)         44           BF201         5p         OC201         46           BF220         13p         OC200         41           BF224         4p         OC201         66	P 2N3906 8p 2N4000 15p 2N4026 15p 2N4026 15p 2N4031 15p	2.5 34p 33 4p 330 15p ZZHUL	
BC148/A/B/C 5p BC149/A/B/C/S 5p BC157/A 7p BC157/A 5p BC158/A/B 5p	BF244C FET         74p         OC202         66           BF245         6p         OC603         50           BF256FET         4p         OC701         50           BF256LB/LC FET         3p         ON222         23	P 2N4062. 4P P 2N4285 17p P 2N4891 30p P 2N4918 15p P 2N4918 15p	25 7p 200 10p	
BC159/B/C         5p           BC171B         4p           BC172         6lp           BC172c         7lp           BC173         4p	BF262         29p         P7029         30           BF263         29p         PXB103         25           BF274         8p         RCP701A/B/D         10w           BF324         3pp         30         30	P 2N5147 15p P 2N5247 FET 40p P 2N5293 80v 36w 30p T 2N5294 80v 36w 30p P 2N5295 60v 36w 30p	1.6 6p 40 4p 250 8p	
BC177A         10½p           BC178A/B/C         10¾p           BC179B         14p           BC182/AL         5p           BC182L         3p	BF336         16p         R1039 (2010)         54           BF355         15p         R2008B         £1.1           BF394B         3p         R2010B         £1.1           BF451         6p         R2306 100 v40w 26         26           BF494         10p         R2306 100 v40w 26         £1.1	P 2N5296 60v 36w 30p 8 2N5297 80v 36w 36p 8 2N5449 3p P 2N5484 37p	16 4p 100 6p 680 11p 22 4p 150 6p 750 27p	
BC183A/AL/L/LC         3p           BC184L         5p           BC186         21p           BC187         8p           BC204         11p	BF615         27p         S3017         25           BF615         27p         SB240         2d           BF617         27p         SB240         2d           BF035         15p         SF1357         2d           BF035         15p         SJE5039         5d	2N5494 60v 50w 36p           5p         2N5915 (16068)           5p         450Mhz 6WT R.F. 12v           5p         £2.50	25 4p 160 5p 1000 20p 30 4p 200 13p 33 4p 220 7p 30 Volt 10 7p 100 13p 640 50p 200 25 5p 200 20p	
BC212L/B         5p           BC213L         5p           BC213LA         3p           BC213LB         4p           BC213LB         5p	BFR38         68p         TIP29 40v 30w         22           BFR86         19p         TIP30         22           BFS21 FET pair         £3         TIP30C 100v 30w	21         36p           2p         2N6028 PUJT         3p           2p         2N6101 80v 75w         55p           2N6106 80v 40w         44p	35 Volt 52 220 220 00 975	
BC214L 34p BC237A 74p BC238 5p BC238B/C 74p	BF528 Dual MOS 50p         TIP31 50v 40w         22           BFT30         15p         TIP32A 60v 40w         22           BFT39         15p         TIP32C 100v 40w         22           BFT39         15p         TIP32C 100v 40w         22	2p         2N6111 40v 40w 36p           2p         2N6124         24p           2N6178 100v 25w 30p         30p           5p         2N6254         36p	10         3p         4/         3p         3do         22p         100         000000000000000000000000000000000000	
BC239C         71p           BC251         3p           BC257B         71p           BC258B/C         71p           BC259C         71p	BFT60 6p TIP41 40V 65W 13 BFT61 15p TIP42C 100V 65W BET70 15p 33	5p 2N6288 30v 40w 36p 2N6290 60v 40w 30p 3p 2N6292 80v 40w 30p 3p 2N6385 Pwr. Darl. 54p 5p 2N6486 40v 75w 36p	15 3p 100 6p 1000 20p 16 7p 150 8p 2200 36p 9 9 5	OP POST.
BC302         15p           BC304         15p           BC307         7p           BC308B/C         71p           BC309B         71p	BFW11 FET 46p TIP117(Pwr. Dar)4 BFW30 15p TIS60GY BFW31 15p TIS61 BFW57 18p TIS73L	5p         2N6488 90v 75w         36p           3p         2S701         18p           3p         2SA12         42p           3p         2SA50         36p           4p         2SA80         36p	25         5p         220         7p           32         5p         250         15p           50'Volt         50'Volt         9p         9p           3         7p         16         7p         250         9p           3.3         4p         25         5p         330         9p           4         7p         33         5p         470         22p           5         7p         47         6p         500         22p	brus 3
BC327         5p           BC328         6p           BC338         5p           BC382L         71p           BC384B         71p	BFX12         23p         TIS91 UN13unc.           BFX29         11p         TIS92GY         1           BFX30         16p         TIS98         8           BFX37         16p         TK24         2	3p         2SA83         36p           4p         2SA141         36p           3p         2SA142         36p           0p         2SA234         50p	8         7p         50         7p         1000         22p           10         4p         100         9ip         64 Volt         64 Volt           0.64         6p         8         8p         50         12p	
BC546 5p BC547/A/B 5p BC548/A/B/C 5p BC549C 5p BC556 5p	BFX85         14p         U14710         2           BFX85         20p         ZT403P         3           BFX89         20p         ZT1486         £1.           BFY39         20p         ZTX300         2	9p 2SA518 38p	2 34p 15 8p 100 8p 2.2 34p 20 8p 150 74p 2.5 3p 22 6p 220 8p HSU3 4 8p 33 5p 330 8p	h goods
BC557/B         5p           BC558A         5p           BC559         5p           BC612L         4p           BCW71R         1p	BFY51         15p         ZTX341           BFY52         15p         2G103         3           BFY75         15p         2G302         1           BFY90         40p         2G309         3	8p         2SA634         80p           9p         2SB56         18p           3p         2SB75         25p           2p         2SB77         25p           0p         2SB135         25p	4,7 4p 47 6p 470 121p 100 10p 70 Volt 1000 60p	ded wit
BCX32 10p BCX34 10p BCX36 10p BCY11 28p	BLY10 23p 2G339A 2 BR101   Prog. 20p 2G371 1 BRY39 - Uni- 23p 2N456A 7 BRY56   Junct. 29p 2N597 1 BSV64 36p 2N598 1	Op         2SB136         25p           8p         2SB156         60p           '1p         2SB175         20p           6p         2SB176         20p           16p         2SB187         25p	4,7 4p 47 6p 470 121p 70 Volt 1000 60p 100 10p HUGHES MICRO ELECTRONICS 400MW ZENER DIODES 200 IN CLEAR PLASTIC HINGED LID COMPONENT BOX £2.30 3V, 3.6V, 3.9V, 4.3V, 4.7V, 11V or 13V END OF LINE STOCK ITEMS AND COMPUTER & AUDIO BOARDS/ASSEMBLIES WITH VARYING CONTENTS INCLUDE ZENER, GOLD BOND,	pts inclu
BCY56         10p           BCY70         8p           BCY71         8p           BCY72         8p	BSV79 50p 2N601 £1 BSV80 50p 2N644 2 BSV81 75p 2N706A 10 BSX19 15p 2N706	.50         2\$B457         25p           22p         2\$C1061         50v         50p           9p         2\$D234         60v         25w         50p           9p         2\$D234         60v         25w         50p           9ip         2\$D234         60v         25w         50p	COMPONENT BOX £2.30 3V, 3.6V, 3.9V, 4.3V, 4.7V, 11V or 13V END OF LINE STOCK ITEMS AND COMPUTER & AUDIO BOARDS/ASSEMBLIES WITH VARYING	VT receil
BCY79B 15p BC211 32p BD113 57p BD115 35p BD(BRC)116 45p BD132 15p	BSX21         10p         2fv314         1           BSX78         Bp         2N918         1           BSY40         30p         2N929         1           BSY95A         10p         2N884         2	15p         16120 (MPSU55/           12p         BD238)         19p           16p         40235         50p           28p         40250         36p		
BD133         28p           BD135         22p           BD136         14p           BD137         28p	BU105/04         78p         2N1091           BU133 750v 30w         74p         2N1132           BU204         50p         2N1302           BU208         90p         2N1303	16p sink) 40p 14p 40316 40v, 50w 36p 16p 40372 (2N3054 + Ht.	CIRCUITS, ETC.	orde
BD138 28p BD137/8 mtch pr 60p BD139 17p BD140 26p BD142 35p	D45C2 40v 30w 2N1485 60v 25w 3 220HFE 50MHz 50p 2N1487 50 D1693 £2.85 2N1490	25p 5ink) 40p 26p 40349v2 160v 1w 30p 40394 60v 1w 30p 40409 + Ht. sink 90v 63 3w 40p 64 40410 + Ht. sink 90v	MARKED FULL SPEC DIGITAL I.C.'s Branded - New 25 for £1 Mixed 7 MILLION CARBON FILM RESISTORS PURCHASED	Paymentwith
BD156 50p BD182p 70v 117w 44p BD201 86p BD202 64p	GET111 45p 21/1507 GET120 30p 2N1711 M103G MOSFET 30p 2N1711 MA393 25p 2N1716	3w         40p           18p         40602 VHF Mosfet           13p         36p           15p         36p           28p         40633 NPN 40w         36p           28p         40633 NPN 40w         36p		
BD203 86p BD204 86p BD232 52p BD233 20p BD235 35p	MD7000 E2:25 2N2192A ME2 43p 2N2221/A ME0412 14p 2N2221/A ME0012 202222A	40911 (2N6261 + Ht. 9p sink) 40p 8p 10p	packed tight and on top of each other to ceiling of wa ehouse. PACK OF 100 FOR 25p	Terms:

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ICES SO LOW, THE POLICE NVESTIGATED ME TWICE IRIES, ETC. MUST BE ACCOMPANIED BY A TAMPED ADDRESSED ENVELOPE	OPTO ELECTRONICS Photo Diodes: BPX40, BPX42, BPY10, CQY77, CQY17, BPY68, BPY89, BPY77 36p, Wire end neons 4p, Photo transistors: BPX43, BP103, 2N5777 Darlindron, 366, OCP71 40p, LED's (Mullard Sei- mans) Red .2" 8p, 125" 9p; Green .2" 11p; .125" 12jp; Micro Yellow LD481 7jp, PhOTO SILICON CON- TROLLED SWITCH BPX66 PNPN 10 amp 36p CA3062 Photo Detector and power amp. £1 7 SEGMENT L.E.D.	AA119         7p           AA113         9p           AA215         15p           B1         11p           BA116         30p           BA128         21p           BA145         21p           BA145         21p           BA182 Varicap         6p           BAX14         21p           BA3182 Varicap         6p           BB103 Varicap         6p           BB104 Varicap         16p           BB109 Varicap         24p           BB113 Varicap         43p           BB139         £1           BY206         71p           BY207         23p           BY402         24p           BY402         24p           BY402         24p           BY206         71p           BY206         71p           BY206         3p           Centercell         3p           Centercell         3p	OA5         25p           OA7         25p           OA10         25p           OA47         7p           OA70         10p           OA75         11p           OA79         11p           OA85         8p           OA200         2p           OA202         2ip           IGP10         11p           IN862         2p           IN916         2ip           IN9358         7ip           IN9368         7ip           IN9348         7ip           IN9368         7ip           IN9368         7ip           IN9368         7ip           IN9438         7ip           IN3664         2ip           IN418         1p	BRIDGE RECTIFIERS           4         60V         BC30 C350         23p           1         1,600         BYX10         34p           0.6         110         EC433         20p           1         50V         W005         19p           1         400         OSH01-200         25p           1         200V         W02 Ex Equip         15p           1         400V         W04         28p           1         400V         W04         28p           1         400V         W08         27p           1         800V         W08         27p           1         1000         W10         36p           14         75V         IBIBY234         11p           15         150V         9F2         70p           21         500V         9F4         85p           3         50         KBS005         30p           3         100         KB501         30p           3         400         KB506         30p           3         600         KB506         30p           3         600         KB506         30p
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AT & PO 2nd Class of 0RDER E3 (excluding SMALL ORDER HAND FOTAL ALSO INCLU	isolator I.R. diode and NPN Photo-Darlington amp 26p CNY17/1 opto coupler 70p Cold cathode tubes I.T.T. G517A or 5870L 60p SPECIAL OFFER IN4004 or IN4006. Sealed manufacturers carton of 300. £6.75	BYX20-200 200 BYX22-200 300 BYX38-300R 300 BYX38-600 600 BYX38-900 900 BYX38-1200 1200 BYX42-300 900 BYX42-300 900 BYX42-400 900 BYX42-400 900 BYX46-300R 300 BYX46-500R 500 BYX46-500 600 BYX46-500 600 BYX46-300R 300	10mA 6p 25 72p 14 25p 24 48p 24 60p 24 65p 23 60p 24 65p 10 36p 10 46p 10 92p 10 £1.07 15 £1.05 15 £2.00 15 £2.00 16 47p	4         50         \$2260F Sensitive-gate         36p           7         400         \$2620D         45p           4         400         \$2261D Sensitive gate         38p           8T         106         70p         8T 107         £1           TRIACS           Amp         Volt         3p           2.5         600         2N5757         44p           4         400         T2716D/40730         74p           6         200         T2500B/41014         54p           6         400         T2500D         72p
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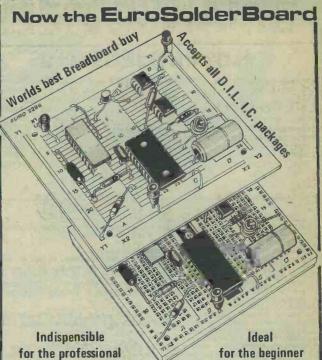
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CA758 (MC1311) 36p CA750 TV Horiz system 72p CA3007 RF Amp 75p CA3028A 75p CA3028A 75p CA3028A 75p CA3028A 67p CA3044 12120 CA3046 Transistor Array 40p CA3060 72p CA3060 72p CA3080 OP AMP 59p CA3080 OP AMP 59p CA3080 OP AMP 59p CA3086 22p CA3086 22p CA3086 22p CA3083 54p CA3093 Prog. Sw. Pwr. OP Amp. 36p CA3094 Prog. Sw. Pwr. OP Amp. 36p CA3095 222 CA3146E 90p CA3123 77p CA3146E 90p CA3123 800 OL 0140 OP Amp 36p CA3124 E222 CA3146E 90p CD4000 Dual 3 input Nor + Inv. 12p CD4000 Dual 3 input Nor + Inv. 12p CD4001 Dual 4 Input Nor 13p CD4010 Dual 4 Input Nor 13p CD4010 Dual 4 Bit Shift Register 36p CD4016 Ouad Bilateral Latch 54p CD4017 Decade Count/Divide 54p CD4018 Preset Divide N Count 43p CD4018 Preset Divide N Count 43p CD4019 Quad 2 Input Mor 14p CD4019 Quad 2 Input Nor 14p CD4019 Quad 2 Input Nor 14p CD4019 Quad 2 Input Nor 14p CD4025 Triple 3 input Nor 14p CD4026 Preset Bin/Dec 54p CD4021 Nor 14 Stage Binary Count 54p CD4027 dual JK master/slave 30p CD4028 H Bit Shift Register 54p CD4027 dual JK master/slave 30p CD4028 J Triple 3 input Nor 14p CD4025 H Bit Par. in out Shift 54p CD4043 Quad Nor R/S Latch 54p CD4043 Quad Nor R/S Latch 54p CD4043 Analogue Multi/Demulti 54p CD4045 A Bit Par. in out Shift 54p CD4045 A Bit Par.	TICA270CW/AW	35½p
CA3001 HF Amp 886 CA3028A 75p CA3044 £120 CA3046 Transistor Array 400 CA3064 Transistor Array 400 CA3064 Transistor Array 400 CA3065 Array 400 CA3065 300 CA3080 OP AMP 590 CA3083 65p CA3083 65p CA3083 65p CA3083 700 CA3089 540 CA3089 540 CA3093 Frog. Sw. Pwr. OP Amp. 36p CA3093 Frog. Sw. Pwr. OP Amp. 36p CA3093 Frog. Sw. Pwr. OP Amp. 36p CA3093 700 CA3123 739 CA3132 FM 52.22 CA3132 FM 52.22 CA3132 FM 52.22 CA3132 FM 52.22 CA3132 FM 52.22 CA3132 FM 52.22 CA3132 FM 52.22 CA3146E 500 CA3132 FM 52.22 CA3146E 500 CA3183 800 CA3183 800 CA3183 800 CA3189 730 CA319 1(LM3900) Quad OP Amp 360 CD2500E 30mA/Seg Deml Point Dvr 90p CD2600E 30mA/Seg Deml Point Dvr 90p CD4000 Dual 3 input Nor + Inv. 12p CD4006 La Stage Static Shift Reg. 36p CD4001 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binary Full Adder 54p CD4010 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binary Full Adder 54p CD4013 Dual D Flip Flop 36p CD4014 Dual 4 Bit poit Mori 13p CD4013 Dual J DFlip Flop 36p CD4014 Stage Binary Count 54p CD4015 Preset Divide N Count 43p CD4014 Stage Binary Count 54p CD4014 Stage Binary Count 54p CD4015 Preset Divide N Count 43p CD4014 Stage Binary Count 54p CD4021 Nual J Input Nor CD4023 Triple 3 Input Nor CD4024 Mat Nor CD4025 Hit Par. In out Shift 54p CD4026 Mat Nor R/S Latch 54p CD4043 Quad Nor R/S Latch 54p CD4043 Analogue Multi/Demulti 54p CD4044 Quad Nand R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4043 Analogue Multi/Demulti 54p CD4044 Quad Nand R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4045 A LiNE LCD driver/count 72p CD4066 Quad Bilsteral Switch 72p CD4066 Quad Bilsteral Switc	CA758 (MC1311)	36p
CA3046 I ransistor Array 400 CA3060 72p CA3060 72p CA3080 OP AMP 59p CA3080 OP AMP 59p CA3080 OP AMP 59p CA3083 54p CA3083 54p CA3083 54p CA3093 Prog. Sw. Pwr. OP Amp. 36p CA3094 Prog. Sw. Pwr. OP Amp. 36p CA3095 222 CA3146E 900 CA3183 800 CA3183 800 CA3199 CA3183 800 CA3199 CA3183 800 CA3199 CD 200 Duaid OP Amp 36p CD2500E 30mA/Seg Dcml Point Dvr. 90p CD4000 Duai 3 input Nor + Inv. 12p CD4000 Duai 3 input Nor + Inv. 12p CD4000 Duai 4 Stage Static Shift Reg. 36p CD4001 Duai 4 Bit Shift Register 36p CD4017 Decade Count/Divide 54p CD4018 Preset Divide N Count 35p CD4018 Preset Divide N Count 35p CD4019 Quad 2 Input Mor 14p CD4012 Divide Dy 8 Count/Divide 36p CD4018 Stage Static Shift Register 54p CD4019 Quad 2 Input Mor 14p CD4019 Quad 2 Input Mor 14p CD4019 Quad 2 Input Mor 14p CD4025 Triple 3 input Nor 14p CD4026 Dec. Count 7 Seg. Out 72p CD4027 duai JK master/slave 30p CD4028 JR/MC14028 BCD/Decimal 42p CD4028 JR/MC14028 BCD/Decimal 42p CD4043 Quad Nor R/S Latch 54p CD4043 Quad Nor R/S Latch 54p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4045 A Bit Par. In out Shift 54p CD4048 Mitri Par. Jin out Shift 54p CD4048 Hex Inverter Buffers 36p CD4049 Hex Inverter Buffers 36p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4045 A LiNE LCD driver/count 72p CD4066 Quad Bilateral Switc		86p
CA3060       72p         CA3080 OP AMP       59p         CA3080 OP AMP       59p         CA3083 OF AMP       54p         CA3093 OF Og. Sw. Pwr. OP Amp.       36p         CA3083 Prog. Sw. Pwr. OP Amp.       36p         CA3083 Prog. Sw. Pwr. OP Amp.       36p         CA3123 F       72p         CA3146E       90p         CA3183 B       80p         CA3183 B       80p         CA3183 B       80p         CA3183 B       80p         CA3000 Dual 3 input Nor + Inv.       12p         CD4000 Dual 3 input Nor + Inv.       12p         CD4000 Dual 3 input Nor + Inv.       12p         CD4001 Dual 4 Bit Bintry Full Adder       54p         CD4010 Dual 2 Onp. Pair + Inv.       12p         CD4002 Dual 4 Input Nand       13p         CD4013 Dual D Flip Flop       36p         CD4014 B Bit Shift Register       36p         CD4015 Dual 4 Input Nand       13p         CD4016 Quad Bilateral Latch       36p         CD4017 Decade Count/Divide       54p	CA3028A CA3044	£1.20
CA3060 OP AMP 59p CA3080 OP AMP 59p CA3083 65p CA3083 65p CA3084 7000 CA3086 29p CA3093 74p CA3093 74p CA3094 Prog. Sw. Pwr. OP Amp. 36p CA3094 Prog. Sw. Pwr. OP Amp. 36p CA3094 Prog. Sw. Pwr. OP Amp. 36p CA3094 Prog. Sw. Pwr. OP Amp. 36p CA3123 72p CA3123 72p CA3124 Figure 100 Case 100 Cas	CA3054 CA3054	69p
CA3146E 90p CA3146E 90p CA3183 90p CA3183 90p CA3183 90p CA3183 97p CA31401 (LM3900) Quad OP Amp 36p CD25006 30mA/Seg Dcml Point Dvr 90p CD4000 Dual 3 input Nor + Inv. 12p CD4000 Dual 4 Input Nor CD4000 Dual 4 Comp. Pair + Inv. 12p CD4006 18 Stage Static Shift Reg. 36p CD4007 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binsry Full Adder 54p CD4010 Hex Buffers 30p CD4013 Dual 1 Phip Flop 36p CD4013 Dual 1 Phip Flop 36p CD4014 8 Bit Shift Register 36p CD4018 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4021 8 Bit Shift Register 54p CD4021 B Bit Shift Register 54p CD4022 Triple 3 Input Nor T4p CD4026 Dec. Count + 7 Seg. Out 72p CD4028 Triple 3 Input Nor T4p CD4028 Shift Nerset Bin/Dec 54p CD4033 Dec. Count. 7 Seg. Out 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4034 Ouad Nor R/S Latch 54p CD4043 H Tar. In out Shift 54p CD4043 Analogue Multi/Demulti 56p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4045 A Hill Par. In out Shift 54p CD4045 A Bit Par. In out Shift 54p CD4045 A LINE LCD driver/count 72p CD4046 A Du256 word X T Bi	CA3060 CA3065	36p
CA3146E 90p CA3146E 90p CA3183 90p CA3183 90p CA3183 90p CA3183 97p CA31401 (LM3900) Quad OP Amp 36p CD25006 30mA/Seg Dcml Point Dvr 90p CD4000 Dual 3 input Nor + Inv. 12p CD4000 Dual 4 Input Nor CD4000 Dual 4 Comp. Pair + Inv. 12p CD4006 18 Stage Static Shift Reg. 36p CD4007 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binsry Full Adder 54p CD4010 Hex Buffers 30p CD4013 Dual 1 Phip Flop 36p CD4013 Dual 1 Phip Flop 36p CD4014 8 Bit Shift Register 36p CD4018 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4021 8 Bit Shift Register 54p CD4021 B Bit Shift Register 54p CD4022 Triple 3 Input Nor T4p CD4026 Dec. Count + 7 Seg. Out 72p CD4028 Triple 3 Input Nor T4p CD4028 Shift Nerset Bin/Dec 54p CD4033 Dec. Count. 7 Seg. Out 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4034 Ouad Nor R/S Latch 54p CD4043 H Tar. In out Shift 54p CD4043 Analogue Multi/Demulti 56p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4045 A Hill Par. In out Shift 54p CD4045 A Bit Par. In out Shift 54p CD4045 A LINE LCD driver/count 72p CD4046 A Du256 word X T Bi	CA3080 OP AMP CA3083	59p 65p
CA3146E 90p CA3146E 90p CA3183 90p CA3183 90p CA3183 90p CA3183 97p CA31401 (LM3900) Quad OP Amp 36p CD25006 30mA/Seg Dcml Point Dvr 90p CD4000 Dual 3 input Nor + Inv. 12p CD4000 Dual 4 Input Nor CD4000 Dual 4 Comp. Pair + Inv. 12p CD4006 18 Stage Static Shift Reg. 36p CD4007 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binsry Full Adder 54p CD4010 Hex Buffers 30p CD4013 Dual 1 Phip Flop 36p CD4013 Dual 1 Phip Flop 36p CD4014 8 Bit Shift Register 36p CD4018 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4021 8 Bit Shift Register 54p CD4021 B Bit Shift Register 54p CD4022 Triple 3 Input Nor T4p CD4026 Dec. Count + 7 Seg. Out 72p CD4028 Triple 3 Input Nor T4p CD4028 Shift Nerset Bin/Dec 54p CD4033 Dec. Count. 7 Seg. Out 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4034 Ouad Nor R/S Latch 54p CD4043 H Tar. In out Shift 54p CD4043 Analogue Multi/Demulti 56p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4045 A Hill Par. In out Shift 54p CD4045 A Bit Par. In out Shift 54p CD4045 A LINE LCD driver/count 72p CD4046 A Du256 word X T Bi	CA3086 CA3089	
CA3146E 90p CA3146E 90p CA3183 90p CA3183 90p CA3183 90p CA3183 97p CA31401 (LM3900) Quad OP Amp 36p CD25006 30mA/Seg Dcml Point Dvr 90p CD4000 Dual 3 input Nor + Inv. 12p CD4000 Dual 4 Input Nor CD4000 Dual 4 Comp. Pair + Inv. 12p CD4006 18 Stage Static Shift Reg. 36p CD4007 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binsry Full Adder 54p CD4010 Hex Buffers 30p CD4013 Dual 1 Phip Flop 36p CD4013 Dual 1 Phip Flop 36p CD4014 8 Bit Shift Register 36p CD4018 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4021 8 Bit Shift Register 54p CD4021 B Bit Shift Register 54p CD4022 Triple 3 Input Nor T4p CD4026 Dec. Count + 7 Seg. Out 72p CD4028 Triple 3 Input Nor T4p CD4028 Shift Nerset Bin/Dec 54p CD4033 Dec. Count. 7 Seg. Out 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4034 Ouad Nor R/S Latch 54p CD4043 H Tar. In out Shift 54p CD4043 Analogue Multi/Demulti 56p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4045 A Hill Par. In out Shift 54p CD4045 A Bit Par. In out Shift 54p CD4045 A LINE LCD driver/count 72p CD4046 A Du256 word X T Bi	CA3090AQ CA3093	
CA3146E 90p CA3146E 90p CA3183 90p CA3183 90p CA3183 90p CA3183 97p CA31401 (LM3900) Quad OP Amp 36p CD25006 30mA/Seg Dcml Point Dvr 90p CD4000 Dual 3 input Nor + Inv. 12p CD4000 Dual 4 Input Nor CD4000 Dual 4 Comp. Pair + Inv. 12p CD4006 18 Stage Static Shift Reg. 36p CD4007 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binsry Full Adder 54p CD4010 Hex Buffers 30p CD4013 Dual 1 Phip Flop 36p CD4013 Dual 1 Phip Flop 36p CD4014 8 Bit Shift Register 36p CD4018 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4019 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4018 Preset Divide N Count 3p CD4021 8 Bit Shift Register 54p CD4021 B Bit Shift Register 54p CD4022 Triple 3 Input Nor T4p CD4026 Dec. Count + 7 Seg. Out 72p CD4028 Triple 3 Input Nor T4p CD4028 Shift Nerset Bin/Dec 54p CD4033 Dec. Count. 7 Seg. Out 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4034 Ouad Nor R/S Latch 54p CD4043 H Tar. In out Shift 54p CD4043 Analogue Multi/Demulti 56p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out Shift 54p CD4045 A Hill Par. In out Shift 54p CD4045 A Bit Par. In out Shift 54p CD4045 A LINE LCD driver/count 72p CD4046 A Du256 word X T Bi	CA3094 Prog. Sw. Pwr. OP Amp. CA3097	36p
CA3146E       90p         CA3183       80p         CA3189       72p         CA3189       72p         CA3191       1.000 (Luad OP Amp 36p)         CD2500E 30mA/Seg Dcml Point Dvr 90p       CD4000 Dual 3 input Nor + Inv.       12p         CD400D Dual 3 input Nor + Inv.       12p         CD400D Dual 4 Comp. Pair + Inv.       12p         CD400D Dual 4 Stage Static Shift Reg.       36p         CD400D Dual 4 Bit Bitinary Full Adder       54p         CD4010 Hex Buffers       30p         CD4011 Dual 4 Input Nand       13p         CD4012 Dual 4 Input Nand       13p         CD4013 Dual 1 Delip Flop       36p         CD4014 B Bit Shift Register       36p         CD4017 Decade Count/Divide       54p         CD4018 Preset Divide N Count       43p         CD4019 Quad 2 Input Multiplex       25p         CD4021 A Stage Binary Count       36p         CD4021 Preset Divide N Count       35p         CD4022 Triple 3 Input Nor       14p         CD4022 Divide by 8 Count/Divide       36p         CD4022 Triple 3 Input Nor       14p         CD4022 Divide Dy Count 7 Seg. Out       72p         CD4022 Synch. Preset Bin/Dec       54p         C	ČA3123 CA3132EM	
CD4006 18 Stage Static Shift Reg. CD4006 18 Stage Static Shift Reg. CD4007 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binary Full Adder CD4013 Dual 4 Finput Nand 13p CD4013 Dual 4 Finput Nand 13p CD4013 Dual 4 Finput Nand 13p CD4016 Quad Bilateral Latch 36p CD4017 Decade Count/Divide 54p CD4018 Preset Divide N Count 34p CD4018 Preset Divide N Count 34p CD4018 Preset Divide N Count 34p CD4018 Dist Shift Register 54p CD4021 8 Bit Shift Register 54p CD4022 Divide by 8 Count/Divide 54p CD4022 Triple 3 Input Nand 19p CD4025 Triple 3 Input Nand 19p CD4025 Triple 3 Input Nand 19p CD4026 Dec. Count + 7 Seg. Out 72p CD4028 Winch Ad028 BCD/Decimal 42p CD4033 Dec. Count. 7 Seg. Out 72p CD4033 Dec. Count. 7 Seg. Out 72p CD4034 Ouad Nor R/S Latch 54p CD4043 A nalogue Multi/Demulti 54p CD4045 4 Bit Par. In out Shift 54p CD4045	CA3146E CA3183	90p
CD4006 18 Stage Static Shift Reg. CD4006 18 Stage Static Shift Reg. CD4007 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binary Full Adder CD4013 Dual 4 Finput Nand 13p CD4013 Dual 4 Finput Nand 13p CD4013 Dual 4 Finput Nand 13p CD4016 Quad Bilateral Latch 36p CD4017 Decade Count/Divide 54p CD4018 Preset Divide N Count 34p CD4018 Preset Divide N Count 34p CD4018 Preset Divide N Count 34p CD4018 Dist Shift Register 54p CD4021 8 Bit Shift Register 54p CD4022 Divide by 8 Count/Divide 54p CD4022 Triple 3 Input Nand 19p CD4025 Triple 3 Input Nand 19p CD4025 Triple 3 Input Nand 19p CD4026 Dec. Count + 7 Seg. Out 72p CD4028 Winch Ad028 BCD/Decimal 42p CD4033 Dec. Count. 7 Seg. Out 72p CD4033 Dec. Count. 7 Seg. Out 72p CD4034 Ouad Nor R/S Latch 54p CD4043 A nalogue Multi/Demulti 54p CD4045 4 Bit Par. In out Shift 54p CD4045	CA3189 CA3401 (LM3900) Quad OP Amp	73p 36p
CD4006 18 Stage Static Shift Reg. CD4006 18 Stage Static Shift Reg. CD4007 Dual Comp. Pair + Inv. 12p CD4008 4 Bit Binary Full Adder CD4013 Dual 4 Finput Nand 13p CD4013 Dual 4 Finput Nand 13p CD4013 Dual 4 Finput Nand 13p CD4016 Quad Bilateral Latch 36p CD4017 Decade Count/Divide 54p CD4018 Preset Divide N Count 34p CD4018 Preset Divide N Count 34p CD4018 Preset Divide N Count 34p CD4018 Dist Shift Register 54p CD4021 8 Bit Shift Register 54p CD4022 Divide by 8 Count/Divide 54p CD4022 Triple 3 Input Nand 19p CD4025 Triple 3 Input Nand 19p CD4025 Triple 3 Input Nand 19p CD4026 Dec. Count + 7 Seg. Out 72p CD4028 Winch Ad028 BCD/Decimal 42p CD4033 Dec. Count. 7 Seg. Out 72p CD4033 Dec. Count. 7 Seg. Out 72p CD4034 Ouad Nor R/S Latch 54p CD4043 A nalogue Multi/Demulti 54p CD4045 4 Bit Par. In out Shift 54p CD4045	CD2500E 30mA/Seg Dcml Point D	Vr 90p
CD4007 Dual Comp. Pair + Inv.       12p         CD4008 48 Ht Binsry Full Adder       54p         CD4010 Hex Buffers       30p         CD4012 Dual 4 Input Nand       13p         CD4013 Dual 5 Hip Flop       36p         CD4014 8 Bit Shift Register       36p         CD4017 Decade Count/Divide       56p         CD4018 Preset Divide N Count       43p         CD4019 Quad 2 Input Multiplex       25p         CD4018 Preset Divide N Count       54p         CD4018 Dual 5 Hinput Nand       15p         CD4018 Divide by 8 Count/Divide       36p         CD4021 The Count       36p         CD4018 Divide by 8 Count/Divide       36p         CD4021 Thiple 3 Input Nand       19p         CD4022 Divide by 8 Count/Divide       36p         CD4023 Triple 3 Input Nand       19p         CD4024 Dec. Count + 7 Seg. Out       72p         CD4023 Quad Exclusive or       36p         CD4033 Dec. Count. 7 Seg. Output       72p         CD4034 Quad Nor R/S Latch       54p <td>ob tool buart input not</td> <td>1ZP</td>	ob tool buart input not	1ZP
CD4010 Hex Buffers       30p         CD4013 Dual J Flip Flop       36p         CD4013 Dual J Flip Flop       36p         CD4013 Dual J Flip Flop       36p         CD4014 8 Bit Shift Register       36p         CD4017 Decade Count/Divide       54p         CD4019 Quad 2 Input Multiplex       25p         CD4019 Quad 2 Input Multiplex       25p         CD4019 Quad 2 Input Multiplex       25p         CD4018 Preset Divide N Count       34p         CD4021 8 Bit Shift Register       54p         CD4022 Triple 3 Input Nard       19p         CD4025 Triple 3 Input Nor       14p         CD4026 Dec. Count + 7 Seg. Out       72p         CD4028 Synch. Preset Bin/Dec       54p         CD4032 Ouad Exclusive or       36p         CD4033 Dec. Count. 7 Seg. Output       72p         CD4034 1 A St. Rip. carry Bin Count       54p         CD4035 Triple 4 Bin Par. in out Shift       54p         CD4043 Quad Nor R/S Latch       54p         CD4043 Quad Nor R/S Latch       54p <td>CD4007 Dual Comp. Pair + Inv.</td> <td>12p</td>	CD4007 Dual Comp. Pair + Inv.	12p
CD4013 Dual D Flip Flop       36p.         CD4014 B Bit Shift Register       36p.         CD4016 Quad Bilateral Latch       36p.         CD4017 Decade Count/Divide       54p.         CD4018 Preset Divide N Count       43p.         CD4019 Quad 2 Input Multiplex       25p.         CD4013 Preset Divide N Count/Divide       36p.         CD4014 Stage Binary Count       54p.         CD4021 S Bit Shift Register       54p.         CD4022 Divide by 8 Count/Divide       36p.         CD4022 Triple 3 Input Nor       19p.         CD4025 Triple 3 Input Nor       14p.         CD4027 dual JK master/slave       30p.         CD4028 Synch. Preset Bin/Dec       54p.         CD4028 Synch. Preset Bin/Dec       54p.         CD4028 Synch. Preset Bin/Dec       54p.         CD4030 Quad Exclusive or       56p.         CD4031 Count. 7 Seg. Output       72p.         CD4032 Dec. Count. 7 Seg. Output       72p.         CD4033 Dec. Count. 7 Seg. Output       72p.         CD4034 St Bit Par. in out Shift       54p.         CD4035 Count And Nor R/S Latch       54p.         CD4043 Quad Nor R/S Latch       54p.         CD4044 Quad Nor R/S Latch       54p.         CD4045 4 Bit Par. In out shift	CD4010 Hex Buffers	30p
CD4016 Quad Bilateral Latch       36p         CD4017 Decade Count/Divide       54p         CD4018 Preset Divide N Count       43p         CD4019 Quad 2 Input Multiplex       25p         CD4019 Quad 2 Input Multiplex       25p         CD4021 014 Stage Binary Count       54p         CD4021 014 Stage Binary Count       36p         CD4022 014 Stage Binary Count       36p         CD4022 014 Stage Binary Count       36p         CD4022 Triple 3 Input Nord       19p         CD4022 Triple 3 Input Nord       19p         CD4022 Triple 3 Input Nord       19p         CD4022 Dec. Count + 7 Seg. Out       72p         CD4028 MCI 4028 BCD/Decimal       42p         CD4028 Synch. Preset Bin/Dec       54p         CD4039 Quad Exclusive or       35p         CD4032 Dec. Count. 7 Seg. Output       72p         CD4038 Dec. Count. 7 Seg. Output       72p         CD4039 Ouad True/Comp. Buffer       54p         CD40401 14 St. Rip. carry Bin Count       54p         CD4043 Ouad Nor R/S Latch       54p         CD4044 Quad Nand R/S Latch </td <td>CD4013 Dual'D Flip Flop</td> <td>36p</td>	CD4013 Dual'D Flip Flop	36p
CD4018 Preset Divide N Count 43p CD4019 Quad 2 Input Multiplex 25p CD4020 14 Stage Binary Count 54p CD4021 18 Bit Shift Register 54p CD4022 Divide by 8 Count/Divide 36p CD4022 Triple 3 Input Nand 19p CD4022 Triple 3 Input Nand 19p CD4022 Triple 3 Input Nand 19p CD4022 Triple 3 Input Nor 14p CD4026 Dec. Count + 7 Seq. Out 72p CD4026 Dec. Count + 7 Seq. Out 72p CD4026 MC14028 BCD/Decimal 42p CD4028 MC14028 BCD/Decimal 42p CD4029 Synch. Preset Bin/Dec 54p CD4030 Quad Exclusive or 36p CD4033 Dec. Count, 7 Seq. Out 72p CD4033 Dec. Count, 7 Seq. Output 72p CD4038 CD4038 54p CD4038 54 Bit Par, in out Shift 54p CD4040 14 St. Rip. carry Bin Count 56p CD4044 Quad Nand R/S Latch 54p CD4043 Quad Nor R/S Latch 54p CD4045 4 Bit Par. In out shift 54p CD4046 Micro Power PH. Lock Loop 36p CD4046 Micro Power PH. Lock Loop 36p CD4046 Hex Buffers 36p CD4046 Hex Buffers 36p CD4046 Hex Buffers 36p CD4046 Hex Buffers 36p CD4055 A nalogue Multi/Demulti 36p CD4056 Mex Buffers 72p CD4056 72p CD4056 72p CD4056 Vord X 1 Bit St. RAM 55.30	CD4016 Quad Bilateral Latch	36p
CD402014 Stage Binary Count       54p         CD4021 Bit Shift Register       54p         CD4022 Divide by 8 Count/Divide       36p         CD4023 Triple 3 Input Nand       19p         CD4025 Divide by 8 Count/Divide       36p         CD4026 Dec. Count + 7 Seg. Out       72p         CD4026 Dec. Count + 7 Seg. Out       72p         CD4027 Mull JK master/slave       30p         CD4028 MC14028 BCD/Decimal       42p         CD4039 Synch. Preset Bin/Dec       54p         CD4039 Dec. Count. 7 Seg. Out       72p         CD4039 Dec. Count. 7 Seg. Output       72p         CD4039 Dec. Count. 7 Seg. Output       72p         CD4038 Dec. Count. 7 Seg. Output       72p         CD4038 Tel. Rip. carry Bin Count       56p         CD4040 14 St. Rip. carry Bin Count       56p         CD4041 Quad True/Comp. Buffer       54p         CD4043 Quad Nord R/S Latch       54p         CD4044 Quad Nend R/S Latch       54p         CD4045 & Bit Par. In out shift       54p         CD4044 Quad Nend R/S Latch       54p         CD4045 Bit Par. In out shift       54p         CD4045 Haverter Buffers       36p         CD4046 Micro Power PH. Lock Loop       36p         CD4045 Havelffers	CD4018 Preset Divide N Count	54p. 43p
CD4021 8 Bit Shift Register       540         CD4022 Divide by 8 Count/Divide       36p         CD4023 Triple 3 Input Nand       19p         CD4023 Triple 3 Input Nond       19p         CD4025 Dec. Count + 7 Seg. Out       72p         CD4027 dual JK master/slave       30p         CD4028 Opc. Count + 7 Seg. Output       72p         CD4030 Quad Exclusive or       36p         CD4033 Dec. Count. 7 Seg. Output       72p         CD4033 Dec. Count. 7 Seg. Output       72p         CD4035 4 Bit Par, in out Shift       54p         CD4043 Dued True/Comp. Buffer       54p         CD4043 Ouad Nor R/S Latch       54p         CD4044 Quad Nand R/S Latch       54p         CD4045 4 Bit Par. in out shift       54p         CD4044 Quad Nand R/S Latch       54p         CD4045 4 Bit Par. In out shift       54p         CD4045 4 Bit Par. In out shift       54p         CD4045 4 Bit Par. In out shift       54p         CD4045 Hanlogue Multi/Demulti       54p         CD4045 A halogue Multi/Demulti       54p         CD4045 A halogue Multi/Demulti	CD402014 Stage Binary Count	54p
CD4026 Dec. Count + 7 Seg. Out CD4027 Quel JK mater/slave 30p CD4028/MC14028 BCD/Decimal 42p CD4028 Synch. Preset Bin/Dec 54p CD4030 Quad Exclusive or 36p CD4031 CD4032 72p CD4032 F1.20 CD4032 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4033 A Bit Par, in out Shift 54p CD4034 Bit Par, in out Shift 54p CD4034 Quad Nor R/S Latch 54p CD4043 Quad Nor R/S Latch 54p CD4043 A Quad Nor R/S Latch 54p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out shift 54p CD4045 A halogue Multi/Demulti 36p CD4045 A halogue Multi/Demulti 36p CD4055 72p, CD4056 Cuad Bilateral Switch 72p CD4066 Quad Bilateral Switch 72p	CD4021 8 Bit Shift Register	54p
CD4026 Dec. Count + 7 Seg. Out CD4027 Quel JK mater/slave 30p CD4028/MC14028 BCD/Decimal 42p CD4028 Synch. Preset Bin/Dec 54p CD4030 Quad Exclusive or 36p CD4031 CD4032 72p CD4032 F1.20 CD4032 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4033 A Bit Par, in out Shift 54p CD4034 Bit Par, in out Shift 54p CD4034 Quad Nor R/S Latch 54p CD4043 Quad Nor R/S Latch 54p CD4043 A Quad Nor R/S Latch 54p CD4044 Quad Nor R/S Latch 54p CD4045 A Bit Par. In out shift 54p CD4045 A halogue Multi/Demulti 36p CD4045 A halogue Multi/Demulti 36p CD4055 72p, CD4056 Cuad Bilateral Switch 72p CD4066 Quad Bilateral Switch 72p	CD4023 Triple 3 Input Nand CD4025 Triple 3 Input Nor	19p
CD4028/MC14028 BCD/Decimal 42p CD4029 Synch, Preset Bin/Dec 54p CD4030 Quad Exclusive or 36p CD4031 £120 CD4032 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4033 Dec. Count. 7 Seg. Output 72p CD4035 & Bit Par, in out Shift 54p CD4037 772p CD4043 & Bit Par, in out Shift 54p CD4038 CD4040 14 St. Rip. carry Bin Count 56p CD4040 14 St. Rip. carry Bin Count 56p CD4040 14 St. Rip. carry Bin Count 56p CD4040 14 St. Rip. carry Bin Count 56p CD4044 Quad Nor R/S Latch 54p CD4044 Quad Nor R/S Latch 54p CD4044 Glued Newr PH. Lock Loop 36p 2D4046 Micro Power PH. Lock Loop 36p 2D4046 Micro Power PH. Lock Loop 36p 2D4047 monostable 72p CD4049 Hex Inverter Buffers 36p CD4051 Analogue Multi/Demulti 54p CD4053 Analogue Multi/Demulti 54p CD4056 72p CD4056 72p CD4066 Word X 1 Bit St. RAM 55.30	CD4026 Dec. Count + / Seg. Out	72p
CD4030 Quad Exclusive or         36p           CD4032         72p           CD4033 Dec. Count. 7 Seg. Output         72p           CD4035 A Bit Par, in out Shift         54p           CD4038         72p           CD4039         72p           CD4037         72p           CD4038         72p           CD4037         72p           CD4038         72p           CD4037         72p           CD4038         72p           CD404014 St. Rip. carry Bin Count         56p           CD404014 Ouad True/Comp. Buffer         54p           CD4040 Quad Nor R/S Latch         54p           CD4043 Quad Nor R/S Latch         54p           CD4044 Quad Nand R/S Latch         54p           CD4045 A Bit Par. In out shift         54p           CD4044 Quad Nand R/S Latch         54p           CD4045 A Bit Par.         54p           CD4044 Quad Nand R/S Latch         54p           CD4044 Par.         10 out shift         54p           CD4049 Hex Inverter Buffers         36p           CD4051 Analogue Multi/Demulti         54p           CD4053 Analogue Multi/Demulti         54p           CD4054 A LINE LCD driver/count         72p	CD4027/d0a15K master/stave	42p
CD4037     72p       CD4037     54p       CD404014     54p       CD404014     54p       CD404014     14 St. Rip. carry Bin Count       Sep     54p       CD404014     0uad True/Comp. Buffer       CD4043     0uad Nor R/S Latch       54p     54p       CD4043     0uad Nor R/S Latch       54p     54p       CD4045     Bit Par. In out shift       54p     54p       CD4045     Bit Par. In out shift       54p     54p       CD4046     Micro Power PH. Lock Loop       36p     72p       CD4048     Starter       CD4049     Hex Inverter Buffers       36p     72p       CD4051 Analogue Multi/Demulti       54p     72p       CD4053     72p       CD4056     72p       CD4056     72p       CD4056     72p       CD4056     72p       CD4056     72p       CD4061AD 256 word X 1 Bit St.       RAM     55.30       CD4066 Quad Bilateral Switch     72p	CD4030 Quad Exclusive or	36p
CD4037     72p       CD4037     54p       CD404014     54p       CD404014     54p       CD404014     14 St. Rip. carry Bin Count       Sep     54p       CD404014     0uad True/Comp. Buffer       CD4043     0uad Nor R/S Latch       54p     54p       CD4043     0uad Nor R/S Latch       54p     54p       CD4045     Bit Par. In out shift       54p     54p       CD4045     Bit Par. In out shift       54p     54p       CD4046     Micro Power PH. Lock Loop       36p     72p       CD4048     Starter       CD4049     Hex Inverter Buffers       36p     72p       CD4051 Analogue Multi/Demulti       54p     72p       CD4053     72p       CD4056     72p       CD4056     72p       CD4056     72p       CD4056     72p       CD4056     72p       CD4061AD 256 word X 1 Bit St.       RAM     55.30       CD4066 Quad Bilateral Switch     72p	CD4031 CD4032	£1.20 72p
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CD4040 14 St. Rip. carry Bin Count 56p CD4043 Quad Nor R/S Latch 54p CD4043 Quad Nor R/S Latch 54p CD4044 Quad Nand R/S Latch 54p CD4044 Quad Nand R/S Latch 54p CD4044 Quad Nand R/S Latch 54p CD4045 Bit Par. In out shift 54p CD4046 Micro Power PH. Lock Loop 36p CD4047 monostable 72p CD4048 Stransport 72p CD4048 Hax Inverter Buffers 36p CD4050 Hex Buffers 36p CD4051 Analogue Multi/Demulti 54p CD4053 Analogue Multi/Demulti 54p CD4055 72p CD4056 72p CD4056 72p CD4056 72p CD4056 X 1 Bit St. RAM 5530 CD4063 72p CD4066 Quad Bilateral Switch 27p	CD4037	72p
CD4044 Quad Nend R/S Latch 54p CD4045 4 Bit Par. In out shift 54p D4046 Micro Power PH. Lock Loop 36p D4046 Micro Power PH. Lock Loop 36p D4047 monostable 72p CD4049 Hex Inverter Buffers 36p CD4051 Analogue Multi/Demulti 36p CD4051 Analogue Multi/Demulti 54p CD4053 Analogue Multi/Demulti 54p CD4055 72p CD4056 72p CD4056 72p CD4056 72p CD4056 72p CD4063 72p CD4063 72p CD4066 Quad Bilateral Switch 27p	CD4040 14 St Bip carry Rin Count	56p
CD4045 4 Bit Par. In out shift     54p       D50404 Micro Power PH. Lock Loop     36p       D4047 monostable     72p       CD4048     36p       CD4049 Hex Inverter Buffers     36p       CD4050 Hex Buffers     25p       CD4053 Analogue Multi/Demulti     54p       CD4055     72p       CD4056     72p       CD4055     72p       CD4056     72p       CD40661AD 256 word X 1 Bit St.     72p       CD40663     72p       CD4066 Quad Bilsteral Switch     27p		54p
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MC837P	4p	2	10p	64	15p Volt	220	22p
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SN75150 SN75235N	18p	-			-		_
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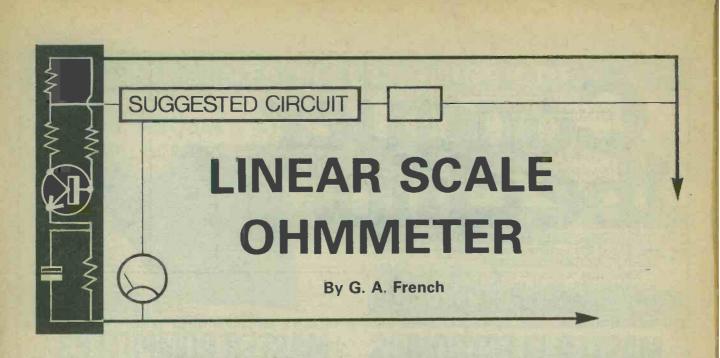
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give results which have the

accuracy of a true laboratory

measuring instrument, but the

accuracy is very much better than is that given with the

testmeter ohms ranges them-

selves. Also, all the resistance

readings are directly propor-

tional to meter voltage indica-

tions, and are in consequence

free from scale cramping as

well as being very easy to

The basic mode of operation

of the adaptor is shown in

CURRENT

The more inexpensive analogue multi-testmeters have resistance ranges and scales which are notoriously inaccurate and difficult to read. The scales are cramped at the high resistance end and accuracy normally worsens with falling voltage in the internal meter battery. It is, in particular, not an easy matter to obtain meaningful indications of resistance when these approach and exceed  $1M\Omega$ .

The circuit to be described this month is for a low cost adaptor which can be used with an analogue testmeter when the latter is switched to a suitable voltage range, and it provides linear indications of resistance in five ranges, the lowest range being 0-1k $\Omega$  and the highest range being 0-10M $\Omega$ . The adaptor does not

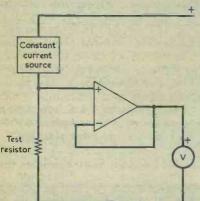


Fig.1. The basic circuit for obtaining linear resistance readings by means of an analogue voltmeter

Fig.1. Here, a constant current source causes a constant current to be passed through the test resistor whose value is to be ascertained. Since the curng rent is constant the voltage across the resistor is proportional to its resistance value. The voltage at the upper end of the test resistor is applied to an

evaluate.

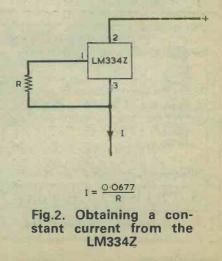
SOURCE

CONSTANT

The voltage at the upper end of the test resistor is applied to an operational amplifier having an extremly high input resistance. The op-amp output is returned to the inverting input and the op-amp thus functions as a voltage follower. The voltage across the resistor, and hence an indication of its value, is then read from the analogue voltmeter connected to the op-amp output.

This method of measuring resistance is not, of course, new but the fairly recent introduction of modern devices to the home constructor component market, including in particular an integrated constant current source, makes it possible to make up delightfully simple circuits which are capable of measuring resistances up to  $10M\Omega$  by the constant current technique.

The integrated constant current source is the LM334Z, and this is available from Maplin Electronic Supplies. It is encapsulated in a 3-pin T092 package and, for operation at room temperatures, requires only one external resistor to establish the value of the constant current. The device is connected as shown in Fig.2 and the constant current, in amps, is equal to 0.0677 divided by the value of the external resistor. The range of constant currents available extends from  $1\mu A$  to 10mA. The first current is given when the resistor has a value of  $68k\Omega$ and the second current when the resistor has a value of  $6.8\Omega$ .



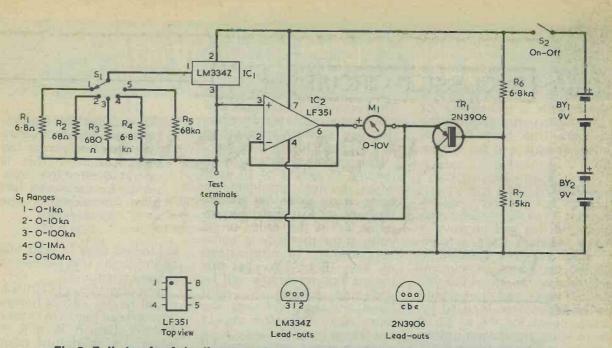


Fig.3. Full circuit of the linear scale ohmmeter, which offers resistance readings up to  $10M\Omega$ . Meter M1 is a testmeter switched to read 0 – 10 volts. In the insets showing pinning for IC1 and TR1, the lead-outs point towards the reader.

### **ADAPTOR CIRCUIT**

The full circuit of the adaptor is given in Fig.3, and it will be seen that the range switch S1 switches in five different values of external current control resistor. On Range 1 the constant current is 10mA, on Range 2 it is 1mA and on Range 3 the current is 100µA. Range 4 gives 10µA and Range 5 allows 1µA to flow. A current of 10mA through a  $1k\Omega$  resistor causes 10 volts to be developed across that resistor, and a current of 1mA through a  $10k\Omega$  resistor similarly produces a voltage of 10 volts. On each of the five ranges the maximum resistance shown for the range causes a voltage of 10 volts to be given. Lower resistances within the range produce proportionately lower voltages.

The resistor being measured is connected to the test terminals and the voltage on the upper terminal is applied to the non-inverting input of the op-amp, IC2, which is connected as a voltage follower. The i.c. chosen for the circuit is an LF351 with an input resistance of 1 tera-ohm, or 1 million megohms. This has J-fet inputs and does not need the

**NOVEMBER 1980** 

handling precautions that are required with devices having MOS inputs, although it is wise to solder to the input pins with an iron having a reliably earthed bit. The only main precaution needed is to ensure that neither of the LF351 inputs is taken negative of the negative supply pin, as damage to the device can then result.

The LF351 ceases to function as a voltage follower if the input at the non-inverting input passes below about 2 volts positive of the negative supply rail and so it is necessary to raise the voltage follower and meter circuit above the negative rail by a suitable voltage. This is achieved with the aid of the potential divider consisting of R6 and R7, the junction of these two resistors connecting to the base of emitter follower TR1. The voltage at the emitter of TR1 is around 4 volts positive of the negative rail, and falls as battery voltage drops. A precise voltage is not required as the lower test terminal and the negative side of the analogue meter are both returned to the emitter, and the constant current from IC1 is not affected by the voltage at this point. The emitter follower copes comfortably with the varying currents required on

the different resistances ranges. The standing current in the potential divider is about 2.2mA. A 3.9 volt zener diode could have been employed instead of the emitter follower, but it would have required a standing zener current of at least several milliamps to bring it on to the flat part of its characteristic.

As has been already explained, the voltage applied to the non-inverting input of the LF351 is 10 volts maximum on each range. The input voltage varies therefore, from zero to 10 volts. With the 4 volt delay given at the emitter of TR1, the LF351 output voltage swing, relative to the negative rail, is from 4 to 14 volts. Such a swing is comfortably within the capability of an LF351 having an 18 volt supply. In practice, the circuit provides accurate resistance readings even when the supply voltage drops to 14 volts, as the reference voltage at TR1 emitter also falls.

The total current consumed by the circuit is about 4mA on Ranges 5, 4 and 3, this rising to around 5mA on Range 2 and 15mA on Range 1. The two 9 volt batteries may be any type, ranging from PP3 to PP9, as preferred.

The meter, M1, is a testmeter

switched to read 0-10 volts. The circuit will function with quite insensitive meters, and it is only required that the meter resistance should be  $1,000\Omega$ per volt or more. A 0-10 volt meter with a sensitivity of  $1,000\Omega$  per volt will consume an extra 1mA from the batteries when it is at full deflection.

### ACCURACY

Quite fair accuracy will be given if R1 to R5 are 5% resistors, and this will improve if these resistors have a tolerance of 2%. The LM334Z is quoted as having a current accuracy within 3% of the calculated value, and there would be little advantage in using 1% components for R1 to R5 if these were significantly more expensive than 2% types. If difficulty is experienced in obtaining the  $6.8\Omega$  resistor required for R1 in close tolerance, it may be made up from an  $11\Omega$  and an  $18\Omega$  resistor in parallel. These have a combined calculated value of 6.83 Ω. R6 and R7 can be standard 5% 1/4 watt components.

The circuit may be made up in any convenient case with S1 and S2 on the front panel, in company with two terminals for the test resistor and two terminals to which the testmeter connects. Resistance readings are then proportional to testmeter voltage reading. If, for instance, a reading of 3.4 volts is given on Range 4, the test resistor has a value of  $340k\Omega$ . The prototype circuit gave perfectly satisfactory results when the test resistors approached and reached  $10m\Omega$  in value.

The analogue meter gives a reading in excess of 10 volts when the test terminals are open-circuit and the voltage then applied to it, with new batteries, is of the order of 14 volts. If the meter f.s.d. value happens to be 10 volts (instead of, say, 15 or 20 volts) this means that its needle will be deflected against the right hand end-stop, although the extra current flowing should not cause any damage to a normal meter movement. If this effect is disliked, a pressto-break push button can be added to the circuit, it being connected across the test terminals as shown in Fig.4. The button is pressed, to take a reading, after the test resistor has been connected to the terminals. Incidentally, the fact that the voltmeter gives a reading in excess of 10 volts with the test terminals open-circuit provides an automatic check on battery voltage. If the open-circuit voltage readings

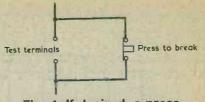


Fig. 4. If desired, a pressto-break push button may be wired across the test terminals. This is operated after the resistor to be measured has been connected to the terminals

fall to less than 11 volts this is an indication that the batteries require changing.

The dissipation in IC1 is low on all ranges except Range 1, where it can rise to in excess of 100mW (which is still well within its maximum rating). There may be a slight drift in readings if the ohmmeter is used for long periods on Range 1 due to the consequent rise in temperature inside the device, but there should be no problems in the short term. The adaptor should not be left switched on with Range 1 selected for excessive times when the push-button of Fig.4 is incorporated, as apart from any other effect there would be an excessive drain on the batteries.



Now available from Magenta Electronics is their new 1980-81 catalogue. With 52 large pages and a separate 6 page Price List, this gives details of a very wide range of electronic components, including printed circuit materials, cases, semiconductors, coils, transformers, resistors, capacitors, connectors and testmeters. Detailed illustrations of components are provided. Five pages of the catalogue are devoted to circuit ideas which can be built by the experimenter.

An unusual feature is the inclusion of a large quantity of kits for constructional projects published in this magazine and others. These kits include our current "INStructor" series and, in all, there are well over 150 projects listed. Each kit provides, nuts, bolts, and all hardware needed, as well as the purely electronic parts.

The Magenta Electronics 1980-81 catalogue can be obtained from Magenta Electronics Ltd., 98 Calais Road, Burton-on-Trent, Staffs, DE13 OUL by sending payment of six 10p stamps.

**RECENT PUBLICATIONS** 

THE PERSONAL COMPUTER BOOK. By Robin Bradbeer. 226 pages, 210 X 140mm. (8) X 51/2in.) Published by Input Two-Nine. Price £5.25.

Robin Bradbeer is a free-lance writer on personal computing, as well as being Senior Lecturer in the Department of Electronic and Communications Engineering at the Polytechnic of North London. In this book (which is distributed by MCB Publications Limited, 198/200 Keighley Road, Bradford, BD9 4JQ, Tel. 0274 499821) he sets out to explain what the personal computer can do and how the beginner in the computer world can obtain and take advantage of a suitable machine for his own particular requirements.

The text is concise, pleasant in style and easy to follow. The first five of the seven chapters which make up the body of the book introduce the computer, indicate how the reader can start with computers, and discuss hardware and software. The sixth chapter gives specifications and details for some 50 commercially produced computers and systems which are currently available, as well as for 15 printers and 10 visual display units. Chapter 7 describes the uses to which the computer can be put. There are eight appendices, the first four of which cover binary arithmetic, interface standards, addresses of manufacturers and distributors, and computer clubs and specialist groups in the U.K. The fifth appendix gives an extensive list and evaluation of computer magazines in the U.K., U.S.A. and the Continent, and the sixth a selected bibliography. A glossary of terms appears in the seventh appendix, and advice is given on building kit systems in the final appendix.

This is a comprehensive and carefully prepared publication offering much useful information to the newcomer to computers.

**110 IC TIMER PROJECTS FOR THE HOME CONSTRUCTOR**. By Jules H. Gilder. 125 pages, 230 x 145mm. (9 x 5½in.) Published by Newnes Technical Books. Price £3.10.

The 555 must surely be one of the most widely employed i.c.'s in use today. Basically intended as a monostable or astable multivibrator, it has appeared in very many widely varying applications since its introduction. The book under review gives an excellent idea of the versatility possessed by this unique integrated circuit.

The book commences with a description of the internal circuitry and pin functions of the 555 and then deals with monostable and astable circuits. In both instances, circuits are presented which offer enhanced performance. The section on astable circuits includes, for example, an astable with continuously variable duty cycle and an astable with crystal frequency control.

The following sections cover logic circuits, timer-based instruments, automobile applications, alarm and control circuits, and power supplies and converters. As the title of the book promises, there are 110 different circuits. These are all good and practical and will not only be of considerable interest to the experimenter as well as the constructor but may also, in themselves, spark off further ideas and uses for the 555.

A GUIDE TO AMATEUR RADIO, 18th Edition. By Pat Hawker, G3VA. 144 pages, 245 x 180mm. (9<sup>1</sup>/<sub>2</sub> x 7in.) Published by Radio Society of Great Britain. Price £2.40.

A Guide To Amateur Radio first appeared in 1933, and it has now arrived at its 18th edition. Its continuing popularity gives clear evidence that it is a welcome occupant of the bookshelf of anyone having an interest in amateur radio, whether this be for transmitting or simply for listening.

The prime aim of the book is to advise the newcomer to amateur radio, and to assist in the obtaining of a transmitting licence. But in so doing it also provides a great deal of technical information which will help any beginner in electronics in general and in short wave radio in particular. Among the subjects covered are communications receivers, transmitters, aerials, workshop practice and electronic fundamentals. The book also gives specific information on amateur matters, including amateur station operation, the function of the RSGB, international amateur organisations and working to pass the transmitting licence examination.

The book is well set up and laid out, with clear illustrations and diagrams. In cases of difficulty it can be obtained direct from Radio Society of Great Britain, 35 Doughty Street, London WC1N 2AE, for £2.99 post paid.

NOVEMBER 1980

### AND NEWS

### HANDHELD DMM REDUCED IN PRICE

Fluke's latest low cost handheld DMM, the  $3\frac{1}{2}$  digit 8022A Troubleshooter model, has just been reduced in price from £89 to £75, making it even more attractive in design, service and field test work. The 8022A is a simple yet versatile general purpose DMM providing 6 functions (AC + DC Volts, AC + DC

Amps and high and low Ohms) with 24 ranges, including 3 diode test ranges.

A clear 3<sup>1</sup>/<sub>2</sub> digit LCD display makes it easy to read with the single row of buttons down one side allowing single handed operation when required. Extensive overload protection up to 6 kV for transients and 1000 V or 20 Amps make it practically indestructible in use. The LCD display on the 8022A also provides a battery low, over-range and negative polarity indication. Basic DC accuracy is 0.25%.

The safety leads are especially designed to prevent accidental shock and a full range of accessories are available including probes for high voltage, high current, RF, temperature as well as a battery eliminator for bench work.

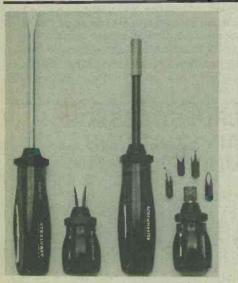


### "BROADCASTING TECHNOLOGY FOR THE 1980's"

The IBA has published a 12-page, fully-illustrated, description of current engineering progress and development work being carried out at Crawley Court, Winchester under the title "Broadcasting Technology for the 1980s". This describes the engineering of the "Fourth Channel" under the IBA Act 1979; recent developments for Independent Local Radio; the present state of digital technology in broadcasting, including ORACLE teletex and digital techniques for the application of space satellites

space satellites. In a foreword by Tom Robson, IBA's Director of Engineering, he writes: "The 1980s offer the challenge of the all-digital systems; the new methods of video distribution including optical fibres and satellites; the exploitation of bandwidth at SHF and beyond; the new mobility of lightweight equipment – especially where microwatts of power consumption can match the micro size of the devices. It is a challenge that will call for new skills and new training for a very different world." Contents: UHF transmitters and the Fourth Channel; RTS award for SABRE; ILR – the new force in sound radio; the ubiquitous digit – digital video processing; IBA digital 'firsts'; the expanding world of teletext; space satellites – questions for the future; IBA – an engineering service. Copies are available, free of charge, from IBA Engineering Information Service, Crawley Court, Winchester, Hants. SO21 20A

20A



The Screwmaster ratchet range, I to  $r - the 8\frac{1}{2}$ " screw-driver, the 3" Chubby, the new bit holder Screwmaster and the new Chubby with their set of bits.

### 'FOUR SCREWDRIVERS IN ONE'

'Four screwdrivers in one' is the latest addition to the Steadfast Screwmaster family of screwdrivers from J. Stead & Co Ltd, of Netherlane, Ecclesfield, Sheffield.

The new screwdriver adaptors incorporate the patented roller ratchet Screwmaster mechanism which was successfully launched earlier this year. There is a sleeve at the end of the driver shaft to hold all types of standard  $\frac{1}{4}$ " A/F screwdriver bits.

The bit holder is available in two sizes. The Chubby model is only 3" in length - a size which has already proved its popularity as a ratchet Screwmaster. The larger model is  $8^{1}_{4}$  in length. Both sizes come with either a magnet or retaining clip at the base of the sleeve to hold the bit in place.

The magnet will also attract a screw – a particular feature if working in tight or awkward conditions. However, in situations where a magnetised blade is not suitable the retaining clip is the alternative.

The adaptor and bits are available separately or in kit form comprising an adaptor, together with No 1 Pozidriv, No 2 Pozidriv, 3/16" and  $\frac{1}{4}$ " flat bits.

Retail prices range from £3.35 to £4.90, plus VAT, for the bit holders and £5.95 to £7.50, plus VAT, for the kit form.

### . . COMMENT

As we grow increasingly security conscious, our pockets progressively fill with keys of all shapes and sizes. Apart from the wear and tear these may cause, the danger of loss is always present. An obvious solution is an electronic combination switch, which can be ideal for controlling electrically operated devices such as alarm systems and solenoid releases, etc. To meet this need we are featuring in this issue an article – 'CMOS Combination Switch'.

Since a combination switch involves expenditure in addition to the expense of the device it controls, it is essential to design its circuit such that costs are kept to a minimum. This requirement is achieved in the foregoing design to such an extent that it costs little more than an electrical locking switch!

### SATELLITE DATA BUOY

McMichael has successfully installed a satellite data collection platform (DCP) on a buoy which is moored off the coast of the Isle of Wight.

Data is collected, processed and stored from the on-board sensors and at regular time intervals is transmitted back to the home base in Slough via satellite, satellite receiving station (in W. Germany) and then telex.

Due to the processing capability of the DCP, the transmission time is much reduced and thereby saves battery power during transmission.

A wide beam aerial on the buoy mast ensures that data is still transmitted even during rough weather, which was simulated in this trial by using a buoy with a shortened keele which was kindly supplied by the British Met. Office.

Such data buoys can be moored anywhere in the world whilst the user receives his information at the home base and will gradually replace weather ships which are increasingly expensive to operate, as well as being used on large lakes and other inland water.

There has been interest in this project from many parts of Europe and the Dutch Water Authority have asked for an extension to the trial period whilst they undertake some of their own measurements.

### CASIO ENTERING THE MUSIC BUSINESS

CASIO is a name that most people readily associate with quality calculators and digital watches. Some also recognise Casio as a manufacturer of cash registers and computers.

Casio are breaking new ground by entering the music business with a product called Casiotone. It is a keyboard instrument that can reproduce the sounds of piano, organ, violin, flute, and a couple of dozen other instruments that are normally struck, plucked, bowed or blown. Moreover it is polyphonic – able to play chords of up to eight notes simultaneously.

Technology employed is a development from Casio's earlier electronics expertise. Casiotone's, functions are performed by two-chip LSI circuitry. It makes sounds that are pleasing to professional musicians, yet it will sell at a price appealing to amateurs.

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### "PATHÉTIQUE" RECORDED USING 3M DIGITAL EQUIPMENT



The first classical recording in England using the 3M Mincom digital multi-track mastering system took place during June at the Kingsway Hall in London.

Carlos Païta conducted the National Philharmonic Orchestra under the leadership of Mr. Sidney Sax for a recording of Tchaikovsky's Symphony No. 6, "The Pathétique". The 3M Mincom 4-track digital recorder was on hire from the Townhouse Recording Studios in Shepherd's Bush. The digital recording was backed up by both 8-and 2-track analogue recorders with Dolby noise reduction equipment on hire from Decca.

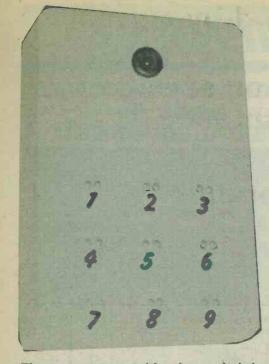
The programme was mixed and edited at the Roundhouse Recording Studios on 20th and 21st June using the studio's 3M Mincom 32-track and 4-track digital recorders and electronic editor, as well as new cross fade units also manufactured by 3M. Cutting will shortly take place at The Townhouse, again using the 3M digital preview unit. The Townhouse can probably claim the best cutting facilities in Europe at present since its acquisition of the 3M digital equipment and a new Neuman lathe. The digital recording of Tchaikovsky's "Pathéti-

The digital recording of Tchaikovsky's "Pathétique", with Carlos Païta conducting the National Philharmonic Orchestra, is due for release in October.

### MICROPROCESSOR APPLICATIONS UNDERWATER

The introduction of microprocessors to the underwater scene represents yet another step forward on the road to extending our knowledge of a range of environmental conditions, and provides yet another precise tool for the better information of designers, engineers and offshore operators. If programmed to recognise and record particular non-typical events events and conditions, microprocessors can fill in vital gaps in information which may be missed by instruments performing routine sampling.

The subject is to be dealt with in a one day seminar to be held in November under the auspices of the Society for Underwater Technology of 1 Birdcage Walk, London SW1H 9JJ.



The prototype combination switch is housed in a neat plastic case. Each of the combination numbers is selected by touching the corresponding contact pair with a finger.

As we grow increasingly security conscious, our pockets progressively fill with keys of all shapes and sizes. Apart from the wear and tear these may cause, the danger of loss is always present. An obvious solution is an electronic combination switch, which can be ideal for controlling electrically operated devices such as alarm systems and solenoid releases, etc.

Since a combination switch involves expenditure in addition to the expense of the device it controls, it is essential to design its circuit such that costs are kept to a minimum. This requirement was achieved with the present design to such an extent that it costs little more than an electrical locking switch.

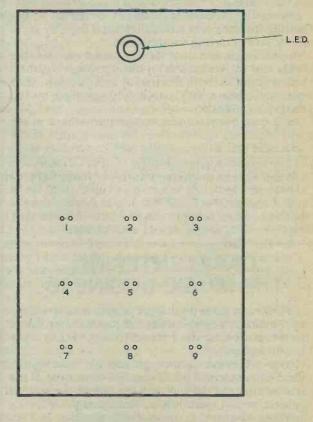
One of the most expensive parts of a combination switch can be the keyboard, press-buttons or other circuit controlling switches which are employed. It was therefore decided to use a "touch contact" system, which may be home produced at almost zero cost. One word of caution, however: touch contacts require clean dry conditions, and are only suitable for indoor applications. CMOS devices can be readily, controlled by touch contacts and they incur very low battery current consumption. The design incorporates two CMOS chips, these being a 4081 (quad 2-input AND gate) and a 4001 (quad 2-input NOR gate).

### **OPERATING THE DEVICE**

The switch is activated by touching contacts 1,2, 3, 4 (as numbered in the circuit diagram) in that order. If any other numbered contact is touched the circuit is de-activated and remains in that state for a period of seconds or minutes according to the values chosen for C2 and R9. Thus, the potential thief has to touch the correct contacts in the proper order the *first time* to

# CMOS COMBINATION SWITCH

### By M. P. Horsey



#### o o - pairs of contacts

Fig.1. Front panel layout of the combination switch. Pairs of contacts are bridged by a finger in the correct sequence to operate the switch. Touching an incorrect contact pair disables the switch for a pre-set period.

### Negligibly low stand-by current.

# •Automatic inhibit on selection of incorrect contacts.

### Finger-tip operation

secure operation of the switch. If a wrong contact is touched, even the correct sequence will not operate the switch until the pre-determined delaying period has elapsed.

The layout of the front panel is shown in Fig.1 and the photographs. The pairs of touch contacts are numbered 1 to 9 but these numbers do not correspond with the numbers allocated to the touch contacts in the circuit diagram. The wiring combination to the contacts is carried out as desired by the constructor and it could, for example, produce a correct sequence of 9, 5, 8, 1, with all the other contacts setting up the delay inhibit period.

Most readers will be familiar with the NOR gates in the 4001 chip. The output of each gate will be high, at logic 1, only when both inputs are low, at logic 0. If either input or both inputs are at 1 the gate output goes to 0. In the 4081, each AND gate produces an output 1 only when both inputs are at 1. If either or both inputs are at 0, the output of the gate is also at 0.

### THE CIRCUIT

The circuit appears in Fig.2. In this diagram, both C2 and C3 are discharged at the instant of applying the 9 volt supply. At this instant, pin 8 of IC2 is held low, because of R7 and R8, and the gate output will be high. This output, coupled via the discharged capacitor C2, keeps pins 12 and 13 high and pin 11 low. The two gates concerned latch into this state. The low output at pin 11 passes to pins 1 and 2 of the third gate of IC2, whereupon pin 3 of that gate goes high, causing C3 to charge via R10 after a very short delay.

However, C3 is discharged at the instant of supply application, thereby biasing the top AND gate of IC1 so that it takes up the state where pin 3 is low, as also is pin 2. These last two pins stay low after C3 has charged. The low output at pin 3 of IC1 is applied to pin 5 of the second AND gate, whose output at pin 4 is consequently low. Following the chain of AND gates, pin 10 is also low as, finally, is pin 11.

When contacts 1 are touched, pin 2 of IC1 goes high, as also does pin 3, and the top AND gate latches into this new state. Pin 5 of the second AND gate is now high so that, if contacts 2 are bridged by the finger, its output goes high also, at pin 4. The progression continues by touching contacts 3 and then contacts 4, whereupon pin 11 of IC1 goes high, turning on transistor TR1 and energising the relay. The relay

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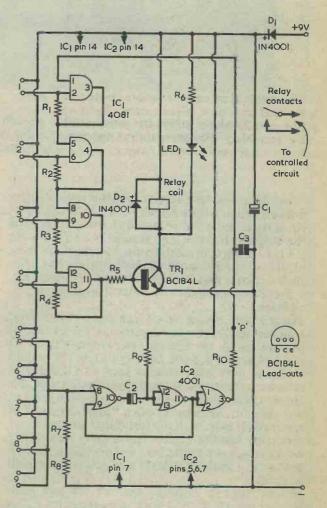
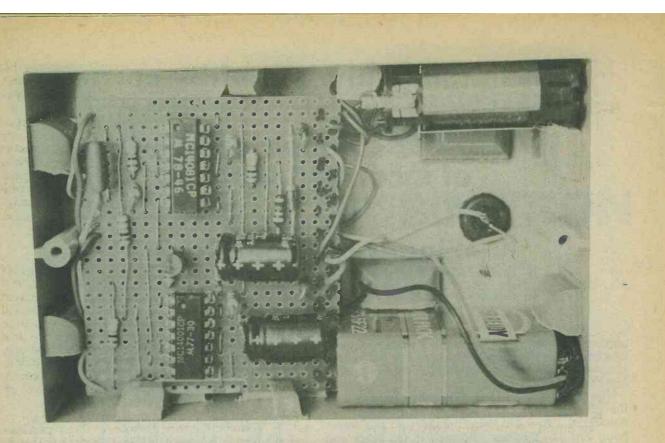


Fig.2. The circuit of the combination switch. Contact sets 1 to 4 have to be touched in numerical order to actuate the switch. Touching any of the other contact sets inhibits the switch for a period dependent upon the values of C2 and R9. Contact set numbering does not, of course, correspond with the numbers on the front panel of the switch.



Looking inside the combination switch case. The author's unit is powered by a PP3 battery and incorporates a dry reed relay, which is mounted opposite the battery.

contacts then turn on whatever circuit is controlled by the combination switch. Diode D2 protects the transistor against high inductive voltages when the relay is subsequently released. The transistor also turns on the l.e.d. in its collector circuit, thereby indicating that the correct sequence of touch buttons has been selected. LED1 and its current limiting resistor, R6, are not essential and may be omitted from the circuit, if desired.

If any of the "wrong" contact pairs, 5 to 9 inclusive, is touched, pin 8 of the NOR gate in IC2 is taken high and its output at pin 10 goes low, as also do pins 12 and 13 of the next NOR gate. Pin 11 of the second NOR gate goes high and, as long as C2 remains discharged, these two gates stay latched in this new condition. The high output at pin 11 causes pin 3 of IC2 to go low. This low is passed via R10 to pin 1 of the top AND gate, with the result that contact sets 1 to 4 become inactive. Even if one or more of the AND gate outputs has already been latched high, the low input at pin 1 of the top AND gate will take them all low again.

When, after a "wrong" contact set has been touched, pin 10 of IC2 goes low, capacitor C2 is discharged. It then begins to charge slowly via R9 until, eventually, pins 12 and 13 of IC2 go sufficiently high to cause pin 11 to go low and pin 3 to go high. The first two NOR gates latch back to their initial state and the combination switch may again be operated by touching contacts 1 to 4 in the correct order. The values of C2 and R9 are chosen by the constructor to suit his particular requirements. A very long time delay is not recommended as it would be irritating for the authorised user to have to wait too long if he accidentally touched a wrong contact set. As an example, a value of  $1M\Omega$  for R9, with  $4.7\mu$ F for

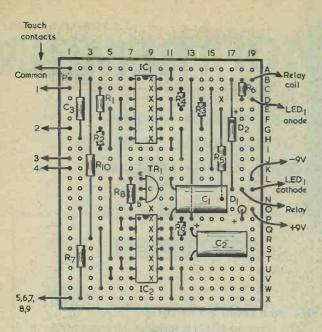
### COMPONENTS

### Resistors

R2 10MΩ 10% R3 10MΩ R4 10MΩ 10%	otherwise stated.) R6 1k $\Omega$ R7 10M $\Omega$ 10% R8 10M $\Omega$ 10% R9 see text R10 1M $\Omega$
R5 4.7kΩ Semiconductors IC1 4081 IC2 4001 TR1 BC184L	D1 1N4001 D2 1N4001 LED1 red l.e.d.
Capacitors C1 100μF electrolytic, 10V. Wkg. C2 see text C3 0.01μF polyester, type C280	

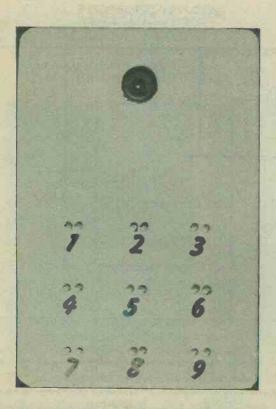
Miscellaneous 9-volt battery type PP3 (see text) Battery connector Plastic case (see text) Relay (see text) 18-off touch contacts (see text) 2-off 14-way i.c. holders

C2, will provide a delay of about 3 seconds. Increasing either R9 or C2, or both, will increase the delay. In practice a delay of about  $3\frac{1}{2}$  minutes is suggested, and this will be obtained with R9 at 4.7M $\Omega$  and C2 at  $100\mu$ F.



X = breaks in copper

Fig.3. Veroboard layout as seen on the component side of the board. The "common" lead from hole A1 connects to one contact of all the contact pairs.



Also mounted on the front panel is a light-emitting diode which lights up when the correct combination has been selected.

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Diode D1 is optional, but is included to ensure that no damage is done should the battery be momentarily connected the wrong way round, as can easily happen with a PP3 type connector.

Point "P" is provided to turn off the combination switch once entry has been gained or an alarm switched off, etc. A push-button may be connected between point "P" and the negative rail, and if this is momentarily pressed it will cause all the AND gates to latch low. This switching option was not used with the author's circuit. Touching one of the "wrong" contact pairs will also, of course, cause the AND gates to latch low.

### **CONSTRUCTION**

The relay can be any type having a coil resistance of  $150\Omega$  or more which will energise reliably at some 8 volts. The prototype used a reed relay. The quiescent current consumed by the circuit is negligibly low and the author's unit was powered by a PP3 battery. Once the switch has operated the current drawn is that required by the relay plus some 1 to 2mA in the base circuit of TR1 and about 7mA in LED1. If it is desired that the relay remain energised for relatively long periods, some constructors may prefer to use a 9 volt battery that is larger than PP3 size.

Most of the components are assembled on a 0.1 in. matrix Veroboard having 19 holes by 24 strips. Component layout is shown in Fig.3. Before soldering components in place, make the breaks in the copper strips as indicated. There are 16 of these breaks. To avoid damage to the two CMOS devices it is advisable to solder i.c. holders to the Veroboard and then plug in the two i.c.'s after all wiring has been completed.

The switch may be housed in any plastic case which will take the Veroboard, relay and battery. That employed by the author measured  $4\frac{1}{2}$  in. high by 3 in. wide. The contact sets consisted of large stainless steel pins, the diameters of the heads being about 2mm. Each pair is mounted with centres about 4mm. apart, 18 pins being required for the 9 contact pairs. The holes drilled in the front panel of the case allow the pins to be tight push fits. The wires from the Veroboard to the pins are routed to provide the required combination. Avoid overheating the pins when soldering as the plastic may then melt, causing the pins to become loose. Contacts can also be provided by small screws with nuts, the screws having heads with a bright plated finish.

The Veroboard is held in place by pieces of plastic glued to the sides of the case. In the prototype it is at the same end of the case as the contact pins, and its underside is kept well clear of the pin ends. Any other means of mounting the Veroboard which will not interfere with its circuit operation can be used. The l.e.d. is a push fit in a rubber grommet which is also mounted on the front panel.

The completed switch is checked by touching the 4 sequence contact sets in correct order. The l.e.d. should then light up. Next, touch one of the remaining contact sets and the l.e.d. should extinguish. It should not be possible to light it again, by touching the 4 correct sequence contacts, until the time delay imposed by C2 and R9 has elapsed.

The prototype has proved reliable in operation over a long period of time. The touch contacts must be kept clean and dry. Avoid spraying the contacts with ordinary cleaning agents such as are used for dusting, etc., as these can easily produce an insulating film which makes the contacts inoperative.



### TREMOLO

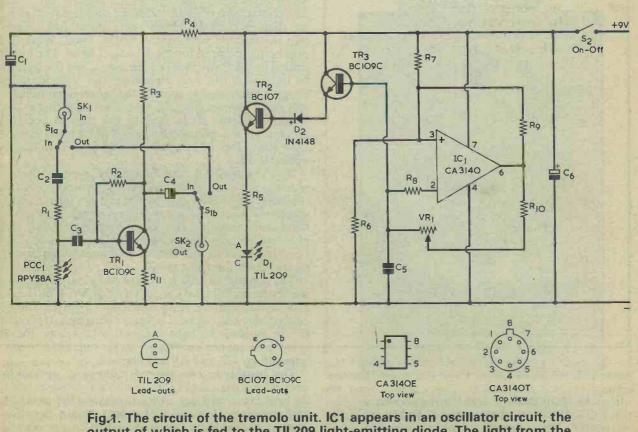
By I. M. Attrill

Although it is one of the easiest forms of electronic musical effect, tremolo is still used very frequently these days. Tremolo units can be simple and inexpensive, and they make an ideal project for the constructor who is interested in electronic music. The design featured in this article produces very low levels of noise and distortion, and is suitable for use with a low level input such as that provided by a guitar pickup. It can handle inputs of up to several hundred millivolts r.m.s. without clipping, and is therefore also suitable for use with many organs and tone generators, etc.

The unit is powered by an internal 9 volt battery and is self-contained. The tremolo frequency is variable from about 2.5 to 10Hz.

The tremolo effect is produced by amplitude modulating the input signal at tremolo frequency. This can be achieved by a unit consisting of two stages: a modulator which has a voltage gain proportional to a voltage fed to its control terminal, and a low frequency oscillator which provides the control voltage. The frequency of the oscillator should be variable to give what is considered the most desirable effect, and it should cause the signal amplitude to be varied smoothly.

Fig.1 shows the complete circuit of the tremolo unit.



output of which is fed to the TIL209 light-emitting diode. The light from the l.e.d. falls on PCC1 and amplitude modulates the signal applied to TR1.

### **MODULATION UNIT**

Easily made circuit adds brilliance to guitars, organs and electronic music. Opto coupling gives smooth modulation.

### COMPONENTS

Resistors (All fixed values  $\frac{1}{4}$  watt 5% unless otherwise stated) R1 8.2kΩ **R**2 1.8MΩ 10% R3 4.7kΩ R4 470Ω 1.2kΩ **R**5 100kΩ **R**6 **R7**  $100k\Omega$ **R**8  $10k\Omega$ **R**9 100kΩ **R**10 33kΩ R11 820Ω VR1  $100k\Omega$  potentiometer, linear

### Capacitors

C1 100 $\mu$ F electrolytic, 10V. Wkg. C2 1.5 $\mu$ F polyester type C280 C3 0.1 $\mu$ F polyester type C280 C4 4.7 $\mu$ F electrolytic, 10V. Wkg. C5 1.5 $\mu$ F polyester type C280 C6 100 $\mu$ F electrolytic, 10V. Wkg. Semiconductors TR1 BC109C TR2 BC107 TR3 BC109C IC1 CA3140E or CA3140T D1 TIL209 D2 IN4148

Photoconductive Cell PCC1 RPY58A

Switches S1 d.p.d.t. (see text) S2 s.p.s.t. rotary

Sockets SK1 3.5mm. jack socket (see text) SK2 3.5mm. jack socket (see text)

Miscellaneous Case (see text) 9-volt battery type PP3 Battery connector Veroboard, 0.1in. matrix 2 control knobs Nuts, bolts, wire, etc.

Layout inside the case There is plenty of room available for the 9 volt battery.

The input signal at socket SK1 is applied via S1(a) and d.c. blocking capacitor C2 to the attenuator consisting of R1 and PCC1. The output from the attenuator is fed via C3 to the common emitter amplifier, TR1, and the output signal at TR1 collector passes through C4 and S1 (b) to the output socket, SK2. Due to the unbypassed emitter resistor, R11,

the voltage gain of TR1 is of the order of 6 times only. The transistor has an input impedance at its base of several hundred kilohms.

PCC1 is a cadmium sulphide photo-resistor which has a minimum resistance of  $200k\Omega$  in total darkness. This resistance falls to less than  $1k\Omega$  under reasonably bright conditions. The signal input to TR1 base

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can in consequence be varied by varying the light intensity on PCC1, and this effect produces the tremolo amplitude modulation.

The light which falls on PCC1 is provided by the light-emitting diode, D1. The tremolo oscillator employs operational amplifier IC1 in a circuit which has appeared in previous articles in this magazine, and whose functioning will only be briefly described here. The upper plate of C5 couples via the protection resistor, R8, to the inverting input of the i.c., whilst the non-inverting input is connected to the three equal value resistors, R6, R7 and R9. At switch-on, C5 will be discharged, whereupon the i.c. output will be high and the non-inverting input will be at twothirds of the supply potential. C5 will then charge via R10 and VR1 until its upper plate takes up the same potential as that at the non-inverting input. The i.c. output then triggers to the low state, causing the non-inverting input to be at about one-third of supply potential, and C5 commences to discharge. When the voltage across C5 falls to that at the non-inverting input the output triggers to the high state and C5 charges once more. The oscillation then continues, with C5 alternately charging and discharging. Oscillator frequency is controlled by VR1.

A roughly triangular waveform is given at the

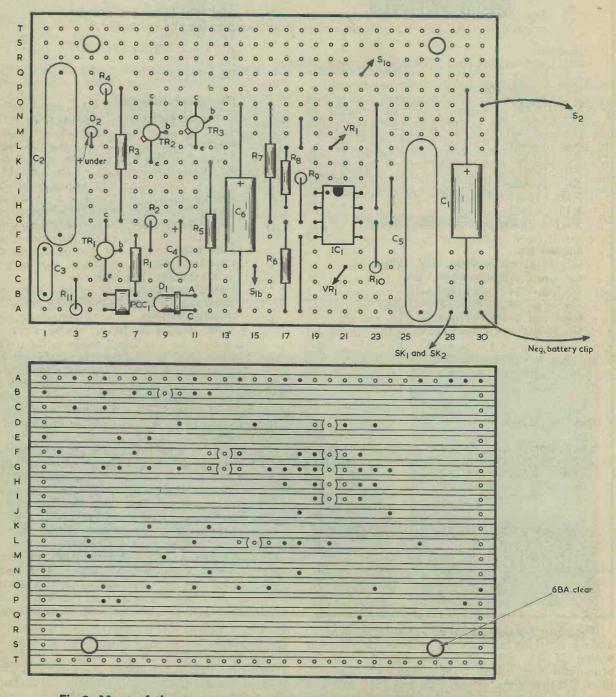


Fig.2. Most of the components are assembled on a Veroboard panel. This diagram shows the component and copper sides of the panel.

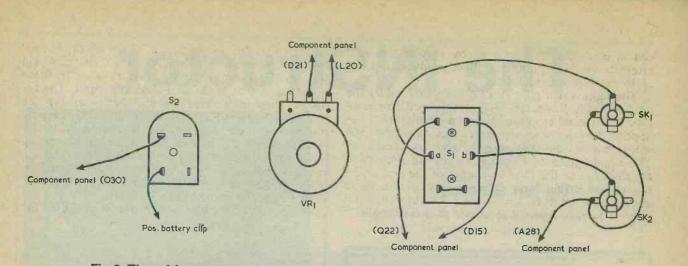


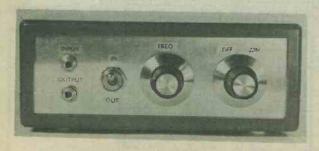
Fig.3. The wiring to the components on the front panel. The letter and number references apply to the corresponding Veroboard holes shown in Fig.2

upper plate of C5 and this is applied to the base of emitter follower TR3. TR3 couples via D2 to the base of a second emitter follower, TR2, with the result that the input impedance at TR3 base is very high and there is negligible loading on the oscillator. The emitter of TR2 drives the 1.e.d., D1, and thereby causes the intensity of the light it emits to vary at oscillation frequency. D2 is interposed between the two transistors to increase the voltage drop from TR3 base to TR2 emitter and to thereby ensure that D1 is fully extinguished when the voltage from the oscillator is at its minimum level.

Switch S1(a)(b) can be used to bypass the unit, so that the tremolo effect can be switched out when it is not required. VR1 controls the tremolo frequency. S2 is the on-off switch for the unit, and current consumption from the 9 volt battery is only 3.5mA. The RPY58A specified for PCC1 is available from Maplin Electronic Supplies. Capacitor C4 is specified as 10V. Wkg., but it will be quite in order to employ a capacitor having a higher working voltage.

### CONSTRUCTION

The prototype tremolo unit is housed in a Verocase type 75-1238D, which has approximate dimensions

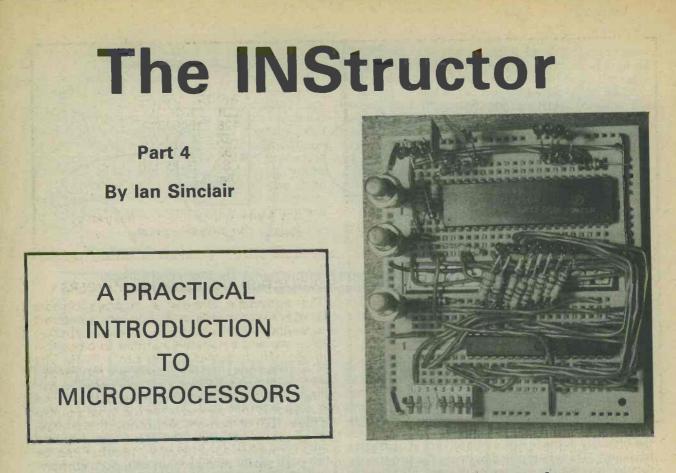


On the front panel the input and output sockets are to the left. Switch S1(a)(b) is to the right of these, followed by VR1 and S2. of 153 by 84 by 59mm. The general layout can be seen from the accompanying photographs, whilst Fig.3 shows the layout of the front panel components as seen from the rear. S1(a)(b) is a d.p.d.t. toggle switch. However, if the unit is to be used with a guitar, or in other circumstances, where it would be preferable for S1 (a)(b) to be a foot operated switch, the components should be housed in a strong case such as a diecast box. S1 (a)(b) could then be a heavy duty successive action push button switch mounted on the top of the case. The input and output jack sockets were 3.5mm. with the prototype but any other jack sockets, such as  $\frac{1}{4}$  in. types, can be employed if these are more convenient.

Most of the circuitry is assembled on a 0.1in. matrix Veroboard having 30 holes by 20 copper strips. The layout is shown in Fig.2. The two mounting holes should be drilled after the board has been cut out, following which the breaks are made in the copper strips. IC1 has a MOSFET input stage and is susceptible to damage by high static voltages. It should be the last component to be soldered into circuit, and the soldering iron must have a reliably earthed bit. Either the CA314OE (8 pin d.i.1. version) or the CA314OT (T099 cased) can be employed. The pin layout of both types is shown in Fig. 1.

PCC1 and D1 must be oriented so that the light output of D1 is directed at the sensitive surface of PCC1. If PCC1 is examined it will be found that the lead-out wires can be clearly seen connecting to one side of the component. It is the other side which is light-sensitive and which should face D1. The two components should be positioned so that they are touching each other, or are virtually so.

The wiring from the board to the front panel components is very straightforward and is illustrated in Fig.3. When this wiring is completed it is merely necessary to mount the component panel to the bottom of the case using 6BA bolts and nuts with spacing washers under the panel. After a final check of the wiring the unit is then ready for testing and use. It will, of course, be necessary to fit the case lid for correct operation, in order that external light cannot fall on PCC1.



### Subtraction and the use of negative numbers.

In Part 3 we saw how the microprocessor deals with addition, and the use of the carry/link bit to "catch" the ninth bit in an addition. We're going to take a further look at this carry/link bit now because it's important in any arithmetical operation. If, for example, you have the carry/link bit set from some previous operation it will, unless you clear it, be automatically added in to a later addition. This may give you the impression that the microprocessor doesn't add very well!

### STATUS TO ACCUMULATOR

Last time, we detected whether the carry/link was set by doing an addition, and checking to see if an extra 1 had been added in. This is a rather roundabout method, so we'll start this month with something easier. It's a single byte instruction, coded as CSA, meaning Copy Status to Accumulator. The effect of this instruction, which is 00000110, is to set all the flip-flops of the accumulator to the same pattern as the flip-flops of the status register. As we've mentioned, the status register is a collection of flip-flops, each of which is used for some different signal. The carry/link bit is bit number 7 of the status register, so that when we carry out the CSA instruction the 7th data bit of the accumulator is set to 1, and we can use the display routine to look at the effect on the data l.e.d.'s. Fig. 1 shows a short routine which gives you a bit of practice in this – it starts, after reset, with the instruction SCL, set carry/ link, which sets the carry/link to logic 1. The next step, CSA copies this into the accumulator and

then the two display steps let you see the l.e.d.'s lit. Providing you remembered to reset (so that all registers started at zero), the display will show 10000000, with only D7 lit, indicating that bit 7 of the status register was set. Remember that counting of these flip-flops always starts at 0, so that D7 is what one would normally call the 8th bit.

Just to make certain, the next part of the program in Fig. 1 resets the carry bit. The NOP instruction allows the 8060 to return to normal memory addressing, then CCL clears the carry/link (to zero). Once again, we look at the result by using CSA followed by the display routine. This time, all the data l.e.d.'s are extinguished showing that the carry/link was cleared.

Being able to inspect the carry/link in this way is useful, and it also lets us look at the other bits in the status register. If, by the way, you're wondering why the word "link" is used along with

	RESET SCL 00000011 CSA 00000110 Display NOP 00001000 CCL 00000010 CSA 00000110 Display
F	ig.1. Reading the status register.

 RESET

 LDI
 11000100

 196
 11000100

 ADI
 11110100

 244
 11110100

 CSA
 00000110

 Display

### Fig.2. Setting the carry/link by a binary addition.

RESET

00101101 complements to 11010010 Then add 1 to get 11010011 which is the 2's complement, the number in its negative (SIGNED) form.

Fig.4. Forming a 2's complement.

"carry" – be patient, all will be revealed later. Meantime, let's flex our muscles a bit and check if a binary sum has caused the carry/link to be set. The program is shown in Fig.2 – try it, and see if the carry/link is set.

That was straightforward, so now try something for yourself. Question is, does a decimal add cause the carry/link bit to be set? Try adding 10000011 and 10010101 (DCB for decimal 83 and 95), remembering that the add instruction has to be DAI (11101100).

Once you've sorted that out for yourself, it's time for a puzzle. Fig.3 shows the program, which is a simple binary addition of 16000000 and 11000000, followed by a look at the status register. Try it out. You'd expect to find D7 lit, because there's certain to be a carry – but why is D6 lit? The answer briefly is that mhis is another bit of the status register which has been set by this addition. It's called the overflow, and very shortly we'll see why it has been set. 
 RESET

 LDI
 11000100

 128
 1000000

 ADI
 11110100

 192
 11000000

 CSA
 00000110

 Display
 1

Fig.3. A sum which sets another part of the status register.

### SUBTRACTION AND NEGATIVE NUMBERS

The arithmetic circuits of microprocessors consist entirely of adders - so how do we subtract one number from another? The answer is that we don't - we add a negative number to a positive number, which comes to the same thing. The obvious next question is - how do we represent a negative in binary? Since we have only the two digits 1 and 0, we have to make use of these, and the answer is that we use a 1 in the most significant place (D7) of the number to show that a negative number is intended. Now this conversion of a positive number to a negative number can be done very easily by a process called 2's comple-ment. The number we're going to subtract must consist of seven bits, because the eighth (D7) is reserved for the "negative sign" bit, and we convert it to negative form by writing all eight bits (Fig.4) and then complementing – swopping each 0 for a 1, and each 1 for a 0. When this has been done, we add 1 to the lowest place (D0) and that's our number in negative form, ready to add to the other one. A substraction of this type, incidentally, involves discarding any carry from D7 so that if we carried out two substractions in a row we would normally have to clear the carry/link bit. Do we hear an objection? Using seven bits only lets us use numbers up to 01111111, 127 in decimal. It's no problem, really because we can take larger numbers eight bits at a time, and only the highest bit of all needs to be used as a sign bit. Fig.5 shows an example of 15-bit arithmetic, with the 16th bit used as the sign bit. When this is done,

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 RESET

 SCL
 00000011

 LDI
 11000100

 5
 00000101

 CAI
 11111100

 3
 00000011

 Display
 NOP

 NOP
 00000110

 CSA
 00000110

 Display
 Display

Fig.6. A simple subtraction program.

only the lower byte of the number which is to be substracted has 1 added to it, the upper byte is only complemented.

Back to the hardware. The instruction for subtraction is Complement and Add, and we'll use this in its immediate form, CAI, whose binary code is 11111100. This will only complement the number and add it in, it won't add 1. The reason is that 1 only has to be added to the lower byte, when a two byte number is used, so that addition is not part of the instruction. How do we add 1? We could, of course, load in a 1 and add it, but a much simpler method is to set the carry link before the complement and add step. This way, the 1 is automatically added in at the time when the complement is added - simple. Now try this out. A simple binary subtraction program is shown in Fig.6-we're subtracting 00000011 from 00000101 (decimal 3 from 5) to get the expected answer of 00000010 (decimal 2). The program now goes on to check the status register - which should show the carry which we ignore. Now what happens if we do this the other way round and subtract 5 from 3? Fig. 7 shows the program steps. Try it out and jot down the answer, then find out what's happened to the status register.

No carry in the status register? The answer looks a bit odd too, doesn't it? The reason is that this is a negative answer, and the 1 in the eighth (D7) place indicates this. To make sense of it we have to convert it back to ordinary form by reversing the 2's complement process. This is done by subtracting 1 or by adding 11111111, which is the two's complement of -1, giving the sum 1111101, which is complemented to 00000010, or decimal 2. We can make the microprocessor go 
 RESET

 SCL 00000011

 LDI 11000100

 3 00000011

 CAI 11111100

 5 00000101

 Display

 NOP 00001000

 CSA 00000110

 Display

 Fig.7. A subtraction which gives a negative answer.

through this routine by using the program in Fig.8. Note that we have started with the result of the subtraction loaded in, because it saves having to start again from scratch. When we copied the status register to the accumulator we lost the data which was in the accumulator.

### **EXTENSION REGISTER**

There is a way of preserving that data byte, though, when we want to copy status, and it's shown in the programs of Figs. 8 and 9. The two new instructions are XAE and LDE. XAE means Exchange Accumulator and Extension, and it swops the byte in the accumulator with the byte in another register called the extension register. If we always start a program with a reset, we can expect that the extension register will be full of zeros when we make this step, so that the result is to tuck away the byte which was in the accumulator and leave a clear accumulator. We can then copy the status register and look at it, knowing that the two sets of bytes are preserved. To look again at the byte which is now in the extension register we use the instruction LDE -Load Accumulator from Extension. This doesn't erase the byte in the extension register, it merely copies it into the accumulator.

Now for some problems. Suppose we use the microprocessor to add two numbers, and one or both of these numbers starts (8th place, D7) with a 1. Does the microprocessor treat this as a negative number, or does it have some magical way of being able to tell the difference between, say 10011010, meaning plus 154, and 10011010, meaning – 102?

RESET LDI 11000100 -2 1111110 ADI 11110100 -1 11111111 CCL 00000010 XAE 00000010 CAE 01111000 Display	RESET         SCL       00000011         LDI       11000100         3       00000011         CAI       11111100         5       00000101         XAE       00000001         CSA       00000110         Display       NOP         NOP       00001000         LDE       01000000         Display
Fig.8. "Unscrambling" a 2's comple- ment number.	Fig.9. Using the extension register to store an answer.

10011010 as an unsigned number is 2 + 8 + 16 + 128 = 154.

10011010 taken as a signed number is found by subtracting 1 (or ADDING 11111111) to get 10011001, then complementing to get 01100110. This number is 102 in decimal and, because it was a signed number, we must write it as - 102.

Fig.10. Two identical binary numbers with different meanings.

See Fig. 10. The answer is simpler than you might think – it can't! The microprocessor simply carries out binary arithmetic and what you make of the numbers is entirely up to you. As a help, though, there's a warning signal which lets you know if you have to be careful about an answer. It's called the overflow bit (OV) and it's bit 6 (counting 0 to 7) on the status register.

To see this in action, try out the four addition programs which are shown in Fig. 11, note the answers and check the OV bit by using the CSA Display, procedure. The reasons go like this. Ir example (a) the numbers are positive, no matter how you look at it, and the answer is also positive There's no carry out of the 7th or 8th place (D6 or D7). The answer is correct, and the OV is not set. In example (b) there are carries out of D6 and D7, and the carry out of D7 causes the carry/link to be set. If we think of this as straightforward unsigned arithmetic, then we're adding decimal 197 to decimal 233, and getting (remembering the 9th bit in the carry/link) the answer decimal 430. Looks all right, but suppose we were thinking of these two as negative numbers? If we were using signed arithmetic, then 11000101 is decimal - 59, and 11101001 is decimal - 23, giving the answer -82, which is correct. No matter what we make of the figures, then, the arithmetic is correct.

Now for (c). If we take both numbers as unsigned, so that the highest order 1 (in D7) represents decimal 128, then the answer is correct (remembering the carry bit). What if we take the

16 +	00111101 + 00011001
d by get 0110. t was 2.	01010110 (a)
	11000101 + 11101001
	10101110 (b)
u y	10100010
е	+ 10011001
), u	00111011
r. 6	(c)
0	01010110
n	+ 01110011
e V,	11001001
n	(d)
r r	Fig.11. Additions which cause differ- ent effects on carry and overflow.

D7 1's as meaning negative sign? 10100010 then means decimal - 94, and 10011001 is decimal -103, adding to - 197. The biggest number we can handle with seven digits, though, is 128, and the eighth digit has become 0, indicating a positive number! This is an overflow, and the overflow status is set to warn us. If we are using signed numbers, then the 0 at the start is false; the number is really negative. If we reverse the 1's complement on the result, we get the correct answer, - 197.

Next, example (d). Once again, the sum causes no difficulties with unsigned arithmetic, but the answer looks wrong when we think of these as positive numbers, and the answer as negative! Once again, the overflow bit is set as a warning

RESI	
LDI	11000100
	11100111 lower A
SCL	00000011
CAI	11111100
	01011001 lower B
XAE	00000001 stores result in extension
LDE	01000000 brings it back (but leaves a copy in the extension)
Disp	play so that you can look at it
	11000100
	00101000 upper A
CAI	
	00011010 upper B
Displ	
The	lower byte of the answer is still in the extension.
11101	to the answer is sum in the extension.
	Fig 12. The carry used in double but
	Fig.12. The carry used in double-byte subtractions.
	Subtractions.

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### Mnemonic

LDE	01000000
XAE	00000001
ANE	01010000
ORE	01011000
XRE	01100000
DAE	01101000
ADE	01110000
CAE	01111000

Code

### Operation

Load the accumulator from the extension. Exchange accumulator and extension AND extension with accumulator OR extension with accumulator X-OR extension with accumulator Decimal add extension to accumulator Binary add extension to accumulator Complement and add extension to accumulator

Note. In each of these instructions, apart from the exchange instruction, the number stored in the extension register is not cleared nor changed. The accumulator ends up with the result of the instruction.

### Fig.13. The extension register instructions summarised.

that two positive numbers don't produce a negative answer. What the overflow flag warns, then, is that we can use the D7 bits as part of the number, but that its sign is wrong. This bit is used as a warning only – it's never added in anywhere. If it has to be checked in a program, the procedure is to copy the status register to the accumulator (carrying any valuable data byte in memory or in the extension register) and then use logical AND to the byte with the pattern 01000000 – we'll explain that later. These status register bits, incidentally, are often called "flags", so that we refer to the carry/link flag and the overflow flag as being set (to 1) or reset (to 0).

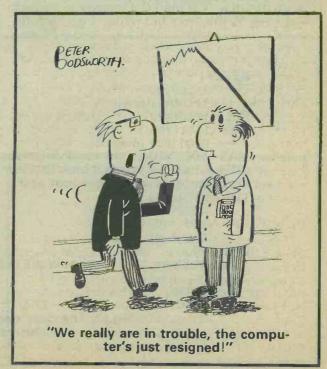
Now what does the carry/link bit do when we subtract? Common sense leads to the conclusion that it simply does exactly as it always does – if it's set, it adds in on the next byte. to see what this does we can try a two byte subtraction, as shown in Fig. 12. What we're doing is subtracting (B) 0001101001011001 from (A) 0010100011100111, and it's done in two bytes, as you would expect. We start by loading the lower byte of the number (A), 11100111 and setting the carry/link so that when we complement and add immediate (CAI), we have carried out a 2's complement subtraction.

We can display this byte and also preserve it in the extension register, and then load in the upper byte of number A. Using the complement and add on the upper byte of number B then gives the result of upper byte subtraction, which can be displayed in the usual way. Note that we only need to set the carry/link bit for the lower byte, after that everything is automatic – the carry/link is reset when the first complement and add is put in, then set again by the carry from this stage, to be added in at the next complement and add step.

Now for some homework. Could you write and try out a program so that you could look again at the lower byte of the answer, which was stored in the extension register? Remember that this will be stored only if you haven't reset. Just one final point. All the arithmetic operations we've looked at so far can also be carried out on numbers in the extension register, using the commands shown in Fig.13. This lets us carry out arithmetic operations on two bytes separately, but the same carry/link is used, so that we need to take great care that a carry from an operation in one register is not taken to the other register unless we want it. Obviously, if we're carrying out double byte arithmetic we'll want the carry, but whatever we're doing we mustn't forget it!

Next month - Logic Operations.

(To be continued)



# COIL -COUPLED

### S. W. CONVERTER

By R. A. Penfold

# INDUCTIVE COUPLING TO M.W. radio — no wires.19 to 67 metres.Full superhet selectivity.

In conjunction with an ordinary medium wave superhet receiver having a ferrite aerial this unit permits reception on the short wave broadcast bands in the range of 4.5 to 16MHz. The range gives full coverage of the 19, 25, 31, 41 and 49 metre bands. The 20 and 40 metre amateur bands are also covered, but the converter is not recommended for amateur bands reception and is really intended as a means of increasing the number of broadcast stations which can be received with an ordinary domestic radio.

There is no need to make any modifications to the receiver with which the converter is used, and there are no direct connections between the converter and the receiver. The converter radiates a strong local signal at a nominal frequency of 1.6MHz, or 188 metres, and this is picked up by the ferrite aerial of the radio. It is merely necessary to place the converter near the receiver and then tune the latter to a quiet point around 1.6MHz. Short wave tuning is then carried out with the converter controls, which alter the received short wave signal frequencies to the 1.6MHz reception frequency of the radio.

### **BLOCK DIAGRAM**

The stage line-up of the converter is shown in the block diagram of Fig. 1. The aerial signal is selected by the input tuned circuit and is applied to the mixer stage. So also is the output of a tunable oscillator. The difference frequency is then extracted from the mixer by a tuned radiator coil which is resonant at 1.6MHz. The signal and oscillator tuning capacitors are ganged and the two tuned circuits are set up so that the oscillator frequency is always higher than the aerial signal frequency by 1.6MHz. The principle is the

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same as that encountered in the first stages of a conventional superhet, and the aerial and oscillator coils specified would produce an intermediate frequency of 1.6MHz in a normal superhet application.

As with the superhet, this line-up can be subject to interference by image signals. If, for instance, it is desired to receive a signal of 10MHz, the oscillator would be set to 11.6MHz to give the difference frequency of 1.6MHz. An image signal at 13.2MHz, which also gives a difference frequency with the oscillator of 1.6MHz, could then break through. It is the

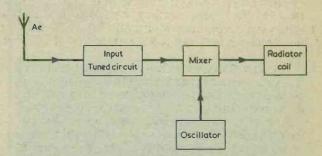


Fig. 1. The stage line-up of the short wave converter. The radiator coil causes an output signal at a nominal frequency of 1.6MHz to be coupled into the ferrite rod aerial of a medium wave receiver. No interconnecting wires are needed between the converter and the receiver.

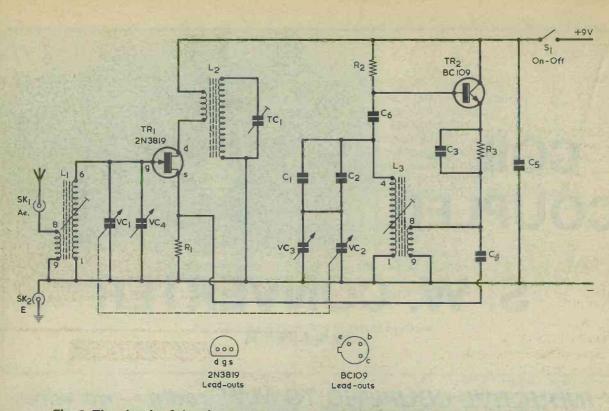


Fig. 2. The circuit of the short wave converter. L1 is the aerial input coil, L3 the oscillator coil and L2 the radiator coil.

function of the aerial tuned circuit to boost the strength of the required aerial signal and to attenuate the unwanted image signal.

The mixer output is coupled to the radiator coil. A tuned coil is used here since, although it makes initial alignment of the converter a little more difficult, it produces a much stronger field strength than does an untuned coil. The coil is, in fact, the coil of a standard medium wave ferrite aerial. The local field strength from the radiator coil is quite high, but it rapidly falls in strength with distance from the coil, and there is no danger of the converter radiating sufficiently strongly to cause interference with other receivers.



Two sockets, for aerial and earth, are mounted on the rear panel of the case.

### **CIRCUIT OPERATION**

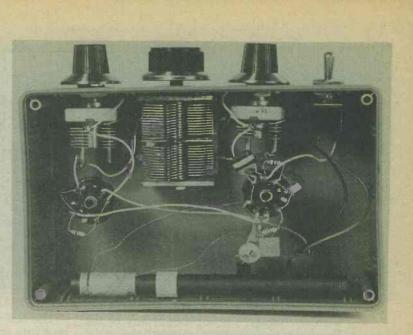
The circuit of the converter is shown in Fig. 2. The aerial is coupled to the tuned winding of L1 via the coupling winding, and the tuned winding connects directly to the gate of the mixer, TR1, which has a very high input impedance. The main tuning capacitor is VC1, with VC4 acting as an aerial trim capacitor. There is a third coupling winding on L1 which is not used in the present circuit, and this winding is not shown in the circuit diagram. Source bias for TR1 is provided by R1.

TR2 is the oscillator, and is employed in the emitter follower mode. The tuned winding of L3 couples via C6 to the transistor base, and positive feedback is provided via the coupling winding in the emitter circuit of TR2. Like L1, L3 has a third winding which is not used, and which is not shown in Fig. 2. The main oscillator tuning capacitor is VC2, which is ganged with VC1. VC3 is an oscillator trim capacitor and functions as the bandspread control. Fine tuning is carried out by VC3, which is much easier to adjust than VC2 because of the limited frequency coverage it offers. C1 and C2 in parallel form the oscillator padding capacitor and ensure good tracking over the range covered. The oscillator output couples through C4 to the source of TR1.

The difference frequency present at the drain of TR1 is applied to the coupling winding on the output coil, L2. The large winding is tuned to the output frequency by TC1.

C5 is the supply bypass capacitor and S1 the on-off switch. The total current consumption is only about 2.5mA, which allows many hours of operation from the PP3 battery.

Layout of components in the converter. The ferrite rod for the radiating output coil is secured to the rear panel of the case with a plastic clip.



#### **COMPONENTS**

Most of the components are readily available from various suppliers. VC1, 2 is a 2-gang Jackson type 02 component without trimmers. The ferrite rod is secured in position with a plastic clamp, and a nylon "P" cable clip intended for wires of 10mm. diameter was employed in the prototype. This clip is available from Maplin Electronic Supplies. A metal clamp must not be used. In the author's unit, TC1 was a miniature film dielectric trimmer with a maximum capacitance of 65pF. This can also be obtained from Maplin Electronic Supplies. If difficulty is experienced in obtaining any of the Denco items they can be purchased direct from the manufacturer at Denco (Clacton) Ltd., 357/9 Old Road, Clacton-on-Sea, Essex, CO15 3RH.

The converter must be housed in a non-metallic case since a metallic one would shield L2 and prevent it from radiating the output signal. The author employed a plastic case measuring 160 by 100 by 60mm., and any plastic case of about this size, or slightly larger, which will accommodate the components with the same layout can be used. Parts of L1 and L3 project below the bottom of the case, which must be fitted with four cabinet feet at the corners to provide clearance.

#### CONSTRUCTION

One of the 160 by 60mm. sides of the author's case forms the front panel. Appearing on this are, from left to right, switch S1, VC3, the 2-gang capacitor VC1,2, and VC4. S1, VC3 and VC4 are secured to the front panel in the normal manner by means of their bush mounting nuts. The hole for VC1,2 takes its  $\frac{1}{4}$  in. spindle only. If this component is examined it will be found that there are two 4BA tapped holes in the bottom of its metal frame, and the capacitor is mounted by passing two 4BA bolts through holes in the bottom of the case, passing spacing washers over these bolts inside the case to provide clearance for the ceramic mounts below the capacitor frame and then screwing the bolts into the two 4BA tapped holes. The bolts should be short, so that their upper ends pass only marginally inside the capacitor metal frame. This is an important point to observe because, if the mounting bolts are too long, their upper ends can damage the fixed or moving vanes of the capacitor.

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#### COMPONENTS

- Resistors (All  $\frac{1}{4}$  watt 5%)
- **R1**  $1k\Omega$ **R2**  $1M\Omega$
- **R3**  $2.7k\Omega$

#### Capacitors

- 470pF polystyrene 470pF polystyrene 180pF ceramic plate **C1**
- C2
- **C3**
- **C**4 0.015µF ceramic plate or polyester.
- C5 0.1µF polyester. 220pF ceramic plate
- **C**6
- VC1,2 365pF + 365pF 2-gang variable, type 02 (Jackson)
- VC3
- 25pF variable, type C804 (Jackson) 50pF variable, type C804 (Jackson) VC4
- 65pF trimmer, film dielectric TC1

#### Inductors

- Transistor tuning coil, Blue, Range 4T (Denco) Ferrite rod aerial type MW5FR (Denco) Transistor tuning coil, White, Range 4T L1
- L2
- L3
- (Denco) Semiconductors

TR1 2N3819

TR2 **BC109** 

#### Switch

S1 s.p.s.t. toggle Sockets SK1 4mm. insulated socket, red SK2 4mm. insulated socket, black

Miscellaneous Plastic case (see text) 9 volt battery type PP3 **Battery connector** 2 B9A valveholders 3 control knobs Plastic clip, 10mm. (see text) 4 cabinet feet Nuts, bolts, wire, etc.

The positions of the holes in the front panel and in the case bottom are measured off from the capacitor itself. With the capacitor employed by the author, the first 4BA bolt was positioned about 15mm. back from the front panel and the second about 34mm. back from the front panel, both being in line with the spindle hole in the panel. However, there is a possibility that the 4BA tapped holes may be positioned differently with some capacitors and so measurements must be taken from the actual component which is to be used. Since the process of marking out and drilling the three holes for the 2-gang capacitor is a little critical, it is recommended that these three holes be drilled first before making the holes for the other three front panel controls.

VC1,2 has a solder tag common with its metal frame on its underside and two p.v.c. covered wires about 6in. long are soldered to this before it is finally mounted. These wires, shortened as necessary, will later be soldered to the moving vane tags of VC3 and VC4. To avoid damage, do not mount any of the variable capacitors until all the remaining holes in the case have been drilled out.

The ferrite rod coil, L2, is mounted near the upper edge of the rear panel, and a hole is required for securing its plastic clamp. SK1 and SK2 are mounted lower down, near the S1 end of the case.

Two mounting holes of about 6.5mm. diameter are required in the bottom of the case for L1 and L3. L1 is positioned behind VC4 and L3 behind VC3, and they should have the tag orientation shown in Fig. 3. Now that all the holes in the case have been drilled the components may be finally mounted. L1 and L3 are secured by the plastic nuts supplied with them. These nuts should be made finger tight only, as the plastic threads in the nuts or formers can be stripped if they are over-tightened.

#### WIRING

The converter is wired up as shown in Fig. 3. It is not advisable to solder directly to the pins of L1 and L3 as the heat of a soldering iron can cause the plastic of the coil formers to melt. In consequence a B9A valveholder is passed over the pins of each coil, and connections are soldered to the valveholder tags. Tags 3 and 4 of L1 are used as dummy anchor tags, as also are tags 2, 3, 6 and 7 of L3. TC1 is mounted by soldering one of its tags to SK2. The front section of the 2-gang capacitor is VC1 and the rear section is VC2. On L2, the tuned winding is the long winding, and the coupling winding is the short winding. All wiring should be kept reasonably short and direct.

The battery fits in the space behind S1.

#### **AERIAL AND EARTH**

The converter is designed for use with an ordinary long wire aerial consisting of some 10 to 40 metres of aerial wire strung up as high as possible and preferably well clear of buildings and other large objects. Where such an aerial is not practicable a short length of ordinary connecting wire, say about 3 metres long, strung around the walls of a room can be used instead. This will give quite acceptable results, but the performance will not of course be as good as is given with a proper outside aerial.

An earth connection is not essential, and it will probably be found to give only a modest increase in signal strengths. The best type of earth is given by a metal pipe buried outside in the soil. A lead, which is as short as possible, connects the pipe to SK2 of the converter.

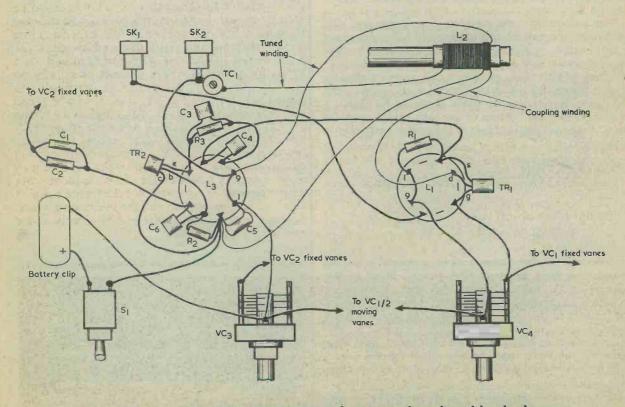
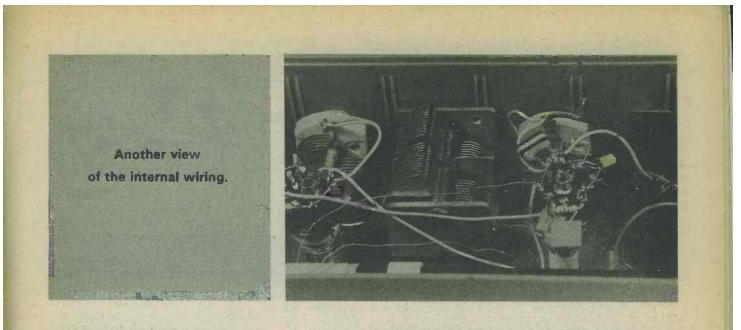


Fig. 3. Wiring up the converter. For ease of presentation, the wiring is shown spread out. In practice, wiring should be kept reasonably short and direct.



#### **ADJUSTMENT AND USE**

First tune the radio receiver to the high frequency end of the medium wave band as near as possible to 1.6MHz and search for a quiet spot on the band. It is unlikely that it will be possible, after dark, to find a spot which is completely free of any signal, but it should be possible to find a spot where there are only weak background signals which will not significantly interfere with the much stronger output signal from the converter.

L1 and L3 are supplied with their cores fully screwed in, and these should initially be adjusted so that about 2 to 3mm. of metal screw thread protrudes from the plastic of each coil former. The coil former for L2 should be at or near the end of the ferrite rod, although its precise positioning is not very critical. With the medium wave receiver close to the converter, switch on the latter and adjust VC1,2 in search of signals. If none can be found, repeat the operation



The front panel components, from left to right, are on-off switch S1, VC3, the 2-gang capacitor and VC4.

with TC1 at various settings. When a station has been found, tune it in as accurately as possible by means of VC3, and then adjust TC1 for optimum results.

After this, many stations should be received at good strength, with VC4 being adjusted to peak the signals. Next, tune in a signal with VC1,2 at about half maximum capacitance and set VC4 for about half its maximum value. Adjust the core of L1 to peak the signal. It should then be possible to peak received signals with VC4, whatever the setting of VC1,2. It may be found that there are two peak settings for VC4, with different stations appearing at each peak. The correct setting is the one with the vanes of VC4 more fully meshed. It is the image response which is being peaked at the other setting.

The positioning of converter and medium wave receiver relative to each other does not seem to be especially critical. However, this may not be the case with some radios, whereupon a little experimenting will soon indicate the positioning which gives the greatest degree of coupling. There will probably be a few relative positions where there is practically no signal transfer, and these should obviously be avoided. Note that the receiver must always be tuned to the same spot on the band when employed with the converter as, otherwise, TC1 will need to be readjusted each time the converter is used. It may sometimes happen that a station appears quite strongly on what was originally a quiet part of the band. It may then be necessary to search for a new frequency, but often the directional properties of the ferrite aerial in the receiver can be used to null the interfering signal. This simply involves rotating the receiver for minimum interference.

#### BACK NUMBERS

For the benefit of new readers we would draw attention to our back number service.

We retain past issues for a period of two years and we can, occasionally, supply copies more than two years old. The cost is 73p, inclusive of postage and packing.

Before undertaking any constructional project described in a back issue, it must be borne in mind that components readily available at the time of publication may no longer be so.

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From time to time, the subject of clandestine transmitters is dealt with in this series of articles. Several readers have expressed an interest in these transmissions – an interest shared by the writer – therefore as an opening gambit this month, we again deal with some clandestines recently logged.

deal with some clandestines recently logged. "A Voz de Resistencia de Angola" on 4950 at 1820, YL with songs in Portuguese, OM with a harangue in vernacular, YL announcer in a programme of African songs. Carrier abruptly off the air at 1900 without anthem. This is a pro-Unita transmitter which sometimes also identifies as "A Voz de Verdade".

"Radio Freedom From South Yemen" on 9960 at 1900, OM with a revolutionary song in Arabic – very militant and rousing – then suddenly off the air. All this after a preceding harangue in Arabic. This is an anti-communist and South Yemen government transmitter.

"Radio Homeland" on 15555 at 1747, two OM's with a discussion in Persian followed by a programme of Persian songs and music in typical style until 1759, at which time there were several announcements by a YL and then sign-off without any anthem. "Radio Homeland" is an anti-Persian government transmitter.

Readers wishing to log any of the above listed transmitters would be well advised to tune to the frequencies shown well in advance of the times quoted, for each logging will be seen to have been made at the signing-off time. Perhaps someone will even succeed in hearing the opening transmissions. If so, let me know.

#### AROUND THE DIAL

In which are listed some of the more interesting transmissions logged within recent weeks. Note however that due to publication delay, a few of the time and frequency details may have changed – but not many. To hear the type of programmes stated, all the reader must do is to locate the correct channels at the times quoted here. This article is penned solely for your guidance over the short wave spectrum.

#### • PORTUGAL

Lisbon on 21530 at 1610, OM with a newscast of world events. (OM = Old Man - a male announcer).

Station identification at 1613 "International Service of Radio Portugal". All in the English programme intended for the Middle East, scheduled from 1600 to 1630.

#### • **BELGIUM**

Brussels on 6010 at 1605, YL with a local newscast and details of recent events in Belgium in an English programme presumably intended for African consumption. (YL = Young Lady – female announcer).

#### SPAIN

Madrid on 9765 at 2030, time-check 'pips', OM with news of internal affairs and events in the English programme for Europe, scheduled from 2010 to 2110.

#### • WEST GERMANY

Cologne on **17875** at 0626, YL with station identification and announcements at the end of the English programme for West Africa, scheduled from 0600 to 0630.

#### • FINLAND

Helsinki on 15265 at 1940, OM with the English programmed "Northern Report". The English programme is radiated from 1930 to 2000 to both Europe and Africa.

#### • NORTH KOREA

Radio Pyongyang on 6575 at 2000, OM and YL with station identification and National Anthem various announcements after a preceding interval signal. This was followed by details of English transmissions from Pyongyang. All this in the English programme for Europe, scheduled from 2000 to 2150. Radio Pyongyang on a measured 9977 at 1642, YL

Radio Pyongyang on a measured **9977** at 1642, YL with a talk about communal projects in North Korea. This programme in English is beamed to the Middle East and Africa and is scheduled from 1500 to 1630 according to announcements.

#### SOUTH KOREA

Seoul on 6480 at 1918, YL announcer, OM song in Korean, in the English programme for Europe, scheduled from 1900 to 2000.

Seoul on 9870 at 1629, OM and YL with an English

programme for the Middle East and North Africa, scheduled from 1600 to 1700. YL with station identification at 1637.

#### CHINA

Radio Peking on 9530 at 2039, OM with a newscast of world events from the Chinese point of view in an English programme intended for European consumption, scheduled from 2030 to 2230.

#### • INDIA

AIR (All India Radio) Delhi on ±5335 at 1344, a programme of local songs and music in an English programme for South East Asia, scheduled from 1330 to 1500. YL with station identification and schedule details at 1500.

#### PAKISTAN

Radio Pakistan on 15485 at 1742, YL with the "World Service" English programme to the U.K. OM station identification in English then into an Urdu transmission also for the U.K. at 1744.

#### • ISRAEL

Jerusalem on 17685 at 1927, OM with station identification after a newscast of both local and world events in an English programme announced for Africa, Europe and North America from 1900 to 1930. A programme in French followed at 1930.

#### AUSTRALIA

Melbourne on 21680 at 0659, YL with station identification, details of frequencies, times and target areas. Time-check 'pips' at 0700 followed by a newscast of both local and world events, OM announcer. On this channel the programme is directed both to Asia and the U.K. Also logged on 9570 in parallel to the same target areas and on 15240 directed to the Pacific Area. Other announced channels were 11740, 15115, 17725 and 17870 but a rapid survey resulted in either (a) very poor reception or (b) co-channel QRM.

Melbourne on 15240 at 0558, somewhat earlier than the above transmission, OM announcements in English, including frequencies and target areas in an English programme intended for Africa and the Pacific. Time-check 'pips' at 0600 followed by a newscast in English.

#### SOUTH AFRICA

RSA (Radio South Africa) Johannesburg on 21535 at 0605, OM with station identification, announcement of frequencies and target areas followed by the news in an English programme for West Africa and Europe, scheduled from 0600 to 0700.

#### • GRENADA

Radio Free Grenada on 15105 at 2022, YL with announcements in English, then into a programme of local music and songs. At 2030, OM with a talk about African origins.

#### USSR

Radio Moscow on 15350 at 1825, OM with a programme in Swahili to Africa (where else?), typical African music and at 1830 news about African affairs from the USSR point of view.

#### • AFGHANISTAN

Kabul on 4740 at 1845, OM announcer in Pashto,

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local music and songs. This is the Home Service 1 which is scheduled from 0125 to 0330 and from 1340 to 1940.

#### • UGANDA

Kampala on a measured 5027 at 1903, OM with a newscast in English mainly about local affairs. This is the National Programme which uses English, Swahili and French at various times in the schedule, which is from (weekdays) 1300 to 2100 and from 1400 to 2100. Saturdays and Sundays there is an additional transmission from 0300 to 0545 and on Sundays the afternoon programmes commence at 1430. The power of this recently installed transmitter is 250kW.

#### • NICARAGUA

La Voz de Nicaragua on 5950 at 0155, OM with identification "La Voz de Nicaragua", announcements in Spanish then YL with a folk ballad.

#### COLOMBIA

Em. Nuevo Mundo, Bogota, on 4755 at 0054, OM with songs in Spanish, local-style music. This one radiates on a 24-hour schedule and the power is 1kW.

La Voz del Cinaruco, Arauca, on **4865** at 0114, OM with announcements in Spanish, local pops on records. The schedule here is from 0900 to a variable 0330 and the power is 1kW.

#### • COSTA RICA

Faro del Caribe (Lighthouse of the Caribbean), San Jose, on 5055 at 0130, OM with station identification in Spanish followed by light orchestral music. The schedule is from 1030 to 0400 (closing time is variable to 0430) and the power is 5kW.

#### VENEZUELA

Radio Valera, Valera, on **4840** at 0106, OM's with a discussion in Spanish on both local and world sporting events. Radio Valera operates from 0900 to 0400 (variable closing time) and the power is 1kW.

#### • BRAZIL

Radio Ribamar, Maranhao, on 4785 at 0058, OM with a love song in Portuguese. OM with station identification at 0100. The schedule is from 0800 to 0400 and the power is 5kW.

Radio Tabajara, Joao Pessoa, on a measured 4797 at 0103, YL with local pop song, OM announcer. The schedule is from 0730 to 0400 and the power is 2kW. The frequency is liable to vary, nominal is 4795.

The frequency is liable to vary, nominal is 4795. Radio Difusora Taubate, Taubate, on 4925 at 0120, OM announcer in Portuguese, OM with a ballad about the lovelorn. The schedule is from 0830 to 0300 and the power is 1kW.

Radio Cultural do Para, Belem, on 5045 at 0127, OM with local pops in typical style. The schedule is from 0900 to 0300 (variable) and the power is 10kW.

Radio Bare, Manaus, on 4895 at 0117, OM with announcements in Portuguese, local-style dance music on records. The schedule is from 0800 to 0400 (closing time is variable) and the power is 5kW.

Radio Clube do Para, Belem, on 4855 at 0110, local-style pops, OM with lots of commercials. This one operates from 0700 to 0400 and the power is 10kW.

Radio Nacional Brasiliera, Brasilia, on 15125 at 2030, OM with identification and a talk about Brazilian postage stamps in an English programme for Europe, scheduled from 2000 to 2100.

# TWO 20dB AMPLIFIERS

1 $M\Omega$  Input impedance. Flat response from zero to 1MHz. Switching option for a.c. or d.c. working.

The second 20dB amplifier, described this month, has a voltage gain of 10 times, is d.c. coupled (with a switched option for a.c. coupling at the input) and has an input impedance of about  $1M\Omega$ . Its response is flat from zero to approximately 1MHz. It is primarily intended for use ahead of an oscilloscope when measuring small direct voltages or when investigating waveforms containing relatively low frequencies which are too small in amplitude to provide a useful display with the oscilloscope on its own.

There are other uses for the amplifier and it can be employed to boost the sensitivity of an audio millivoltmeter. It can also be used in conjunction with an ordinary multimeter to provide a sensitive high impedance voltmeter. If, for example, the multimeter is switched to read 0-5 volts, an input of only 500 millivolts to the amplifier will produce full-scale deflection in the meter. The consequent sensitivity is  $2M\Omega$ per volt, as compared with the  $20k\Omega$  per volt of many standard multimeters. The amplifier can similarly boost the sensitivity of a multimeter switched to a low a.c. volts range. Note, however, that the maximum d.c. output of the amplifier is plus or minus 7 volts, and the maximum a.c. output is approximately 5 volts L.m.s.

#### THE CIRCUIT

An operational amplifier is an obvious choice for the unit, and such an amplifier appears in the circuit diagram of Fig.3. The op-amp employed is the CA3130, which has CMOS input and output stages with a bipolar section providing the main voltage gain of the device. With the value specified for the compensation capacitor, C4, the CA3130 gives a unity gain bandwidth in excess of 10MHz and a slew rate of over 20 volts per microsecond.

The op-amp is used in the non-inverting mode with the closed loop gain controlled by the negative feedback resistors, R3 and R2. The gain is equal to the sum of R3 and R2 divided by the value of R2, giving the required figure of 10 times. Both these resistors must be close tolerance types, either 2% or preferably 1%. They are specified as  $\frac{1}{2}$  watt because close tolerance resistors are normally available in this rating. rather than in  $\frac{1}{4}$  watt. The calculated level of voltage gain can, of course, only be maintained at frequencies where the op-amp open loop voltage gain is at least 10 times. The frequency at which the gain of an operational amplifier becomes inadequate to maintain the closed loop gain is given roughly by dividing the unity gain bandwidth by the closed loop gain so that the theoretical closed loop frequency response becomes 1MHz. Tests on the prototype bear this out in practice.

The circuit has dual balanced supply rails with a central earth rail, as is the convention with d.c. coupled operational amplifiers. Two small PP3 batteries provide power, and are connected to the CA3130 via voltage dropping resistors R4 and R6. The resistors are needed because the maximum supply rating for the CA3130 is plus and minus 8 volts. The supply current is a fraction under 1mA, producing a voltage drop of about 2 volts across each resistor and a nominal supply of plus and minus 7 volts.

R1 biases the non-inverting input to earth and causes the input impedance of the amplifier to be 1M\$2. This impedance reduces somewhat at high fre-

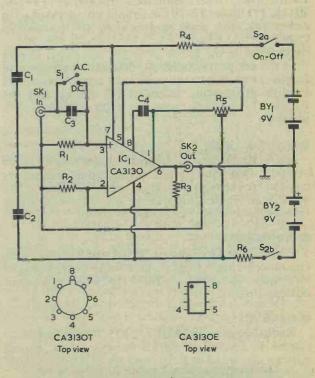


Fig.3. The d.c. coupled 20dB amplifier. This incorporates an operational amplifier type CA3130.

#### Part 2

By M. V. Hastings



The amplifier is housed in an all-metal case which provides screening. This is particularly important with the present design because of its high input impedance.

Mounting holes 6BA clear

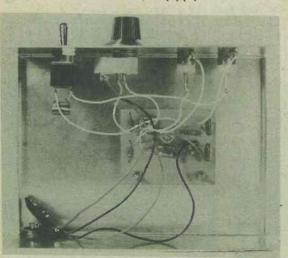
quencies because of stray capacitances and the input capacitance of the CA3130. There is a d.c. path from input socket SK1 when S1 is closed and the circuit is then direct coupled throughout. Opening S1 causes C3 to be in series with the input to provide d.c. blocking, and the low frequency response has a - 3dBpoint at approximately 10Hz.

An offset null control is required to ensure that the quiescent output voltage is zero. The offset null adjustment is given by R5. S2 (a) (b) is the on-off switch and controls the two outside supply rails. C1 and C2 are supply bypass capacitors.

#### CONSTRUCTION

The amplifier must be housed in an all-metal case to screen it from mains hum and other sources of interference. The prototype is fitted in a metal instrument case type BC1, which has approximate dimensions of 6 by  $4\frac{1}{2}$  by 2in. This case is available from Harrison Bros., P.O. Box 55, Westcliff-on-Sea, Essex, SS0 7LQ.

The front panel layout can be seen in the photographs. From left to right the items mounted on the front panel are SK1, SK2, S2(a)(b) and S1. The two



The printed circuit module is secured to the bottom of the case with two 6BA bolts and nuts, together with spacing washers.

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 $BV_2$  Pos.  $BV_1$   $BV_1$   $BV_1$   $BV_1$   $C_1$   $BV_2$   $C_1$   $C_2$   $C_4$   $C_4$   $C_4$   $C_4$   $C_4$   $C_5$   $C_1$   $C_1$   $C_2$   $C_4$   $C_4$   $C_5$   $C_1$   $C_5$   $C_5$ 

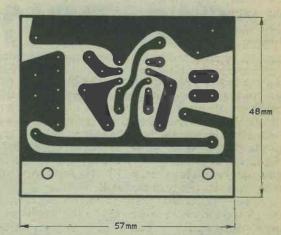


Fig.4. The printed circuit layout pattern. This will take either a CA3130T or a CA3130E.

sockets are flush mounting coaxial types. A solder tag is fitted behind the panel, under the upper securing nut of each socket. C3 is soldered to the tags of S1. The remaining components are assembled on the

printed circuit board, which is shown full size in Fig.4.

#### COMPONENTS

#### Resistors

R1 1M $\Omega$  watt 5%  $300\Omega \frac{1}{2}$  watt 1% or 2% **R**2 **R3** 2.7k $\Omega$   $\frac{1}{2}$  watt 1% or 2% **R4** 2.2k $\Omega$   $\frac{1}{4}$  watt 5% **R5** 100k $\Omega$  pre-set potentiometer, 0.1 watt horizontal R6 2.2kf 1 watt 5%

#### Capacitors

C1	0.1µF polyester type C280
C2	0.1µF polyester type C280
C3	0.1µF polyester type C280
C4	10pF ceramic or polystyrene

Semiconductors IC1 CA3130T or CA3130E

#### Switches

S1 s.p.s.t. toggle S2 (a) (b) d.p.s.t. rotary

#### Sockets

SK1 coaxial socket, flush mounting SK2 coaxial socket, flush mounting.

#### Miscellaneous

Metal instrument case (see text) Printed circuit board 2-off 9-volt batteries type PP3 2-off battery connectors Control knob Nuts, bolts, wire, etc.

The board is produced and wired up in the normal way, but it should be remembered that the i.c. has CMOS inputs. The i.c. should therefore be the last item to be soldered to the board, and the soldering iron must have a bit which is reliably earthed. Either the CA3130T (TO-99 case) or the CA3130E (8 pin d.i.l.) will fit into the printed circuit layout. The wiring



On the front panel are the input and output coaxial sockets, the on-off switch and the a.c. – d.c. input selec-tion switch.

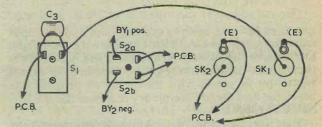
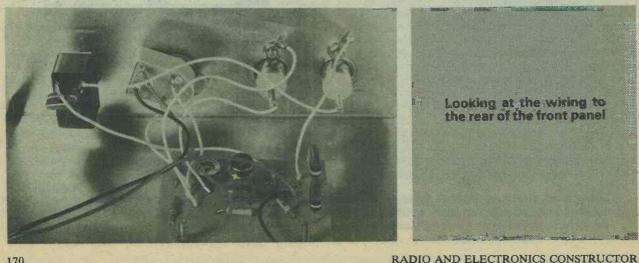
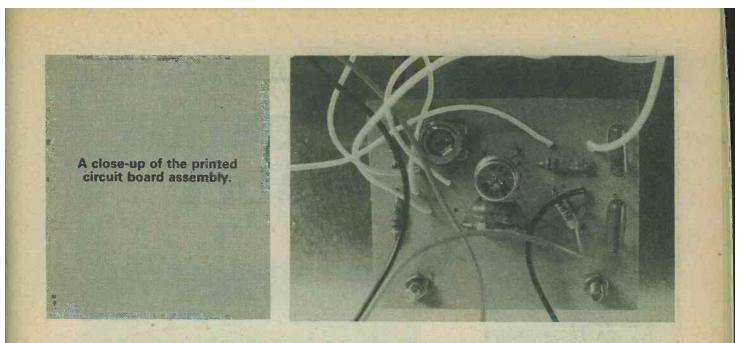


Fig.5. Wiring to the components mounted on the front panel. Use a continuity tester to confirm the appropriate tags of S2(a) (b) before wiring to this switch, as some switches may have a different tag layout

between the board and the front panel components is illustrated in Fig.5. The board is mounted to the base of the case with two 6BA bolts and nuts, spacing washers being used to keep the board underside clear of the inside of the case.

Since this amplifier has a lower bandwidth than the unit described last month, there is no need to use





screened leads inside the case for the connections to SK1 and SK2. External test leads connecting to these two sockets should, however, consist of screened wires.

#### ADJUSTMENT

The only adjustment required to the completed amplifier is the setting up of R5. Connect a multimeter set to read 0-5 volts or more d.c. across the output of the amplifier with negative to chassis. Adjust R5 so that its slider is about 45 degrees anti-clockwise of its central setting and switch on the amplifier. There will probably be a positive deflection in the meter, or a small negative deflection, and R5 is then adjusted for a zero reading. The multimeter is then switched to its lowest d.c. volts range and R5 finally trimmed for the zero reading.

The amplifier is then ready for use.

(Concluded)

# **NEW DEFENCE AID**

#### HELICOPTER NIGHT VISION

Successfully installed on a Sea King helicopter is the new stabilized night vision system pioneered by Marconi Avionics Limited. This gives what is virtually all-round vision and has been developed under contract from the UK Ministry of Defence.

The system, produced by the Marconi Electro-Optical Surveillance Division at Basildon, is for mounting beneath the nose of a helicopter and it consists of a stabilized platform bearing a night vision sensor. In the case of the Sea King installation, the sensor is a Marconi Avionics V325 Intensified Isocon camera, which is already in production and is in service with the Royal Air Force. It can produce useful television pictures at light levels down to overcast starlight.

A very novel feature of the use of the night vision sensor is that it can be rapidly directed, from side to side and downwards, in synchronism with the pilot's head movement. By projecting an image of the night scene into the pilot's eye via his helmet-mounted display, he can "see" in the dark wherever he happens to be looking, including straight downwards.

The system has an exceptionally fast response (over 100 degrees per second) to directional commands and is highly stabilized against the effects of helicopter vibration and its movements in pitch and yaw. This exceptional performance means that the system can follow virtually every movement of the pilot's head, whether fast or slow. As a result, pilots soon become used to the new equipment, and very rapidly gain confidence in operating with it.

The platform has been constructed to the stringent standards already proven with the highly successful "Heli-Tele" daylight viewing system, now in service with the British Army and the security services of several other nations.

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# RADIO DISTORTION FAULT

You don't have to know what's inside an i.c.!

"What," asked Dick cheerfully, "is white on the top and bottom, green in the middle and jumps all round the room?"

Smithy glanced at him malevolently.

"Look," he prounounced sternly, "there's just an hour to go before we shut up the shop for the weekend, and we've only got this single a.m.-f.m. portable to clear up before we leave. Which means that I don't intend to waste time with these ridiculous jokes of yours.

"Shall I give you a clue?"

"I don't even want a clue!" "It could," said Dick unabashed, "alternatively be brown on the top and bottom instead of white. But it would still be green in the middle and it would still jump all round the room.

Smithy put the portable radio on his bench and studiously ignored his assistant.

"Give up?" repeated Smithy irately. "I'm not going to start!" "I'll tell you then," said Dick jubilantly. "It's a frog sand-wich!"

#### I.C. AMPLIFIER

An expression of total incomprehension spread over Smithy's face.

"Is that your joke?" "That's it," confirmed Dick. "Good, isn't it?"

"I think it's dreadful. Now,

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for goodness' sake let's get on with finding the fault in this radio."

"What is it," asked Dick, "that's white on the top, white

"We will now," interrupted Smithy, "get on with this set. Okay?"

"Well yes, all right then," replied Dick, bringing his thoughts back to the more important matters of the moment. "What's wrong with it?

Smithy inspected a label attached to the carrying handle of the radio.

"This just says 'Distortion'," he remarked. "Let's hear what it sounds like."

He pressed the medium wave button on the radio and swung its tuning control across the scale. The set was quite lively and picked up a number of signals without any significant second channel whistles. The volume was at a level suitable for listening in a quiet room and there was no noticeable distortion. Smithy tuned in a musical programme and turned up the volume. Distortion at once became evident, increasing in proportion with the loudness of the sound from the speaker.

Smithy pressed the long wave button and tuned to the Radio 4 signal on 1,500 metres. Exactly the same kind of distortion was present here. Finally,

he pulled out the telescopic aerial, pushed the f.m. button and tuned in the local v.h.f. signals. There was no change in the distortion.

"That distortion must be being introduced in the a.f. stages," volunteered Dick.

'I wouldn't argue with that at all," stated Smithy, "unless we've got a really weird fault we wouldn't get distortion in both the a.m. and the f.m. sections before the volume control. Besides, the a.f. signal going to the volume control must almost certainly be all right because there's no distortion at low volume levels. Would you like to start getting the back off the set while I get the service manual out? With a fault like distortion it's a good idea to have the circuit of the receiver in front of you, and it will often save a lot of time in looking for the snag. Oh, and check battery voltage on load, of course.

As Smithy walked over to the filing cabinet, Dick removed the battery cover and checked the battery voltage with the receiver switched on. His testmeter indicated a satisfactory voltage slightly in excess of 9 volts. He then started to remove the set back. Whilst he was doing so, Smithy returned with the service manual and opened it out at the circuit diagram. He quickly located the section showing

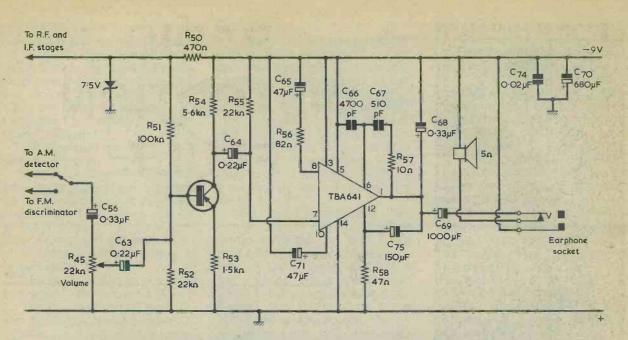


Fig.1. The a.f. stages of the a.m.-f.m. radio examined by Dick and Smithy. Apart from a slight simplification at the a.m.-f.m. switch section and the volume control, this is taken from the circuit of the Ferguson Model 3182 Mains/Battery Portable Radio and shows the same R and C numbers. The  $0.22\mu$ F and  $0.33\mu$ F electrolytic capacitors are tantalum.

the a.f. stages, and studied it silently. (Fig.1.) "Oh no," breathed a voice at

his right ear.

Startled, Smithy turned his head to see his assistant gazing unhappily over his shoulder at the circuit.

"What are you oh-no-ing about?"

"Those a.f. stages," wailed Dick. "There's an i.c. at the output!"

"Blimey," snorted Smithy. "We've had integrated circuits in domestic equipment for nearly the last ten years. Why should you suddenly start moaning about them now?"

"I've always moaned about them," complained Dick. "Just look at the circuit we've got here. We've got a triangle representing the i.c. amplifier, and a whole lot of resistors and capacitors connected to its pins. What we don't know is what exactly is inside that triangle!"

#### **COMPONENT FUNCTIONS**

"That's no problem," said Smithy. "With a fairly simple integrated circuit such as the a.f. amplifier one we've got here it's quite adequate to have just a rough general idea of

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what's going on inside the i.c. without bothering about precise circuitry. I'll agree that things tend to get a bit mindbending when we get on to i.c.'s like microprocessor chips, and with these the likes of you and me have just got to take the manufacturer's word that if we connect the chip up correctly and put in the right inputs we'll get the proper outputs. But, as i say, there's no need to have such an approach so far as the simpler\_i.c.'s are concerned.'

The Serviceman turned the pages of the service manual and examined the Components List for the radio.

"Here's something interesting," he went on. "The maker of this radio has done us a favour by not only listing the components but also stating what their functions are. But before we look at these there are some obvious facts about this audio i.c. which are staring us in the face." "What are they?"

"That pins 3 and 5 of the i.c. connect to the negative supply rail and that pin 14 is the positive supply input. Also, that pin 1 is the output pin.'

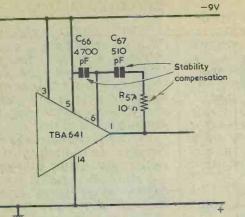
"Ah yes," agreed Dick, "there isn't much deduction needed there! The fact that the negative rail is drawn in this circuit above the positive rail can be a bit confusing at first when you're used to having the two rails the other way round."

'That's true," said Smithy "but you often meet that sort of thing in these radio circuits. Now let's look at some of the components.

We can start off with C66, C67 and R57. All these are described in the manual as giving stability compensation." (Fig.2(a).)

That will be like the compensation components which are needed with some op-amps," put in Dick. "The idea is that the capacitors roll off the upper frequencies." "Exactly," confirmed

Smithy. "If the i.c. was allowed to amplify at frequencies which were too high it would be virtually impossible to prevent stray feedback and the whole thing would burst into oscillation. So, when you find relatively low value capacitors, or relatively low value capacitors and resistors in series, connecting to the pins of an unfamiliar audio amplifier i.c. you can be quite certain that they're intended to provide



(ā)

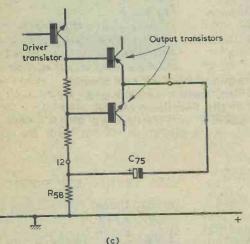


Fig.2(a). Relatively low value capacitors around the audio i.c. provide frequency compensation.

(b). A high value capacitor from the output to another pin, in a circuit such as is shown here, provides bootstrap coupling.

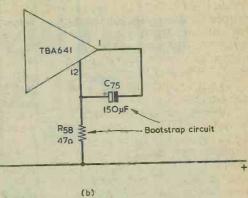
(c). The internal bootstrap circuitry will have the basic form illustrated here.

(d). External negative feedback components. The audio gain of the i.c. is controlled by the value of the resistor.

(e). Inside the i.c. an internal resistor will couple back from the output to an early inverting input.

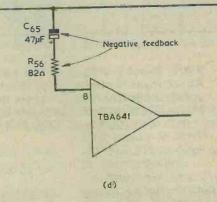
frequency compensation." "What would happen if you

had a fault there?" "Well," said Smithy, "the most likely thing is that you'd get an open-circuit in a compensation loop. If this opencircuit caused the i.c. to go into violent oscillation at a supersonic frequency you might get a loud hiss from the speaker, or you may even hear nothing at all from it. At the same time, the i.c. will quite possibly draw an excessively high current from the power supply. Should the oscillation be of a mild nature the i.c. might work reasonably well on high level a.f. signals but may burble away quietly to itself with low input signals or with no input

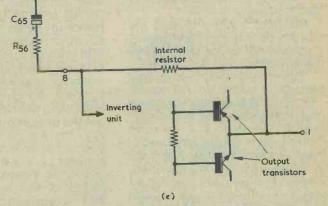


-9V

-91



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signal. Whenever you get symptoms like that with an unknown audio i.c. the compensation components and their connections should be amongst the first things you should check."

"There's another capacitor," said Dick, "which connects the output pin to pin 12 of the i.c. But this one's an electrolytic with a value of  $150\mu$ F." (Fig.2(b).) "That one's bound to be a

"That one's bound to be a bootstrap coupling capacitor," said Smithy, "and you can recognise it by the fact that it connects to the output and has the

sort of high value you'd expect in an a.f. coupling circuit feeding a fairly low impedance. Inside the i.c. the capacitor will couple to the collector load of the driver transistor which feeds the output transistors in a standard bootstrap arrangement." (Fig.2(c).)

"What would happen if you had trouble there?"

"Well," replied Smithy, "if the capacitor went open-circuit the i.c. wouldn't give as much amplification to one set of half-cycles as it does to the other and you'd get distortion when the signal went above a certain level."

"That's what we've got with this radio," commented Dick. "True," confirmed Smithy,

"True," confirmed Smithy, "so we'll check the bootstrap capacitor in a minute. It's more difficult to predict what would happen if the capacitor went short-circuit, but there would most probably be a high loss of output power with distortion at all levels. There would probably be no output at all if the  $47\Omega$  resistor coupling pin 12 to the positive rail went opencircuit because there would then be no collector supply voltage for the internal driver transistor."

Dick turned his attention to another part of the circuit.

"There's an  $82\Omega$  resistor and a  $47\mu$ F capacitor going from pin 8 of the i.c. to the negative rail," he announced. "What are they for?" (Fig.2(d).) "The resistor," said Smithy,

"The resistor," said Smithy, looking down at the service manual, "will be for gain control and the capacitor for d.c. blocking. The service manual refers to them as 'negative feedback' and as 'negative feedback and d.c. blocking' respectively."

"How do they work?"

"There'll be an internal resistor inside the i.c. from the output to an inverting point at or near the input of the internal amplifier. This and the external resistor then complete a standard negative feedback loop which allows the i.c. gain to be varied by the external resistor. If that external resistor goes low in value the amplifier gain will increase and if it goes high in value the amplifier gain will reduce." (Fig.2(e).)

"What happens if the resistor or the capacitor go open-

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#### circuit?"

"Theoretically, the amplifier gain drops to unity but in practice the i.c. might go unstable because of the lack of a low impedance between the feedback pin and the supply rail. The manufacturer of the i.c. will normally specify upper and lower limits for the resistor value and the set-maker using the i.c. will, obviously, employ a resistor whose value is within these limits."

"So," said Dick slowly, "if I come across an unfamiliar audio amplifier i.c. and I see that one of its pins connects to one of the supply rails via a fairly large value electrolytic and a small value resistor, can I expect the resistor to be a component which controls the amplifier gain, with the electrolytic giving d.c. blocking?" "You can," stated Smithy.

"You can," stated Smithy. "Incidentally, the resistor value usually lies between some  $50\Omega$  and  $200\Omega$ ."

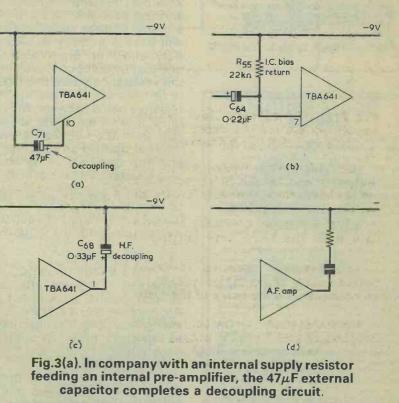
#### **BYPASS CAPACITOR**

"Hey," said Dick cheerfully, "this a.f. amplifier i.c. is not so difficult after all. We don't know exactly just what is lurking inside the i.c. but we do know what jobs the external components connecting to it are supposed to do!"

"Which," chimed in Smithy, "makes fault-finding a lot easier when a snag develops in any of those external components. Well now, there's another electrolytic coupling an i.c. pin to the negative rail. That's the  $47\mu$ F component connecting to pin 10." (Fig.3(a).)

"And what's that for?"

"It's a decoupling or bypass capacitor," stated Smithy, glancing for confirmation at the service manual. "Inside the i.c. there will be a decoupling supply resistor feeding a preamplifier stage and the decoupling is completed by the



(b). Audio i.c. input bias arrangements tend to vary between different i.c. types, but a resistor coupling to the negative rail is one of the more common circuits.

(c). The  $0.33\mu$ F capacitor between the i.c. output and the negative rail maintains a fairly low output load impedance at the higher audio frequencies.

(d). A more common approach is to couple the i.c. output to one of the supply rails via a capacitor and very low value resistor in series.

#### ELECTRONIC

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Giant pack, 25 each (1,425) £13.60 BNC Cable mtg socket  $50\,\Omega$  25p; 5 plus: 20p; PL259 UHF Plug and Reducer 75p; 5 plus: 67p; SO239 UHF Socket panel mtd. 60p; 5 plus: 50p; Nicad rechargeables physically equiv. to zinccarbon types: AAA (016) £1.80; AA(U7) £1.30; C(U11) £3.35; PP3 £5.55. Any 5 plus: less 10%. Any 10 plus less 20%.

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external 47µF capacitor."

"Why do you need decoupling there?"

"Mainly to reduce hum if the i.c. is run from an a.c. mains power supply. If the i.c. is powered by a battery you can often leave out that external decoupling capacitor without running into any difficulties. Now, let's look next at the  $22k\Omega$  resistor connecting to the input of the amplifier. The service manual refers to this as an 'i.c. bias return' resistor."

Smithy pointed to the resistor in the circuit. (Fig.3(b).)

"Now, most audio output amplifier i.c.'s," he continued, "require a resistor between the input pin and the negative rail to set up the input bias conditions, and you can expect trouble if the resistor shifts seriously in value. However, you may encounter input bias arrangements which are different from this. The golden rule here is to assume that whatever resistor or resistors connect between the i.c. input and any voltage supply points will have a primary or secondary effect on input biasing, and that troubles could result if the resistance value or values is not correct. If there are no resistive paths between the i.c. input and either of the supply rails then the i.c. has its own internal biasing arrangements and the question of external bias resistance does not arise.

"There's another electrolytic," said Dick. "There's a  $0.33\mu$ F electrolytic between the output pin and the negative rail."

Smithy frowned.

"That's a little unusual," he remarked. "Let's see what the service manual says about it. Ah, here we are. It's a tantalum electrolytic and it's described as giving 'high frequency decoupling'."

"What's it for?"

"It's to maintain a low impedance at the output as audio frequency increases," pronounced Smithy, "whereupon it counteracts the effect of rising impedance in the speaker and aids stability. Just a minute."

Smithy took a pen out of his pocket, pulled his note-pad towards him and carried out a quick calculation.

'That 0.33µF capacitor," he

stated, "will have a reactance of about  $100\Omega$  at 5kHz, and so it doesn't hold the impedance all that low at high audio frequencies. But it will hold it low enough to ensure that there isn't any risk of high frequency instability."

"You said just now," Dick reminded him, "that the  $0.33\mu$ F capacitor was slightly unusual. Why's that?"

"It's because you usually have a capacitor and resistor in series at the output to reduce high frequency impedance, instead of having a capacitoron its own. The capacitor and resistor form what is called a Zobel network and typical values are around  $0.1\mu$ F to  $0.3\mu$ F for the capacitor, with the resistor having quite a low value of around 1 $\Omega$ . Incidentally, if the i.c. doesn't have a Zobel network or a single capacitor coupling its output to one of the supply rails then you can safely assume that the i.c. is capable of working quite happily without it." (Fig.3(d).)

Smithy looked down at his watch.

"Ye gods," he gasped. "That's twenty minutes gone already, and we haven't even started looking for the fault in this radio!"

"Not to worry, Smithy," Dick consoled him. "At least you've told me how to work my way round an audio amplifier i.c. even when I've got hardly a clue as to what exactly is inside it. And if we do find the fault in this radio before knocking-off time I'll stay on afterwards if necessary to do the actual repair."

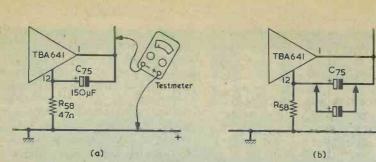
#### FAULT FINDING

"Very well," said Smithy briskly. "We'll start off with obvious fault-finding checks first. Can you get at the printed board?"

"I can."

"Right then, testmeter ready! Switch on with the volume turned low and check the voltage between chassis and the output pin of the i.c."

Dick selected a voltage range on his testmeter, turned on the radio and checked the voltage between the circuit points referred to by Smithy. (Fig.4(a).)



#### Fig.4(a). Dick first measured the voltage at the output of the audio i.c.

(b). He next checked the bootstrap capacitor by temporarily bridging it with another capacitor of similar value.

"I'm getting a reading of about 4.4 volts." "Good," stated Smithy.

"That's near enough to central voltage for us to assume, for the time being at any rate, that the input bias circuit for the i.c. is all right. Bootstrap circuit next! You may remember I said that an open-circuit bootstrap capacitor could possibly cause the distortion. So that's the next thing for you to check."

"How do I do that?"

"Turn up the receiver volume to give a distorted output and then bridge the capacitor with another 150µF capacitor.

Dick looked through a box of spare components on Smithy's bench.

"I can't find a 150µF," he stated after a few moments. "Will 100µF do?"

"Yes, of course," said Smithy impatiently. "Come on!"

Dick turned up the volume of the receiver and applied the 100µF capacitor across the  $150\mu F$  capacitor on the printed board. There was a click in the speaker as he connected the 100µF capacitor into the bootstrap circuit but there was no alteration in the distortion level. (Fig.4(b).) "Okay," said Smithy. "Well,

that seems to eliminate the bootstrap circuit. Turn the volume down and let me think for a moment."

"Perhaps the i.c. is duffy," suggested Dick, as he reduced the volume level of the radio. "Perhaps it is," agreed

Smithy, "but it will still be the last thing we suspect."

"Why's that?"

"First, because i.c.'s are usually nearly as reliable as

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discrete silicon transistors, and secondly because changing it is too big a job to contemplate at this stage. There's a third reason, too.

"What's that?"

"We don't have a replacement in stock! Now let's recap. The distortion is appearing after the volume control and, for the moment, we'll say that there's no fault which is blantantly obvious around the i.c. So we'll do some quick checks around the transistor which precedes it. Testmeter ready again! Check the voltage between the transistor base and chassis."

Dick applied the testmeter prods to the printed board once more. (Fig.5(a).)

"There's about 1.2 volts here," he announced.

"That seems reasonable," said Smithy slowly. "Try the voltage between the collector and chassis next."

"Okeydoke."

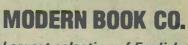
Dick once more put the meter prods onto the board. (Fig.5(b).) "I'm getting about 8.9 volts

here," he said.

A beatific smile spread across Smithy's lips and he

rubbed his hands together. "Heh, heh," he cackled, tak-ing on the eldrich tones of a witch. "Blood! I smell blood!"

And there was blood, indeed, to smell. The next checks showed that there was only 0.2 volt across the collector load resistor and that this resistor had its true and proper value of 5.6k $\Omega$ . Whereupon anyone armed with a pocket calculator and having a glimmering of Ohm's Law will at once tell you that the transistor



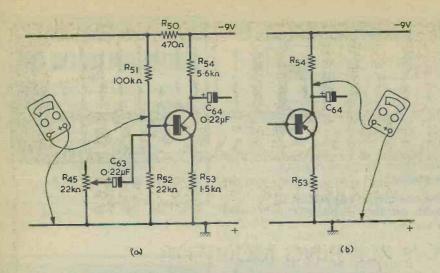
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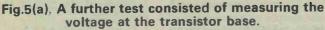
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(b). This was followed by a check of the transistor collector voltage.

collector current was equal to 0.2 volt divided by  $5.6k\Omega$  or 0.035714mA to 5 significant figures or, if you want even greater accuracy, 35.7143 $\mu$ A to 6 significant figures. The only remaining resistor to check was the  $1.5k\Omega$  transistor emitter resistor, and the value of this had risen to some 10 times its nominal value. At the reduced collector current available to the transistor, it simply had not been able to handle signals above a low level without introducing distortion.

Dick did not even have to remove the board to fit a new  $1.5k\Omega$  resistor. He merely had to snip the leads of the faulty resistor close to its body and then quickly solder the new resistor to these leads. It could be argued that Smithy had devoted all his time to explaining the component functions around the audio amplifier i.c., whereas the fault in *this* particular radio was in a transistor stage that had nothing to do with the integrated circuit. But then who ever said that, in servicing, the fault has to lie in the stage you're devoting all your attention to?

#### LAST WORDS

"That's great," grinned Smithy happily as Dick switched off the radio, which now had a completely acceptable performance so far as distortion was concerned. He consulted his watch. "Blimey, there's still a couple of minutes to go before packing-up time."

"Good," said Dick quickly. "Then you can tell me what's white on the top, white on the bottom and blue in the middle."

"Trust you to spoil everything," sighed Smithy. "All right, what *is* white on the top, white on the bottom and blue in the middle."

"It's a frog sandwich," replied Dick triumphantly. "It's a frog sandwich which has been left too long in the freezer!"

**BOOK REVIEW** 

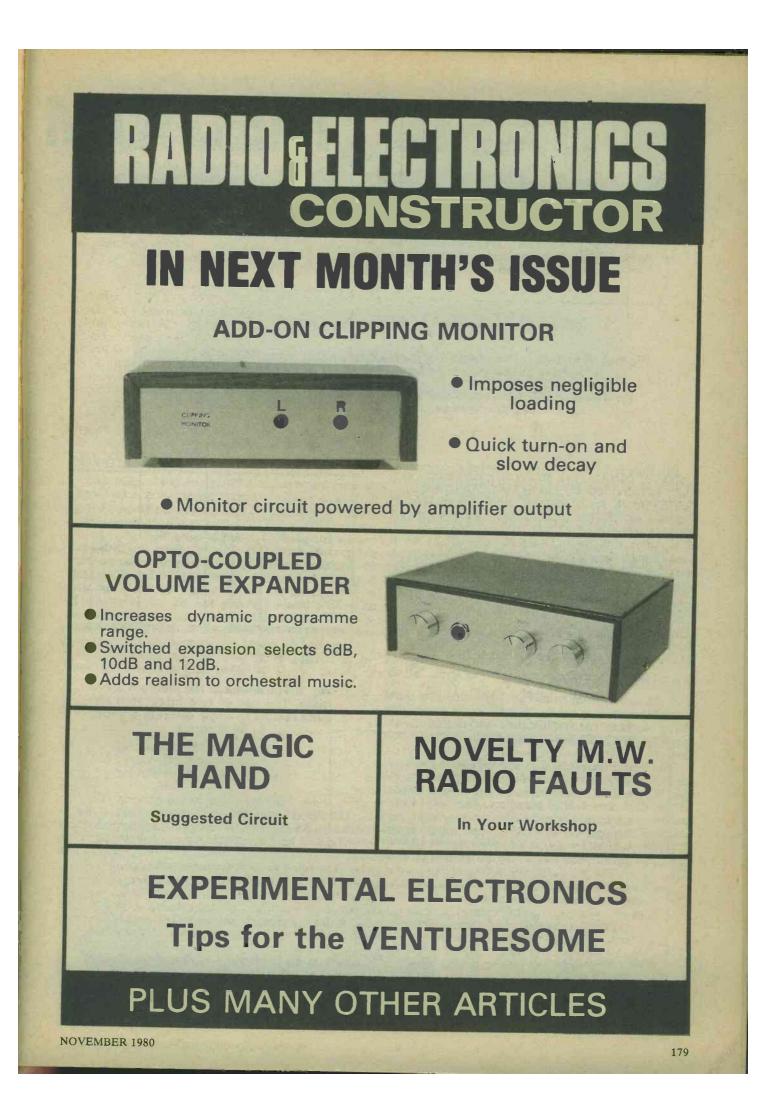
ELECTRONIC MUSIC PROJECTS. By R. A. Penfold. 110 pages, 180 x 105mm. (7 x 4in.) Published by Bernard Babani (Publishing) Ltd. Price £1.75.

Electronic music effects and sound generators are very popular and it is possible, in many cases, to produce impressive results with the aid of quite simple circuitry. In this book, R. A. Penfold gives details of some 24 music projects which can be readily assembled by the home constructor.

The first chapter covers guitar effects units, and describes two types of tone booster, a fuzz box, a waa-waa unit, an automatic waa-waa unit, and a sustain unit incorporating an opto-isolator. General effects are described in the second chapter, which deals with a tremolo generator, a reverberation unit, an automatic phasor, an audio modulator and an envelope shaper.

The third chapter is devoted to sound generator projects, and includes white and pink noise generators, a glissando tone generator with switched output filters, a vibrato oscillator and a stylus organ. In the fourth and last chapter we find details of an electronic guitar tuning fork, a guitar practice amplifier, a metronome with visible flash, automatic and voice operated faders, a simple sound mixer and a sound-to-light unit with which the apparent speed of a moving light display increases with the volume of the music being played.

This listing shows the wide range of projects dealt with. Each is presented in circuit form with a components list and accompanying text describing circuit operation and giving advice on constructional points. This is another good book from a well-known and prolific author.



# BASIC MEDIUM RADIO By T. F. Weatherley

2 A 2 4 4 6 5

Simple single band loudspeaker radio.

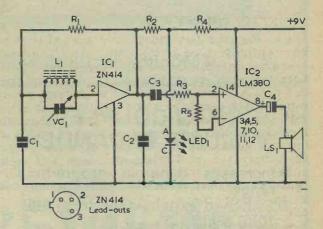
This little radio was originally devised as a practical project in an electronics course to introduce modern components and constructional techniques. The two integrated circuits used have been available for some time but they do serve to illustrate the premise that quite sophisticated equipment can be built with very few discrete components.

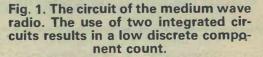
The heart of the receiver is the popular ZN414. This integrated circuit contains, to quote the specification, "a ten transistor tuned radio frequency circuit using C.D.I. technology to provide a complete r.f. amplifier, detector and a.g.c. circuit on one chip". The chip is housed in the standard TO18 package and could be mistaken for a BC108. The a.f. amplification in the receiver is provided by an LM380.

#### **CIRCUIT OPERATION**

As can be seen from Fig. 1 the circuit is quite straightforward. It can be thought of as comprising three sections: r.f. amplification and detection, a.f. amplification and a power supply. The last is a 9 volt battery.

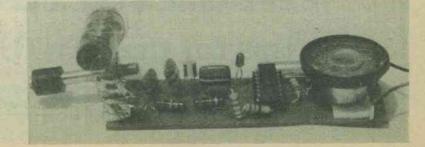
The ferrite aerial coil, L1, and VC1 form the inout tuned circuit, with C1 providing an earth return. R1 and R2 are the ZN414 bias and load resistors respectively. C2 is an r.f. filter capacitor. The ZN414 produces an a.f. output of some 30mV r.m.s. and this is coupled through C3 and R3 to the input of the LM380. A  $10M\Omega$  resistor, R5, controls the LM380 gain, and the output is coupled to the speaker through C4.

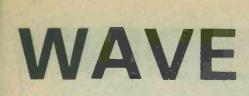


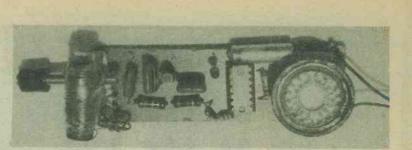


The ZN414 requires only 1.5 volts and this is provided by R4 and LED1. The voltage dropped across the l.e.d. is held at a stable value of about 1.5 volts, and the l.e.d. also indicates that the receiver is turned on. The full 9 volts is applied to the LM380.

A side view of the printed board assembly.







The completed receiver. The prototype employed a very small speaker, but any size speaker may be used.

Inexpensive design requiring few components.

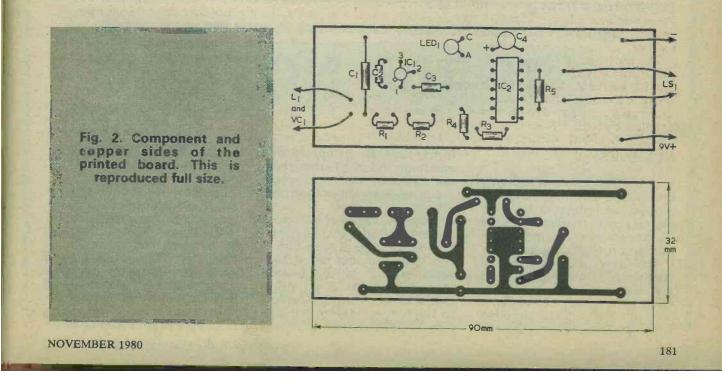
#### CONSTRUCTION

The radio is built on a printed circuit board measuring 90 by 32mm., or approximately  $3\frac{1}{2}$  by  $1\frac{1}{4}$ in. This board is shown full size in Fig. 2. After the board has been prepared, the components are fitted and soldered into place as shown. The resistors, and capacitor C4, can be mounted horizontally or vertically as space permits. IC2 can either be soldered to the board directly or mounted in an i.c. holder. In the original, Veropins were used for the connections to the battery, the speaker and to L1 and VC1. The author was lucky enough to obtain a 1in.  $8\Omega$  loudspeaker locally and this was positioned on the board as shown in the photographs. A larger  $8\Omega$  loudspeaker can, of course, be used and this will need to be mounted away from the board.

#### FERRITE AERIAL

The ferrite aerial rod has a diameter of 3/8 in. and is only about 1 in. long. This was found by experiment to be the shortest length which still enabled the project to be worth-while. The 1 in. length was obtained from a longer rod by the usual method for breaking ferrite rod. First, a groove is filed all around the rod at the point where it is to be broken with a V-edge file, and the body of the rod is then gently tapped against the edge of a wooden bench or table. The winding consists of 50 turns of 32 s.w.g.

The winding consists of 50 turns of 32 s.w.g. enamelled wire close-wound to form a coil about  $\frac{1}{2}$  in. long at the centre of the rod. The wire gauge is not very critical and slightly thicker or thinner winding wire would also be suitable. It is helpful to fix some Sellotape, sticky side out, on the rod first, since this



#### COMPONENTS

Resistors (All <sup>1</sup>/<sub>4</sub> watt 5% unless otherwise stated) **Ř**1  $100k\Omega$ **R2**  $1k\Omega$ **R3**  $1M\Omega$ 560Ω **R**4 **R**5 10MΩ 10% Capacitors 0.01µF polyester, type C280 C1 0.1µF polyester, type C280 C2 C3  $0.1\mu$ F polyester, type C280 C4  $100\mu$ F electrolytic, 10 V. Wkg. VC1 60-200pF trimmer (see text) Inductor

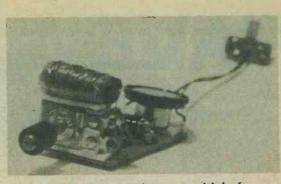
L1 see text

Semiconductors IC1 ZN414 IC2 LM380 LED1 red l.e.d.

SpeakerLS18Ω speaker (see text)Miscellaneous9 volt batteryPrinted board materialsI.C. holder, 14 way d.i.l. (if required)Control knob adaptor (see text)

holds the turns in place. The whole winding is finally covered with more tape, and the enamel is scraped away from the two coil ends so that these may be soldered to the tags of VC1. With the prototype VC1 is a mica compression

With the prototype VC1 is a mica compression trimmer having a value of about 60pF minimum and about 200pF maximum. It can be adjusted for station selection by means of a screwdriver. However, this

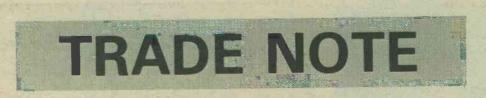


Looking at the trimmer which functions as the tuning control. A homemade adaptor allows this to be adjusted by a control knob.

does not appear to have universal appeal and so a mechanical adaptor which allows a tuning knob to be employed was fabricated. The adjusting screw for the trimmer was 6BA, and this was removed. A length of rod having a diameter of  $\frac{1}{4}$  in. and a central hole capable of taking 6BA studding was located in the spares box. The head was cut from a  $\frac{1}{2}$  in. 6BA bolt, and one end of the threaded section affixed inside the rod. The rod assembly was then screwed into the trimmer in place of theoriginal adjusting screw and an ordinary control knob fitted at the other end of the  $\frac{1}{4}$  in. rod. Similar means of making up an adaptor which allows the trimmer to be adjusted by means of a control knob should suggest themselves to the constructor.

The completed receiver has quite an impressive performance, despite its simplicity. The prototype can pull in a dozen or more stations at the author's home in East Anglia and the a.g.c. provided by the ZN414 makes listening a pleasure.

Current consumption from the 9 volt supply at normal volume levels is around 30mA, and so a fairly large battery is preferable if long listening periods are envisaged.



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# SATISFACTORY SOLDERING

ES FOR NEWGOMER

By D. Snaith

8 tips to start you soldering successfully.

Quite a few newcomers to the home-constructor hobby run into difficulties when they attempt to make their first soldered joints. The ability to solder is essential if the hobby is to be truly rewarding and, if you have never soldered before, you do need a little practice to get the "feel" of things. About half an hour's experience in soldering together odd wires and tags should be more than adequate, and after this you should be able to tell with some reasonable certainty whether or not you have completed a good joint as soon as the solder has set solid.

#### SOLDERING FACTS

Fig. 1(a) shows a tinned copper wire which is to be soldered to a tag. Although both the wire and the tag may look clean, they will in fact almost certainly be covered with a thin oxide coating. The molten solder has to get past that oxide coating if it is to make a proper joint with the pure metal underneath, and for this reason we apply not only solder to the joint but also *flux*. The flux is a chemical which, when heated, breaks down the oxides. We use cored solders for electronic work, and these consist of solder wires with, typically, five cores containing a suitable flux. So, when we apply the solder we automatically apply the flux as well.

In Fig. 1(b) we apply the soldering iron tip to the work (i.e. the items to be soldered together) so that, preferably, the tip is in contact with both pieces. At the same time we apply the solder to the iron tip and the work. What happens first is that the solder melts at the iron tip and the heated flux at once gets to work on the oxides in the immediate vicinity, allowing the molten solder to "wet" the work pieces. Heat from the iron tip travels quickly through the molten solder to the work pieces, with which the solder is now in

intimate contact. The rate of heat flow is faster than would be given by just placing the iron tip on its own on the work. We apply a little more solder, which now flows over the work pieces and then we remove the solder and the soldering iron. If the solder *flows* over the work pieces we have made a good joint. The completed joint should be smoothly covered with just enough solder to do the job. There will almost certainly have been a little more flux than was needed, and what is left sets hard on the outside of the solder.

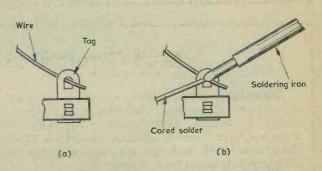


Fig. 1(a). The wire shown here is to be soldered to the tag (b). The tip of the hot soldering iron and the solder are both applied at the same time to the work pieces. The molten solder first speeds the transfer of heat from the iron to the work pieces, and then flows over the pieces to produce the joint.

#### SOLDERING SNAGS

Now, here are the things which can go wrong and produce an unreliable solder joint.

1. The soldering iron is applied for too short a time. The solder may run over the work and even cover it but, because the work pieces were not raised to a sufficiently high temperature, has not made a proper joint with them. The result is a rough solder outline, and the joint looks as though you've been trying to solder with sealing wax.

2. The soldering iron has been applied for too long. This may not result in a poor joint but it can cause overheating of components whose lead-out wires form part of the joint. One of the main reasons for initial soldering practice is to find how to arrive at a happy medium between this snag and snag No. 1.

3. There is too little solder in the joint. This will be visually obvious. There should be enough solder to give a sound mechanical joint.

4. Too much solder in the joint. Another conflicting snag, and again visually obvious. A joint with too much solder may in fact be sound, but big blobs of solder look unsightly. What's more, they take too long to cool down after the iron has been removed, with possible overheating of components in consequence. Also, there is a greater risk of snag No. 5 occurring.

5. The work pieces are moved just before or at the instant when the solder sets. Solder does not go direct from the liquid to the solid state as it cools, but passes through a "pasty" condition. If the work pieces are moved when the solder is pasty a poor joint results.

6. The work pieces are too oxidised or dirty. All semiconductor device leads, and those of nearly all modern resistors and capacitors should solder straightaway when using cored solder. But the flux may not be able to break down high levels of oxide. Before attempting a joint with wire which looks dirty or highly oxidised, scrape off the dirt or oxide with a sharp knife and then tin the lead with cored solder. After this it will solder directly.

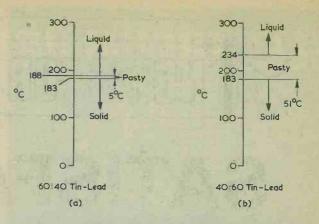


Fig. 2(a). A solder alloy consisting of 60% tin and 40% lead exhibits a very low temperature range in which it is in the pasty state. (b). A 40:60 tin-lead alloy goes from solid to pasty at the same temperature as does 60:40 solder, but the pasty range extends to a very much higher temperature before the solder becomes liquid.

7. Dirty soldering iron bit. Always keep the iron tip nice and shiny with molten solder. This allows heat to travel quickly to the work pieces.

8. Wrong solder. Avoid "electrical grade" or 40:60 solder like a plague. Always use a 60-40 tinlead alloy or Multicore "Savbit" (which has a little copper added). Fig. 2(a) shows the temperatures at which 60:40 alloy is solid, pasty and liquid, whilst Fig. 2(b) shows the temperatures for the same states in 40:60 alloy. The 60:40 solder is pasty from 183deg. C to 188deg. C, a range of 5deg. C only which in practice is just right. The 40:60 alloy is pasty from 183deg. C to 234deg. C, a range of no less than 51deg. C. The 40:60 solder is hopeless for the small closely spaced solder joints which are encountered in modern electronics.

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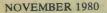
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(Continued on page 189)





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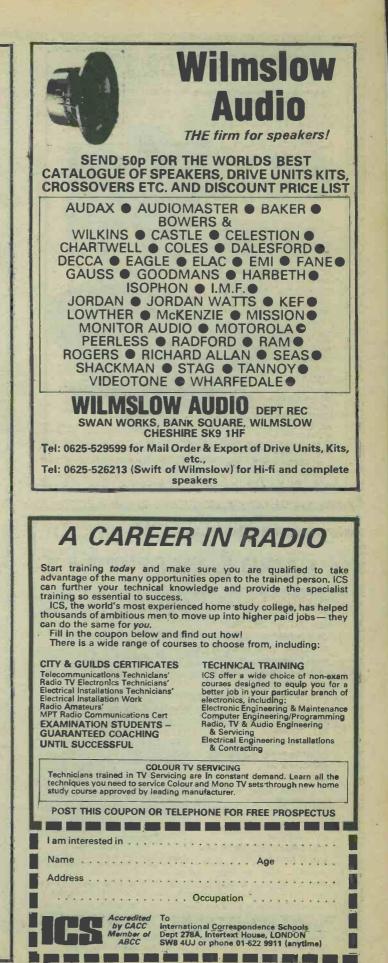
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(Continued on page 191)

#### SMALL ADVERTISEMENTS (Continued from page 190)

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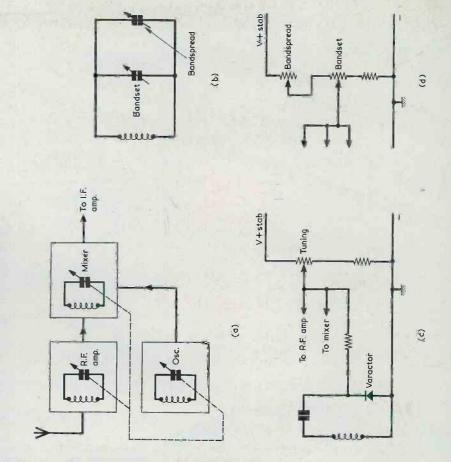
ELECTRONICS DATA

# BANDSPREADING

The r.f. amplifier, mixer and oscillator stages of a short wave superhet appear in (a). The r.f. amplifier and mixer input tuned circuits, and the oscillator tuned circuit, are all tuned by a 3-gang variable capacitor. Since tuning at short wave frequencies is sharp the 3-gang capacitor has to be adjusted through a high ratio tuning drive which must have a positive action free of mechanical "backlash".

An alternative approach towards ease of tuning is to connect a small value variable capacitor in parallel with each main tuning capacitor, as in (b). The main tuning capacitor is then the "bandspread" capacitor and the small value capacitor the "bandspread" capacitor. The bandset capacitor is set roughly to the frequency desired, final tuning being carried out with the bandspread capacitor. In our example the bandspread capacitor could be a 3-gang component, with its sections in parallel with the sections of the main 3-gang capacitor. Alternatively, a single-gang capacitor having a very low value can be connected across the oscillator tuning capacitor only, since it is the oscillator which effectively selects the signal frequency to be amplified in the receiver i.f. stages.

In (c) the oscillator tuned circuit is tuned by a reverse biased variable capacitance, or varactor, diode, whereupon the potentiometer becomes the receiver tuning control with the voltage at its slider being passed also to similar varactor tuned circuits in the r.f. and mixer stages. If a low value potentiometer is inserted in series with the main tuning potentiometer, as in (d), it will function as a bandspread control and provide fine tuning.



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