

# wireless world



APRIL 1982 70p

**Stage lighting system**

**30W Dmosfet  
audio amplifier**

**Receivers for  
optical fibre  
communication**

Australia A\$ 2.40  
Belgium BFR. 74.00  
Canada C\$ 3.25  
Denmark DKR. 28.25  
Germany DM. 6.50  
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Holland DFL. 8.00  
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Spain PTS 240.00  
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**TEK** MULTI-PURPOSE  
OSCILLOSCOPES

SO ADVANCED  
THEY COST YOU LESS

# The Tektronix 2200 Series. Simply great.



Tektronix traditions of excellence in designing and manufacturing oscilloscopes are recognised all over the world. But rather than rest on past laurels, we have veered dramatically from the well established design paths we ourselves have laid down.

With the 2213 priced at £670\* and the 2215 at £850\*, these 60 MHz dual trace oscilloscopes are an entirely new form of instrument.

Their most remarkable characteristic is the way in which major design advances have provided full-range capabilities at prices significantly below what you would expect to pay. How has this been accomplished? To begin with, we have reduced the number of mechanical parts by more than half. This not only saves manufacturing time, it lowers costs and improves reliability.

Board construction has been greatly simplified and the number of boards reduced. Board connectors have also been reduced substantially and cabling cut by an amazing 90%.

The 2213 and 2215 have a high efficiency regulated power supply which does away with the need for a heavy power transformer. There are no line-voltage adjustments. Just plug the instrument into a power socket supplying anything from 90 to 250 volts, 48-62 HZ, switch on and you are ready to measure. Power saving circuitry has eliminated the cooling fan, resulting in further economies in size and weight.

These scopes have it all. Dual trace. Delayed sweep for fast, accurate timing measurements. Single time base in the 2213, dual time bases in the 2215. An advanced triggering

system, automatic focus and intensity. Beam finder - and much more.

Interested? Then why not telephone your nearest Tektronix office or circle the enquiry number for further information.

## Performance Specifications

**Bandwidth**  
Two channels, DC-60 MHz to 20 mV/div, 50 MHz to 2 mV/div.  
**Light Weight**  
6.1 kg (13½ lbs), 6.8 kg (15.0 lbs) with cover and pouch.  
**Sweep Speeds**  
Sweeps from 0.5s to 0.05 µs (to 5 ns/div with ×10 magnification).  
**Sensitivity**  
Scale factors from 100 V/div (10× probe) to 2 mV/div (1× probe). Accurate to ± 3%. AC or DC coupling.

Also available from Electroplan.  
\* Prices subject to change without notice.

**Tektronix UK Limited**  
PO Box 69, Harpenden, Herts. AL5 4UP  
Tel: Harpenden 63141 Telex: 25559

Regional Telephone Numbers: Maidenhead  
0628 73211, Manchester 061 428 0799,  
Livingston 32766, Dublin 850685/850796

PT206

**Tektronix**  
COMMITTED TO EXCELLENCE

WW-003 FOR FURTHER DETAILS

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APRIL 1982 70p

Stage lighting system

30W Dmosfet  
audio amplifier

Receivers for  
optical fibre  
communication

WIRELESS WORLD APRIL 1982 VOL 88 NO 1555

Australia A\$ 2.40  
Belgium BFR. 74.00  
Canada C\$ 3.25  
Denmark DKR. 28.25  
Germany DM. 6.50  
Greece DRA. 160.00  
Holland DFL. 8.00  
Italy L. 3100  
Norway NKR. 24.00  
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U.S.A. \$ 3.75



# THROUGH-LINE POWER METER

## leads by a head

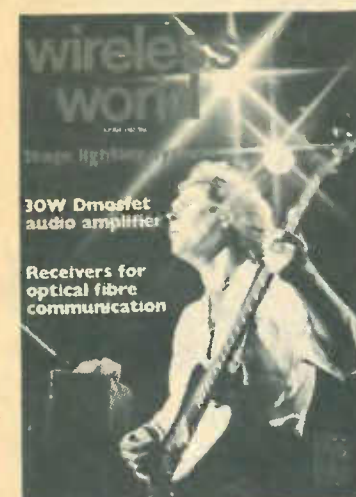


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WETHERBY LS22 4DH  
TELEPHONE (0937) 61961  
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- Single detector head covers wide frequency and power band
- 25MHz to 1GHz ■ 20mW to 100W and VSWR from 1 to 3
- Head can be used 1.5m from meter (e.g. inside closed car boot)
- Fully portable—works from internal battery or vehicle battery
- Mains adaptor/charger and rechargeable battery available
- Manufactured, tested and inspected to Min. Def. Std. 0524.



Front cover picture illustrates the article on microprocessor stage lighting systems, starting this month.

### NEXT MONTH

Digital filters — a new series giving theory, design techniques and microprocessor implementation.

Program exchange by telephone — design of software systems for loading source-code programs into memory.

Orchestral sound, halls and timbre — or 'Why does it sound so beautiful?' Denis Vaughan examines the Kingsway Hall and puts forward a theory to account for its excellence.

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# wireless world

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TELEVISION  
RADIO  
AUDIO

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## EP4000 EPROM EMULATOR PROGRAMMER

- ★ Programs 2704/2708/2716(3)/2508/2758 2516/2716/2532/2732
- ★ Emulates same devices with a single keypress
- ★ 300ns access time in emulation mode
- ★ Editing facilities — data entry, match, display, shift, move, clear, define, block program, etc.
- ★ Input/output as standard — RS232 (ASC11-hex), 20mA, printer, cassette & DMA
- ★ Video output for memory map display
- ★ Expandable with 2764 adaptor & Bipolar Prom modules
- ★ Fully buffered cold ZIF socket
- ★ Price £545 + VAT + £12 delivery



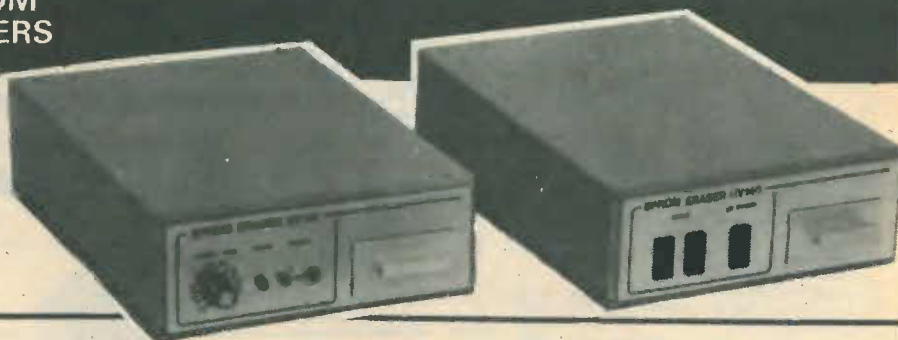
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- ★ Programs same devices as EP4000
- ★ No personality cards needed
- ★ Simple operation
- ★ Blank check & verify functions
- ★ Powered down master & copy sockets
- ★ Individual socket LED indicators
- ★ Mode indicators for blank check, program verify, and socket power down
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WIRELESS WORLD APRIL 1982

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2409 TRUE RMS Average and Peak 2Hz-200KHz.....	£250.00
<b>Hewlett Packard</b>	
3400A True RMS 1mV-300V 10Hz-10MHz.....	£600.00
<b>Marconi</b>	
TF2603 RF Millivoltmeter 300µV Sensitivity 50KHz-1.5GHz.....	£525.00
TF2604 Electronic Voltmeter AC 20Hz-1.5GHz 300mV-1KV DC 10mV-1KV 0.2R-500MΩ.....	£350.00

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<b>Dymar</b>	
1785 AM/FM Modulation meter 30-480MHz.....	£295.00
<b>Hewlett Packard</b>	
331A Distortion analysers 5Hz-600KHz to 0.1% voltmeter 300µV-300 volts at 2%.....	£350.00
332A Distortion Meter 5Hz-600KHz.....	£495.00
333A Distortion Meter with Auto null.....	£675.00
8407A/B412A Network Analyser.....	£1950.00
8555A Plug-in 10MHz-18GHz.....	£5000.00

<b>Racal</b>	
9009 Automatic AM/FM modulation meter 30-150MHz manual tuning 8-1500MHz.....	£395.00

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<b>Marconi</b>	
TF2303 AM/FM Modulation meter AM to 225MHz FM to 520MHz.....	£475.00
TF2370 Spectrum Analyser 30Hz-110MHz 0.1dB and 5Hz resolution.....	£6500.00
TK2374 Zero loss probe for TF2370.....	£375.00

<b>Tektronix</b>	
R491 Spectrum Analyser 10MHz-40GHz.....	£3500.00
7603 Main Frame with 7L13 plug in 1KHz-1.8GHz 30Hz-3MHz resolution — 128dB sensitivity.....	£9850.00

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<b>Boonton</b>	
63H Inductance Bridge 0-110mH Bridge frequency 5-500KHz.....	£1250.00

<b>Marconi</b>	
TF1245A + TF1246 "Q" meter.....	£1100.00
TM4520 Set of Inductors.....	£350.00

<b>Rohde &amp; Schwarz</b>	
LRT (BN6100) Inductance Meter 1µH-100µH 2.2-285KHz.....	£395.00

<b>Wayne Kerr</b>	
SR268 Source and Detector.....	£875.00

<b>Telequipment</b>	
D66A 25MHz Dual Trace.....	£350.00

<b>Watanabe</b>	
MC641 6 Channel 250mm Chart Recorder.....	£1495.00

<b>Yokogawa</b>	
3047 2 Channel 2 cm/HR — 60cm/Min.....	£435.00

<b>SIGNAL SOURCES</b>	
<b>Hewlett Packard</b>	
4204A Decade LF Oscillator 10Hz-1MHz 1mV-10V into 600Ω.....	£695.00
606B AM Signal Generator 50KHz-65MHz AM 0-95%.....	£850.00
608F 10-455MHz AM/PCM Modulation 0.1µV-1V output.....	£600.00
616B 1.8-4.2GHz int or ext PCM/FM 0.1µV-0.224V.....	£1000.00
616B UHF Signal Generator 1.8 to 4.2GHz Int pulse Mod.....	£1000.00

<b>FREQUENCY COUNTERS</b>	
<b>Fluke</b>	
8642 LCR 0.1%.....	£750.00
8130 10MHz.....	£425.00

1920A with Option 13 9 Digit 1GHz.....	£750.00
1925A Multifunction, EMI Proof 9 Digit 125MHz.....	£625.00
1953A Counter Timer Opt 04, 07, 14, 15, 0-1.25GHz with prescalers, I.E.E.E. interface.....	£975.00

<b>Hewlett Packard</b>	
5340A 8 Digit 10Hz-18GHz.....	£3750.00

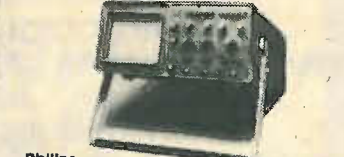
<b>Marconi</b>	
TF2430 unused condition, 7 digit 10Hz-80MHz 25MV Sensitivity.....	£175.00
TF2432 10Hz-560MHz 10mV sensitivity.....	£325.00

<b>DVM's AND DMM's</b>	
<b>Fluke</b>	
8022A 3½ digit hand held.....	£65.00

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7055 Microprocessor DMM Scale Length 20,000 AC/DC volts, resistance, 1µV resolution.....	£600.00
7065 Microprocessor DMM Scale length 1,400,000 AC/DC volts, resistance.....	£695.00

### OSCILLOSCOPES

<b>Marconi</b>	
TF2213/1 + TK2214 X-Y Display and memory.....	£550.00



<b>Philips</b>	
PM3212 25MHz Dual Trace Portable.....	£475.00

<b>SE Labs</b>	
SM121 6 Channel Monitor, 12" crt, internal sweep.....	£395.00

<b>Tektronix</b>	
465 Dual Trace Portable Oscilloscope, DC — 100MHz, 5mV-5V/div, full delayed sweep.....	£1395.00
465 with DM40.....	£1450.00
475 Dual Trace 200MHz Portable.....	£2000.00
7603 100MHz Mainframe with 7A18N and 7B53N.....	£3000.00
7704A 250MHz Mainframe c/w 7A22 Diff. Amplifier, 7A26 Dual Channel, 7B80 Timebase and 7B85 Delaying Timebase.....	£4610.00
S1 Sampling Head, As New.....	£450.00
7D14 Digital Counter plug-in 525MHz.....	£850.00

<b>Telequipment</b>	
D66A 25MHz Dual Trace.....	£350.00

<b>Watanabe</b>	
MC641 6 Channel 250mm Chart Recorder.....	£1495.00

<b>Yokogawa</b>	
3047 2 Channel 2 cm/HR — 60cm/Min.....	£435.00

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8690A/8699B RF Sweeper System 0.1-4GHz in 2 ranges, Max O/P 10mW to 2GHz and 6mW to 4 GHz.....	£2300.00
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TF144H/4 AM Signal Generator 10KHz-72MHz 2µV-2V.....	£750.00
TF2002B AM/FM 10KHz-88MHz.....	£1200.00
TF2170B Synchronizer for TF2002B.....	£450.00
TF995B/2 AM/FM 200KHz-200MHz.....	£495.00
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TF2008 AM/FM 10KHz-510MHz built in sweeper, Output 0.2µV-200mV.....	£3500.00
6070 Signal Source 400-1200MHz.....	£695.00



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PM5715 Pulse Generator 1Hz-50MHz.....	£675.00

<b>PM6456 Stereo Generator.....</b>	<b>£250.00</b>
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<b>Radiometer</b>	
SMG1 Stereo Generator.....	£375.00

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<b>Siemens</b>	
D2040 Selective Level Analyser and Voltmeter 10Hz-60KHz.....	£1200.00

<b>D2072 + W2072 Level Meter and Oscillator 50KHz-100MHz.....</b>	<b>£2200.00</b>
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<b>W2006 + D2006 Carrier Level Test Set 10KHz-17MHz — 100 to +10dB.....</b>	<b>£1650.00</b>
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<b>W2007 + D2007 Carrier Level Test Set 6KHz-18.6MHz — 120 to +20dB.....</b>	<b>£1800.00</b>
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<b>Wandel and Goltermann</b>	
PF-1 Digital Error Rate Measuring Set Consisting of PFM-1 Digital Error Rate Meter and PFG-1 Pattern Generator.....	£2490.00

<b>SPM-6 and PS-6 Level Measuring Set 6KHz-18.6MHz — 110dB to +20dB, Mains / battery operation.....</b>	<b>£2150.00</b>
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<b>D2072 + W2072 Level Meter and Oscillator 50KHz-100MHz.....</b>	<b>£2200.00</b>
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<b>PCM-1 PCM Test Set, PDA-64 PCM Signalling Analyser, PSM-4 Level Measuring Set Scanner, PDG-1 Digital Signal Generator, PDA-1 PCM Digital Signal Analyser.....</b>	<b>P.O.A.</b>
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### MISCELLANEOUS

<b>Dymar</b>	
2085 AF Power meter 30Hz-30KHz 10µW-50W input imp 1.2-1000Ω.....	£250.00

<b>Fluke</b>	
3010A Logictester Self Contained, Portable, Full Spec. on Request.....	£8500.00

<b>Hewlett Packard</b>	
355E 12dB Programmable Attenuator unused.....	£90.00

<b>4329A High Resistance meter 500KΩ-2 x 10<sup>11</sup>Ω test voltages 10-1000V.....</b>	<b>£500.00</b>
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<b>8405A Vector Voltmeter 1-1000MHz.....</b>	<b>£2000.00</b>
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<b>8403A Modulator Fitted With 87328 PIN MODULATOR.....</b>	<b>£1500.00</b>
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<b>8412A Phase Magnitude CRT display for network analyser.....</b>	<b>£1500.00</b>
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<b>8482H Power Sensor 100KHz-4.2GHz AS NEW.....</b>	<b>£250.00</b>
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<b>8745A S Parameter Test Set, Fitted with 11604A Universal Arms 0.1-2GHz.....</b>	<b>£2750.00</b>
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<b>59308A HP-IB Timing Generator.....</b>	<b>£300.00</b>
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<b>Marconi</b>	
TF2162 M.F. Attenuator 0-111dB.....	£135.00

<b>TF2163S UHF Attenuator 0-142dB 50Ω impedance DC-1GHz.....</b>	<b>£250.00</b>
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<b>TF2331 AF Distortion Meter 20Hz-20KHz.....</b>	<b>£395.00</b>
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<b>TF2500 AF Power Meter, 7 ranges 100µ watts to 25 watts.....</b>	<b>£275.00</b>
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<b>TF2807A PCM Multiplex tester.....</b>	<b>£1500.00</b>
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<b>TF2950/5 mobile Radio Test Set AM/FM.....</b>	<b>£1550.00</b>
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<b>TM8339 AC/DC mixer for use with TF2702.....</b>	<b>£250.00</b>
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<b>Philips</b>	
PM5519 Colour TV Pattern Generator AS NEW.....	£650.00

<b>PM9380 Camera and Accessories (as new).....</b>	<b>£200.00</b>
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<b>Rohde and Schwarz</b>	
MSC Stereo Coder 30Hz-15KHz.....	£500.00

<b>Tektronix</b>	
141A PAL Test Signal Generator.....	£1750.00

<b>1481C PAL TV Waveform Monitor.....</b>	<b>£2375.00</b>
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<b>191 Constant Amplitude Sig Gen. 350KHz-100MHz 5mV-5.5V.....</b>	<b>£350.00</b>
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<b>TM504 mainframe with SG503 + PG506 + DM501 + TG501.....</b>	<b>£3500.00</b>
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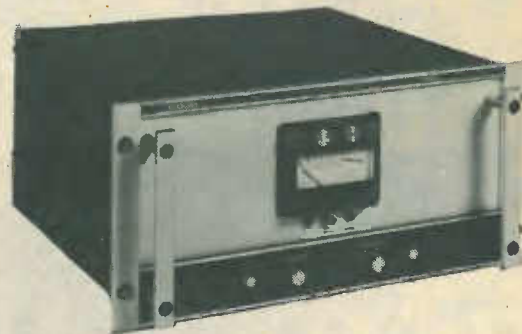
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- ★ POWER RESPONSE DC - 45KHz  $\pm$  1dB.
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- ★ INTERLOCK CAPABILITY FOR UP TO EIGHT UNITS.
- ★ 3-YEAR PARTS AND LABOUR WARRANTY.
- ★ UNITS AVAILABLE FROM 100VA-12KVA.



Model — M600

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PROFESSIONAL INDUSTRIAL ELECTRONICS

WW - 020 FOR FURTHER DETAILS

## Happy Memories

Part Type	1 off	25-99	100 up
4116 200ns	.95	.85	.65
4116 250ns	.90	.80	.60
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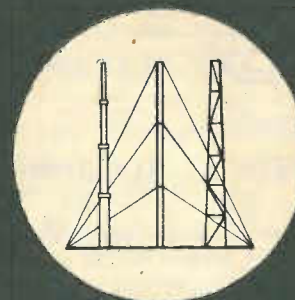
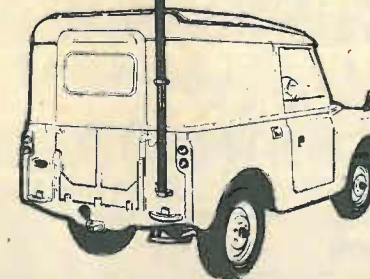
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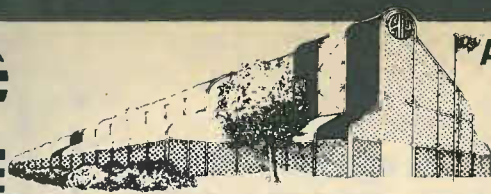
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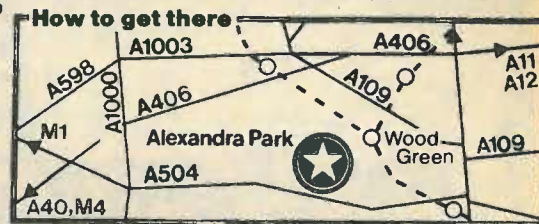
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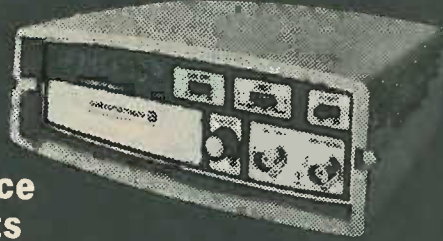
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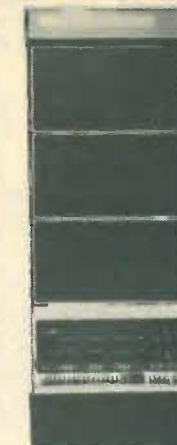
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4003 0.13	4083 0.13	4724 0.95	7453N 0.14	74157N 0.55	74369N 1.85	74LS115N 0.19	74LS256N 0.35	74LS257N 0.35	74C08 0.20	8088/8088		8088/8088
4008 0.50	4093 0.80	4725 0.94	7454N 0.14	74158N 0.55	74370N 1.85	74LS116N 0.19	74LS258N 0.35	74LS259N 0.35	74C10 0.20	8088/8088		8088/8088
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4010 0.30	4503 0.50	40098 0.54	7472N 0.27	74162N 0.55	74373N 1.85	74LS119N 0.19	74LS264N 0.35	74LS265N 0.35	74C20 0.20	8088/8088		8088/8088
4010AE 0.24	4506 0.70	40106 0.69	7473N 0.28	74163N 0.55	74374N 1.85	74LS120N 0.19	74LS266N 0.35	74LS267N 0.35	74C30 0.20	8088/8088		8088/8088
4011 0.11	4507 1.05	40118 0.65	7474N 0.28	74164N 0.55	74375N 1.85	74LS121N 0.19	74LS268N 0.35	74LS269N 0.35	74C42 0.20	8088/8088		8088/8088
4013 0.25	4508 1.50	40161 1.05	7475N 0.28	74165N 0.55	74376N 1.85	74LS122N 0.19	74LS270N 0.35	74LS271N 0.35	74C48 1.03	8088/8088		8088/8088
4015 0.50	4510 0.55	40162 1.05	7476N 0.30	74166N 0.70	74377N 1.85	74LS123N 0.19	74LS272N 0.35	74LS273N 0.35	74C48 1.03	8088/8088		8088/8088
4016 0.22	4511 0.45	40163 1.05	7480N 0.26	74167N 1.25	74378N 1.85	74LS124N 0.19	74LS274N 0.35	74LS275N 0.35	74C48 1.03	8088/8088		8088/8088
4017 0.40	4512 0.55	40174 1.05	7481N 0.20	74170N 1.25	74379N 1.85	74LS125N 0.19	74LS276N 0.35	74LS277N 0.35	74C48 1.03	8088/8088		8088/8088
4018 0.38	4513 1.25	40175 1.05	7482N 0.20	74171N 1.25	74380N 1.85	74LS126N 0.19	74LS278N 0.35	74LS279N 0.35	74C48 1.03	8088/8088		8088/8088
4020 0.55	4515 1.05	40192 1.05	7485N 0.75	74174N 1.25	74381N 1.85	74LS127N 0.19	74LS280N 0.35	74LS281N 0.35	74C48 1.03	8088/8088		8088/8088
4021 0.55	4516 0.80	40193 1.08	7488N 0.24	74175N 0.75	74382N 1.85	74LS128N 0.19	74LS282N 0.35	74LS283N 0.35	74C48 1.03	8088/8088		8088/8088
4022 0.55	4518 0.35	40194 1.08	7489N 1.05	74176N 0.75	74383N 1.85	74LS129N 0.19	74LS284N 0.35	74LS285N 0.35	74C48 1.03	8088/8088		8088/8088
4023 0.15	4520 0.80	40195 1.08	7490N 0.30	74177N 0.75	74384N 1.85	74LS130N 0.19	74LS286N 0.35	74LS287N 0.35	74C48 1.03	8088/8088		8088/8088
4024 0.15	4521 1.30	40196 1.08	7491N 0.35	74178N 0.90	74385N 1.85	74LS131N 0.19	74LS288N 0.35	74LS289N 0.35	74C48 1.03	8088/8088		8088/8088
4025 0.15	4522 0.85	40197 1.08	7492N 0.35	74179N 0.75	74386N 1.85	74LS132N 0.19	74LS290N 0.35	74LS291N 0.35	74C48 1.03	8088/8088		8088/8088
4026 1.05	4527 0.80	7400N 0.10	7493N 0.35	74180N 0.75	74387N 1.85	74LS133N 0.19	74LS292N 0.35	74LS293N 0.35	74C48 1.03	8088/8088		8088/8088
4027 0.26	4528 0.85	7401N 0.10	7494N 0.70	74181N 1.22	74388N 1.85	74LS134N 0.19	74LS294N 0.35	74LS295N 0.35	74C48 1.03	8088/8088		8088/8088
4028 0.55	4529 0.70	7402N 0.20	7495N 0.60	74182N 0.70	74389N 1.85	74LS135N 0.19	74LS296N 0.35	74LS297N 0.35	74C48 1.03	8088/8088		8088/8088
4029 0.55	4531 0.85	7403N 0.11	7496N 0.45	74183N 1.20	74390N 1.85	74LS136N 0.19	74LS298N 0.35	74LS299N 0.35	74C48 1.03	8088/8088		8088/8088
4030 0.35	4532 0.85	7404N 0.12	7497N 1.40	74184N 1.20	74391N 1.85	74LS137N 0.19	74LS300N 0.35	74LS301N 0.35	74C48 1.03	8088/8088		8088/8088
4035 0.67	4534 0.40	7405N 0.12	7498N 1.10	74185N 1.20	74392N 1.85	74LS138N 0.19	74LS302N 0.35	74LS303N 0.35	74C48 1.03	8088/8088		8088/8088
4040 0.50	4536 0.20	7406N 0.22	74104 0.62	74186N 3.00	74393N 1.85	74LS139N 0.19	74LS304N 0.35	74LS305N 0.35	74C48 1.03	8088/8088		8088/8088
4042 0.50	4538 0.85	7407N 0.22	74105 0.62	74187N 3.00	74394N 1.85	74LS140N 0.19	74LS306N 0.35	74LS307N 0.35	74C48 1.03	8088/8088		8088/8088
4043 0.50	4539 0.80	7408N 0.15	74107 0.26	74188N 3.00	74395N 1.85	74LS141N 0.19	74LS308N 0.35	74LS309N 0.35	74C48 1.03	8088/8088		8088/8088
4044 0.50	4540 0.80	7409N 0.15	74109 0.35	74189N 3.00	74396N 1.85	74LS142N 0.19	74LS310N 0.35	74LS311N 0.35	74C48 1.03	8088/8088		8088/8088
4045 0.50	4541 0.80	7410N 0.15	74110 0.54	74190N 3.00	74397N 1.85	74LS143N 0.19	74LS312N 0.35	74LS313N 0.35	74C48 1.03	8088/8088		8088/8088
4046 0.60	4553 2.70	7411N 0.18	74111 0.68	74191N 3.00	74398N 1.85	74LS144N 0.19	74LS314N 0.35	74LS315N 0.35	74C48 1.03	8088/8088		8088/8088
4047 0.88	4554 1.20	7412N 0.19	74112N 0.68	74192N 3.00	74399N 1.85	74LS145N 0.19	74LS316N 0.35	74LS317N 0.35	74C48 1.03	8088/8088		8088/8088
4049 0.24	4555 0.35	7413N 0.27	74116N 1.98	74193N 3.00	74400N 1.85	74LS146N 0.19	74LS318N 0.35	74LS319N 0.35	74C48 1.03	8088/8088		8088/8088
4050 0.24	4556 0.40	7414N 0.51	74118N 0.85	74194N 3.00	74401N 1.85	74LS147N 0.19	74LS320N 0.35	74LS321N 0.35	74C48 1.03	8088/8088		8088/8088
4051 0.55	4557 0.20	7416N 0.27	74120N 1.20	74195N 3.00	74402N 1.85	74LS148N 0.19	74LS322N 0.35	74LS323N 0.35	74C48 1.03	8088/8088		8088/8088
4052 0.55	4558 0.80	7417N 0.27	74121N 0.95	74196N 3.00	74403N 1.85	74LS149N 0.19	74LS324N 0.35	74LS325N 0.35	74C48 1.03	8088/8088		8088/8088
4053 0.55	4559 3.50	7418N 0.28	74122N 0.34	74197N 3.00	74404N 1.85	74LS150N 0.19	74LS326N 0.35	74LS327N 0.35	74C48 1.03	8088/8088		8088/8088
4054 1.30	4560 0.20	7421N 0.30	7422N 0.34	74198N 3.00	74405N 1.85	74LS151N 0.19	74LS328N 0.35	74LS329N 0.35	74C48 1.03	8088/8088		8088/8088
4055 1.30	4561 1.00	7423N 0.22	74213N 0.40	74199N 3.00	74406N 1.85	74LS152N 0.19	74LS330N 0.35	74LS331N 0.35	74C48 1.03	8088/8088		8088/8088
4056 1.30	4562 0.50	7425N 0.22	74215N 0.40	74200N 3.00	74407N 1.85	74LS153N 0.19	74LS332N 0.35	74LS333N 0.35	74C48 1.03	8088/8088		8088/8088
4059 5.75	4566 1.70	7426N 0.22	74216N 0.40	74201N 3.00	74408N 1.85	74LS154N 0.19	74LS334N 0.35	74LS335N 0.35	74C48 1.03	8088/8088		8088/8088
4060 0.75	4568 1.45	7427N 0.22	74218N 0.50	74202N 3.00	74409N 1.85	74LS155N 0.19	74LS336N 0.35	74LS337N 0.35	74C48 1.03	8088/8088		8088/8088
4063 1.15	4569 1.70	7428N 0.22	74219N 0.50	74203N 3.00	74410N 1.85	74LS156N 0.19	74LS338N 0.35	74LS339N 0.35	74C48 1.03	8088/8088		8088/8088
4066 0.30	4572 0.22	7432N 0.23	74241N 0.65	74204N 3.00	74411N 1.85	74LS157N 0.19	74LS340N 0.35	74LS341N 0.35	74C48 1.03	8088/8088		8088/8088
4067 4.30	4580 3.25	7437N 0.22	74242N 0.65	74205N 3.00	74412N 1.85	74LS158N 0.19	74LS342N 0.35	74LS343N 0.35	74C48 1.03	8088/8088		8088/8088
4068 0.18	4581 1.40	7438N 0.22	74243N 0.65	74206N 3.00	74413N 1.85	74LS159N 0.19	74LS344N 0.35	74LS345N 0.35	74C48 1.03	8088/8088		8088/8088
4069AE 0.14	4582 0.70	7440N 0.14	74244N 0.65	74207N 3.00	74414N 1.85	74LS160N 0.19	74LS346N 0.35	74LS347N 0.35	74C48 1.03	8088/8088		8088/8088
4070 0.16	4583 0.80	7441N 0.54	74245N 0.75	74208N 3.00	74415N 1.85	74LS161N 0.19	74LS348N 0.35	74LS349N 0.35	74C48 1.03	8088/8088		8088/8088
4071 0.16	4584 0.75	7442N 0.62	74246N 0.75	74209N 3.00	74416N 1.85	74LS162N 0.19	74LS350N 0.35	74LS351N 0.35	74C48 1.03	8088/8088		8088/8088
4072 0.16	4585 0.45	7443N 0.62	74247N 0.75	74210N 3.00	74417N 1.85	74LS163N 0.19	74LS352N 0.35	74LS353N 0.35	74C48 1.03	8088/8088		8088/8088
4073 0.16	4586 0.40	7444N 0.62	74248N 0.75	74211N 3.00	74418N 1.85	74LS164N 0.19	74LS354N 0.35	74LS355N 0.35	74C48 1.03	8088/8088		8088/8088
4075 0.16	4703 4.48	7445N 0.62	74249N 0.75	74212N 3.00	74419N 1.85	74LS165N 0.19	74LS356N 0.35	74LS357N 0.35	74C48 1.03	8088/8088		8088/8088
4076 0.55	4704 4.24	7446N 0.62	74250N 0.75	74213N 3.00	74420N 1.85	74LS166N 0.19	74LS358N 0.35	74LS359N 0.35	74C48 1.03	8088/8088		8088/8088
										RAM		
										2102 1.70		
										2112 3.40		
										2116 4.20		
										2121 1.49		
										2128 1.49		
										2132 1.59		
										2136 1.59		
										2144 1.59		
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										2152 1.59		
										2156 1.59		
										2160 1.59		
										2164 1.59		
										2168 1.59		
										2172 1.59		
										2176 1.59		
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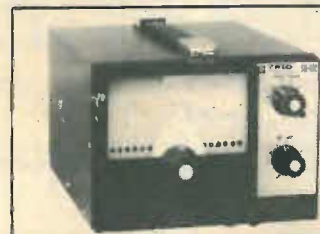
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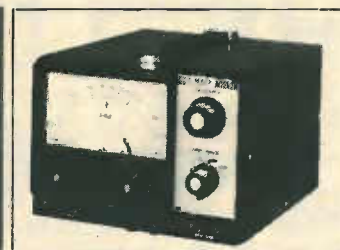
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**SG402 A.M. Signal Generator** — 100KHz to 30MHz in 6 bands — 100mV of O/P with variable attenuator — Int. and Ex. A.M. — Solid State — Lightweight and portable — Large clear easy to read frequency dial.



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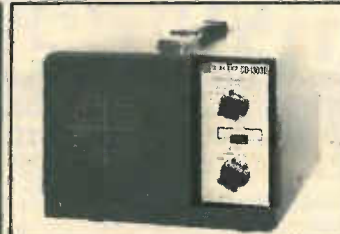
**FG270, Function Generator** — 0.1Hz to 1MHz in 6 ranges — sine, square and triangle — 20V p-p open circuit output — < 1% distortion — D.C. offset — TTL O/P — Ext. VCO for sweep tests. FG271 as above plus 0.02Hz to 2MHz in 7 ranges — Int. sweep — Pulse, Tone Burst and A.M.



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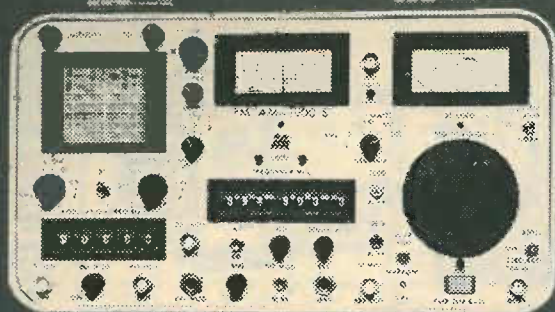
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**AC Volts:** 600 VRMS maximum for frequencies between 25 Hz and 25 kHz

**Ohms:** Using the modified probe, part number PB-114, Ohms can be measured on scales X1 to X10 K  
**% AM:** Measured on the RF signal applied to the FM/AM-1000 unit

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For further information contact Mike Taylor



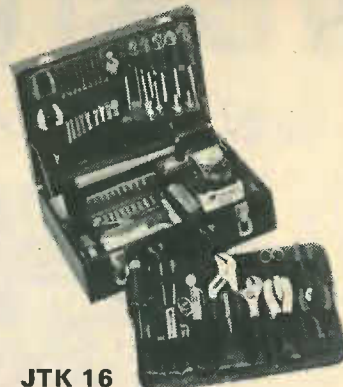
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# The WERSI Concept

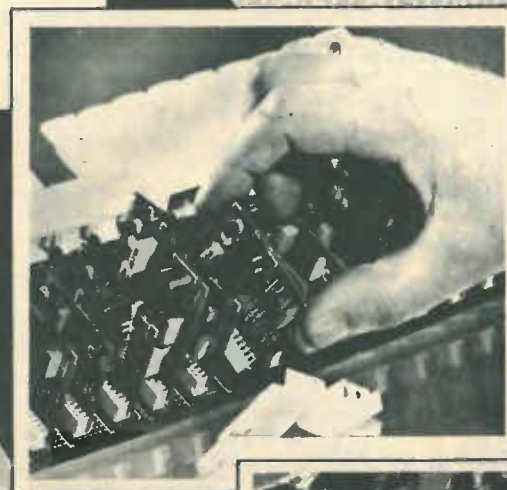
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WERSI presents their new generation of electronic organs and accessories to you, the do-it-yourselfer. All the tools you need are illustrated left. The electronics involved is very revolutionary, making it very easy to understand. Every non-specialist who can read is able to do it. Building a WERSI organ from a kit can save you more than half the cost of a similarly equipped ready-made instrument and that means with WERSI and your own initiative and involvement you can afford a sophisticated electronic organ.

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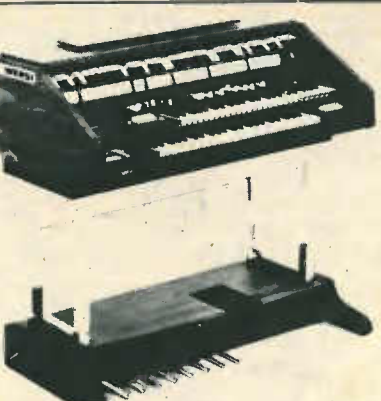
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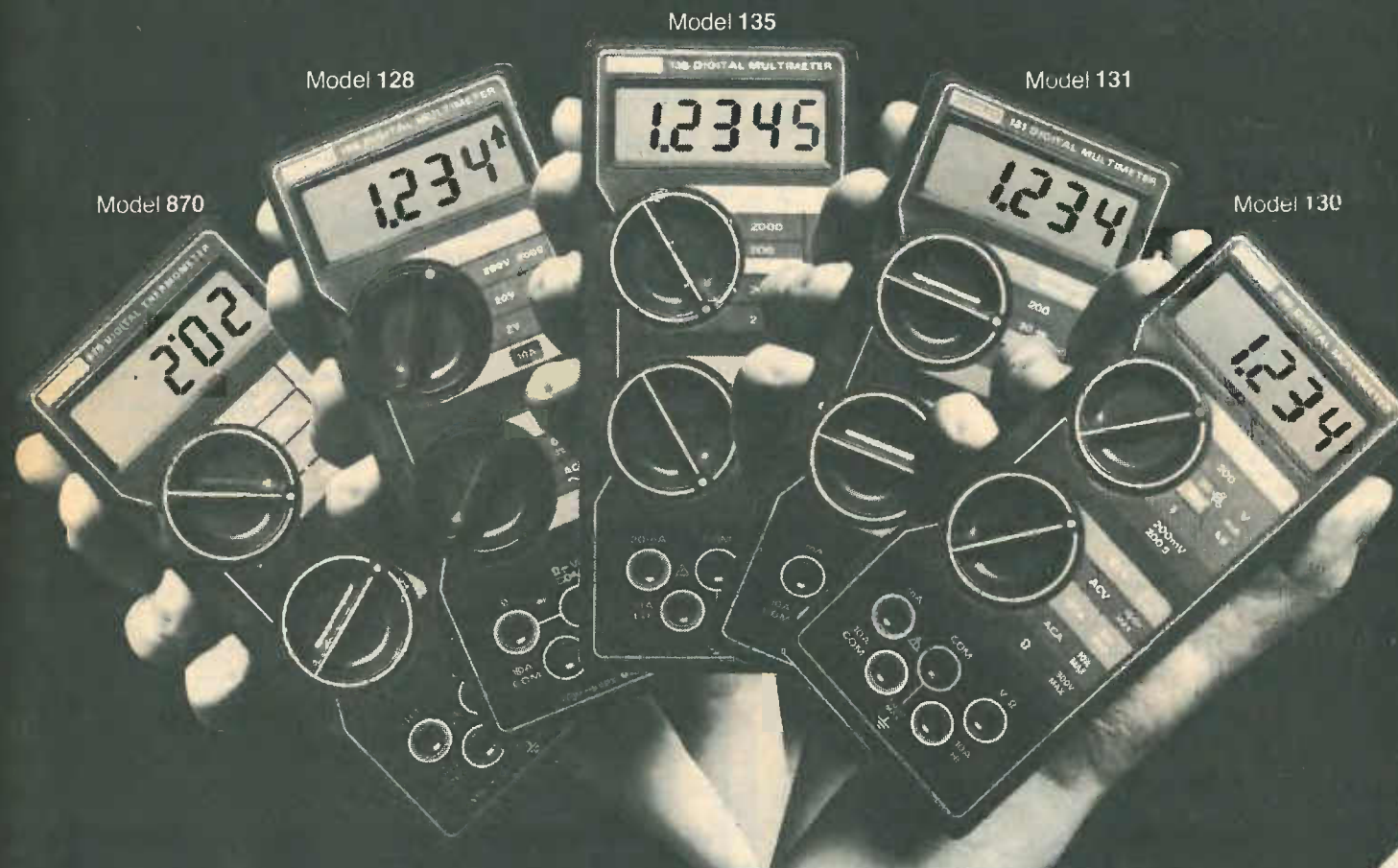
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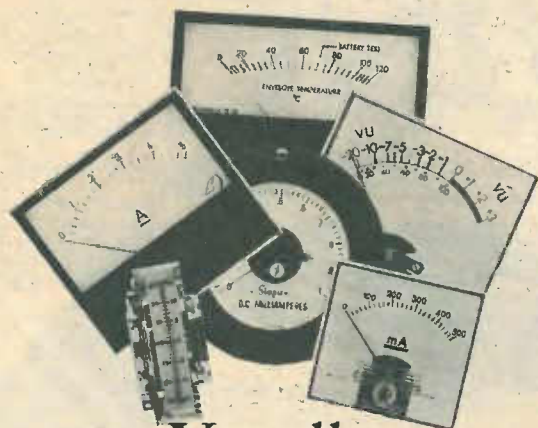
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We have the UK manufacturing facilities, experience and skills to give you the panel meter you want. With all aspects of panel meter construction under our control it means you can specify and get the sensitivity, movement ballistics and scale you want. It all adds up to greater flexibility and a wider choice. You want them quickly? - of course! Low quantities or large quantities present no problems. Next time why not give us a call - ask for Colin Williams, tell him what you want - you could be surprised at what he may have to tell you!

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Ohms 1Ω-20MΩ

Only £36.75  
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Similar Basic spec. as 2033 except able to measure down to 0.1µA AC/DC and 0.1Ω and with greater accuracy.  
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8610A

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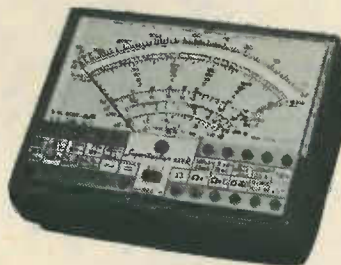
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for use with SABTRONICS Multimeters. Enables you to hold a signal on display.  
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Amps AC 250µA-5A  
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Frequency 0-5000Hz  
Capacity 0-20,000µF  
Size with case  
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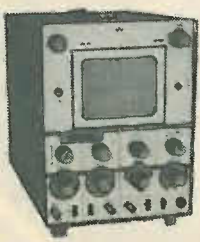


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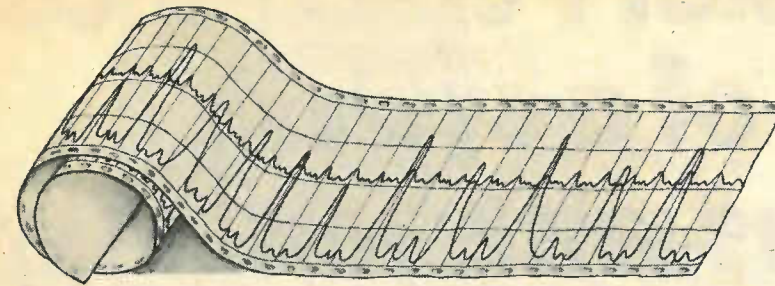
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## Data recording and analysis:

meet the time shrinker!



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**Magnetic tape cartridge** Because it records on a standard 1/4 inch magnetic tape cartridge in ECMA/ANSI format, the output can be replayed at high speed into a computer, calculator or other data processing equipment. Alternatively, the internal replay facility of the data logger can be used. *No other data logger has this capability.*

**Individual conditioning cards** Individual, plug-in signal conditioning cards are used — one for each of the 20 input channels (expandable up to 100). As a result, each customer receives a bespoke instrument ready to handle mixed

analogue and digital inputs from most transducers. Cards are available at low cost to condition virtually every type of electrical signal, to reconfigure the instrument for different projects. *No other data logger offers these facilities.*

**Exceptional versatility** The M1600L is available either as a mains powered, free-standing, laboratory instrument or in the portable weatherproof form operating from its internal batteries. For more permanent installation in existing systems, it can be supplied in chassis form for mounting in a 19 inch rack. *No other data logger displays this versatility.*

The M1600L is now widely adopted for projects in energy, transportation, agricultural and environmental research. If you would like further details, please

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CE 608	38	—	CPS 80	HS50	30Vus	110dB	775mV	0.0035%	1.5Hz-50KHz	80-120-25
CE1004	44	70	CPS150	HS50/100	30Vus	110dB	775mV	0.0035%	1.5Hz-50KHz	80-120-25
CE1008	65	—	CPS150	HS50/100	30Vus	110dB	775mV	0.0035%	1.5Hz-50KHz	80-120-25
CE1704	85	121	CPS250	HS100/150/FM1	30Vus	110dB	775mV	0.0035%	1.5Hz-50KHz	80-120-25
CE1708	125	—	CPS250	HS100/150/FM1	30Vus	110dB	775mV	0.0035%	1.5Hz-50KHz	80-120-25
CE3004	170	250	CPS250	HS150/FM2	30Vus	110dB	775mV	0.008 %	1.5Hz-50KHz	161-102-35
CPR1X	output	775mV	REC1	—	3Vus	70dB	2.8mV	0.008 %	10Hz-50KHz	138-80-35
MC1X	output	2mV	REC1	—	3Vus	65dB	70/150uV	0.008 %	10Hz-50KHz	80-120-35
XO2/3	output	775-2500mV	REC1	—	9Vus	90dB	775mV	0.01 %	Preset	150-50-20

\*Power output is quoted in WRMS and is given for two modules off the same power supply. Higher powers can be obtained if using our dual power supplies or one module per PSU or if using a stabilised power supply.

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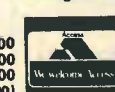
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# Sinclair ZX81 Personal Computer - the heart of a system that grows with you.

1980 saw a genuine breakthrough - the Sinclair ZX80, world's first complete personal computer for under £100. Not surprisingly, over 50,000 were sold.

In March 1981, the Sinclair lead increased dramatically. For just £69.95 the Sinclair ZX81 offers even more advanced facilities at an even lower price. Initially, even we were surprised by the demand - over 50,000 in the first 3 months!

Today, the Sinclair ZX81 is the heart of a computer system. You can add 16-times more memory with the ZX RAM pack. The ZX Printer offers an unbeatable combination of performance and price. And the ZX Software library is growing every day.

## Lower price: higher capability

With the ZX81, it's still very simple to teach yourself computing, but the ZX81 packs even greater working capability than the ZX80.

It uses the same micro-processor, but incorporates a new, more powerful 8K BASIC ROM - the 'trained intelligence' of the computer. This chip works in decimals, handles logs and trig, allows you to plot graphs, and builds up animated displays.

And the ZX81 incorporates other operation refinements - the facility to load and save named programs on cassette, for example, and to drive the new ZX Printer.



Every ZX81 comes with a comprehensive, specially-written manual - a complete course in BASIC programming, from first principles to complex programs.

## Kit: £49.<sup>95</sup>

### Higher specification, lower price - how's it done?

Quite simply, by design. The ZX80 reduced the chips in a working computer from 40 or so, to 21. The ZX81 reduces the 21 to 4!

The secret lies in a totally new master chip. Designed by Sinclair and custom-built in Britain, this unique chip replaces 18 chips from the ZX80!

### New, improved specification

- Z80A micro-processor - new faster version of the famous Z80 chip, widely recognised as the best ever made.

- Unique 'one-touch' key word entry: the ZX81 eliminates a great deal of tiresome typing. Key words (RUN, LIST, PRINT, etc.) have their own single-key entry.

- Unique syntax-check and report codes identify programming errors immediately.

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- Graph-drawing and animated-display facilities.

- Multi-dimensional string and numerical arrays.

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- Randomise function - useful for games as well as serious applications.

- Cassette LOAD and SAVE with named programs.

- 1K-byte RAM expandable to 16K bytes with Sinclair RAM pack.

- Able to drive the new Sinclair printer.

- Advanced 4-chip design: micro-processor, ROM, RAM, plus master chip - unique, custom-built chip replacing 18 ZX80 chips.

## Built: £69.<sup>95</sup>

### Kit or built - it's up to you!

You'll be surprised how easy the ZX81 kit is to build: just four chips to assemble (plus, of course the other discrete components) - a few hours' work with a fine-tipped soldering iron. And you may already have a suitable mains adaptor - 600 mA at 9 VDC nominal unregulated (supplied with built version).

Kit and built versions come complete with all leads to connect to your TV (colour or black and white) and cassette recorder.



## Available now - the ZX Printer for only £49.<sup>95</sup>

Designed exclusively for use with the ZX81 (and ZX80 with 8K BASIC ROM), the printer offers full alpha-numerics and highly sophisticated graphics.

A special feature is COPY, which prints out exactly what is on the whole TV screen without the need for further instructions.

At last you can have a hard copy of your program listings - particularly

useful when writing or editing programs.

And of course you can print out your results for permanent records or sending to a friend.

Printing speed is 50 characters per second, with 32 characters per line and 9 lines per vertical inch.

The ZX Printer connects to the rear of your computer - using a stackable connector so you can plug in a RAM pack as well. A roll of paper (65 ft long x 4 in wide) is supplied, along with full instructions.

### How to order your ZX81

BY PHONE - Access, Barclaycard or Trustcard holders can call 01-200 0200 for personal attention 24 hours a day, every day.

BY FREEPOST - use the no-stamp-needed coupon below. You can pay

by cheque, postal order, Access, Barclaycard or Trustcard.

EITHER WAY - please allow up to 28 days for delivery. And there's a 14-day money-back option. We want you to be satisfied beyond doubt - and we have no doubt that you will be.

To: Sinclair Research Ltd, FREEPOST, Camberley, Surrey, GU15 3BR.				Order
Qty	Item	Code	Item price £	Total £
	Sinclair ZX81 Personal Computer kit(s). Price includes ZX81 BASIC manual, excludes mains adaptor.	12	49.95	
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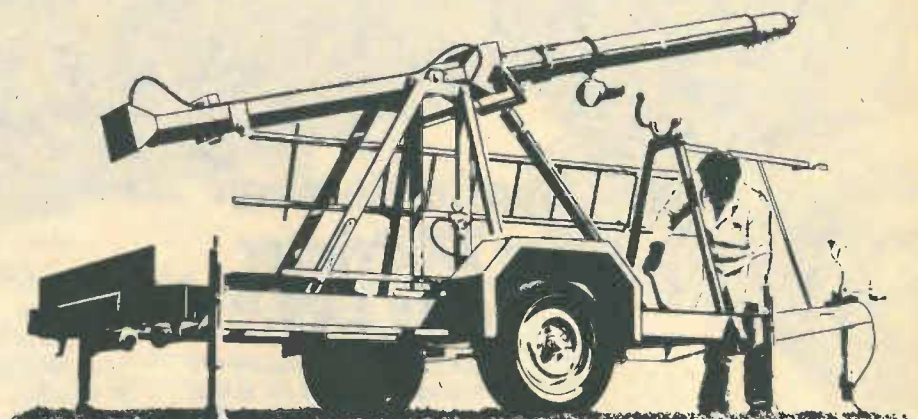
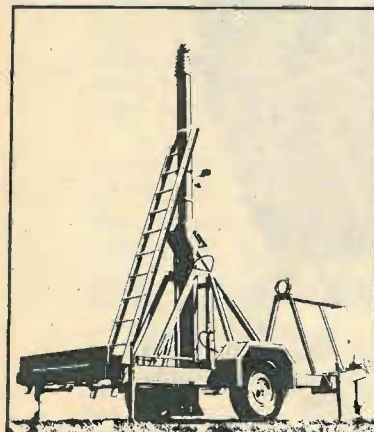
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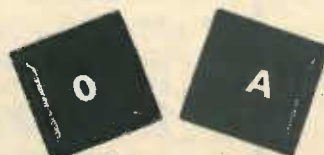
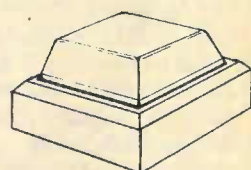
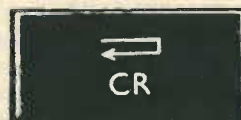
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WIRELESS WORLD APRIL 1982

# The Thinking Cap



**Now you can measure, sort and check  
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The new 3001 Digital Capacitance Meter is yet another superb instrument from G.S.C. Designed specifically for professional laboratories, test and production benches, it offers outstanding accuracy with features and accessories to match. All in a well designed, rugged unit for only £165\*.

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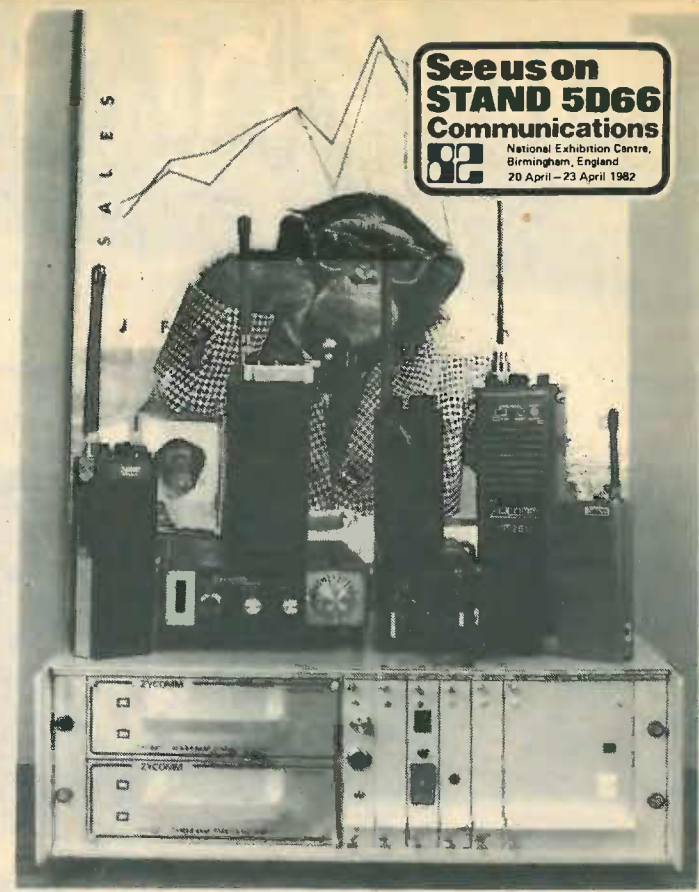
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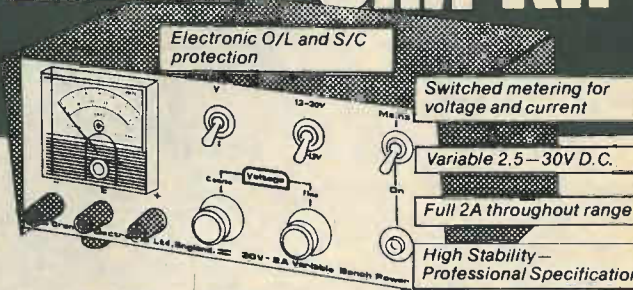
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THP 20 Touch and Hold optional  
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**HAMEG OSCILLOSCOPES**

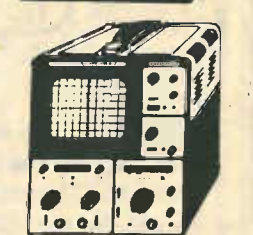
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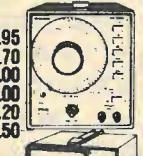
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AG203 10 HZ-1 MHz 5 band max distortion 0.1% Trio £126.50

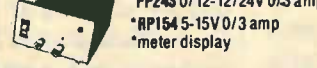
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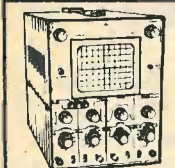
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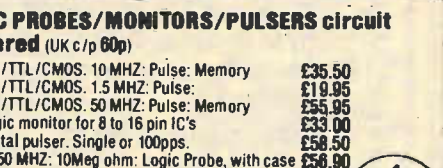
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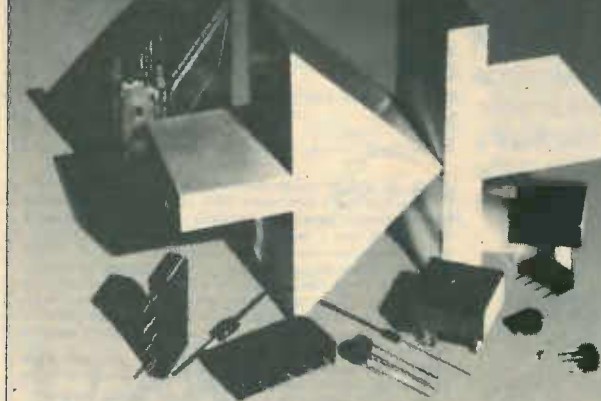
**United States**  
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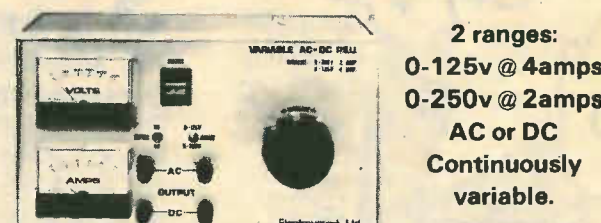
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25+0.99		LM556CN	0.40	4553	2.90	74LS191	0.49		
2114L-300ns	GTE 1.55	LM725CN	3.20	4000	0.11	4555	0.39	74LS192	0.40
(FOR ACORN ATOM)		LM741CN	0.14	4551	0.11	4556	0.40	74LS193	0.40
2708 450ns	1+2.25	LM747CN	0.70	4002	0.13	4585	0.82	74LS194	0.39
		LM748CN	0.34	4006	0.80			74LS195	0.39
				2707	0.15	74LS196	0.50		
2716 450ns	1+2.40	<b>REGULATORS</b>		4008	0.58	74LS197	0.85		
(single +5V)	25+2.25	7805	0.39	4009	0.28	74LS201	0.11	74LS221	0.85
2716 350ns	0.85	7812	0.39	4010	0.35	74LS202	0.12	74LS240	0.85
2532 450ns	1+4.50	7815	0.39	4011	0.12	74LS203	0.12	74LS241	0.85
	25+2.25	78105	0.29	4012	0.15	74LS204	0.12	74LS242	0.70
2732 450ns	1+0.80	78112	0.29	4013	0.29	74LS208	0.13	74LS243	0.70
	25+3.90	78115	0.29	4014	0.50	74LS208	0.12	74LS244	0.85
2732 350ns	7.50	7905	0.55	4015	0.58	74LS209	0.12	74LS245	0.85
4116 200ns	1+0.74	7912	0.55	4016	0.25	74LS210	0.12	74LS247	0.85
	25+0.70	7915	0.55	4017	0.45	74LS211	0.12	74LS248	0.85
4116 150ns	100+0.87	79105	0.55	4018	0.58	74LS212	0.12	74LS249	0.85
	1+0.80	79112	0.55	4019	0.29	74LS213	0.12	74LS251	0.40
4118 200ns	25+0.89	79115	0.59	4020	0.15	74LS214	0.39	74LS252	0.39
	1+3.90	LM309K	0.99	4021	0.80	74LS215	0.12	74LS257	0.40
4118 150ns	8.00	LM317K	3.20	4022	0.82	74LS220	0.12	74LS258	0.39
5516 200ns	12.50	LM323K	4.95	4023	0.17	74LS221	0.12	74LS259	0.39
5516 200ns	7.95	LM338K	4.75	4024	0.35	74LS222	0.12	74LS261	1.99
6116L 200ns	10.85			4025	0.16	74LS226	0.15	74LS266	0.25
6116L 150ns	10.85			4026	0.50	74LS227	0.12	74LS273	0.39
				4027	0.30	74LS228	0.15	74LS279	0.39
<b>280 FAMILY</b>									
		280 CPU	3.40	4028	0.55	74LS230	0.12	74LS283	0.40
		280A CPU	3.99	4031	1.65	74LS232	0.12	74LS290	0.54
		280 CTC	2.99	4033	1.80	74LS233	0.16	74LS293	0.45
		280A CTC	2.99	4034	1.55	74LS237	0.15	74LS295	0.40
		280 DART	10.50	4035	0.72	74LS238	0.15	74LS296	0.39
		280A DART	12.00	4036	0.15	74LS240	0.12	74LS297	0.39
		280 DMA	9.95	4041	0.89	74LS242	0.34	74LS298	0.39
		280A DMA	11.95	4042	0.54	74LS247	0.39	74LS299	0.39
		280 PIO	3.40	4043	0.59	74LS248	0.80	74LS374	0.40
		280 P10	3.75	4044	0.84	74LS249	0.59	74LS375	0.40
		280 SIO-0	10.99	4045	1.85	74LS251	0.14	74LS377	0.85
		280A SIO-0	11.99	4046	0.65	74LS254	0.15	74LS378	0.85
		280 SIO-1	10.99	4047	0.68	74LS255	0.15	74LS379	0.85
		280A SIO-1	11.99	4048	0.54	74LS257	0.19	74LS386	0.25
		280 SIO-2	10.99	4049	0.26	74LS274	0.16	74LS390	0.54
								74LS393	0.59
<b>DATA CONVERTERS</b>									
ZN425E-8	3.45							<b>OIL SOCKETS</b>	
ZN426E-8	3.00							<b>LOW PROFILE - TIT</b>	
ZN427E-8	5.99							8 pin	0.07
ZN428E-8	4.75							16 pin	0.06
ZN429E-8	2.10							16 pin	0.06
ZN432CJ-10	28.09							20 pin	0.13
ZN433CJ-10	2.59							20 pin	0.13
ZN440	50.63							22 pin	0.17
ZN432E-10	14.75							24 pin	0.16
ZN447	8.14							28 pin	0.25
ZN448	8.80							40 pin	0.28
ZN449	3.20								
<b>NEW FLOPPY DISC CONTROLLERS</b>									
FD1771	17.12	280A SIO-2	11.99	4050	0.26	74LS275	0.24	<b>NEW</b>	
FD1791	32.61	MK 3889	11.00	4051	0.59	74LS276	0.24	<b>LOW PROFILE -</b>	
FD1793	45.10	MK 3886-4	14.47	4052	0.60	74LS279	0.19	<b>BOLD</b>	
FD1795	35.33			4053	0.59	74LS283	0.45	8 pin	0.22
WD1391	45.50	<b>8600 FAMILY</b>		4054	1.20	74LS285	0.85	16 pin	0.31
WD1393	45.50	6800	2.99	4055	1.20	74LS286	0.15	18 pin	0.33
WD1395	45.50	6802	3.99	4060	0.79	74LS290	0.30	20 pin	0.35
WD1397	45.50	6803C	11.80	4063	0.95	74LS291	0.75	22 pin	0.40
WD2143-01	10.87	6809	9.25	4066	0.34	74LS292	0.34	24 pin	0.42
WD1691		6810	1.25	4068	0.17	74LS293	0.34	28 pin	0.49
		6821	1.25	4069	0.17	74LS295	0.43	40 pin	0.81
		6840	4.20	4070	0.17	74LS2109	0.21		
		6850	1.50	4071	0.17	74LS2112	0.21	<b>NEW</b>	
		6862	0.81	4072	0.17	74LS2113	0.23	<b>ZERO - INSERTION</b>	
		6871AT	18.70	4073	0.19	74LS2114	0.18	<b>FORCE FLIP</b>	
		6880	1.07	4075	0.17	74LS2122	0.39	1 MHz	6.30
		6887	0.80	4076	0.82	74LS2123	0.39	0.93MHz	2.90
		68488	9.11	4077	0.22	74LS2124	0.99	3.8432 MHz	2.90
		68475	4.18	4078	0.24	74LS2125	0.25	3.6864 MHz	1.85
		68443	13.99	4081	0.14	74LS2126	0.25	6 MHz	1.95
		68B00	4.70	4082	0.19	74LS2132	0.45	8 MHz	1.80
		68B02	2.11	4085	0.34	74LS2136	0.28	14 MHz	3.45
		68B21	2.20	4086	0.89	74LS2138	0.34		
		68B10	2.00	4093	0.39	74LS2139	0.39	<b>NEW UHF</b>	
		68B40	4.70	4502	0.69	74LS2145	0.75	<b>MODULATORS</b>	
		68B50	2.15	4507	0.39	74LS2148	0.90	6 MHz	2.75
		68000C4	110.00	4508	1.90	74LS2151	0.39	8 MHz	4.40
				4510	0.40	74LS2153	0.28		
		<b>6500 FAMILY</b>		4511	0.30	74LS2155	0.28		
		6502	4.25	4512	0.60	74LS2156	0.39	<b>DATABOOKS</b>	
		6520	2.99	4514	1.49	74LS2157	0.31	<b>THOMSON-EFCS</b>	
		6522	4.75	4515	1.49	74LS2158	0.31	6800 Data Book	
		6532	6.95	4516	0.89	74LS2160	0.39	(Inc p&p)	5.95
				4518	0.40	74LS2161	0.39	Data Converter	
		<b>8080 FAMILY</b>		4519	0.28	74LS2162	0.39	Handbook	
		8085A	5.50	4520	0.85	74LS2163	0.39	(Inc p&p)	1.58
		8212	1.70	4521	1.49	74LS2164	0.47	<b>DATASHEETS</b>	
		8216	0.99	4522	1.20	74LS2165	0.47	Photocopied Data	
		8224	1.95	4526	0.79	74LS2166	0.80	Sheets available for	
		8228	3.85	4527	0.88	74LS2173	0.70	most product at 8p	
		8251	3.80	4528	0.70	74LS2174	0.47	per page ex p&p	
		8253	7.95	4532	0.85	74LS2175	0.49	plus A1-Phase	
		8255	1.80	4541	0.99	74LS2181	1.28	telephone	
<b>OWN CHIPS</b>									
ZM450E	7.81								
ZM450E DVM KIT	25.00								
<b>NEW LINEARS</b>									
LM301AN	0.25								
LM308N	0.88								
LM311N	0.88								
LM319N	0.98								
LM324N	0.30								
LM348N	0.59								



# Carston

Used test equipment, calibrated to Manufacturer's original specification.

## ACOUSTIC & VIBRATION

BRUEL & KJAER	
1621 Tunable Band Pass Filter	550
2113 Audio Frequency Spectrometer	1400
2203 Sound Level Meter	450
2215 Sound Level Meter inc. Oct. Filter	1050
2218 Sound Level Meter inc. Leq.	1475
2305B Level Recorder inc. 50 dB pot.	1350
2625 Vibration pick-up amplifier	350
2808 Power Supply/Mains Adapter	90
2972 Tape Signal Gate	200
4230 Sound Level Calibrator	95
4423 Noise Dosimeter	350
4424 Noise Dosimeter	375
CASTLE ACOUSTICS	
CS181 Sound Level Meter & Calibrator	295
C.E.L.	
112 Environmental Noise Analyser	300
144 Environmental Noise Analyser	500

DAWE	
419C Audio White Noise Generator	190
1461CV Vibration Analyser	350
1463B 1/2 Octave Filter	200
1465 Octave Band Filter	150
KISTLER	
504A Charge Amplifier	200
WAYNE KERR	
B731B Vibration Meter inc. probe	270

## BRIDGES & V and I STANDARDS

ADVANCE	
TI Q Meter 100 KHz-100 MHz	160
CINTEL	
2773 Inductance Bridge	160
HEWLETT PACKARD	
4261A Digital Automatic LCR Bridge	975
4342 QLC Meter 22 KHz-70 MHz	1600
MARCONI	
TF868A Universal LCR Bridge	250
MUIRHEAD	
D30A DC Bridge 0.15%	180
PHILIPS	
PM6302 RCL Bridge — direct reading	395
WAYNE KERR	
B224 RCL Bridge 0.1%	500
B521 LCR Bridge	115
B801/CU681/Q801/SR268 VHF	
Admittance Bridge with source and detector transistor adapter & D.C. Control Unit for transistor measurements	750

## COMMS & CABLE TEST EQUIPMENT

DYMAR	
BC282 Battery charger for 883 Radio Telephone	80
883 Radio Telephone — VHF band — hand held	245
HEWLETT PACKARD	
3556A Psophometer 20 Hz-20 KHz	250
MARCONI	
TF2805 Data Line Analyser	600
NORTHEAST ELECTRONICS	
TF537B Psophometer/VU Meter	200
SEIMENS	
U2033 Psophometer	475

## S.T.C.

74184B Selective Level Measuring Set	600
74216A Noise Generator	300
74261A Psophometer	300
74262B White Noise Generator & Receiver	2000
74307C Level Measuring Set	175
74834C Distortion Measuring Set	500
96016 Selective Null Detector	200
GTA-2 Quantization Distortion Tester	800
GTA4B Pattern Generator	900
TEKTRONIX	
1502 TDR Cable Tester CRT + Recorder	2950

## COMPUTER EQUIPMENT

CENTRONICS	
702 matrix printer	500
TEKTRONIX	
4610-1 Hard copy printer for 4010 series computer display terminals	1800

## DIGITAL TESTING EQUIPMENT

HEWLETT PACKARD	
1600A Logic Analyser 16 ch 20 MHz	1400
1600S Logic Analyser 32 ch 20 MHz	2250
1602A Logic Analyser 16 ch 10 MHz	900
1607 Logic Analyser 16 ch 20 MHz	950
TEKTRONIX	
832 Datacom Tester R5232/V24	1150
833 As 832 plus BERT/BLERT feature	1300
7DOIF/DFI Logic Analyser/Formatter	2650
16 ch 50 MHz P/in	
7603/7DOIF/DFI As above with display mainframe	3600

## MAINS TEST EQUIPMENT

COLE	
T1007 Volt/Freq/Spike Monitor Rec O/P	110
DATALAB	
DL019 Mains Interface for DL905	300

# SPECIAL OFFER

## PHILIPS PM2454B £180

A.C. Analogue millivoltmeter.  
Frequency range 10 Hz – 12 MHz.  
12 ranges 1 mV-300 V F.S.D.  
Voltage and dB scale provided.  
D.C. output proportional to meter reading.

## COUNTERS & TIMERS

FLUKE	
1910A-1 125 MHz 7 digit Cntr. AC/Batt	300
1912 520 MHz 7 digit Counter	375
1912A01 As 1912A but inc. re-charging batteries	430
1920A 520 MHz 9 digit Counter inc. Brst. mode	575
1920A14 1250 MHz otherwise as 1920A	750
HEWLETT PACKARD	
5243L 20 MHz 8 Digit Counter	150
5245L 50 MHz 8 Digit Counter	200
5300A/5304A 10 MHz 6 Digit Counter	250
5300A/5305B 1300 MHz 6 Digit Counter	425
5345 500 MHz 11 Digit Counter Timer	2000
MARCONI	
TF2432 560 MHz 8 digit Counter	350
RACAL-DANA	
371 18 GHz 11 digit Counter with Source Locking facility	4950
8110 50 MHz 8 Digit Counter Timer	320
9024 600 MHz 7 1/2 digit Counter	220
9025 1 GHz 8 digit Counter	450
9520 10 MHz 4 Digit	95
9905 200 MHz 8 digit Counter Timer	360
SYSTRON DONNER	
6053 3 GHz 9 digit Counter BCD O/P	790
5103B Strip Printer for 6053/6054	375
TEKTRONIX	
DC501 7 Digit 100 MHz Counter — TM500 Plug-in	180

DRANETZ	
606 3ch Volts Av/Spike/Time/Printer	2950
616 2ch AC 1ch DC Volts/Av/Spike/Time/Printer	3300
GAY	
LDM AC/DC/Spike/Time inc. Printer	1250
MISCELLANEOUS	
A.I. INDUSTRIES	
TCS General Purpose Gas Leak Detector — intrinsically safe	290
BRADLEY	
192 Oscilloscope Calibrator	825
COMARK	
1601BS Thermom 10ch 87 + 1000°C type K N.B. Thermocouples not included	50
CROWCON	
71P Inflammable Gas Detector/Alarm	125
DATALAB	
DL905 Digital Transient Recorder/Display Storage	1050
FLANN	
16/11 Rotary Vane Attenuator WG16	250
HEWLETT PACKARD	
342A Noise Figure Meter	500
X382A Rotary Vane Attenuator WG16	175
MULTIMETRICS	
AF120 Dual H/Pass L7/Pass active filter 20 Hz – 2 MHz	600
PHILIPS	
PM 5501 Colour TV Pattern Generator	199

PM 6455 Stereo FM Generator  
PM 6456 Stereo FM Generator  
RESEARCH INSTRUMENTS  
Micro manipulator — 4 Probes moveable in all planes. Adjustable test table — Watson Barnett optics. Complete system mounted in perspex enclosure  
ROHDE & SCHWARZ  
BN252 Transistor Y Parameter Test Set  
S.T.C.  
74600J Attenuator 0-9 dB 50Ω in 1 dB steps  
74616A Attenuator 0-100 dB 600Ω in 0.1 dB steps

TEKTRONIX  
521PAL Vectorscope  
528 TV Waveform Monitor  
575 Semiconductor Curve Tracer  
1485C TV Waveform Monitor PAL/NTSC  
YELLOW SPRINGS  
YS157 Water Pollution Measurement System

## NETWORK ANALYSERS/ PHASEMETERS

GENERAL RADIO  
1710/11/12/14 0.4-500 MHz 115 dB range  
HEWLETT PACKARD  
8405A Vector Voltmeter 1-1000 MHz  
8414A Polar Display for 8410 N.W.A.  
8745A S Parameter Test Set 0.1-2 GHz  
11570A Accessory Kit for 8405A  
11600A Transistor Test Fixtures  
TO18/TO-72  
11602A Transistor Test Fixtures  
TO5/TO-12  
11604A Universal extension arm for 8745A  
11605A Flexible arm for 8743A

## OSCILLOSCOPES & ACCESSORIES

CROTECH  
(New CROTECH Oscilloscopes)  
3030 15 MHz 1 Trace 5mV built-in component tester  
3033 15 MHz 1 Trace 5mV battery operation  
3034 15 MHz 2 Trace 5mV battery operation  
3035 10 MHz 1 Trace 5mV built-in component tester  
3131 15 MHz 2 Trace 5mV built-in component tester  
3337 30 MHz 2 Trace 5mV with signal delay  
GOULD ADVANCE  
OS1000B 20 MHz 5mV 2 Trace  
OS3000A 40 MHz 5mV 2 Trace 2T base  
HEWLETT PACKARD  
182C 100 MHz Mainframe  
182T 100 MHz Mainframe with digital normaliser interface  
1804A 50 MHz 20mV 4 Trace Plug-in  
1825A Dual Timebase Plug-in  
1805A 100 MHz 5mV 2 Trace Plug-in

PHILIPS  
PM3207 15 MHz 5mV 2 Trace TV trig  
PM3211 15 MHz 2mV 2 Trace TV trig  
PM3212 25 MHz 2mV 2 Trace TV trig  
PM3233 10 MHz 2mV 2Ch fixed delay Dual Beam

TEKTRONIX  
465 100 MHz 5mV 2 Trace 2T base  
465B 100 MHz 5mV 2 Trace 2TB, inc Probes  
475 200 MHz 2mV 2 Trace 2T base  
475A 250 MHz 2mV 2 Trace 2T base  
485 350 MHz 5mV 2 Trace 2T base  
5842 2 T/base plug-in 50 MHz Trig for 5000 series Mainframe  
DD501 Digital Events Delay — P/in for TM500 series

CROWCON  
7A12 105 MHz 5mV 2 Trace Plug-in  
7A18 75 MHz 5mV 2 Trace Plug-in  
7A19 500 MHz 10mV 1 Trace Plug-in  
7A22 1 MHz 10μV Differential Plug-in  
7A24 350 MHz 5mV 2 Trace Plug-in  
7A26 200 MHz 5mV 2 Trace Plug-in  
7B53A 2 Timebase Plug-in 100 MHz Trig  
7B80 Single Timebase 400 MHz Trig  
7B85 Timebase with delay 400 MHz Trig  
7403N 75 MHz 3 slot M/Frame  
7603 100 MHz CRT r/out 3 slot M/Frame  
7704A 200 MHz CRT r/out 4 slot M/Frame  
P6013A X1000 12KV Probe  
TELEQUIPMENT  
D631/V1 15 MHz 2 Trace 1mV

Prices from £  
D63/V5/V5 15 MHz 5mV 2 Trace & fixed delay  
D75/V4/S2A 50 MHz 1mV 2 Trace 2  
T/base  
D83/V4/S2A 50 MHz 1mV 2 Trace 2T  
Big CRT  
D1015 15 MHz 5mV 2 Trace TV trig  
S61 5 MHz 5mV 1 Trace  
TEXSCAN  
DU120 Large CRT XY Display with 2 modulation  
Note: we hold a range of cameras  
P.O.A.

## OSCILLOSCOPES (STORAGE)

HEWLETT PACKARD  
181A 100 MHz Mainframe 5cm/μs  
1703A 35 MHz 10mV 2 Tr 2TB 1000 Div/ms  
TEKTRONIX  
466 100 MHz 5mV 2 Tr 2TB 1350cm/μs  
T912 10 MHz 2mV 2 Tr 1TB 250cm/ms  
T913 25 MHz 3 slot M/frame split screen 5cm/μs  
T913 100 MHz 3 slot M/frame 4.5cm/μs  
T934 400 MHz 4 slot M/frame 2500 cm/μs

## POWER MEASUREMENT

FLUKE  
8821A 10 Hz-20 MHz 4 1/2 digit & Analogue Tms & dBm  
HEWLETT PACKARD  
432A RF-Microwave Powermeter for use with 470 series sensors  
478A Co-ax sensor for 432 meter  
10 MHz-10 GHz  
4X86A Power sensor for 432 meter W.G. 16  
432A/478A combined price  
432A/4X86A combined price  
8481A Type N Coax sensor for 435A  
8482H Co-ax sensor for 435/436  
100 KHz-4.2 GHz  
MARCONI  
TF893A 10 Hz-20 KHz Powermeter  
2502 R.F. Powermeter DC-1 GHz 10W max  
2503 R.F. Powermeter DC-1 GHz  
100W max  
6460/6421 Microwave Powermeter & sensor 0.01-12.4 GHz

## POWER SUPPLIES etc

ADVANCE  
MG5-20 Switching PSU module 5V-20A fixed  
BRANDENBURG  
800 EHT Power supply 3-30 KV-1mA  
FARNELL  
B30/50-30V-5A variable  
B30/20-0-30V-20A variable  
FFSL 5V-20A PSU module  
H60/25-0-60V-25A variable metered  
L308 0-30V variable 1A Metered  
SB30/100-30V-10A variable  
TOPS/2 Twin 5V @ 5A + 15-0-15V @ 1A  
TSV700 0-35V-10A or 0-70V-5A variable metered

## HEWLETT PACKARD

6288B 0-40 V variable 30 A Metered V + I  
6966A 0-36 V variable 10 A Metered  
MARCONI  
E154 0-30V variable 2A metered  
PHILIPS  
PE1646 0-75V variable 6A Metered V + I

## PULSE GENERATORS

ADVANCE  
PG52A Modular pulse generator system — wide range of configurations — cost dependent on modules — typical  
PG5002D 0.1 Hz-1 MHz 50V 100Ω Double pulse R.T. 15ns  
HEWLETT PACKARD  
204C Oscillator 5 Hz-1.2 MHz  
204D Oscillator 5 Hz-1.2 MHz inc. 80dB attenuator  
608E Generator 10-480 MHz AM/Pulse burst mode

## RESEARCH

HEWLETT PACKARD  
6011A 0.1 Hz-20 MHz 16V 50Ω RT 10ns inc burst mode

Prices from £  
8016A Digital word generator to 50 MHz 9 x 32 bit  
LYONS  
PG73N 20 MHz 10V 50Ω R.T. 5ns  
RECORDERS & ACCESSORIES  
BRYANS SOUTHERN  
BS314 Chart 10" 4 Pen 16 speed  
BS316 Chart 10" 6 Pen 16 speed  
DCM  
8100W Wow & Flutter Analyser  
EM  
LVDT Linear Displacement & Transducer  
FYLDE  
154 Bridge supply and Amplifier  
HEWLETT PACKARD  
7015A XY 1 pen A4 size  
7046A XY 2 pen A3 size  
HONEYWELL  
5600B Instrumentation tape recorder 14 ch FM/DR  
MICRO-MOVEMENTS  
M10-120/A Compact UV 10 ch 7 speed recorder (inc. galvos)  
PHILIPS  
PM8041 XY 1 pen A4 size  
PM8251 Chart 10" 1 pen 12 speed  
SE LABS  
994 6 ch galvo preamp + DC bridge supply  
3006 UV chart 6" 6 ch  
6008 UV chart 8" 25 ch 16 speed  
6150/51 UV recorder 12 ch-inc 6 ch amps  
SMITHS  
RE541 Chart 8" 1 pen 8 speed  
RE501/4701 Ch4" + XY 1ch 10 spd  
AC Batt  
SOLARTRON  
3240 Modular Data Logger system  
RANK  
1740 Wow & Flutter meter  
Note: UV recorders are priced less galvos

## SIGNAL ANALYSIS EQUIPMENT

AIRMEC  
210 AM/FM Mod Meter 2.25 MHz-300 MHz  
248A Wave Analyser 5-300 MHz  
853 Wave Analyser 30 KHz-30 MHz  
MARCONI  
TF791D FM Mod meter  
TF2304 Mod meter AM/FM  
TF2330 Wave Analyser 20 Hz-50 KHz  
TF2330A Wave Analyser 20 Hz-76KHz  
TF2331A Distortion meter  
RADFORD  
DM52 Distortion meter 20 Hz-20 KHz  
SOUND TECHNOLOGY  
1700A Distortion Meter 10 Hz-100 KHz inc. oscillator  
WAYNE KERR  
A321 Wave Analyser 20 Hz-20 KHz  
Note: see also "Spectrum Analysers"  
SIGNAL/FUNCTION/ + SWEEP GENERATORS  
GENERAL RADIO  
1362 Generator 220-920 MHz  
GOULD  
J3B Generator 10 Hz-10 MHz D/P level meter & Attn.  
J4 Generator as J3 but no output level meter  
SG21 Generator — Square Wave only 0.3-100 MHz  
HEWLETT PACKARD  
204C Oscillator 5 Hz-1.2 MHz  
204D Oscillator 5 Hz-1.2 MHz inc. 80dB attenuator  
608E Generator 10-480 MHz AM/Pulse

## TELEVISION

8601A Gen/Sweeper 0.1-110 MHz Attn. AM/FM  
8614A Generator 800-2400 MHz AM/FM/Pulse  
8660C/8663A/8660A Synthesised Signal Generator 1-2600 MHz AM/FM digital readout, push button controls, BCD programmable  
8640B Generator 500 KHz-512 MHz AM/FM Phase Lock  
618B Generator 3.8-7.5 GHz  
612 Generator 450-1230 MHz  
614 Generator 0.8-2.1 GHz  
IEC  
F51A Function 1 mHz-10 MHz Sin/Sq/Tri/Pulse/Ramp  
LEVELL  
TG150DM Generator 1.5 Hz-150 KHz battery operated  
MARCONI  
TF144H/4S Generator 10 KHz-72 MHz AM  
TF801D Generator 10 MHz-470 MHz AM  
TF955/2 Generator 0.2-220 MHz AM/FM  
TF1066B/1 Generator 10-470 MHz AM/FM  
TF2000 Generator 20 Hz-20 KHz-111 dB attenuator  
TF2002/3MI Generator 10 KHz-72 MHz AM only  
TF2011/S Generator 96-140 MHz FM only  
TF2012 Generator 400-520 MHz FM  
TF2015 Generator 10-520 MHz AM/FM  
TF2015/1 Generator as 2015 with narrow FM deviation  
TF2015/2/171 Generator system with phase lock synchroniser  
TF2015-1/2171 Generator system with phase lock synchroniser  
TF2171 Synchroniser for 2015  
PHILIPS  
PM5108L Function 0.1 Hz-1 MHz Sin/Sq/Tri/O/P meter — 50 and 600Ω  
PM5127 Function 0.1 Hz-1 MHz Sin/Sq/Tri/Rmp  
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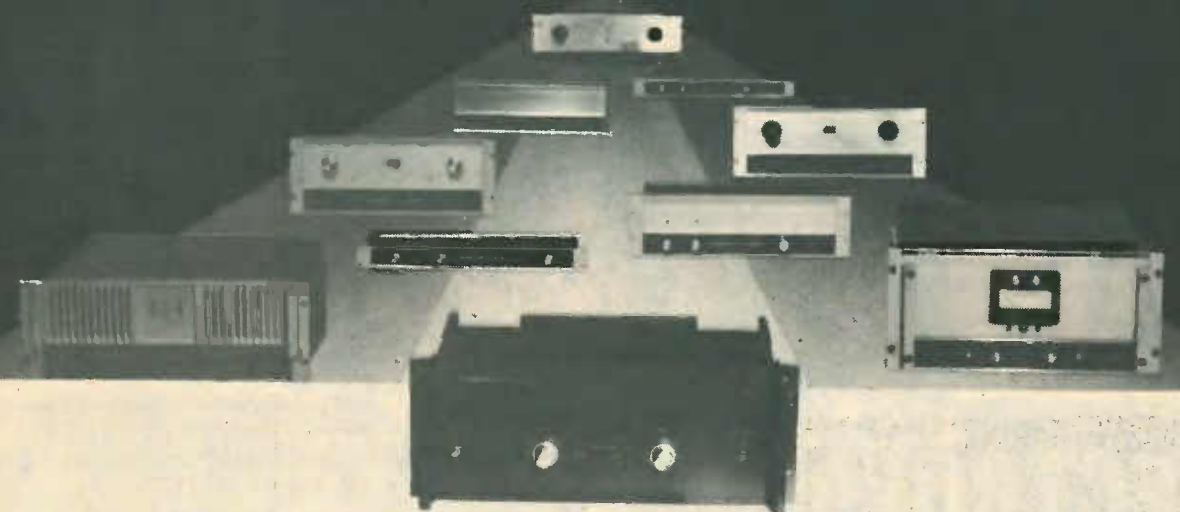
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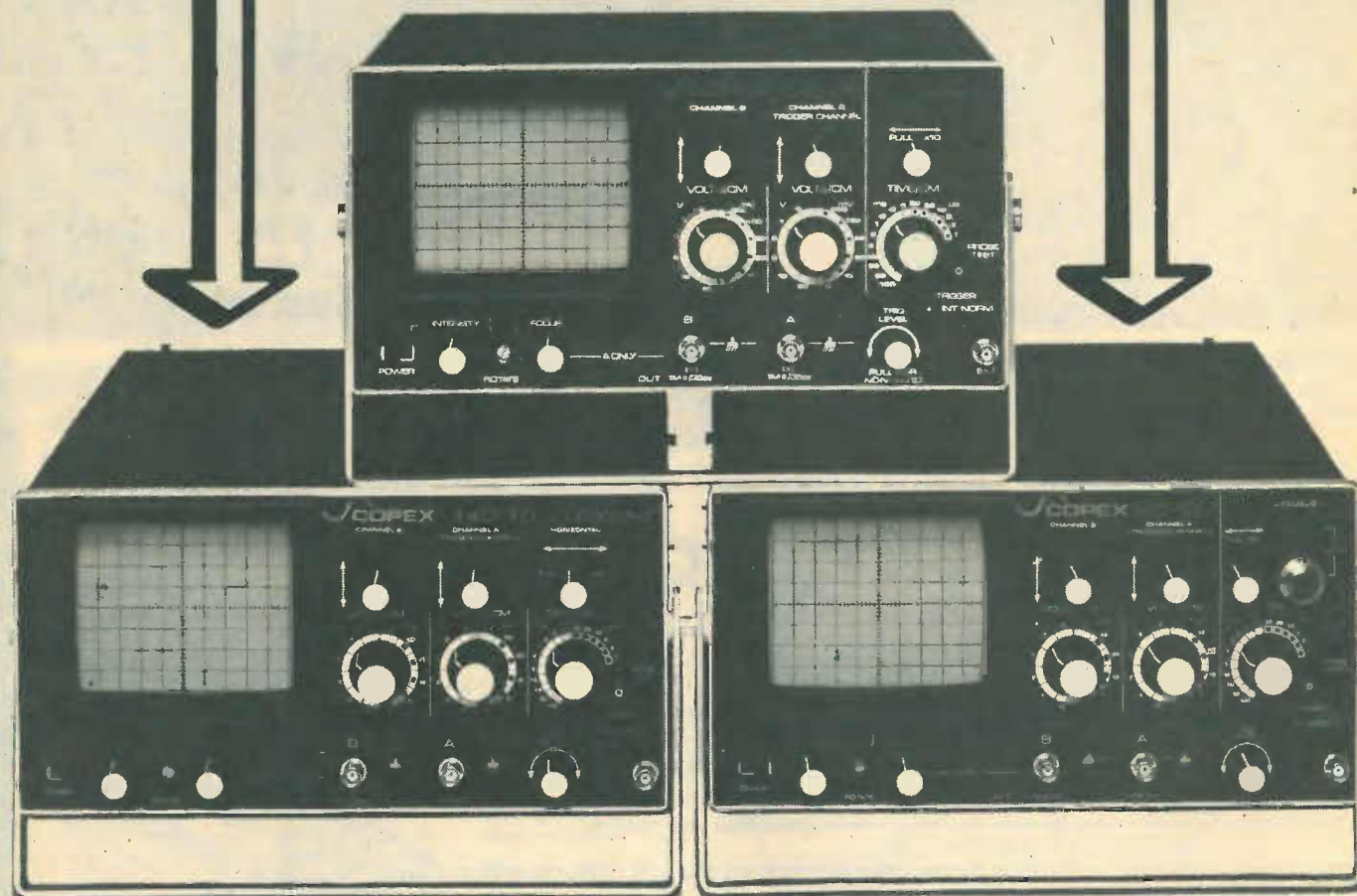


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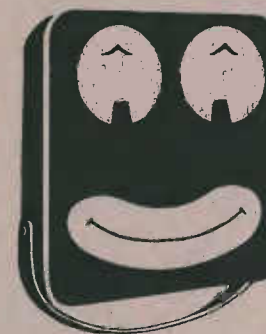
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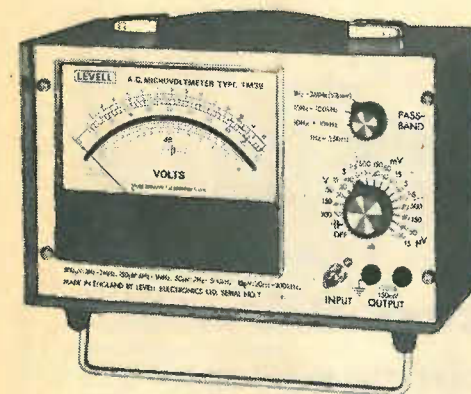
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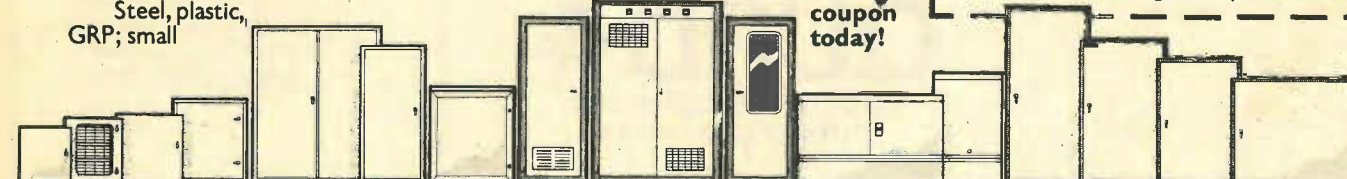
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## Engineering — or dominoes?

During the 1940s, at a grammar school in the north of England, the most wonderful things on display in the glass case outside the science laboratories were a cloud of glass-fibre wool and some coal with a fossil leaf in it. The glass was impossible because everyone knew that glass was hard and brittle and yet here was this soft (though scratchy) stuff made from it, and the coal was just so unimaginably old — older, even, than the physics master who had, some said, discovered fire. Simple things, goodness knows, but worth a couple of lessons in the physics class.

In those days, there was little talk of wireless in the classroom, let alone 'electronics'; classes were taken up with interminable experiments on the latent heat of vaporization and the laborious plotting of magnetic fields. Then, one day, a visiting teacher told the class of his wartime work on radar, speaking of microwaves, 'metallic insulators' and times measured in microseconds. This was a great deal more wonderful than the glass wool and bits of coal and led to rather a lot of daydreaming for some of the class.

Science teaching has advanced greatly in the ensuing 35 years. Microcomputers are becoming commonplace and labs are stocked with oscilloscopes, signal generators and all the other impedimenta of the electronic '80s. Pupils handle circuitry switching at 3ns or oscillators working at several gigahertz or truly compendious i.cs with remarkable nonchalance, if the youngsters seen on television programmes or in the news as competition winners are anything to go by.

It is, it goes almost without saying, necessary for the modern pupil to have the use of advanced, modern equipment. It is right that programming microprocessors should have taken the place of connecting components, in school, as in the world of

work. A micro, given the correct data and program, will do exactly what is expected of it very efficiently, as can be verified by a glance at the storage oscilloscope or logic display, but where is the striving? And, without the striving, where is the learning? Is there a danger of producing a great number of people who call themselves electronic engineers but whose knowledge of electronics stops short at an ability to program and an awareness of the cheapest supplier of interfaces?

The only answer to all these weedy, half-baked questions is that undoubtedly that is exactly what engineers will be like, and quite soon, too: there is no reason why they should be any different. It has been said for years that the microprocessor is a component, to be used as any other component. There can be little advantage to a user in knowing the precise details of the internal working of a micro — it can be regarded as a machine which will do its job when asked. It is not necessary to know the finer points of oscilloscope design to use one to its fullest extent: neither is it absolutely necessary to know more than the capabilities and characteristics of a micro, or any other i.c., to obtain the maximum performance from it. And when the remaining parts of circuits are also integrated, there will be no pressing need to understand the use of power transistors, or passive components, either, unless one has to design the i.cs. 'Systems engineering' will be supreme.

This is not, of course, to say that all engineers will be satisfied without a detailed knowledge of exactly what happens inside the i.cs. Perhaps these people will be the originators — the ones who, because they know more of the internal operation, will be able to apply i.cs with a greater imagination. But do not decry the simple user of modules: he will know all he needs to know.



# MICROPROCESSOR-CONTROLLED LIGHTING SYSTEM

*Stage and theatre lighting control is a complex task – yet a task easily handled by a microprocessor. As even the simplest of microprocessors can be programmed to provide and accept data for controlling a lighting system, these articles concentrate on using an existing microprocessor board to process and store complex lighting patterns set by conventional faders, and cover interfacing from digital data, to human input, to light dimmers. Software for the 8085A processor used in the prototype will be discussed in the third and final article.*

by John D. H. White and Nigel M. Allinson

This system is designed to simplify the control of complex lighting patterns as used in theatres and studios or at pop concerts. The prototype described in these articles made use of a commercially available 8085A processor board to control up to 256 lighting channels with 8-bit accuracy phase control. Here, we discuss the system's hardware and its ability to linearize the relationship between lamp brightness and fader position.

## Background

Before the introduction of high-power semiconductors the brightness of lamps in lighting systems was controlled by variable resistors or inductors. The cost and size of such inefficient power-control methods meant that systems were kept small and were usually difficult to operate. With high-power thyristors, it was possible to construct very compact dimmers which could be controlled remotely. Initially, this improved power control was used to copy the previous systems; however, the compact nature of the dimmers meant that much larger lighting systems could now be built and controlled. At present, "portable" lighting systems with 80 separate output channels are in common use for pop-group concerts and even larger systems are employed in tv studios and theatres.

All lighting-control systems may be split into two separate sections – the power-control section (the dimmers) and the control desk, which is used to control the dimmers. These are usually remote from each other, being connected by multi-core cable. Although the size of lighting systems has increased over the years, the control facilities available have remained rudimentary. A small number of digitally controlled desks are commercially available, though these are expensive and tend to be used in large, fixed installations.

The most common type of circuit used in an analogue control desk is outlined in Fig. 1. Each row of channel faders (presets) is voltage driven by a master fader (master preset). Outputs from each preset for a given channel are then gated together through diodes; thus the final

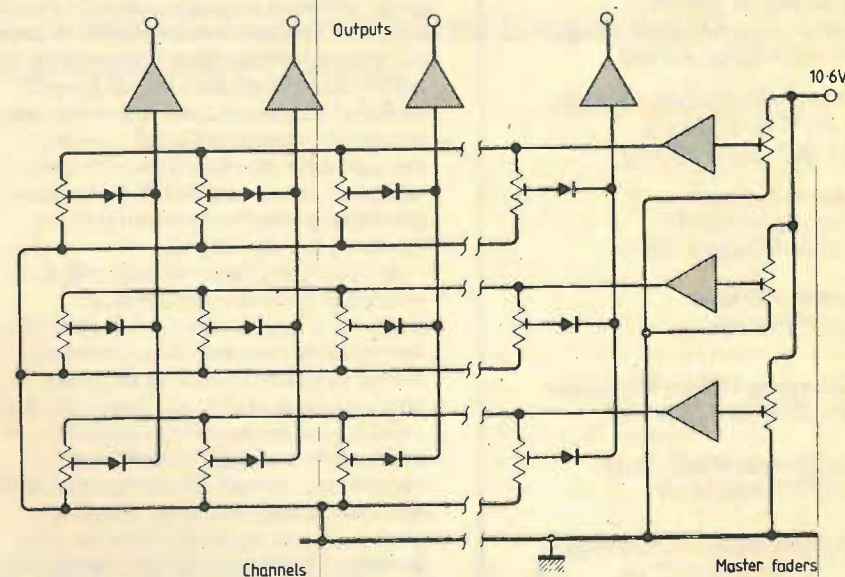


Fig. 1. This type of matrix is often used in analogue lighting-control desks. In this way, lighting patterns stored at preset fader positions can be recalled using the master faders.

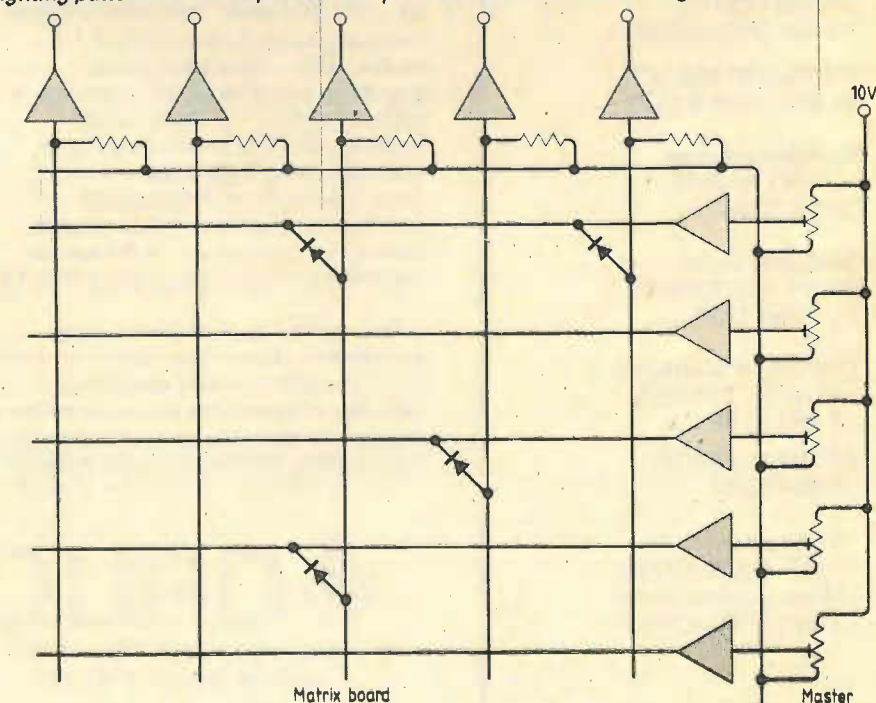


Fig. 2. Using this type of matrix, with plug-in diodes, a great number of lighting patterns can be stored cheaply but the ability to vary lamp brightness continuously is lost.

output from the control desk is the largest preset voltage for each channel. In this way, each master preset can be used to recall a stored lighting pattern (i.e. stored in a row of presets). Because of the cost of faders, the number of master presets is usually fairly small. For pop-group concerts and certain stage applications, the ability to control continuously the brightness of each light is forfeited to allow the storage of a greater number of lighting patterns. The patterns are created and stored by positioning pins, containing diodes, in interchangeable matrix boards, as indicated in Fig. 2.

As the dimmers will use different mains phases (total power requirements may exceed 500kW for a large system), a standard interface format between the control desk and dimmers is necessary. A direct voltage of 0-10V has become the convention in most lighting systems, 0V corresponding to the lamps being off, and 10V to full brightness. Figure 3 shows the schematic lay-out of a typical dimmer module. The d.c. control voltage is compared with a ramp synchronized with the line frequency, hence phase-control of the load is possible.

Before considering the output hardware, one other question that needs answering; how many control bits are required to give apparently stepless light output variations? For a very wide range of lighting conditions, it was found that seven bits were sufficient for "stepless" light control. Since the microprocessor is an 8-bit device and most of the integrated circuits used to construct the system are 4-bit devices, it was decided to use 8-bit codes throughout. This also provides some immunity to the effects of truncation errors in the output code from software calculations.

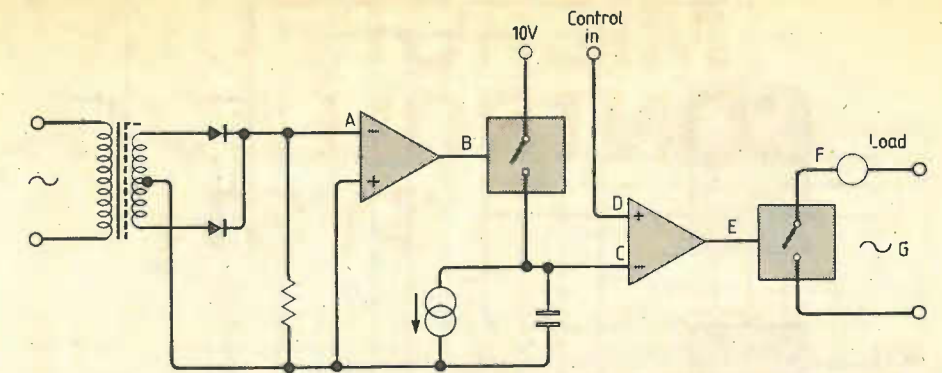


Fig. 3. Outline of a typical circuit. A d.c. control voltage is compared with a ramp synchronized with the line frequency, making phase control at the load possible.

## Circuit description

Because of the large number of output channels each dimmer unit must be kept simple and economical. Also, since one may wish to increase the number of output channels in the future, a modular design is advantageous. The overall output-control layout is shown in Fig. 5. Each dimmer module is enabled so as to accept data from the microprocessor data bus by a 2-bit code derived from the 8 low-order bits of the address bus. Hence up to 256 dimmer modules can be given a unique address. Conventional output ports could have been used to enable data transfer to each dimmer module. However, the 8085A processor instruction set contains only one output-port instruction (OUT port) and this can only be used in a direct-addressing mode, i.e., the second byte of the instruction must contain the port address. The restriction of direct addressing makes this method unsuitable for use in a lighting-control desk because of the large number of outputs required. The solution is to employ mapped-memory output, which uses a section of "memory locations" for

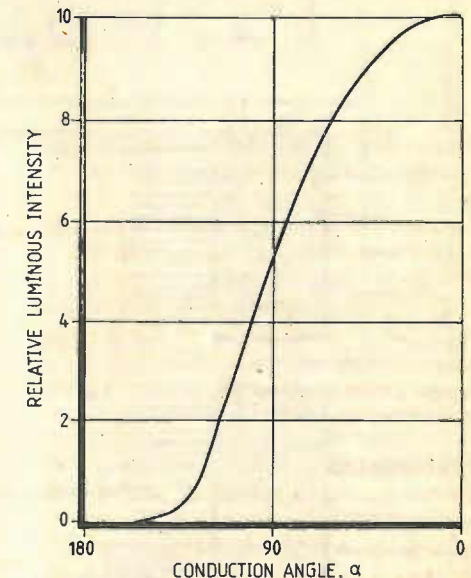


Fig. 4. Measured luminous intensity, as a function of conduction angle, for a 1000W lamp (see text).

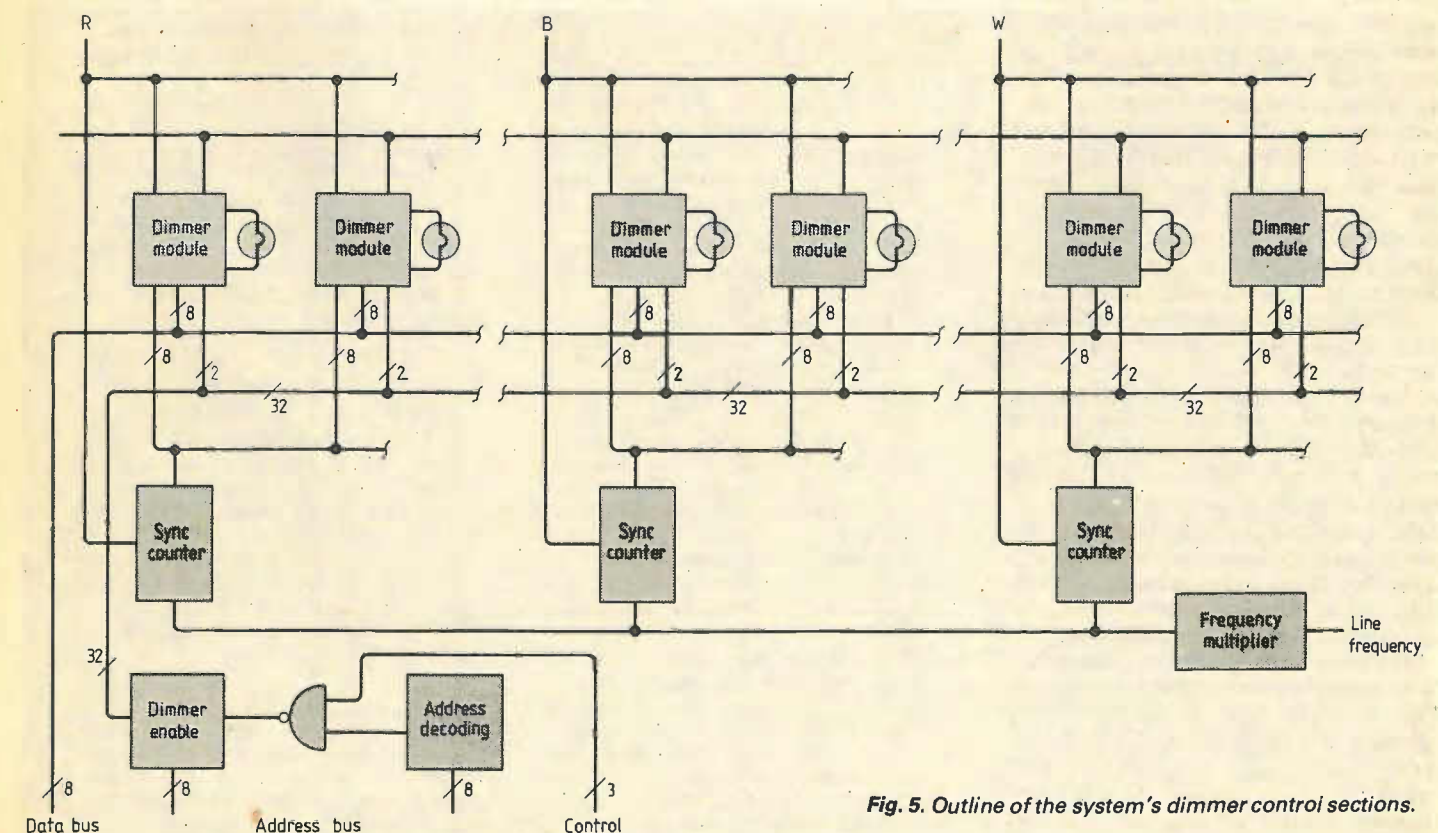


Fig. 5. Outline of the system's dimmer control sections.



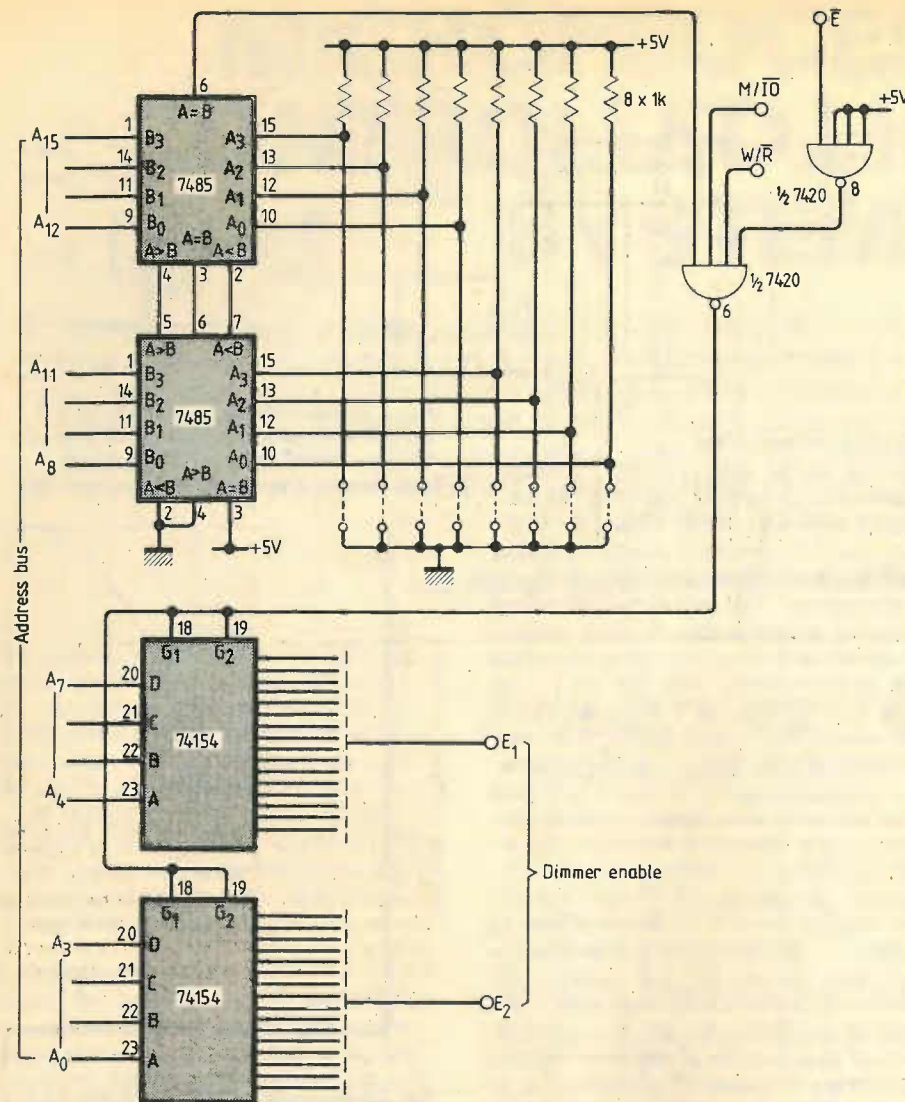


Fig. 6. Address decoding and dimmer enable module.

### Subjective brightness control

For full-wave control using a triac or inverse-parallel connection of two thyristors, the r.m.s. output voltage,  $V_o$ , is:

$$V_o = V_s \sqrt{\frac{\pi - \alpha + \frac{1}{2} \sin 2\alpha}{\pi}}$$

where  $V_s$  is the r.m.s. supply voltage and  $\alpha$  is the conduction angle. This, of course, assumes a purely resistive load. Tungsten lamps have associated with them some inductance and a thermal inertia, which affects their transient behaviour. The perceived brightness of a controlled light source is a complex function of the voltage, and hence the position of the fader on the control desk. A number of factors contribute to this function:

- The resistance of the lamp filament increases over a range of about 20:1 for its entire operating range.

- As the temperature increases, the spectral distribution of the radiant energy changes, approximately in accordance with Planck's distribution law. With increasing temperature, the peak of the radiant energy moves towards shorter wavelengths (i.e., the light is "whiter"). A tungsten-filament lamp may be considered as a near-perfect universal radiator.\*

- Due to the above, the fraction of the total radiant energy visible also changes. Mathematically, the visible output is the convolution of the modified Planck's distribution function and the standard luminosity curve of the human eye.

All these factors can be approximated, with reasonable accuracy, by the simple expression:

$$\text{luminous intensity, } I = kV_c^c$$

where  $k$  and  $c$  are constants for a particular type of tungsten lamp. The type of lamp (maximum voltage, wattage, etc.) has a slight effect on  $c$ . Most references consider  $c$  to lie between 3.2 and 3.5. Our experience suggests a slightly lower value for a wide range of lamp types. The measured luminous intensity as a function of conduction angle for a 1000W PAR64 lamp is given in Fig. 4. This general curve holds for all forms of tungsten lamp, and is used to linearize the relationship between lamp brightness and fader position in this system. It is worth noting here, that measured photometric brightness,  $L$ , of a surface (its luminance) is not generally the same as its subjective brightness,  $B$ . Subjective brightness is determined in part by the luminance of an object, and in part

output. This arrangement allows any instructions which write to memory to be used as output instructions, giving considerable advantages in the software as indirect addressing is permitted. A small amount of extra hardware is, however, required to decode the address lines to enable the outputs.

The digital equivalent to the linear-voltage ramp in an analogue dimmer is an 8-bit binary code counting from 0 to 255 in each line half-cycle. The 8-bit synchronous counter is clocked by 51.2 kHz signal derived by multiplying the line frequency. The counter is reset every line half-cycle by a zero-crossing detector.

Each dimmer module compares the latched 8-bit code from the control desk to the 8-bit code from the counter. When the counter output is greater than the control-desk code, a 51.2 kHz signal is applied to gate the thyristors, hence accurate phase control of the lamps is possible.

The complete lighting system will contain one address-decoding and dimmer-enable module, one frequency-multiplier module, three counter and reset modules (one for each phase used), and one dimmer module per output channel.

### Address decoding and dimmer enable module

The eight high-order bits of the address bus are compared with a bit pattern set by 8 wire-links to determine the location of the 256 output addresses in the memory map. Two cascaded 7485 4-bit magnitude comparators, see Fig. 6, generate a high-level signal when both inputs are equal. This signal, the M/I/O and W/R control signals and the system enable signal,  $\bar{E}$ , enter a NAND gate to give a signal which is high when valid output

by the conditions of observation such as the state of adaptation of the eye and the luminance of surrounding areas. The relationship between luminance and subjective brightness is still an area of active psychophysical research. Engineers are often satisfied with approximate relationships, and, from accumulated experimental evidence, a simple though approximate relationship is:

$$B = aL^\gamma$$

where  $\gamma$  is  $1/3$  or  $1/2$ , for dark or bright surroundings respectively.  $\gamma$ -correction is most commonly encountered in the design of tv displays. However, our experimental work with slowly increasing the brightness of lamps suggested that the best subjective linear increases in subjective brightness was obtained by ignoring  $\gamma$ -correction and simply using the relationship for luminous intensity. The inverse of the above function (i.e., the first two equations combined) is calculated for each discrete step in the dimmer control code.

\*This term is used in preference to black body because a very hot object or surface radiator will radiate visibly; "universal" applies to both absorption and emission. - Ed.

data is present on the data bus. The 8085A processor system employed in the prototype design was a Quarndon Electronics Ltd. QMS 85 8085 development system, which produces an overall system-enable strobe.  $\bar{E}$  will be low whenever the WR, RD or INTA of the 8085A is low. For "write" cycles, the data bus is stable while  $\bar{E}$  is active.

The valid-data signal is used to strobe the G1 and G2 inputs of two 74154 4-to-16-line demultiplexers connected to the eight low-order bits of the address bus. Two dimmer enable signals, E1 and E2, from the 32 outputs of the demultiplexers, give 256 unique addresses for the dimmer modules.

### Frequency multiplier module

A 51.2kHz clock signal for the 8-bit counters, shown in Fig. 7, is obtained by multiplying the line frequency by 1024. The phase-locked loop (NE565) has a feedback divider chain consisting of five 7474 dual D-type flip-flops. The capture range is set at  $\pm 2$ Hz. The t.t.l. input signal to the phase comparator is at half-wave rectified mains frequency. Although t.t.l. compatible, the square-wave output of the v.c.o. will only provide a current of about 1mA, so the output is buffered to drive the counter and divider chain.

### Synchronous counter and reset module

This circuit, shown in Fig. 8, generates a 8-bit binary code which counts from 0 to 255 in half a line period. The 51.2 kHz signal from the frequency multiplier is used to clock two cascaded 74161A 4-bit counters. The CLEAR inputs of these counters are used to reset them at the zero-crossing points of the mains. The full-wave rectified a.c. is applied to the voltage comparator (741). The output of the op-amp is inverted and converted to t.t.l. levels by the following common-emitter stage.

### Dimmer module

The 8-bit code from the control desk, through the data bus, is stored in two 7475, 4-bit bistable latches, Fig. 9. These latches are enabled, i.e., data on the data bus is transferred to their Q outputs, when the dimmer module is addressed by its own 2-bit dimmer enable signal, E1 and E2. Data stored in the latches is compared to the output of the counter by two cascaded 7485s. When the count from the counter is greater than the latch data, the 51.2 kHz signal is gated to the thyristors through some buffer stage and pulse transformer. Some interference and transient protection is provided by the inductor and capacitor.

### System performance

Some advantages of feeding data to a large number of channels have already been mentioned. Also, since the access time for each dimmer is less than the 410ns (the maximum data-bus access time permitted by the processor), no processor WAIT states are involved in transmitting data. This, of course, maximizes the data transfer rate for updating the dimmers and

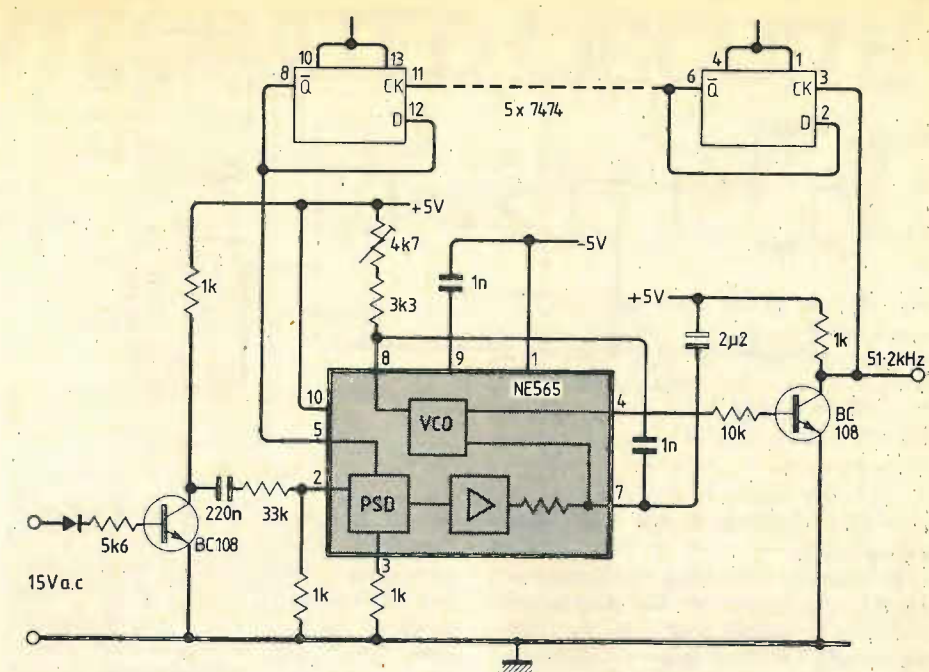
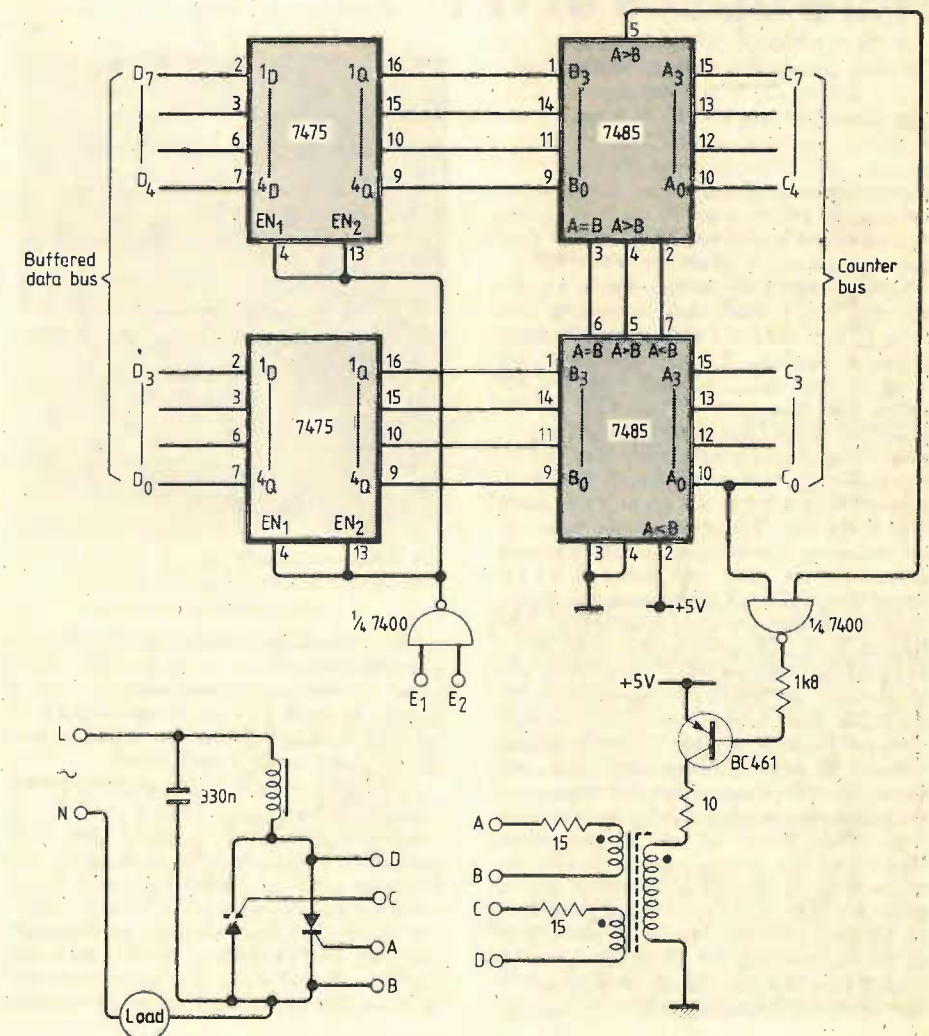
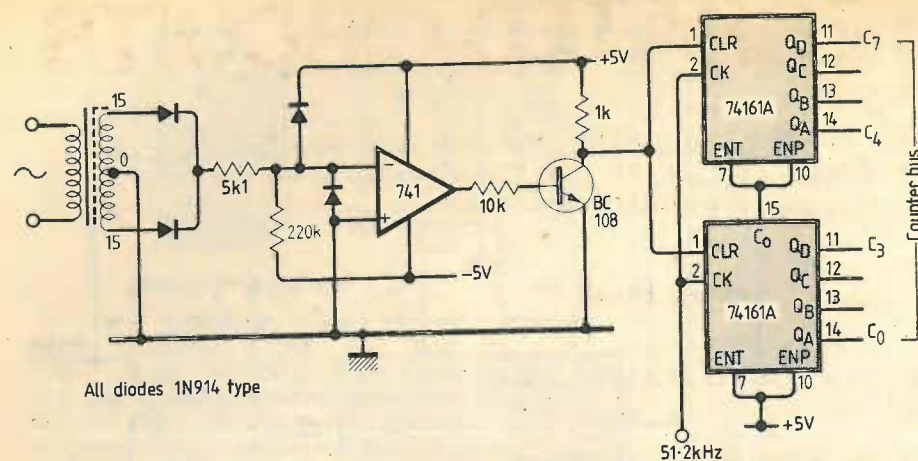


Fig. 7. This circuit is used to multiply the line frequency by 1024 to provide a 51.2kHz clock signal for the 8-bit counters.

Fig. 9. A dimmer module. The 8-bit code from the control desk is stored in two 4-bit bistable latches, and passes to the outputs when the enable signal, derived from E1 and E2, is given. When the counter input to the comparators is greater than the latch output data, the 51.2kHz signal is passed to the thyristors through a buffer stage and transformer.







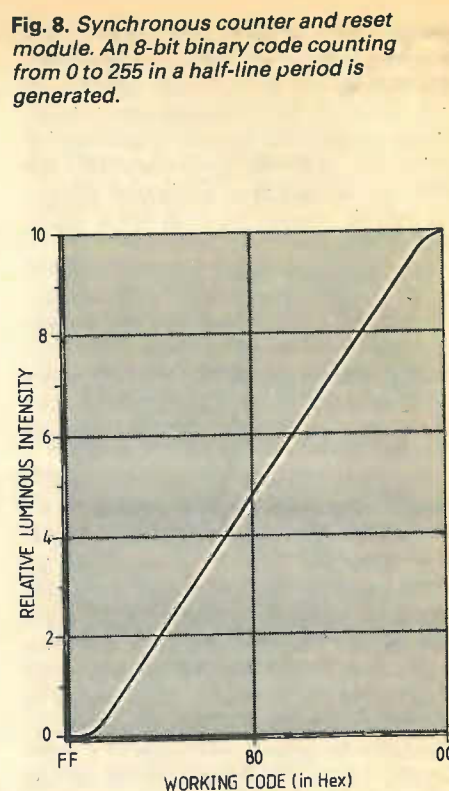
helps to produce a highly interactive lighting system.

The effect of linearizing the luminous output of the lamps with the position of the faders is indicated in Fig. 10. The output code FF corresponds to the lamp being off, and the code 00 corresponds to full brightness. The slight delay at the start is due both to truncation errors in forming the inverse function mentioned earlier and to slight measurement difficulties. It could be removed by incorporating a suitable offset in the output coding, but from an operating point of view there are quite

distinct advantages in having a definite "lamps off" position on the faders. In the system, the 256 values of this inverse function are held in a "look-up table" in the operating software. For a non-microprocessor system, there is no reason why these values could not be contained in a p.r.o.m.

The complete operating system not only provides routines for inputting and outputting data, but also various methods for processing the stored lighting patterns. In the next article, the control desk will be discussed. □

*To be continued*



**Fig. 10.** The effect of linearizing the luminous outputs of the lamps in relation to the fader position.

## Fibre optics at ITT

Joining optical fibres, especially in the field, is very difficult. ITT have developed a fibre optic splicing kit, the OFSK-10. Primarily intended for the joining of 50/125µm telecommunications grade fibres and other fibres of an all-silica construction, the kit uses an electric arc to fuse together the two ends. A V-groove jig has been developed to locate the ends accurately so that very high quality splices can be achieved.

Testing fibres in the field can also be a problem; it is very unlikely that the engineer has access to both ends of a cable but needs some method of locating a fault in a cable which can be up to 15km long, between repeaters. An answer has been provided by ITT in the OFR-3, an optical fibre reflectometer. If a short pulse of high intensity light is launched into an optical fibre, a small proportion of the light is reflected back towards the source from every point in the fibre. The reflections are 'backscatter' caused by imperfections in the molecular structure of the silica. The power of the reflected light, measured at the source end, decays exponentially with time, and by inference, with distance of the pulse into the fibre. The OFR-3 uses a laser to launch a pulse into the fibre and can measure and record the response from the reflections. Joins along the cable can cause extra reflections causing a peak in the response. Faults in the cable will cause drops in the response. The OFR-3 can display that response on an oscilloscope which includes an alphanumeric display of all the relevant parameters. With the use of a cursor any part of the response can be looked at in more detail and the oscillogram with all the data display can be printed out for permanent record. The 'scope and printer are incorporated into the equipment which also fits into a portable case. All the controls and the laser are incorporated in the lid. The laser fits



*The OFR-3 can trace faults in an optical fibre to within six metres over a length of 15000m.*

behind a locked hatch and cannot be switched on unless connected to a cable. Any fault can be traced to within six metres resolution over a distance of 15km. ITT are already working on the OFR-4 which will be able to inspect a cable of even greater length - up to 100km.

ITT are particularly proud of two new applications for fibre optics. There is a plan to link the British and French electricity grids. The one-hour's difference between the clocks in the two countries means that peaks occur at different times and an extra boost can be provided across the channel. To avoid the need for frequency matching, the link will be d.c. G.E.C. are building the U.K. end of the link. Rectification will be by stacked thyristors each of which will work

at a different potential and will therefore have to be isolated from the other in the stack. To avoid using a number of isolating transformers, the switching pulses will be carried to the thyristor gates by fibre optics cables. A special cable has been developed to withstand voltage potentials of up to 5kV/cm. In parallel with the development of the cable has been the design of an i.e.d. edge connector array for providing the individual pulse firing signals for each thyristor. The link is to be commissioned in 1985/86.

Another new application is a cable television link which is to be given a trial by British Telecom to 18 houses in Milton Keynes. The trial will use optical transmission based on p.f.m. (pulsed frequency modulation) in which the tv signal frequency modulates a square wave carrier which then drives an l.e.d. source. All the transmitter and receiver modules including the modulators and demodulators have been supplied by ITT Leeds.

BT are already running a cable tv service in Milton Keynes. For the trial, the programmes are down-converted into baseband and separated into individual channels (0 to 6 MHz PAL, video with sound). In addition a channel is formed consisting of the f.m. radio programmes on carriers in the range 0 to 7MHz. Each channel is fed to its own transmitter and a ten-fibre cable carries the channels to a distribution point. The cable used for the 3.5km primary link contains fibre of better than 4dB/km loss and 400MHz-km bandwidth-distance product. From the distribution point the secondary link of between 50 and 200m goes to each customer. Signal information and channel selection are transmitted back from the customer's end to a microprocessor control which provides the channel switching and can monitor information about transmission on both primary and secondary links. In the home the signal is received optically, demodulated to baseband and then up-converted to u.h.f. so that it can be fed into the aerial socket of an ordinary tv.

# 555-TYPE INTEGRATED CIRCUITS

*The 555 group of i.cs is one of the most popular ever made, with an enormous variety of applications in oscillators and timers. John Linsley Hood explains its internal design and method of operation*

by J. L. Linsley Hood

to the hobby electronics constructor, with several complete books of circuits having been published showing possible applications for this device. Yet, in spite of this, to most of its users, its method of operation remains needlessly obscure, and many attempted applications founder on inadvertent incompatibilities between the internal and external circuitry.

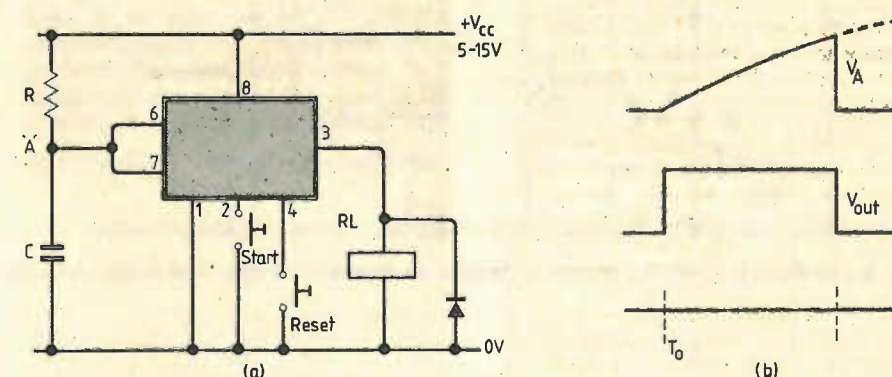
### Circuit description

The 555 is fundamentally intended to give an output voltage waveform, as a 'one-shot' or in a repetitive manner, at a low enough output impedance to operate a reasonably sensitive relay. To simplify calculations for the timing  $RC$  chain – in which the time constant  $RC$ , in seconds, is the time taken for a capacitor  $C$  to charge through resistor  $R$  to 63.2% of the applied voltage – the internal voltage switching levels are chosen so that the external timing capacitor charges through about this voltage differential. A simplified block diagram showing the internal arrangement is given in Fig. 1.

In this, the heart of the circuit is a bistable 'flip-flop' with an external overriding reset input  $R$ . The two normal inputs are the threshold and the trigger connexions, both of which are fed in through relatively high-impedance buffer amplifiers, connected, respectively, to reference voltages of  $\frac{2}{3}V_{cc}$  and  $\frac{1}{3}V_{cc}$ , derived from the 15k resistor chain. Two buffered outputs from the flip-flop are provided through amplifiers  $A_1$  and  $A_2$ , the first of which is a normal 'totem pole' output arrangement, as typically used in t.t.l. logic, to give a fairly low output impedance, and good current-sourcing characteristics. The second output, from  $A_2$ , is derived simply from a single transistor 'open collector' stage.

The way in which the 555 would normally be connected to operate as a 'one-shot' timer driving a relay, is shown in Fig. 2(a). In this the threshold input and the discharge (open-collector amplifier) output are joined together, and taken to the junction of timing resistor  $R$  and timing capacitor  $C$ ; the timing cycle is initiated by

**Fig. 2. 555 as a one-shot relay timer, with manual start and reset.**



**Fig. 1. Operations inside the 555.**

a momentary operation of a push-switch connected to the trigger input. This sets the Q output from the bistable, and both of the non-inverted outputs from  $A_1$  and  $A_2$ , to a high state. In the case of  $A_1$ , this will energize the relay  $RL_1$ , and in the case of  $A_2$ , the result will be that its output becomes an open circuit, so that the timing capacitor  $C$  is free to charge up towards the  $+V_{cc}$  line.

Once the Threshold input level has reached  $\frac{2}{3}V_{CC}$ , the 'reset' input to the bistable,  $R$  in Fig. 1, is taken high, when it reverts to its initial state, with  $A_1$  output 'low' - so that the relay is de-energized - and  $A_2$  at a low impedance. This holds the



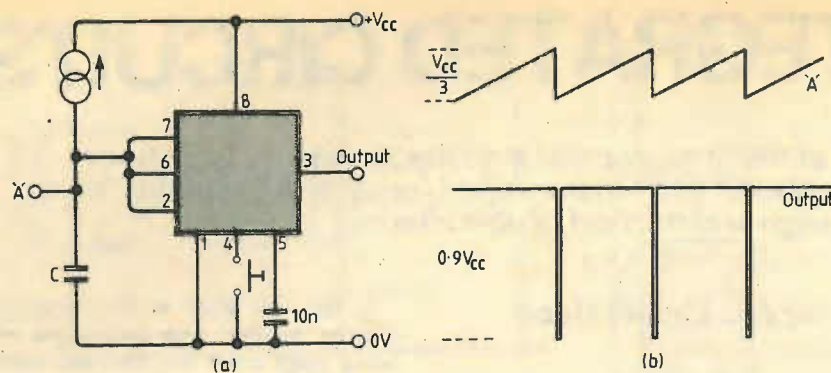


Fig. 3. Connexion for a free-running oscillator, with a frequency determined by the constant-current source and the value of  $C$ .

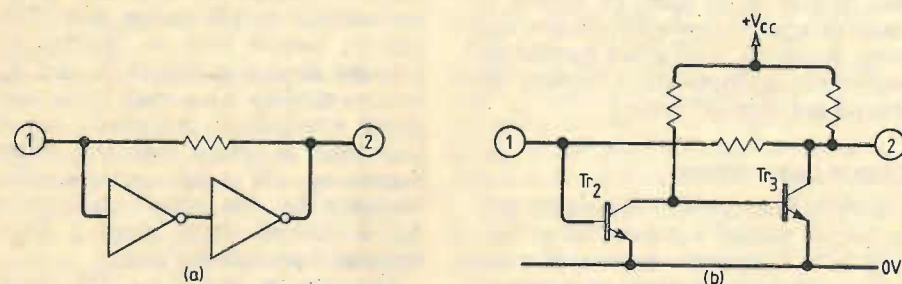


Fig. 4. Flip-flop block of Fig. 1 in logical form at (a) and in its practical arrangement at (b).

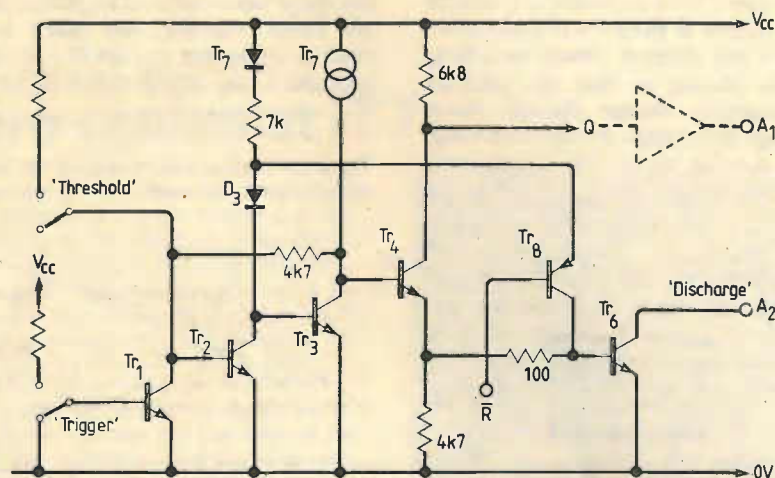


Fig. 5. Flip-flop ( $Tr_2$  and  $Tr_3$ ) shown in relation to threshold, trigger and output circuitry.

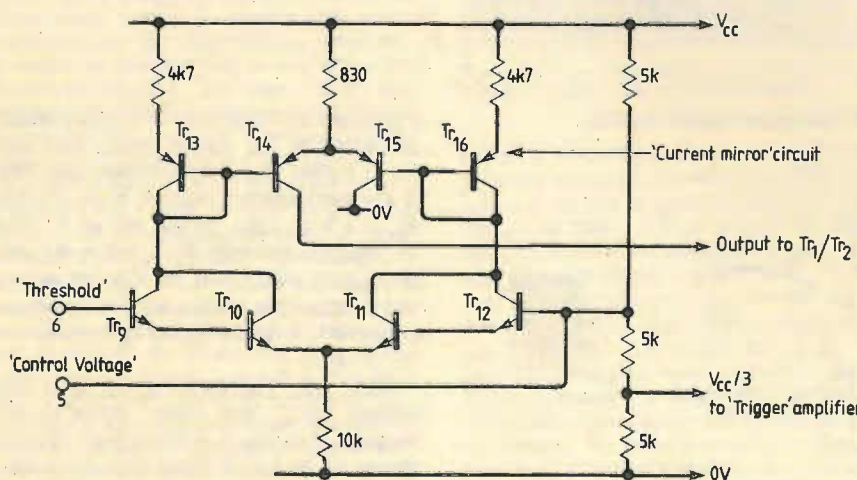


Fig. 6. Input amplifier for threshold voltage.

timing capacitor discharged and at a potential close to the 0 volt line level, ready for a further timing cycle to be initiated, by an input at a level less than  $\frac{1}{3}V_{cc}$  being applied to the Trigger. The output waveforms are shown in Fig. 2(b).

Since the Trigger input is also taken to the bistable through a impedance buffer amplifier, it is practicable to connect this to the timing circuit as well, without imposing too much of a static load. This will convert the circuit into a 'free-running' sawtooth generator, with an output of  $\frac{1}{3}V_{cc}$ , as shown in Figs 3(a) and 3(b). Moreover, if the timing resistor  $R$  is replaced by an appropriate constant-current source, the output at point A will be a highly linear waveform, suitable for use in a time-base generator, and with a sync. input available at the override reset  $R$  of the bistable.

The bistable flip-flop is itself a very simple arrangement, shown schematically in Fig. 4(a) and in its practical form in Fig. 4(b). In this circuit, if the input (1) is taken high, even momentarily, the output will also go high and remain at that state. Similarly, if the input is taken low, the output will also follow, and remain. The fact that the transistor circuit of  $Tr_2$  and  $Tr_3$  can be made to behave like this depends on the characteristic that a transistor turned hard on will have a collector-emitter voltage drop of only some 0.1 to 0.4 volts, depending on construction and  $I_b$  and  $I_c$ , whereas the minimum voltage necessary at the base, for conduction, will be at least 0.5 volts in a silicon device.

The way in which this circuit is organized, with respect to its output circuitry, and its threshold, trigger, and reset inputs, is shown in Fig. 5. Because the transistor  $Tr_8$ , in the reset circuit, acts as a switch directly connected between the positive end of  $D_3$  and the discharge circuit open-collector amplifier, this will cause  $Tr_3$  to be turned off, with  $Tr_4$  and  $Tr_6$  turned on. This will reset both  $A_1$  and  $A_2$  outputs to the low level.

While this input, being connected later in the circuit than the trigger input, will over-ride the trigger signal, if the trigger input is held low, the circuit will revert to the operating condition, with  $A_1$  high and  $A_2$  open circuit, as soon as the reset signal is removed.

The two input amplifiers used in the threshold and trigger circuits, are of similar form, as shown in Figs 6 and 7, using Darlington connected, four-transistor, long-tailed pairs. However, it should be borne in mind, as explained in the first article of this series on the 741, that the integrated circuit manufacturing process does not normally allow the construction of p-n-p transistors, within the i.c., which have a very high current gain, except in the circumstance that their collectors are directly connected to the substrate, (which is normally the 0V line). Since the input p-n-p transistors of the trigger circuit do not meet this condition, they must be of the 'lateral' type, which gives an inferior input impedance to this amplifier to that of the n-p-n input devices

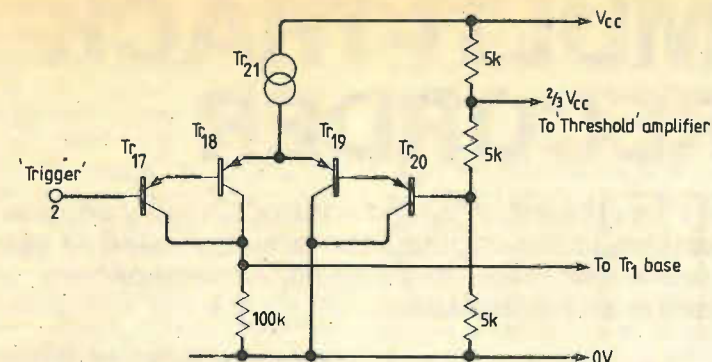


Fig. 7. Trigger input amplifier, using p-n-p transistors.

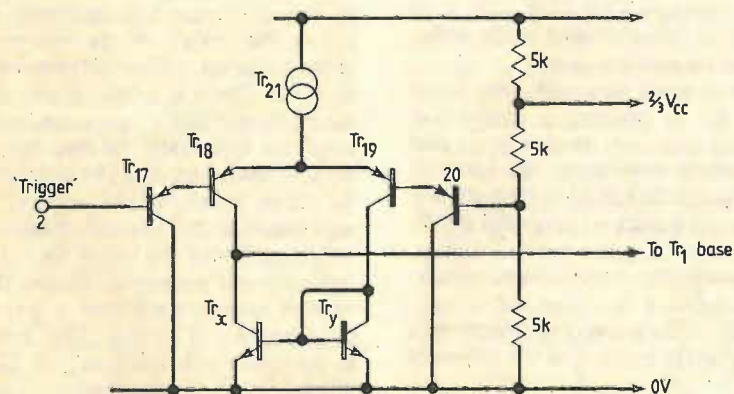


Fig. 8. Improved trigger amplifier, using higher-gain p-n-p transistors and a current-mirror collector load for  $Tr_{18}$ .

used on the threshold circuit input. To compensate somewhat for this deficiency, the trigger amplifier input circuit is operated at a very low collector current. Nevertheless, the input impedance for this circuit is still some five times lower than for the threshold input. In the National Semiconductor LM555, this circuit is modified, and improved, as shown in Fig. 8, to use a better type of input p-n-p transistor, together with a current mirror collector load ( $Tr_x$  and  $Tr_y$ ).

The complete circuit of the 555 is given

in Fig. 9, to show how the separate elements are connected together. Although the circuit is referred to in the data books as linear, because its operation is essentially digital in form, switching rapidly from one stable state to another, there is no need for any of the h.f. compensation of the amplifier elements customary in normal linear devices. This allows very fast rise and fall times at the output, of the order of 100ns, and

Fig. 9. Complete circuit of Signetics NE555.

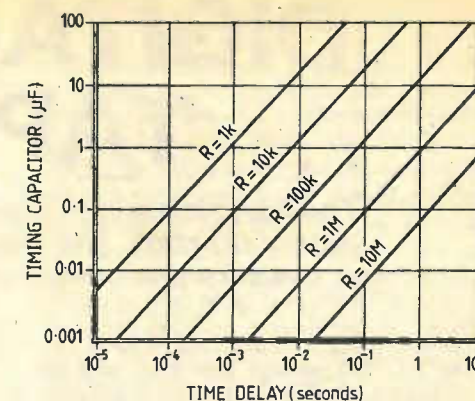
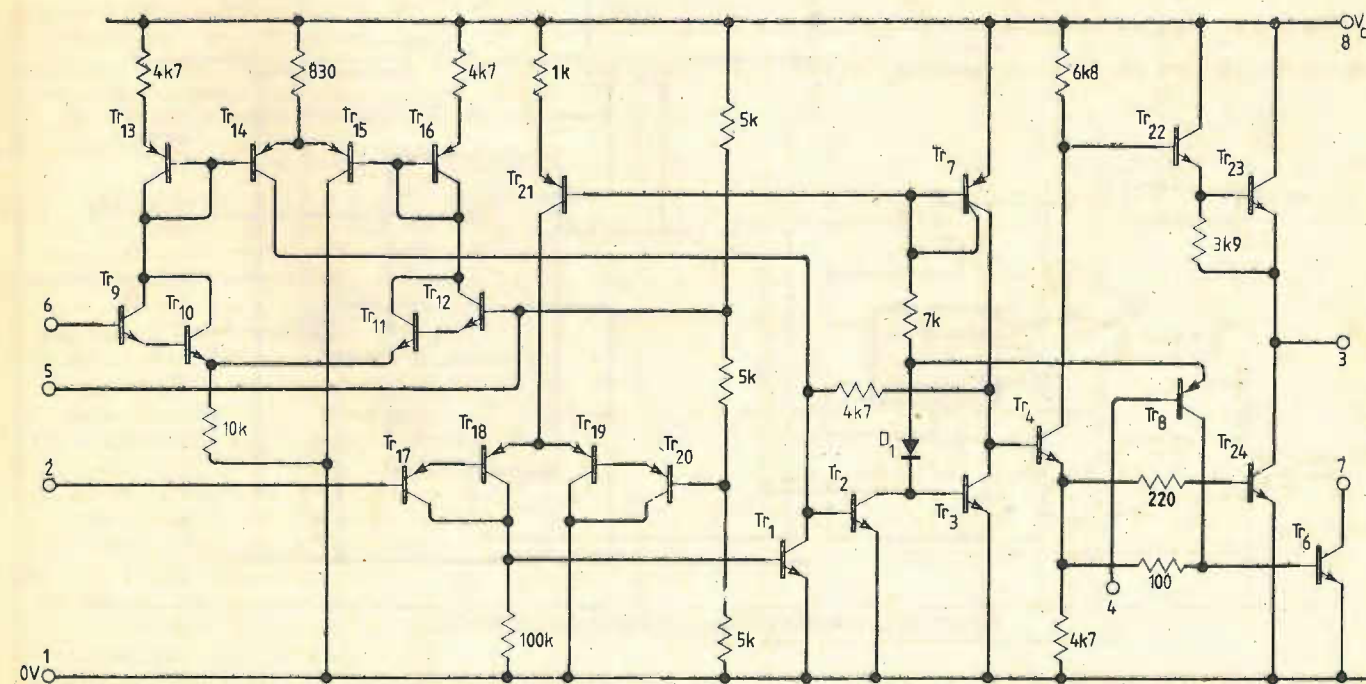


Fig. 10. Time delay as a function of  $R$  and  $C$  in Fig. 1.

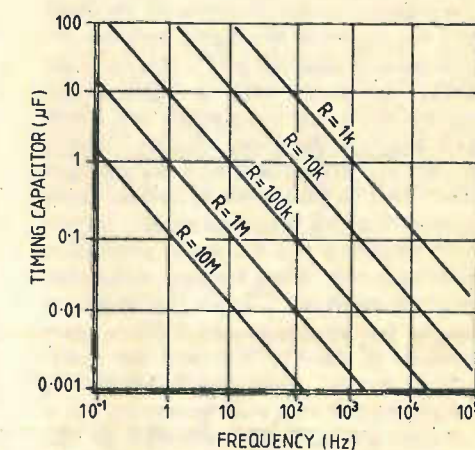


Fig. 11. Variation of Fig. 3 oscillator frequency with  $R$  and  $C$  (constant-I source replaced by  $R$  if sawtooth linearity not important).

repetitive operation at frequencies approaching 1MHz.

Typical time delay and free-running frequency graphs are shown, for completeness, in Figs. 10 and 11. □



# DIGITAL, MULTI-TRACK TAPE RECORDER

The final article in this series describes the motor speed control circuitry and the power supplies. The few modifications to the original tape recorder, used as the basis for this design, are also presented, with advice on adjustment of bias, equalization and signal level.

by A. J. Ewins, B.Tech.

The VLF910 cassette tape-deck used in the Hart version of the Linsley Hood cassette recorder uses only one motor for the capstan drive, take-up spool and rewind spool. In spite of this, and though relatively cheap, its specifications are excellent and the success of the digital recorder design is due in no small part to this excellent deck. The motor used is called a frequency-servo type and consists of a motor unit and tachogenerator. Earlier versions of the VLF910 deck used a motor, type R14-7430, 03Y8D, with a built-in tachogenerator which produced an a.c. output with amplitude and frequency proportional to its speed. When running at the normal tape speed of 1 7/8 in/s, the frequency output was approximately 456Hz. Later versions of the VLF910 deck use a different motor, type MMX-6H2LSB, which, instead of a tachogenerator, has a rotating magnetic disc attached to the motor shaft and an associated Hall-effect i.c. When running at a tape speed of 1 7/8 in/s, the output on one of the pins of the Hall-effect i.c. is a pulse train of frequency about 912Hz. (Although the figure of 912Hz is claimed as approximate with res-

Research Department, London Transport.

pect to a tape-speed of 1 7/8 in/s, it is exactly double that produced by the tachogenerator of the earlier motor).

Both motor types have additional built-in electronics to produce a closed-loop servo system. Although the motors are said to be frequency-servo types, the speed of the motor is not locked to a reference frequency: the frequency so produced by the 'tachogenerators' is converted to a voltage, using a pulse-width discriminator circuit, and then compared to a reference voltage. The stability of the speed of the motor thus depends upon the stability of the reference voltage.

For accurate speed control of the tape-recorder, the motor speed must be locked to a reference frequency. The importance of this speed control is not so great during the recording process, but absolutely vital during playback to ensure that the temporary storage buffers are filled with data at precisely the same rate as they are emptied. Short-term wow and flutter content of the data is not important because the number and length of the tempo-

rary storage buffers are designed to cope with this short-term variation.

The block circuit diagram of the tape-recorder speed control circuit was shown in Fig. 11 in part 2 of the series: Fig. 47 shows the circuit of the reference frequency selector, v.c.o. and phase sensitive detector. The v.c.o. and p.s.d. are contained within the c.m.o.s. phase-locked-loop i.c., type 4046. So that the tape-recorder speed control can be self-contained, the v.c.o. is used as the frequency reference source in the absence of any external reference. Using the values for the timing capacitor and resistor as shown, the 5k $\Omega$  variable resistor is adjusted to give an output frequency of 455Hz. (This is the same as the tape-clock frequency of 22,755Hz divided by 50.) In the absence of an external frequency input, the reset input to the 4017 counter will be at the logic 0 level. The output from the v.c.o. clocks the counter so that eventually the '5' output becomes logic 1, disabling the counter. In this condition, the carry-out, CO, is at logic 0. The output from Nand 2 is thus at logic 1 and the output from Nand 3 is the inverted v.c.o. signal. Nand 4 inverts this signal yet again, presenting a non-inverted v.c.o. signal to the input of the Ex-Or

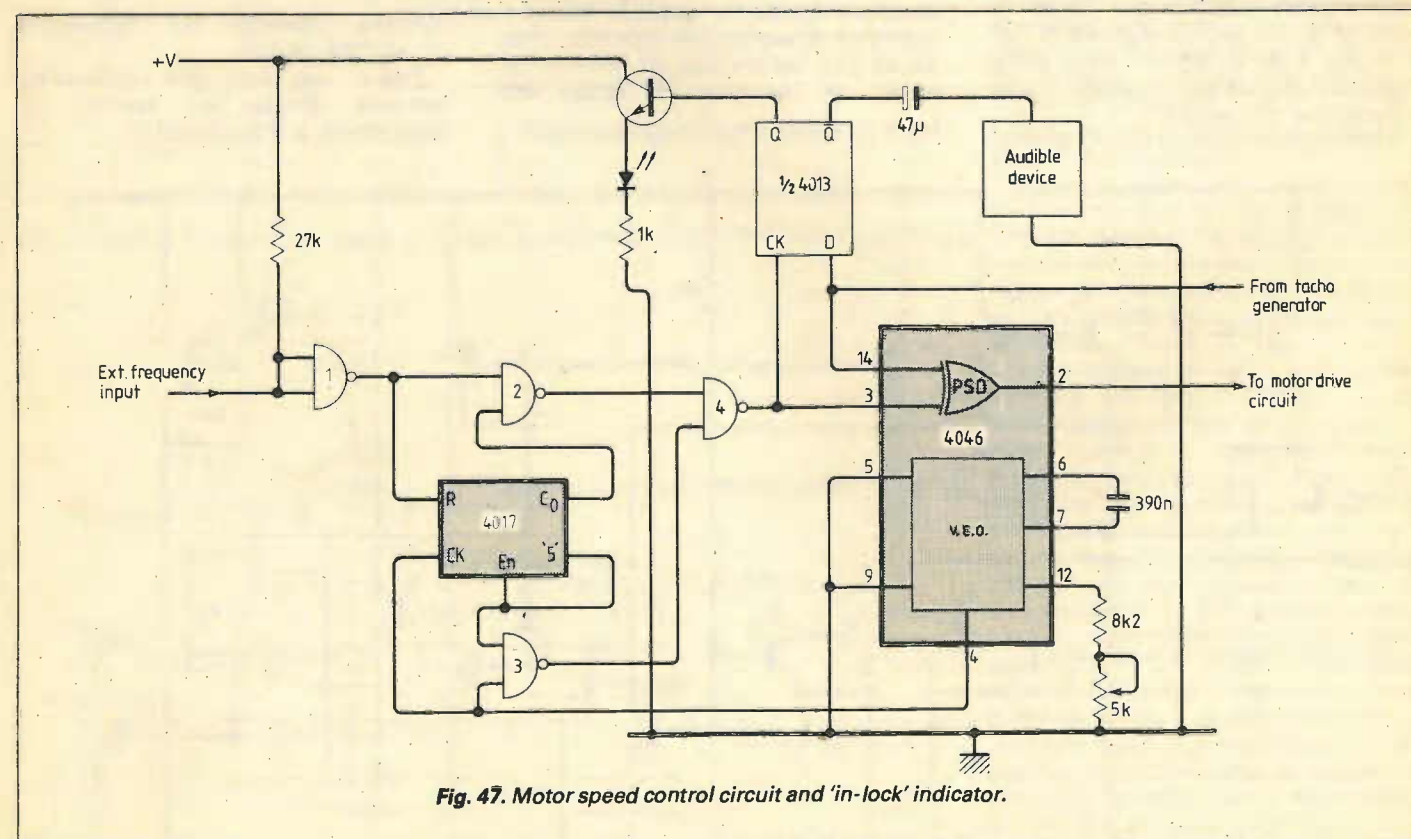


Fig. 47. Motor speed control circuit and 'in-lock' indicator.

p.s.d., whose other input is that from the tachogenerator pulse shaper. When the phase-locked loop of the speed-control system is in lock, the frequency from the tachogenerator pulse shaper is exactly that of the v.c.o., but it leads it in phase by about 90°. Consequently, the D input to the D-type flip-flop is at the logic 1 level when the Ck input goes positive, putting a logic 1 on the Q output of the flip-flop, lighting the l.e.d. and giving a visual 'in-lock' indication. With logic 0 on the Q output, the audible indicator is silent. In the event of a loss of lock the l.e.d. will flash and the audible indicator will warble at a frequency dependent upon the rate of slippage between the two frequencies.

The output from the p.s.d. is passed to the motor drive circuit of Fig. 48(a) or (b). It is filtered by a lead-lag low-pass filter, consisting of the 100k input resistor to the 351 op-amp and the 39 k plus 5μF capacitor (11μF in Fig. 48(b)) feedback loop. The low-frequency gain of the inverting op-amp is limited to unity by the 100k feedback resistor. The resulting out-

put from the op-amp drives the motor via the emitter-follower circuit using a Darlington power transistor, TIP121. The 10k resistor and base-collector feedback capacitor of 1nF provide some necessary high-frequency cut-off to the emitter-follower stage. The values of the filter components were found by trial and error to produce a stable and trouble-free p.l.l. servo system under all conditions of Play, Rewind and Fast Forward operation of the deck.

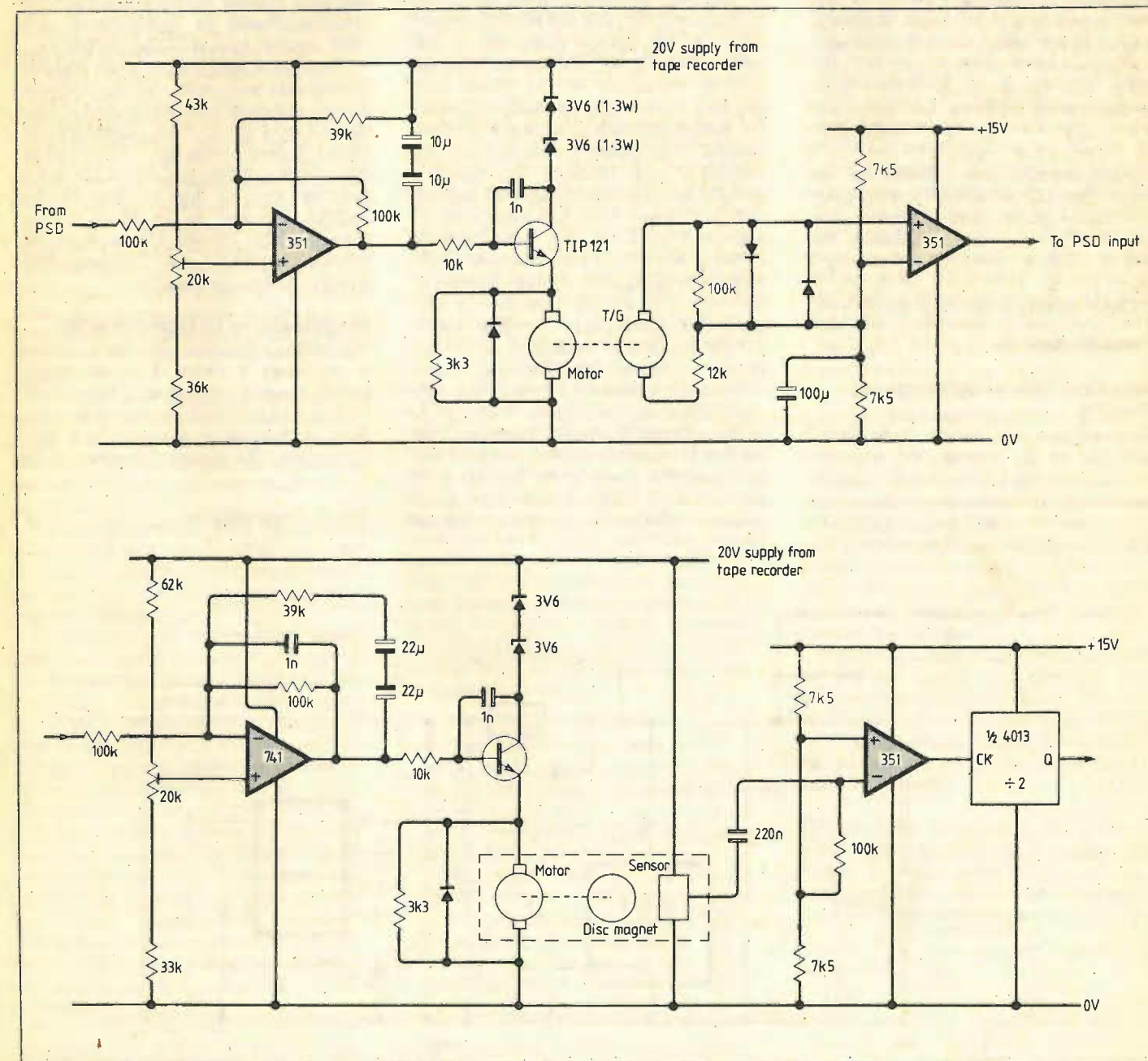
The direct offset voltage produced at the output of the op-amp by the potential divider circuit on the non-inverting input is essential to the self-starting action of the servo system. The 20k resistor should be adjusted such that the p.l.l. finds lock in one or two seconds after pressing the Play, Rewind or Fast Forward keys. If the voltage on the non-inverting input is too low, the p.l.l. will not find a 'lock', the

motor speed remaining too low; if it is too high, the loop will find and lose its 'lock', the motor speed ending up too high. When a satisfactory setting for the 20k resistor has been found it will be observed that the tachogenerator waveform leads the v.c.o. output by a little more than the ideal 90°. This phase difference will change a little under varying load conditions but should not vary so much as to lose lock.

The tachogenerator pulse shaper circuit shown in Fig. 48(a) is that for the motor with the built-in tachogenerator, while that in Fig. 48(b) is for the motor with the mechanically coupled magnetic disc and Hall-effect sensor. Because the output from the speed sensing circuit of Fig. 48(b) is exactly double that of Fig. 48(a), the output from the pulse shaper is divided by 2.

C.m.o.s. circuits of Fig. 47 and the pulse shapers of Figs. 48(a) and (b) are powered from a 15V supply, which is provided by a 15V, 100mA regulator powered by the cassette recorder's 20V stabilized supply line. The 20V supply powering the

Fig. 48. Motor drive circuit and tacho pulse shaper. Version for motor Type R14-7430, 03Y8D is at (a), while that used for motor Type MMX-6H2LSB is shown at (b).





motor drive circuit is that normally supplied to the positive lead of the motor, switched by the various keys of the cassette deck.

### Motor modifications

Both types of motor may be removed from their outer casings by careful removal of the back-plate. For motor type R14-7430, 03Y8D, the built-in electronics should be completely removed. The tachogenerator output is identified by two yellow leads, whilst the motor contacts are two terminal posts to which the internal p.c.b. is soldered. The two yellow leads should be extended, and two wires, red and black, should be soldered to the two terminal posts of the motor, making certain which is the positive and negative terminal. Reversal of these two motor connections will result in the motor running backwards, but no damage will be done.

With the back off the motor type MMX-6H2LSB, the frequency output of the Hall-effect sensor should be identified before any modifications are carried out. This is done by running the motor from a nominal 12V source and using an oscilloscope to identify the frequency output pin of the i.c. Having done this, remove the power transistor of the built-electronics: this automatically breaks the internal servo loop. A low-value resistor from the positive supply line to the positive pin of the motor drive should then be removed, and a link made from the negative pin of the motor drive to the negative supply line. Connections then need to be made to the positive supply line of the built-in electronics, the positive pin of the motor drive, the negative supply line of the built-in electronics and the frequency output pin of the Hall-effect i.c.

### Use of the reference frequency circuitry

When operated with the rest of the digital electronics of the recorder, the reference frequency for the speed control circuit is supplied by the 'reference frequency circuitry', shown in block form in Fig. 11 of part 2. During the recording process, the

reference frequency is the TC frequency of 22,755.5Hz divided by 50, i.e. 455.1Hz. When this source is connected to the external frequency input of the motor speed control circuit, the internal v.c.o. source is automatically 'knocked-out'. The 4017 counter of Fig. 47 is continually reset by the presence of the external frequency source with the result that CO remains at the logic 1 level and the 5 output at logic 0. The external frequency source thus passes through Nands 2 and 4 to the input of the p.s.d., the output of Nand 3 being permanently maintained at logic 1.

On playback, the reference frequency presented to the speed control circuit is that from a v.c.o. whose output frequency is dependent upon the average voltage at its input, which is the filtered output of a p.s.d. comparing the crystal-controlled TC with the recovered TC from the recorded data of one track of the tape-recorder. Thus, on playback, the speed control of the tape is maintained by a p.l.l. servo system within another p.l.l. Some readers may think this a very curious system and wonder why the output from the p.s.d. comparing the crystal and recovered tape-clocks is not simply connected to the motor driver circuit. The answer to this is that the dynamics of the record and playback servo loops are totally different. On record, the tachogenerator is directly coupled to the motor, but on playback the recovered tape clock is mechanically coupled to the motor through the capstan and belt drive. It is not impossible to achieve a p.l.l. by the more obvious method, but it is very unstable and easily disturbed, losing lock, by any vibration of the deck. The solution used here is very much more satisfactory, offering as it does a very convenient method of switching from one reference frequency (on record) to another (on playback), by having a very much lower natural frequency for the p.l.l. of the reference frequency generator than for that of the motor speed control circuit, the instability produced by the belt drive mechanism is removed and there is no instability produced by one p.l.l. upon the other.

### Power supplies

The Hart version of the Linsley-Hood cassette recorder is mains-powered but can very conveniently be made to operate from a 24 volt d.c. source. Because there was a requirements for the recorder to be operable independently of a mains supply it was decided that it, too, should be capable of operating from 24 volts d.c. As a result, the power supply of Fig. 49 was designed and constructed. Since a very large number of c.m.o.s. i.c.s are used in the digital circuitry it was decided that they were worth protecting from any over-voltage spikes. Consequently the 'crowbar' circuit was added: in the event of an over-voltage spike, the thyristor is triggered, causing the fuse in the positive supply rail to the 7815 regulator to blow. An over-voltage of approximately 16 volts is needed to trigger the 'crowbar' circuit.

A switching inverter circuit, shown in Fig. 50, is used to generate the negative rail voltage. The heart of the circuit is the 78S40 switching inverter. Using the values indicated, the output voltage from the switching inverter circuit across the 47µF capacitor should be approximately -18 volts, at a load current of about 120 mA.

This type of switching inverter does not operate very well under varying load conditions, so a shunt regulator is used to drop the -18 volts to -15 volts. Approximately 100mA is drawn from the -15 volt rail by the various analogue and digital i.c.s in the circuitry: there is thus no need for the 2N3053 transistor to be fitted with a heatsink. The 2N2905 transistor of the switching regulator also dissipates little power and needs no heatsink.

### Modifications to tape-recorder

The Miller-coded data recorded onto tape is effectively a series of square-shaped pulses, ranging in frequency from about 5.5kHz to 11kHz, which should be modified, or distorted by the recorder as little as possible. The transient response of the

Fig. 49. Power supplies.

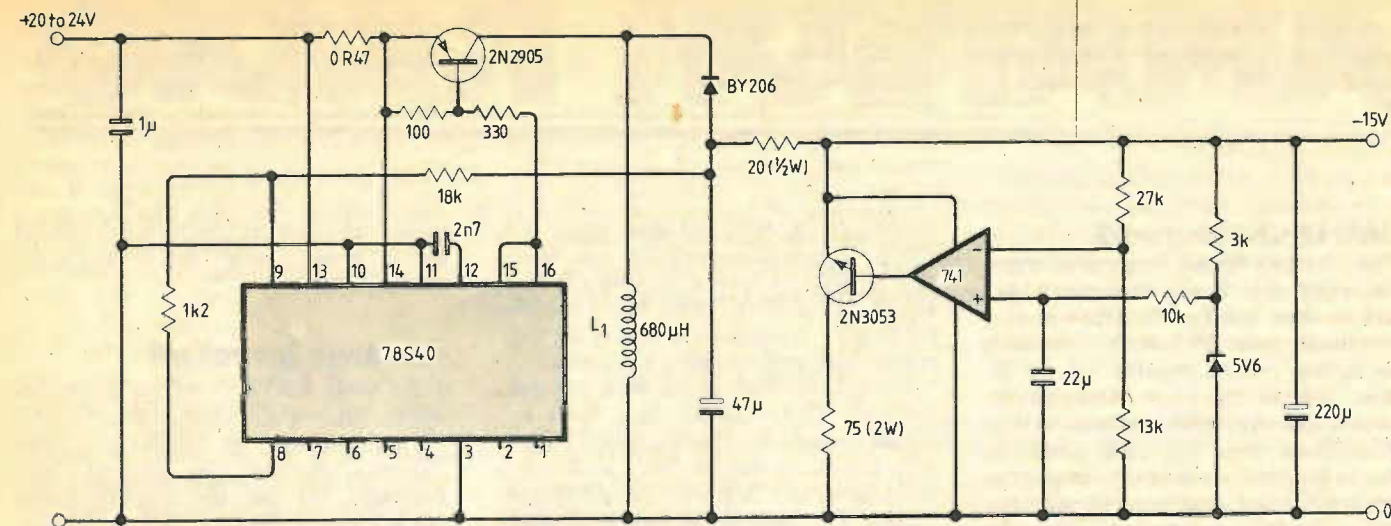


Fig. 50. Circuit diagram of switching inverter and regulator block seen in Fig. 49.

tape-recorder is more important, in its present use, than a flat frequency response.

To obtain the desired record/replay characteristics, the signal level, bias level and equalization must be adjusted. Firstly, the frequency response of any tape-recorder is the wider, the lower the signal level recorded. In normal use, the level of the signal to be recorded is a compromise between frequency response, distortion and signal-to-noise ratio: too high a level results in distortion and too low a level results in a poor signal-to-noise ratio. Signal-to-noise ratio is not a problem in the present use of the tape-recorder since the Miller-coded data is recorded at a constant signal level with no amplitude variation. The recording level can thus be reduced, improving the quality of the signal in terms of frequency response and distortion, provided, of course, it is not reduced to a level where noise imposes itself on the signal.

The level of the high-frequency bias can have a considerable effect upon the recorder's frequency response; high levels of bias producing an attenuation to the high frequency signals but some reduction in distortion.

Finally, adjustment of the equalization characteristic has a great effect upon the amount of high-frequency pre-emphasis and modifies considerably the transient response of the recorder.

In addition to all the possible adjustments mentioned, it must not be forgotten that the quality of the tape used is of prime importance. The author formed a considerable liking for Maxell UDXL II cassette tapes, both C60s and C90s. It is a CrO-type tape, requiring a high bias level and a 70µs equalization characteristic and has all the usual advantages of good frequency response, etc. The cassettes are also very sound mechanically. This is not the only suitable tape available - other tapes may perform just as well - but the tape recorder should be set-up using this tape. Having satisfactorily adjusted the tape-recorder to operate with the digital electronics, other brands of tape may be tried to determine their suitability.

When I began recording the Miller-encoded data on to tape to discover how well

the recorder performed, a problem occurred with the transport mechanism that was not immediately appreciated. The replayed signal, having passed through the peak detector and Miller decoder, was found to contain errors in the data stream which were initially thought to be due to the recorder's limited frequency response. Consequently, I experimented at length with the various adjustments mentioned earlier. Subsequently, the main reason for the errors in the replayed and decoded data was found to be due to jerkiness in the take-up spool of the tape-recorder, which was caused by incorrect operation of the slipping-clutch mechanism driving the take-up spool. The slipping-clutch was not, in fact, slipping, but the brass bush on the end of the slipping-clutch spindle, in contact with the rubber-tyred pulley of the take-up spool mechanism, was slipping jerkily. The problem was effectively cured by taking the slipping-clutch mechanism apart and 'weakening' its compression spring. The author is pleased to be able to say that a second tape-recorder, bought from Hart electronics at a later date, has a cassette deck with a modified slipping-clutch mechanism that gave no such problems. However, as a result of this fault, the author discovered a number of adjustments that should be made to the recorder to improve its record/replay characteristic of the Miller waveform.

- The 0dB recording level of 2.25 volts r.m.s. at the output of the recording amplifier should be reduced by about 4dB to 1.42 volts r.m.s., which corresponds, on playback, to an output from the replay amplifier of about 250 mV r.m.s., i.e. 4dB down on the original 400mV level. The 'VU' meter circuit sensitivity should be adjusted accordingly for a 0dB reading when the output from the recording amplifier is 1.42 volts r.m.s.

- The amount of high-frequency pre-

emphasis should be reduced to a minimum by adjustment of  $V_{r2}$  to maximum resistance on the recording amplifier board.

- The bias oscillator frequency should be raised from about 55kHz to nearer 80kHz by replacing the capacitor,  $C_{23}$  (10nF), of the bias oscillator circuit with one of 6.8nF and by changing  $R_{50}$  from 150 ohms to about 200 ohms.

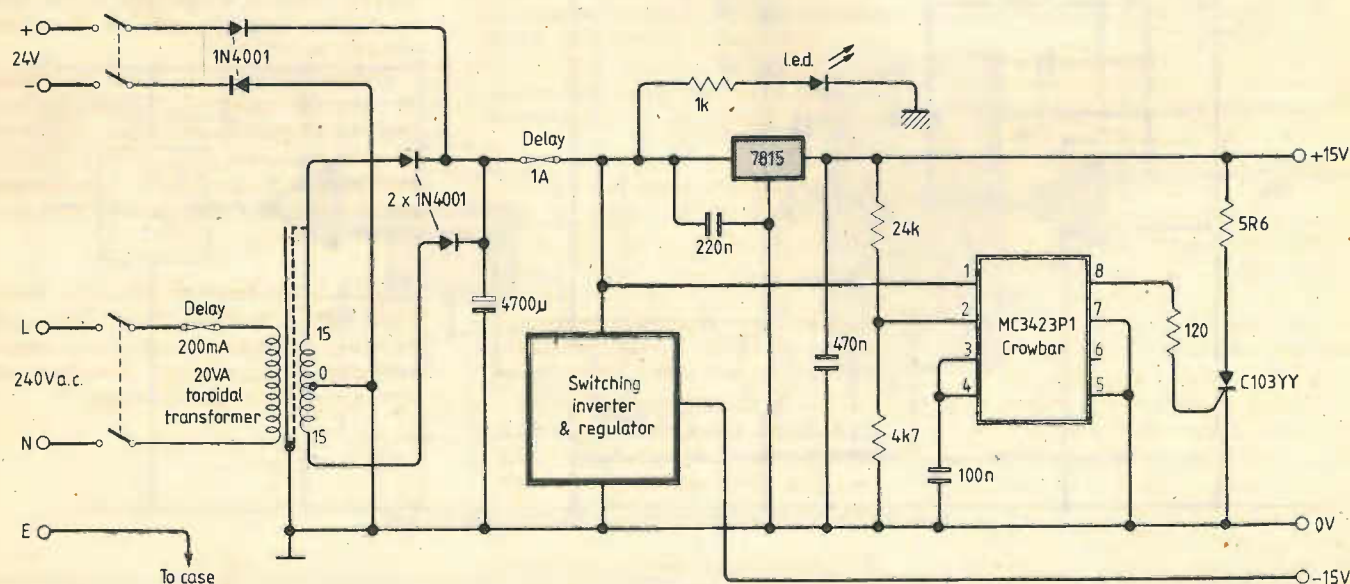
- The 70µs record/playback equalization characteristic should be used and a slight improvement may be obtained by changing the value of  $C_6$ , on the replay board, from 27nF to 18nF.

- The bias level should be high with the 47k variable resistor adjusted for the highest level possible. This should result in a bias voltage, as measured at the junction of the 47k variable resistor, and the 220pF capacitor  $C_{20}$  (L or R), of about 10V r.m.s.

The actual bias level does not appear to be very critical, but a high level produces a steadier signal, on replay, with less amplitude flutter. As the recorded signal has no low-frequency content below 5.5kHz the erasing effect of a high bias is of little consequence and the reduced distortion probably beneficial.

With all the above adjustments carried out, and the cassette deck operating in a mechanically satisfactory manner, little or no errors should be observed in the resulting replayed decoded data. Those errors that do occur should be due only to imperfections in the tape.

This concludes the series of articles. Strip-board layouts prepared by Mr Ewins are available in photocopy form: please write, including a large, stamped and addressed envelope, if you would like copies. □





## 50MHz stays good

In the February WoAR I suggested rather prematurely that "fewer transatlantic signals have been heard on 50MHz this winter although some 28/50MHz cross-band working has proved possible". J. R. R. Baker, GW3MHW, near Aberystwyth, Dyfed, a devoted 50MHz enthusiast, feels my comment does less than justice to what, in his view, has proved to be an even more fascinating period than two years ago at the peak of Sunspot Cycle 21. Then, he admits, there were outstandingly strong 50MHz signals that enabled a number of British amateurs to work all ten American "call areas". Altogether some 150 British amateurs and more than 20 other Western European stations participated in the transatlantic cross-band working. A few European stations, including about a dozen in Holland, were permitted to transmit on 50MHz.

Good results were also achieved during the 1980-1 season, with rather more Central American and Caribbean signals. No high hopes were held for the 1981-2 season, yet GM3MHW considers it has proved as good, in its way, as the two previous years: a few openings in late October, daily openings throughout November (except November 7), almost daily in December, and occasional openings in January 1982. On January 27, GW3MHW made his 449th cross-band contact for the season, compared with about 400 in each of the two preceding years, including many Caribbean and South American stations. Ken Ellis, G5KW contacted 48 of the American States. Several British amateurs made 70/50MHz contacts with Canadian VE1ASJ.

These results, two years after the peak of Cycle 21, are being regarded as so encouraging that it is proposed to publish a regular newsletter for 50MHz enthusiasts (from G4JCC or G4JLH for modest payment to cover postages and stationery).

## The GaAs mosfet

The current availability of lower cost gallium arsenide f.e.t. devices, including dual-gate mosfets at around £5 or less, means that receivers with noise figures of under 1dB and with good dynamic range can now be achieved by amateurs on 144 and 432MHz. Devices include the 3SK97 and 3SK98 developed in Japan for use in television receiver tuners but it is believed that comparable devices will soon become available from European firms. For example, D. J. Robinson, G4FRE, has measured 0.9dB noise figure with 18dB gain (circuit, not total system figures) at 430MHz. On 144MHz the French amateur F6CER has described a receiver front-end comprising a 3SK97 r.f. amplifier,

MD151 doubly-balanced diode mixer and P8000 impedance-converting grounded-gate amplifier, followed immediately by a 9MHz crystal filter. These GaAs mosfets are roughly one-quarter or less of the cost of most high-performance s.h.f. gasfets.

Further advances in the field of super low-noise GaAs mosfets have been reported recently by Hughes Aircraft who, with laboratory devices, have achieved a noise figure of 1.3dB with 10.3dB gain at 12GHz. The GaAs mosfet seem destined to play an increasingly important role at frequencies from about 100MHz upwards.

## From all quarters

Following the example of the British teletext services, the Dutch Teletext service by NOS now includes a page of information for the transmitting amateur.

When last November an incendiary set fire to a key telephone exchange in the Lyons area of France, some 50,000 telephone and telex lines, including trunk lines, were put out of action, local radio amateurs provided a special emergency communications service, handling urgent calls filtered through the police to ensure that all calls were of a non-commercial nature. They used h.f. bands and the FZ8VHF repeater.

Kathy Marsh, VK5NKM, the only amateur in Coober Pedy, an opal-mining town in central South Australia, operates from an unusual "dug-out" home some 20-feet underground. Such buried homes fashioned from former mines are popular in the township since they avoid the high summer surface temperatures (almost 50°C) yet remain comfortably warm in winter. Australia has some 15,000 licensed amateurs in a population of about 15 million people.

Shortly after Australian amateur Ray Naughton, VK3ATN, had climbed to the 45ft level of his 110-foot mast to make everything secure during a gale, a 100mph gust collapsed the tower. He escaped with some broken bones and a stay in hospital.

The Réseau des Emetteurs Français has warned its members that some French c.b. associations are making demands on amateur frequencies in the 28, 144 and 432MHz bands. The society recommends that amateurs should show that they are making full use of these bands.

IARU Region 1 reports that the Irish Radio Transmitters Society will be 50 years old in June but can trace its beginnings to the Dublin Wireless Club founded in June 1913. First president of IRTS was Colonel J. M. C. Dennis, E12B (formerly DNX) who is widely believed to have been the owner of the world's first non-professional experimental wireless station, established in 1898. During World War II,

those Irish amateurs who were not enlisted in the Forces, offered their services as listening stations.

## Awards knocked

Bill Verrall, VK5WV, writing in *Amateur Radio*, has strongly attacked many aspects of the emphasis on DXCC and other "award collecting" by amateur radio operators. He feels that country-chasing has led to such abuses as: "dx nets" claiming exclusive occupancy of spot frequencies; an increasing amount of deliberate jamming and interference; use of illegally high power; split-frequency operation by "rare" stations that spreads interference over many channels; blatant soliciting for "dx-pedition" funds and extraction of payment for QSL cards; and the use of QSL cards bearing political or "religious" messages. He also condemns the recognition of uninhabitable rocks and reefs as "countries" and the risks that this involves for those who set up stations at locations which may at times be entirely covered by the sea; "bootleg" QSL cards that may be entirely fake, or sent or sold to stations with which no contact has been made; and the widespread use of a standard RS(T) report of 59(9).

P. A. Wolfenden, VK3KAU, Federal president of the Wireless Institute of Australia, has pointed out that despite the growth in the number of training courses by clubs and educational bodies, newcomers still need more practical assistance from active and competent amateurs of experience: "the newcomer has to learn the ways of amateur radio, the procedures and the standards, and the various gentleman's agreements about such matters as band plans, correct repeater operating, etc. . . . only a few clubs provide practical 'hands-on' experience".

## In brief

Gerald Stancey, G3MCK identifies the "Early French Resistance suitcase set" in Toulon museum ("Clandestine Radio - the early years" February issue) as an early SOE equipment Type A, Mk II and draws attention to a book published in France "Armement Clandestin" by Pierre Lorain, F2WL which includes details and circuit diagrams of a number of British and German suitcase sets. The photograph by the way was taken by Dick Rollema, PA0SE . . . The 1982 RSGB VHF Convention is at Sandown Park, Esher, on March 20 . . . The Northern Amateur Radio Societies Exhibition is at Belle Vue Leisure Park, Manchester, on April 4 . . . Plymouth Radio Club has its third annual rally at Tamar Secondary School, Paradise Road, Millbridge, on May 30 . . .

PAT HAWKER, G3VA

# E.P.R.O.M. PROGRAMMER

Most commercially available e.p.r.o.m. programmers are expensive as they include software and other facilities to enable them to be used on their own. The cost of a programmer can be significantly reduced if it is designed for use with an existing microprocessor system, as will be shown in these articles. The design presented is for 2708, 2716 and 2532 e.p.r.o.ms, but with small modifications other devices may be programmed.

by H. S. Lynes

Sooner or later, probably all serious microcomputer system users in the hobbyist field will consider incorporating a program in e.p.r.o.m. (erasable programmable read-only memory). Unfortunately, commercial e.p.r.o.m. programmers are expensive and include facilities not essential for the enthusiast, who usually only wants to program the occasional device.

Commercial programmers fall into two main categories: those in the first category are expensive, have built-in data/address display and use 'personality' cards for programming different e.p.r.o.m. types. Units in the second category are very expensive. They have all the facilities of programmers in the first category but also include built-in v.d.u., tape interface, printer port, etc. All these programmers use comprehensive software and have large random-access memories to enable e.p.r.o.ms to be copied or modified at will. But if an existing microprocessor system is used to control an e.p.r.o.m. programmer, these facilities are unnecessary.

I therefore explored the possibility of adding e.p.r.o.m. programming hardware to an existing system. The first problem

Port C connections	PC7	PC6	PC5	PC4	PC3	PC2	PC1	PC0	Hex
2708									
Function	logic	n.c.	26V	12V	n.c.	n.c.	address	22	23
I.c. pin numbers			18	20					
Read	0	—	0	0	—	—	x	x	00
Write									
pulse off	0	—	0	1	—	—	x	x	10
pulse on	0	—	1	1	—	—	x	x	30
2716									
Function	logic	*	25V	n.c.	*	—	address	22	23
I.c. pin numbers		20	21		18	19			
Read	0	0	0	—	0	x	x	x	00
Write									
pulse off	0	1	1	—	0	x	x	x	60
pulse on	0	1	1	—	1	x	x	x	68
2532									
Function	logic	*	25V	n.c.	—	—	address	22	23
I.c. pin numbers		20	21		18	19			
Read	0	0	0	—	x	x	x	x	00
Write									
pulse off	0	1	1	—	x	x	x	x	60
pulse on	0	0	1	—	x	x	x	x	20

Notes: The hex. value is the code, or 'pin-profile', used for port C, ignoring the address. When programming 2716 and 2532 e.p.r.o.s, pin 21 is held high during the read cycle. Functions marked with an asterisk indicate that the port is used as a logic, i.e., the port is tied directly to the e.p.r.o.m. pin. Where x is given, both logic levels are used for addressing. PC7 is used to detect the high-impedance state after reset.

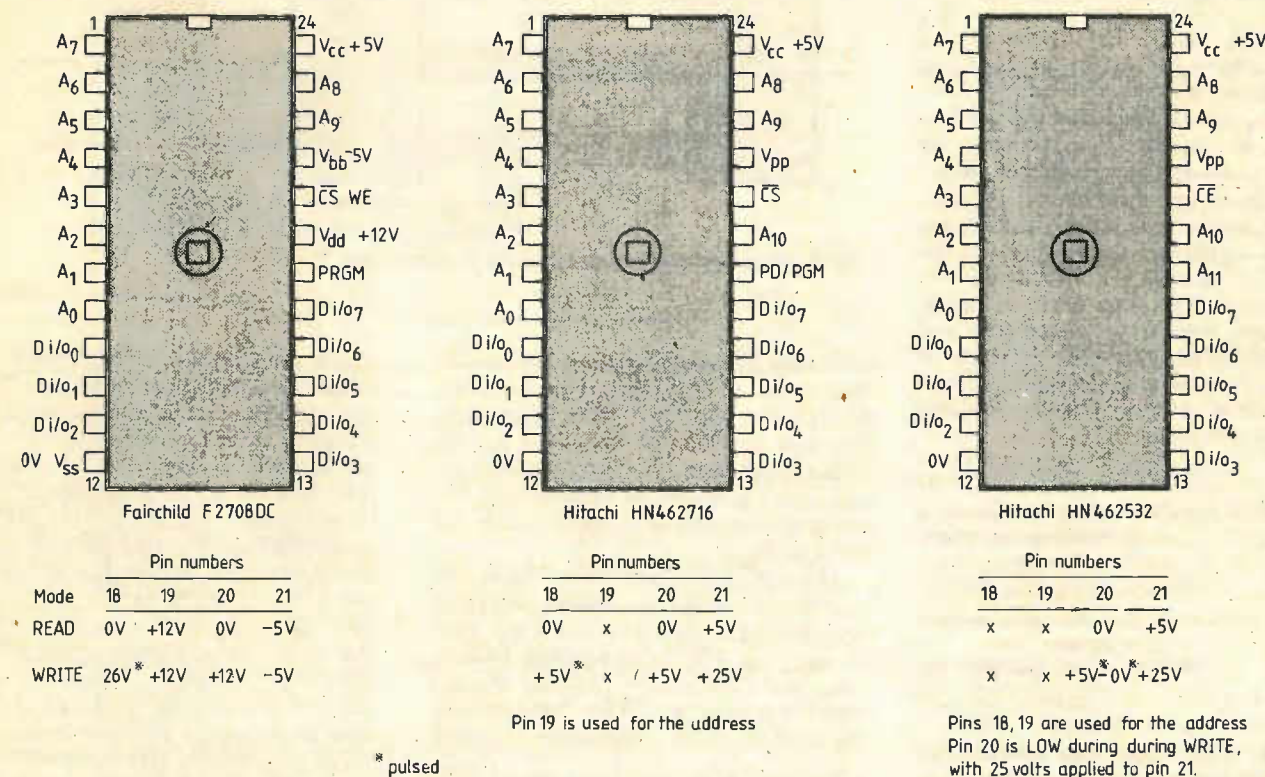
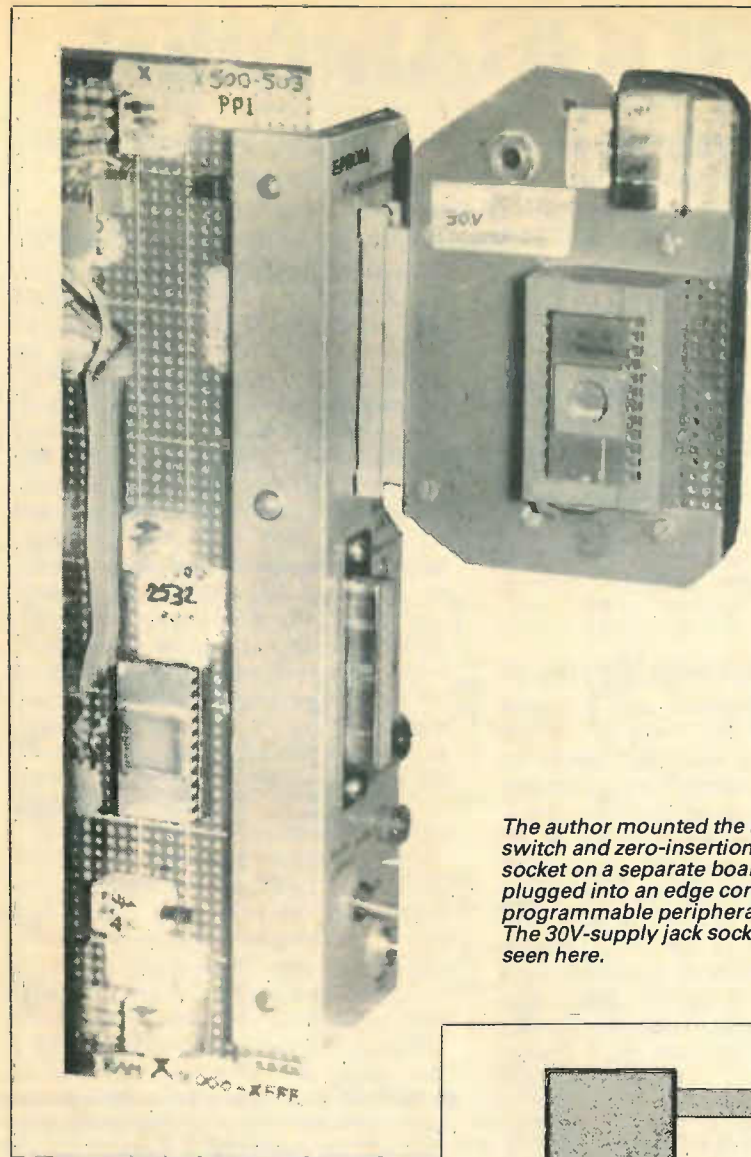


Fig. 1. The three e.p.r.o.ms for which the programmer was designed with tables showing control and programming logic requirements.

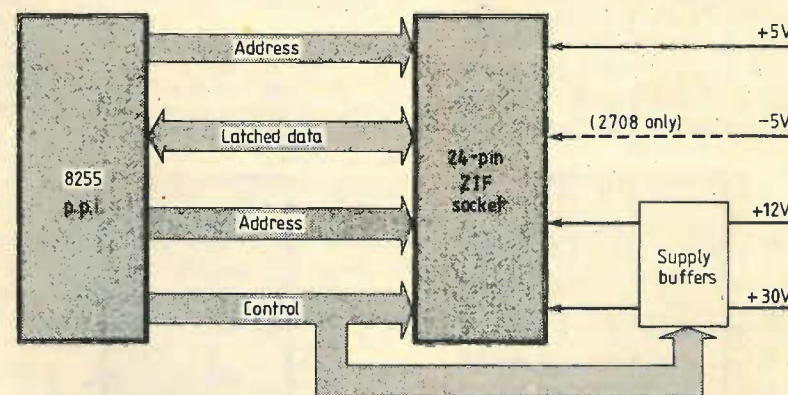




The author mounted the address d.i.l. switch and zero-insertion-force e.p.r.o.m. socket on a separate board which can be plugged into an edge connector on the programmable peripheral interface unit. The 30V-supply jack socket can also be seen here.

**Table 1:** Wiring from the 8255 p.p.i. and supplies to the e.p.r.o.m. programming board. Lines with prefix PA are for addressing and lines with prefix PB are for data. Prefix PC denotes lines used for both address and data.

E.p.r.o.m. socket pin numbers	Supply and p.p.i. lines
1	PA7
2	PA6
3	PA5
4	PA4
5	PA3
6	PA2
7	PA1
8	PA0
9	PB0
10	PB1
11	PB2
12	0V
	0V
13	PB3
14	PB4
15	PB5
16	PB6
17	PB7
18	PC5
19	+12V
20	PC4
21	-5V
22	PC1
23	PC0
24	+5V
(18)	PC3
(19)	PC2
	+30V
	PC7
(20)	PC6
(21)	PC5
	PC4
	Reset
	R/W
	+5V
	02
	spare



**Fig. 2.** Simplified block diagram of the programmer.

encountered was that programming requirements for different types of e.p.r.o.m. can vary considerably. Also, there is no standardization in pin configurations. So, taking into account the popularity, price and availability of various e.p.r.o.ms, it was decided that the programmer should be designed for 2708 and 2716 (5V supply) e.p.r.o.m. types. As the 2532 looked promising at that time it was also included. The latter device is similar to the 2716 both in pin assignments and programming requirements, although its inclusion meant that an additional address line would be needed. Design objectives were thus as follows:

E.p.r.o.m. type	Organization	Requirements
2708 (3-rail)	1024 x 8	500µs programming pulse, sequential programming
2716 (5V)	2048 x 8	50ms t.t.l. programming pulse, bit-selectable programming
2532	4096 x 8	50ms t.t.l. programming pulse, bit-selectable programming

For the 2708, I used data published by Intel, which covers the subject of e.p.r.o.ms at length. This data was used to

define the programming pulse rise-and-fall time limits of 0.5µs-2µs. For the 2716, Mostek data was used (which agrees with Fairchild and Hitachi data), and for the 2532, Hitachi data. The latter manufacturer's data was easiest to understand\*. Pin configurations and level requirements are given in Fig. 1.

Although these three devices are at present the most popular, readers designing new systems using e.p.r.o.ms might want to omit the 2708 programming facility, since one 2716 can be obtained for less than the price of two 2708's. Furthermore, the 2708 must be programmed in small

\* This could be a useful tip for aspiring technical writers - Ed.

stages sequentially - a process often called 'spray-coat' programming. This is inconvenient when developing using 1K x 8 devices but if 2 or 4K devices are used, the method is intolerable. Fortunately, later devices may be programmed bit-by-bit as required. Inclusion of the 2532 programming facility is now justified, since it can be obtained for less than the price of two 2716's. The reasons for not including the 1702 among the chosen e.p.r.o.ms are that in my view, programming of it requires twisted logic, it is relatively expensive and it cannot be used with the software for the chosen devices in read mode.

The programmer was designed for use with a 6800 microprocessor system but is based on an 8255 programmable peripheral

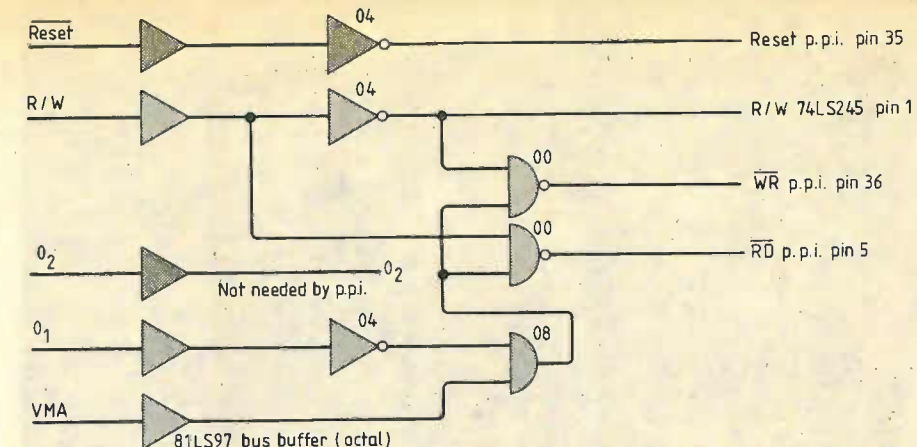
interface (Intel or National Semiconductor). Some extra logic is required to drive the 8255 control pins but this p.p.i. provides three 8-bit ports and programming is relatively simple. If the 6821 had been chosen, two i.c.s would have been required and programming would, in my view, have been more difficult: there is no reason why support devices should not be chosen for their ability to fulfil objectives.

The 8255 is used in mode 0 (see manufacturer's data for further information) with the 8-bit ports A and C as outputs and port B as either input or output depending on the control word stored in one of the device's four memory locations. By changing port B from output to input it is possible to check that data entered into the e.p.r.o.m. has been correctly received. This function corresponds to the verify function of expensive programmers.

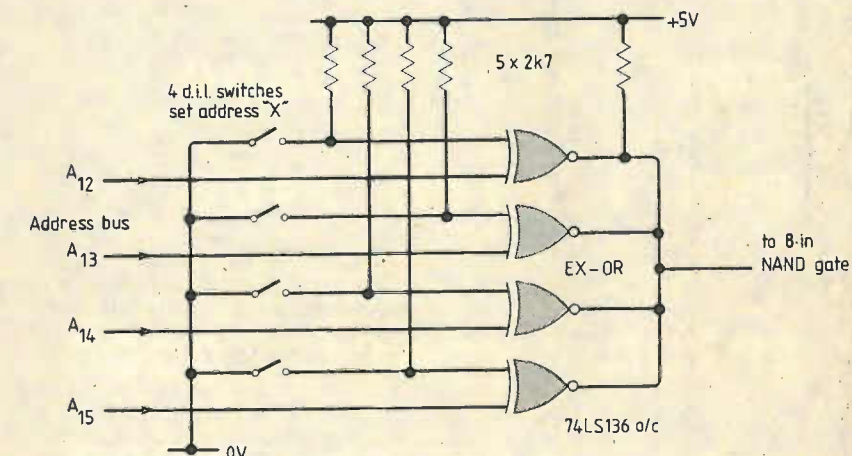
Since e.p.r.o.m. bits are all at logic 1 when the memory is empty, it would be possible to check the amount of memory available in partly full 2716/2532 devices. Unfortunately, the 6800 uses instruction FF to store the index register so confusion could result if the end of the existing program used FF as an instruction or address.

It is advisable to finish programs with three 00's to avoid the risk of placing a new program over the top of an existing one.

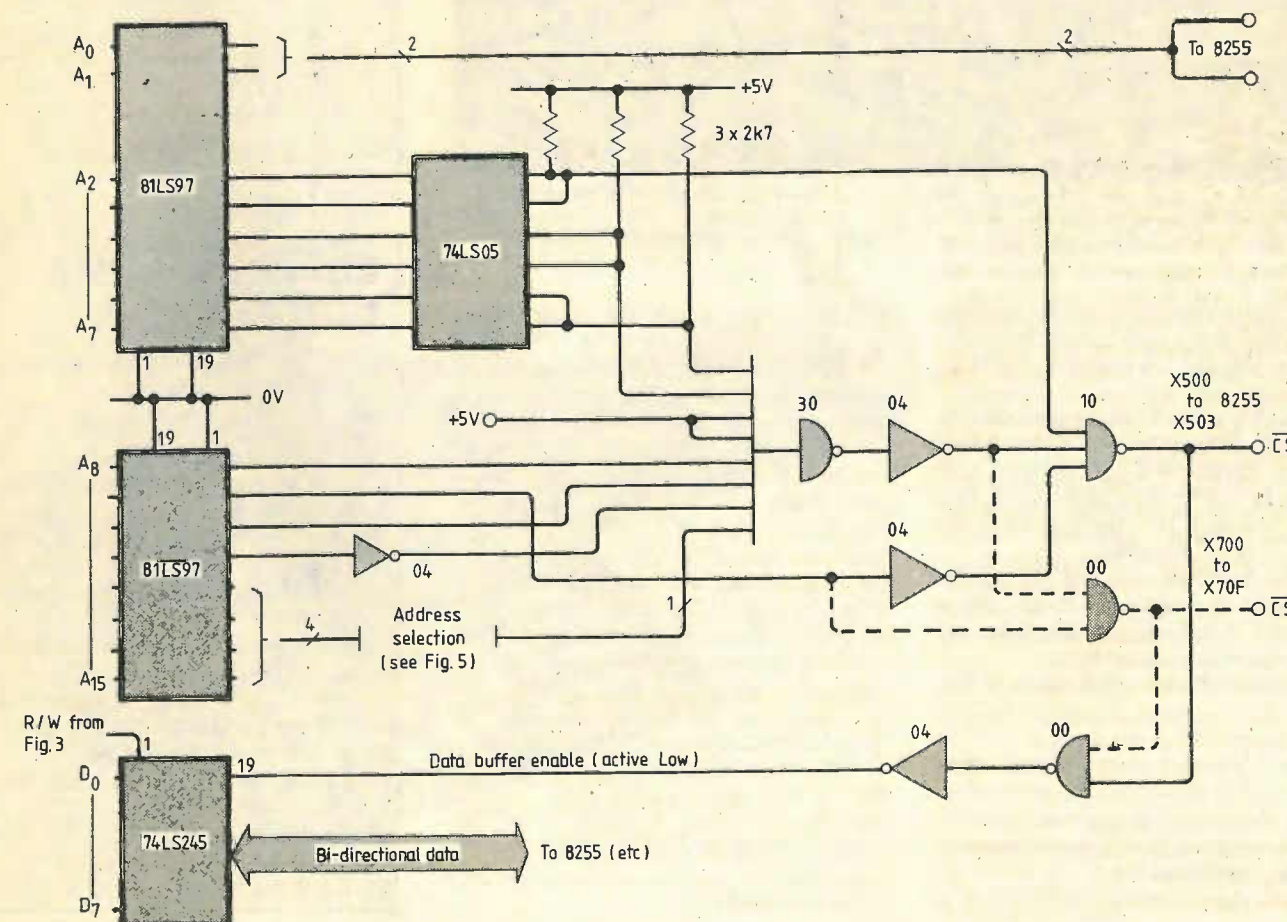
**Fig. 4.** Address decoding for the 8255 and one other device (see text).



**Fig. 3.** Logic for converting outputs from a 6800 processor for use with an 8255 p.p.i. If an 8080 processor is used to control the programmer, this conversion is not required.



**Fig. 5.** Circuit for selecting the most significant digit of the p.p.i. address (see Fig. 4).





State EPROM type

2708 N  
2716 5v N  
2532 Y

Read-? N  
Write? Y

RAM start 0400

RAM finish 1400

EPROM start 0000

finish 1000.

2532

Press G .

This photo is an example of the author's display and illustrates the type of prompting that may be used. Because of the differences between microprocessor systems, a full software listing is not given, but a 'scratch-pad' and software outline will be included in the next article.

Also, a careful note of the current program state of each e.p.r.o.m. should be made. Colour coding the i.cs makes it easy to log their history.

Figure 2 is a block diagram of the programmer, and logic conversion for driving the 8255s RD and WR lines from the 6800 is shown in Fig. 3. If an 8080 processor is used to control the programmer, this conversion is not required.

The 8255 address, see Fig. 4, requires four consecutive locations. In my system the address is fully decoded, but the four most significant address lines can be altered using a d.i.l. switch as shown in Fig. 5. The four locations are from X500 to X503, where X may be from 0 to F depending on the d.i.l. switch setting. Being able to change the address is useful if the 8255 is to be used as a general purpose port, as opposed to being dedicated to e.p.r.o.m. programming.

Table 1 shows lines from the p.p.i. to

the board on which the programming socket, switching between 2708 and 2716/2532 functions, and a voltage regulator were mounted. In the table, pins 18 to 21 of the programming socket are shown connected for programming the 2708. In practice, pins 18 to 21 are connected to a 4-pole, 2-way d.i.l. switch so that they may be taken to PC3, PC2, PC6 and PC5 respectively when 2716/2532 e.p.r.o.ms are to be programmed. PC5 is a 25V signal and PC4 a 12V signal, the conditioning circuits of which will be shown later. PC7 is used to check logic but it could be used to detect changes on pins 18 to 21, or even omitted to reduce the number of lines from the p.p.i. circuit to the programming board. 37 lines were used, as shown in the table but by omitting unwanted lines, combining the 0V rail and bringing in the 30V supply separately, the total may be reduced to 30.

To be continued

## IN OUR NEXT ISSUE

### Digital filter design

Accuracy, versatility and a rapidly declining cost will ensure that digital filters take over from their analogue counterparts. A new series gives their theory, design techniques and microprocessor implementation.

### Program exchange by telephone

There is a growing need to facilitate the easy exchange of programs and data from one person to another. Philip Barker discusses program distribution and the design and implementation of software systems capable of loading source code programs into memory.

### Orchestral sound, halls and timbre

Taking the Kingsway Hall as a model, Denis Vaughan investigates the effect of concert hall shapes and sizes, and the working of the filtering of the outer ear on timbre and perceived directionality.

On sale  
April 21

WIRELESS WORLD APRIL 1982

# NEWS

## Polytechnical computer

The opening of the new computer centre at Coventry Lancaster Polytechnic was accompanied by a civic reception and a protest demonstration by some of the students. The centre has been constructed to house two Harris computers which provide impressive processing power with storage capabilities for a high volume of batchwork and can service some 100 terminals distributed over the Polytechnic campus.

The centre incorporates a Harris 800 computer system which has 2 megabytes of memory, with four 300-megabyte disc drives and one 80-megabyte disc drive, a line printer, card reader, a 9-track magnetic tape unit and a CIL plotter.

Also housed in the same building is a Harris H500 computer, a separate system with one megabyte of memory and one 300-megabyte disc drive with a line printer, a card reader, a magnetic tape unit and a paper-tape reader/punch.

Elsewhere on the campus is a Harris H100 for the polytechnic's Electrical and Electronic Engineering department. Eventually it is planned to connect all three computers together by synchronous links into one processor network.

The system is the biggest Harris system outside the United States and is claimed to be



The Computer Centre at Lancaster Polytechnic, Coventry, specially built to house the computer facilities, including the two Harris Computers and some special terminals.

the largest available to any further educational establishment in the UK.

The student protest was very civil and was not about student grants, despite the presence

of the Parliamentary Under-secretary of State, Department of Education and Science, Mr William Waldegrave; it was about the delay in getting the computer actually working, their work was being delayed by the lack of terminal time as only a few were actually running. Harris assured us that these were teething problems and that they were flying a team of specialists from their factory in Florida to assist in the initialisation of the system.

## Timex to sell Sinclair in the U.S.A.

You may know that the Sinclair ZX81 Microcomputer is manufactured under a sub-contracting agreement by the Timex Corporation in their Dundee factory. The current production rate is about 30,000 units each month, which some clever mathematician has

worked out to be one unit every ten seconds. Timex seem to be impressed by the sales and have come to an agreement with Sinclair Research, whereby they can sell a Sinclair/Timex computer in North America. Sinclair are at present selling in the U.S. by mail order at a rate of

15 thousand a month; Timex have about 170,000 retail outlets in North America, and could sell at a phenomenal rate.

The agreement is for Sinclair to provide the technical expertise and for Timex to manufacture a computer which will include their own brand name. The name is not to be more prominent than the Sinclair marque which will remain on the equipment.

Timex will pay Sinclair a 5% royalty on all hardware that is related to the Sinclair microcomputer, even if it is not originated by Sinclair. They will also pay a 5% royalty on any Sinclair originated software. And they will even pay 2½% on software from any other source as long as it is intended for use on the Sinclair equipment. There will be a cross-licensing agreement for any hardware that Timex may develop themselves.

Clive Sinclair says that he has been looking for a large marketing outlet for his products for some time. He intends to keep Sinclair Research as a compact research and development team, concentrating on improvements to existing products and development of new ones. The date for the probable launch of the Microvision flat tv is given as the last quarter of 1982. It has already been announced that the tv is being incorporated into a desk-top terminal for ICL, and there may be some clue as to the likely format of the next-generation ZX computer in that. Sinclair's research into electric vehicles is continuing.



The Sinclair ZX81 which is to be manufactured, and marketed in North America, by Timex. The ZX81 is shown together with the add-on 16K RAM pack and the ZX Printer.





Having produced new versions of their pre- and power-amplifiers and their electrostatic loudspeakers, Quad have come up with an f.m. tuner, the Quad FM4. It has been styled to match the Quad 44 pre-amplifier with which it is shown. It incorporates a microprocessor which can recall the preset stations from memory and also controls inter-station muting and a.f.c. Manual tuning is used to program the seven preset stations and occasionally to tune in to a station not already programmed. A bar graph displays signal strength and centre tuning. The preset buttons and the tuning knob are the only controls: the microprocessor takes care of everything else.

## Teletext, a new campaign

One way to mass market viewdata is believed to be the growth of private viewdata systems which are compatible with Prestel; used by companies for in-house systems. Another way is the development of a more attractive Prestel package for the consumer.

It was decided at the conference that Prestel could be made more attractive to the consumer by:

- working towards a consumer package, providing an overall viewdata service which would include transactional applications, i.e. the ability to order goods by pressing the appropriate buttons;
- including entertainment and communications as well as 'straight' information;
- examining the tariff structure;
- working towards a reduction in the cost of viewdata receivers;
- improving the quality and attractiveness of the information provided;
- promoting new applications of business viewdata;
- working towards the acceptance of viewdata as the principle means of communication between business and industry.

Further analysis of the view expressed at the conference will lead to the publication of another 'action document'. October has been selected as National Teletext Month as was October last year, this will be used for an intensive campaign to promote teletext.

1982 is Information Technology Year, and as part of the Government's commitment to IT, the Department of Industry is promoting further awareness of Teletext and Viewdata.

According to a survey published in Prestel (page 19191), 65% of the population now know what Ceefax is; for Oracle it's 55%, teletext, 50% Prestel 30% and viewdata 15%. There are still 20% who have no knowledge of any of these. Television viewers with facilities to receive teletext numbered over 300,000 at the end of 1981.

This is a result, claims the DoI, of the promo-

tion campaign launched at a 'Commitment Conference' in January 1981, which brought together the manufacturers of the equipment with the information providers, with television rental and retail traders, software suppliers, trade associations and with representatives from British Telecom, the DoI and the NEDO. One of the chief aims of the campaign was to familiarize consumers with the process of obtaining information from the tv screen. It is believed that such familiarization could lead to more recognition for Prestel, BT's telephone viewdata system.

In February of this year, another Commitment Conference was held in London to plan a further campaign for 1982. Once again the accent would be on promoting teletext to the general public and Prestel to the business community.

## Free specifications and standards

London Information have started a free consultancy service to help engineers identify and acquire the specs and standards or other documentation they may need for their projects. Enquiries are already running at hundreds of 'phone calls a week. The documents are not confined to the electronics industry and London Information have told us that they have recently supplied copies of quarantine regulations for Australian wallabies and building regulations for a middle east sports complex. They provided an electronics firm with the relevant US Mil specs and this resulted in a big export order to the US.

London Information claim to be able to get any available document from anywhere in the world. If they cannot supply the information then they will put companies in contact with a source that can. Further details can be obtained from: London Information (Rowse Muir) Ltd, Index House, Ascot, Berks SL5 7EU. Telephone: 0990 23377.

## Arthur C. Clarke honoured

The science writer, Arthur C. Clarke has been chosen to receive the eighth Marconi Fellowship Award by the Marconi Fellowship council.

The \$35,000 award is given annually in recognition of scientific achievement for the benefit of humanity in the field of communications science and technology.

Clarke predicted the geosynchronous communications satellite as early as 1945 in the *Wireless World* article "Extra-terrestrial relays: can rocket stations give world-wide radio coverage?". We issued a reprint of the article with our October 1981 issue. In it, he addressed very specifically the technical issues involved in such satellites, which have since become such a significant part of the earth's communications.

Clarke's other innovations include the use of satellite platforms for observing the earth in a quantitative manner, the concept of the manoeuvrable solar sail for low-acceleration interplanetary flight, and the concept of the 'space elevator' for reaching orbital altitudes using materials of very high strength/weight ratio which are likely to be developed soon.

Recently, Arthur C. Clarke has been strongly supporting proposals for the use of satellites for communicating with remote communities. Many such systems have been installed in villages in Alaska and Canada.

As far as the general public is concerned, Clarke is best known for his science fiction writings, especially for his collaboration with Stanley Kubrick on 2001: A Space Odyssey. Rumour has it that they are to work together again on another s.f. film.

Mr Clarke is now the Chancellor of the University of Moratuwa in Sri Lanka.

● The Marconi International Fellowship was founded in 1974 by Gioia Marconi Braga, daughter of the Italian inventor, Guglielmo Marconi. It is sponsored by companies and institutions from ten different countries.

## Licence sensation

There is a belief that the Home Office has made another "snafu" and will be forced to rescind part of a new schedule which appears to contain a host of technical errors and misreading of the International Radio Regulations. A four-page Home Office announcement appeared in the London Gazette on February 12 addressed to "all holders of Amateur (Sound) Licence A and Amateur (Sound) Licence B" setting out a new schedule of frequencies, classes of emission and power limitations "as from January 1, 1982." These are regarded as "unacceptable" by the R.S.G.B. which immediately called for urgent discussions with the Home Office. The new schedule, as printed, not only introduces the new international symbols and defines power in terms of output to the aerial in dBW, But also removes 10kHz from the British 1.8MHz band, restricts 3.5MHz transmission to the very low power of 9dBW (carrier power), compared with 20dBW for other h.f. bands, and also introduces an entirely new form of power restriction (30dBW maximum equivalent isotropically radiated power) for all bands above 1.2GHz. There are also many other apparent technical anomalies that are inexplicable in any rational technical terms.

A Home Office spokesman has told us that it was all a terrible mistake based upon a series of mis-prints. It must be pointed out however that publication in the London Gazette makes it a legal announcement.



The Husky 144 by DVW Microelectronics is a sturdy, waterproof microcomputer for data entry in the field

## Xenix and the supermicro

Xenix is the name of a computer operating system for use on 16-bit microcomputers. It has been developed by Microsoft and is an implementation of Unix, a software system originally developed by Bell Laboratories for use on DEC minicomputers, first on the PDP-7 and later on the PDP-11. Xenix is the 16-bit operating system which seems likely to become a standard, much as CP/M has become for the 8-bit processor. One advantage it has is that there are comparatively few codes which are specific to a particular processor; so it can be fairly easily implemented on many 16-bit processors.

All this is by way of introduction to the Bleasdale 600 Xenix computer which uses the Zilog Z8001 16-bit microprocessor. The Z8001 runs at 4Mhz and can address up to 8 megabytes of memory through a 23-bit address bus. The Bleasdale computer is a general-purpose applications for professional system designers and engineers and may be used in simulation, process control, image processing, instrumentation, scientific workstations. It may also be used for office automation equipment, communications networks, banking/financial systems etc. The first customers are the Monotype Corporation, who will use the computer for typesetting, and Precision Software, a financial information services company.

The 600 computer is of modular design, constructed from a range of plug-in p.c.bs which offer a wide range of different configurations. The boards are interconnected using the Multibus system with 24 address lines for up to 16 megabytes of memory.

The computer is manufactured at Bleasdale's factory in Lutterworth, Leicester, and is to be marketed through a network of distributors

## Computers in the field

The computer industry at the moment seems obsessed with 'the man in the field', the roving executive, salesman, engineer or even the journalist. The theory is that these peripatetic representatives can feed in the latest information, deal, sales figures or stories down the line to their parent companies.

One approach to this is illustrated by the new portable terminal by Digital Equipment Corp. The Correspondant is a hard copy printer terminal about the shape and size of an electric typewriter. It can handle plain paper and can have tractor feed as an additional option. It offers 132-column printing with a range of typefaces and because it is bit-map addressable it offers high resolution graphics (132 x 72 dots per inch) and can be used in conjunction with Digital's visual display terminals. What makes it portable is the 'universal' power input which will accept any a.c. mains supply of any voltage or frequency. It may be fitted with an acoustic coupler to communicate with the base computer. Digital are eager to point out however that it is also highly suitable as a fixed printer terminal with an RS232 interface.

The Digital Correspondant is a terminal and must be connected, by whatever means, to a computer to be of any use. An alternative approach is the portable computer. This has the advantage of being able to collect data 'in the

field' and one example, the Husky 144, made by DVW Electronics, has been designed with a tough case and a flat, touch-sensitive keyboard. It can be used literally in the field, out of doors. It has a liquid crystal display of up to 128 characters in four lines. It is battery powered and thus can include an internal memory which does not lose its data and real-time calendar and clock so that entries can be 'tagged' with collection time automatically. The Husky 144 is provided with 144K-bytes of memory and has 'user-friendly' software. A key marked 'Help' may be pressed at any time during operation and a part of the internal 'manual' is displayed on the screen giving information on what to do next.

To communicate with the outside world the Husky 144 can use an RS232 interface for direct communication with a host computer or a printer. It can use an acoustic coupler for telephone contact. It can also be used as its own base station and may be plugged into an optional disk drive for storage and retrieval of files. With a disk drive it can also be operated under CP/M which gives it access to a large library of commercial programs.

Correspondant - Digital's plain paper portable terminal designed 'for executives on the move'



throughout Europe. The majority of the computers are likely to be sold to O.E.M.s. A version of the computer based on the Motorola M68000 processor is being produced and this will also operate on Xenix.

Eddie Bleasdale the managing director of Bleasdale Computer Systems believes that Xenix will be very popular in scientific and educational applications because of the widespread use of Unix in DEC computers. As Bleasdale are in the forefront of users of Xenix, he intends that his company will maintain that position and become a leading centre of expertise in Xenix/Unix.

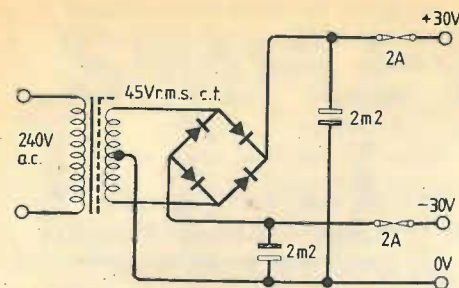
● Zilog have given their official blessing to CP/M and Unix have warned that manufacturers should be wary of 'lookalike' systems. Traditionally a new computer system engendered a

new operating system which became 'machine-dependent'. So if a computer system was selected the operation system went with it and the user became stuck with it. If, however, the operating system were selected first then a number of manufacturers could offer computers which operated the system. CP/M and Unix are suitable candidates but some systems are being marketed as 'Unix-like', for example, but do not have the universal application or constant development of the original. One has a feeling that the warning may not be entirely altruistic; CP/M and Unix both operate on Zilog equipment.









Decoupling capacitors reduce the supply frequency ripple to 5.5V pk-pk at full load. Off load, the supply voltage should not rise significantly above  $\pm 35V$ .

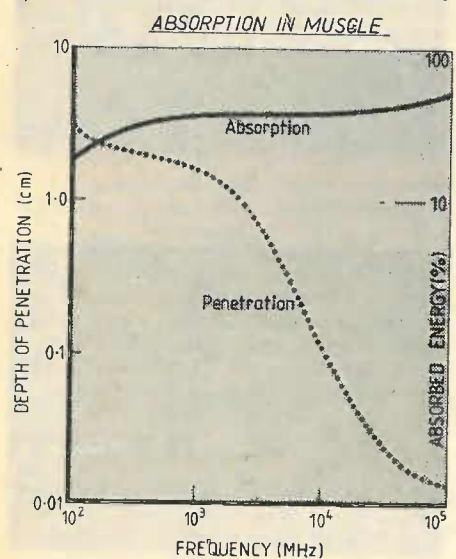
range 15Hz - 100 kHz with the aid of an audio test set or signal generator and oscilloscope. Distortion of the output waveform at high frequency indicates a reactive load: adjust the output choke to restore the waveform. Tailor h.f. frequency response with a compensation capacitor in parallel with  $R_6$ . The l.f. response is controlled by  $R_7$ ,  $C_2$ .

Supply-frequency breakthrough is most discernible in a high-gain circuit. Minimize pick-up at the high-impedance input by a screened cable, grounded at the signal source. Supply-frequency ripple injected through the supply to the input stage of the amplifier can be detected across capacitor  $C_3$ . This is normally at-

## R.f. radiation hazards

Last year we published a news item<sup>1</sup> briefly pointing out the controversy surrounding the r.f. radiation-exposure safety limits accepted by most western countries. In America, the ANSI and ACGIH (American Conference of Governmental Industrial Hygienists) have both suggested new frequency-dependent standards based on the same work and both assuming 0.4W/kg as a safe maximum absorbed energy rate, and it is expected that the Americans will revise their existing 10mW/cm<sup>2</sup> maximum safe level in the near future.

Although we in the UK originally based our maximum safe level (10mW/cm<sup>2</sup>) on that decided in the US some 20 years ago, whether or not we will again follow suit is not clear. According to Mr S. Allen of the NRPB, one possible point of contention is that the two proposed standards mentioned above are based on results from far-field radiation tests. It is



A glass-fibre printed circuit board for the heating-fuel saver will be available for £4.50 inclusive of VAT and UK postage from M. R. Sagin, Nancarras Mill, The Level, Constantine, Falmouth, Cornwall.

nuated by the common-mode rejection of  $Tr_1$  and  $Tr_2$  before being amplified but if this is the source of breakthrough, adjust

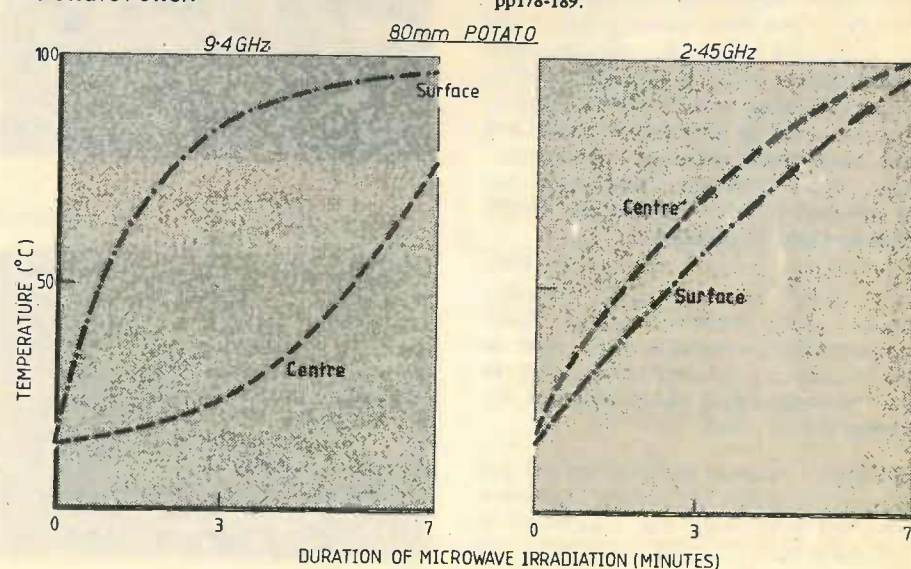
the values of  $C_3$ ,  $R_5$  to suppress the signal amplitude.

If the output stage is destroyed either through short-circuit load or h.f. oscillation, replace both Hexfet devices; it is unlikely other circuit components will have been affected. Repeat set-up procedure with the new devices in circuit. □

accepted that measurements in the near field, and hence assessment of potential health hazards, are more complex than in the far field. Taking into account near-field effects when determining maximum safe-level standards would nevertheless be sensible.

An article recently published in Radio Communication<sup>2</sup> gives a good account of r.f. radiation hazard, as far as the radio amateur is concerned. The authors state that reports of "non-thermal" effects of r.f. radiation, mostly emanating from Eastern Europe, should be "regarded with suspicion", and go on to say, "there is no evidence that r.f. radiation produces long-

The first of these graphs provided by Mr Harlen of the NRPB shows r.f. radiation penetration and absorption versus frequency for a plane slab. Combined effects of penetration and 'focussing' (or geometry and high refractive index) in a potato are illustrated in the two other graphs taken from the Journal of Microwave Power.



term damage of the kind associated with ionizing radiation, i.e., cancer or genetic damage." Not a hint is given that the authors feel the accepted maximum level might be too high.

But not everyone is happy with the situation. Mr Herbert Goldwag, for one, summarizes the opposing point of view in an article called 'Microwave hazards' published in the IEEE Spectrum<sup>3</sup>. □

### References

- 1 Small wavelengths - large doubts, *Wireless World*, October 1981, p42.
  - 2 R.f. hazards and the radio amateur, Blackwell, R. P. and White, I. F., *Radio Communication*, February 1982, p136.
  - 3 Microwave hazards, *IEEE Spectrum*, May 1979, p66.
- Further reading  
Reference Data for Radio Engineers, Howards W. Sams and Co., Inc., p27-46.  
Handbook for Radio Engineering Managers by J. F. Ross, Butterworths, pp372-387.  
Radio hazards in the m.f./h.f. band, Rogers, S. J. and King, S. R., *Non-Ionizing Radiation*, vol. 1, No. 4, pp178-189.

# LETTERS

## SITUATION NORMAL...

In your February issue, Pat Hawker mentions "SNAFU" as a coinage of War II. I think he and your readers may be interested to know its pre-war origin.

During the said war it was my pleasure to work for a time with two clever and humorous American Western Electric telephone engineers, and they told me that their pre-war jobs had been to go to telephone exchanges where there was trouble and rectify it. Upon arrival at the site an engineer would make a brief estimate of how serious was the trouble, establish a telephone link to his headquarters and send back a code word. His home base would therefore know he had arrived where the problems were, have a rough idea of how long it would take to clear them and have a telephone number where he could be contacted if need be. There were three code words: SNAFU - Situation normal, all fouled up" (or words to that effect); TARFU - "Things are really fouled up"; and FUBAR - "Fouled up beyond any repair". The latter would be sent if, for instance, a telephone exchange had been seriously damaged by fire or flood, while SNAFU would be used for a situation where cables or machinery had been damaged but where repairs or replacement would be relatively straightforward.

SNAFU became widely used in many situations during the war, but strangely the other code words were rarely used or were unknown. It would be a pity if this bit of folk lore was lost.

C. H. Banthorpe  
Northwood  
Middlesex

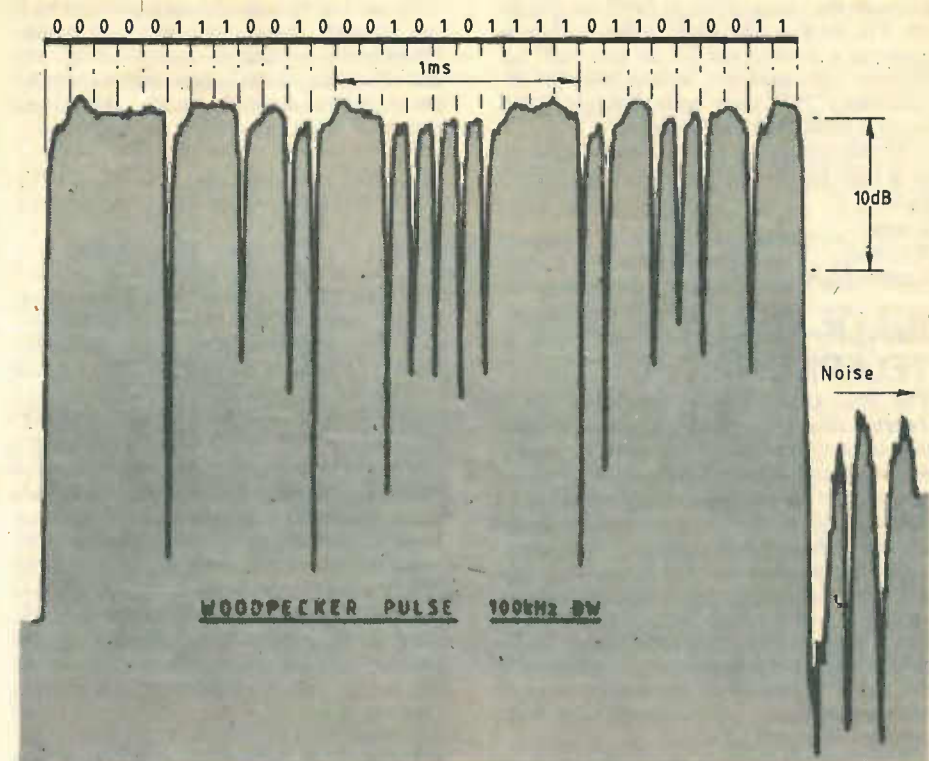
## WOODPECKER

As a radio amateur, I have often been annoyed by the Russian "woodpecker" pulse transmissions which have plagued the h.f. bands for many years<sup>1</sup>. There has been no official explanation of the purpose of these transmissions, and various theories have been expounded in the media, ranging from spy communications to death rays. However, as a result of accidentally coming across some of these signals on a laboratory spectrum analyser, and storing the waveforms on a transient recorder, I think I can shed a bit more light on their structure and purpose.

Figure 1 is based on a printout of a typical pulse, plotted as logarithmic amplitude versus time. The overall duration of the pulse is 3.1ms. The interesting feature is the presence of "glitches" in the top of the pulse, the pattern of which remains the same from pulse to pulse, and they occur at intervals which are multiples of 100  $\mu$ s. This led me to suppose that the glitches formed a binary sequence of length 31 bits.

I also guessed that the glitches arose from phase reversals in the transmitted signal, the finite width of the glitches resulting from the effect of the finite bandwidth of the transmitter and/or spectrum analyser. Thus, arbitrarily assigning a zero to the first data bit, the original modulation pattern could be reconstructed, with 0 representing 0 degrees and 1 representing 180 degrees. This gave the pattern 0000011100100010101111011010011.

This sequence turns out to be a maximum-length, pseudo-random binary sequence<sup>2</sup>, which can be generated by a 5-bit shift register with feedback formed from the parity function of the contents of stages 3 and 5. I subsequently



observed other pulse transmissions with different sequences of the same length, and was able to match these to p.r.b. codes from shift registers with feedback from stages 2,5 2,3,4,5 and 1,2,3,5. Four different codes, implying four different transmitters, agreeing with observations previously reported<sup>1</sup>.

The interesting point about this use of p.r.b. codes arises from the shape of their autocorrelation function. If such a sequence is compared bit-for-bit, with a shifted version of itself, at all possible shifts, then, apart from the position where all 31 bits match, at all other shifts no more than 1 bit matches between the two sequences. Thus, if a woodpecker pulse is fed through a 3.1 ms delay line with 31 equally spaced taps, and the outputs of the taps are vectorially combined with appropriate inversions, so that the inversion pattern itself is the same sequence as the transmitted phase-inversion sequence, then the combined output will be a single pulse of 100 $\mu$ s duration, 31 times the amplitude of the input signal, with virtually no sidelobes.

The conclusion from all this, it seems to me, is that the woodpecker must be simply a pulse compression radar system, with a resolution of 100 $\mu$ s (10 miles), but the sensitivity 31 times that of a 100 $\mu$ s radar of the same power. Not only does the p.r.b. sequence cancel out shifted versions of itself in order to achieve its performance, but it has a high immunity to other codes in the same family, thus reducing cross-interference between separately sited radars on the same frequency. The use of four different sites presumably enables the target to be pinpointed in three dimensions in spite of the poor directivity of h.f. antennas and the variabilities of the ionosphere which is used to extend the range beyond the horizon.

Although this information leads to the possibility of jamming these signals, or at least puzzling the distant radar operator, whether we

shall ever be rid of these wretched signals is another matter altogether.

J. P. Martinez G3PLX  
Gosport

### References

1. Mystery Soviet over-the-horizon tests. *Wireless World*, February 1977 p.53.
2. Pseudo-random binary sequence generators. F. Butler, *Wireless World*, February 1975 p. 87.

## POOR DEAL FOR AMATEUR RADIO

I wish to congratulate you for publishing a letter (February 1980) criticising the RSGB: at last someone has dared to make public the feelings of many RSGB members. I myself have written to the RSGB on several occasions but I have never been privileged with an acknowledgement, not to mention an explanation of their actions.

Whilst the RSGB has been trying desperately to prevent the introduction of c.b. (I, like many, see through their claims of neutrality), radio amateurs have ended up with a very raw deal. Firstly, we have lost 200 kHz of 70 MHz; secondly, only one of the h.f. bands has been introduced; thirdly, despite the introduction of c.b. on 27 MHz (with no Morse), B licencees still need Morse for 70 MHz to 28 MHz. Whilst pip/kay tones are not to everyone's taste, they are used freely on c.b. but are severely restricted on the amateur bands. Selcal type signals are not permitted on the amateur bands whilst they are on c.b. I must add at this point that I am totally pro-c.b. and I am not some jealous, sour-grapes radio amateur.

Furthermore, whilst expending its energy on anti-c.b. propaganda, the RSGB have totally ignored the decline of amateur radio. Little mention is even made in *Rad-Com* of the illegal operation on London repeaters. Why does the



RSGB not close them down or, better still, persuade the Home Office to catch the offenders. The RAE is now a joke. Amateur radio is meant as a technical hobby; the new RAE has virtually eliminated any serious technical requirements. How many radio amateurs repair, let alone build, their own equipment?

As radio amateurs, we have virtually sold our birth right and the RSGB has stood by and let it happen.

B. Reay  
Woolwich  
London SE18

## WALK-ABOUT TELEPHONES

The Post Office and its successor British Telecom have in the past been accused of being slow to meet the demand for telephone instruments other than those of the standard type, but this has now been to a large extent corrected by the availability of types ranging from the elegant baroque to the frivolous Mickey Mouse.

One facility which does not appear in the lists is the hand-held device which allows the user to make and receive calls while at the same time to be free to roam about his house and garden. Radio linkage is one way of making this possible and is the means employed in certain instruments which are obtainable by the general public from suppliers other than Telecom.

This may be because of the possibility of the radio signals involved being received by someone who is not a member of the subscriber's household.

It is unlikely that the prospective user of one of these devices will have been warned that his future conversations may be overheard and even if the point is made he may shrug off the matter and say that he does not mind. A more important factor is that even if the user is indifferent to being overheard this may not apply to those with whom he is in communication and who may have objections to what they are saying being broadcast.

It may be argued that the threat to one's privacy is pretty small since suitably equipped listeners may be thin on the ground in the immediate neighbourhood. However, a single eavesdropper of less than good intent could be at least an embarrassing nuisance or there could be legal implications in a situation where a stranger might seek to profit as a result of information received.

Finally, there may very well be a real need for this type of telephone facility but there are pitfalls in the use of unauthorized equipment. One assumes that a Telecom-approved system awaits the provision of suitable safeguards and defences against illicit tapping of the telephone network.

G. Dann  
Chipstead  
Surrey

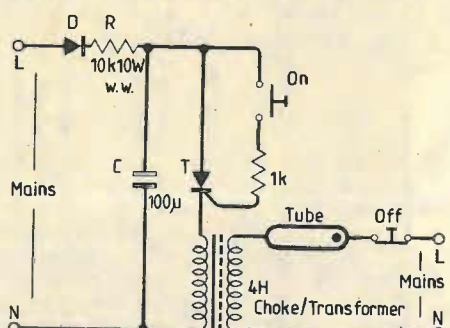
## NANOCOMP E.P.R.O.M. PROGRAMMER

I have been experimenting recently with a photographic flash tube and am concerned about inductive flashes and their erosion of the button in Fig. 1 on page 30 of the January 1982 *Wireless World*.

I think that problem could be reduced by having a low-voltage, high-current winding on the choke core in addition to the 4H. This would make the choke a transformer as well. A suggested outline for a circuit accompanies this.

letter and a description follows.

On the left the main voltage is rectified by D and charges C through R to mains peak voltage. Mains is also applied permanently to the tube and 4H winding in series but, since the tube has not struck, no current flows: the tube is open-circuit.



When the On button is pressed, C discharges via the low-voltage winding, inducing an inductive voltage of, say, 2kV in series with the mains across the open-circuit tube. But as soon as the 2kV causes the tube to strike, it is anticipated that mains current will flow through the tube, using the 4H winding now as the choke. When the Off button is pressed, the tube should go off. In the event of a thyristor short-circuit or capacitor short-circuit the 10kΩ resistor would get warm and only consume a few watts. Normally, when off, only capacitor leakage current should be taken. The operation would depend on a real difference between striking voltage and maintaining voltage in the tube.

J. R. D. Powell  
Harlow  
Essex

## DATA STORAGE

I would like to comment on two articles in the February 1982 issue: "Data recording on audio cassette" and "Economical Z80 development system". To start with, I would like to introduce myself as the designer of SOFTY, which appears in the latter article, and the inventor of TRANSWIFT, a software modem used in SOFTY to store data on cassette tape. The point that I will try to illustrate is that there are more ways of killing a cat than choking it with cream.

Data storage using audio tape is like a serial transmission in a medium of limited bandwidth (forget that the data stays in the medium for an indefinite time). The low-frequency limitations are the bigger nuisance — so why not use a system which has no low-frequency components? If the data recording is for a microsystem why not do it with software? If you are willing to ignore convention you can use a simplified recording and playback circuit.

Most microsystems have a bit of i/o going spare, either on the microprocessor itself or via an 8255 or similar. You could use a separate port for input and for output. You could add some sort of signal conditioning — but it isn't necessary. This circuit will store data using the cheapest cassette recorder at well over 3000 baud-equivalent.

Transmit a zero by putting the port high for a jiffy, then low for the same jiffy. A 1 is transmitted by using bigger jiffies. All binary transmissions are 0s and 1s strung together and the low-frequency components have vanished. You can put this transmission through a capacitor, for instance, without degrading it. You can also store it on tape and get it back unchanged. Recovering the succession of 0s and 1s is a matter of measuring the intervals between zero

crossings. The resistors suspend the port at the transition point. You might recover the data in one of two ways: either you take a positive transition as a starting point, delay for a step interval and then input the bit, or you measure the time between similar transitions and decide whether it represents 1 or 0.

Examination of this transmission shows two important properties: turning it upside down makes no difference to reception, and clock-speed errors don't accumulate — each bit contains a clock. 10% or more difference in speed won't baffle it.

A TRANSWIFT transmission doesn't use start, stop or parity bits. The speed of the transmission is more likely to be restricted by the processor's agility in handling the data than by the bandwidth of the recording system. It is up to the processor to make an intelligent decision about whether it has a valid transmission or not, and where that transmission starts. If the input is to an interrupt this process can be automatic.

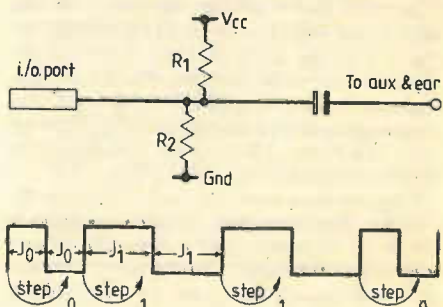
SOFTY2 uses 500μs and 1000μs as the transmission times for a 0 and a 1. To show that a transmission is coming, and to get over the bounce period of the recorder's automatic gain control, a leader of 20 bytes of 'AA' bytes are sent. (AA in hex. is 10101010). Then a hex '69' (which is 01101001), and the data, with no extra bits of any kind.

Recovery uses a routine which samples forward from each positive transition by 750μs and shifts the sample bit into a register. The word in the register is then compared with '55' and 'AA' and either are accepted as valid leaders. A leader counter with a starting value of perhaps 40 is decremented for each valid leader byte, but restored to starting value if an invalid leader is received. When the counter reaches zero the program starts looking for the '69'. The '69' is there for alignment — so that you can chop the succession of bits into bytes in the right places.

To establish the best form of error checking it is necessary to anticipate how the recorder will mess up the data. The usual system of adding a parity bit to each word fails because lateral displacement is common. All error checking systems use redundancy — they transmit extra information to catch errors. SOFTY uses a single byte appended to the transmission which is formed by exclusive-ORing all the data bytes with AA. (I used AA because it happens to be the leader and in the right register at the right time). The reception routine XORs the transmission and shows you the result — if it isn't AA then you have errors. I call this parallel parity.

In case you're wondering how much programming space this takes: A Z80 device (MENTA), designed later, uses 147 bytes for the cassette interface. SOFTY uses about 300.

The article "Economical Z80 development system" supports my claim that the combination of any assembler and a SOFTY makes a powerful design tool. However the process of linking a Nascom to SOFTY described is un-



necessary. Leaving aside the fact that SOFTY2 already has a parallel interface with normal handshake, plus serial routines for 110, 300, 600, 1200 and 2400 bauds, all of which ignore all ASCII characters except 0 to 9 and A to F — by far the simplest solution is to write a TRANSWIFT routine for the assembler's processor to dump the code into SOFTY using the cassette jack-socket. This reduces the hardware to a piece of wire and a jack-plug. In fact, I use a similar system from my Sharp MZ80K. The port used is the Sharp's keyboard i.e.d. — mainly because the connector is provided on the p.c.b.

TRANSWIFT is the simplest and most economical method of implementing a serial data transmission system, and is especially useful if the bandwidth of the medium is limited.

B. Savage  
Dataman Designs  
Dorchester

## THE DEATH OF ELECTRIC CURRENT

Ivor Catt's latest letter suggests that some progress has been achieved in an uphill struggle, for he seems to acknowledge that we are discussing models of reality and not reality itself. However, there is some way still to go, for he seems to regard models as "true" or otherwise. Models can be bad or good or better in relation to their accord with observation, but never true or false. So it is fatuous to assert that a model shows that electric current does not exist.

Certainly, there is much to be said for keeping models simple, but I think that other correspondents have shown that the "insurmountable difficulties" introduced by  $\rho$  and  $j$  exist only in Mr Catt's mind. Further, simple models are not always best: albedo measurements had shown the shortcomings of the green-cheese model of the moon, long before Armstrong arrived to test the flavour!

I was interested by Mr Davidson's achievements with discharging capacitors, but I suspect that those of us not fortunate enough to have a capability for time-domain reflectometry will continue to use the exponential model. This model does have a shortcoming in that it suggests that the discharge current continues for an infinite time, whereas observation shows that it does not. Of course, if we use an electric current model we can account for this by supposing that the discharge current becomes submerged in the noise, currents generated by random motion of the electrons within the conductors. Presumably there is a means of describing the effect using an e.m. wave model?

R. T. Lamb  
College of Engineering Studies  
British Telecom

## DANGERS OF LOW-FREQUENCY SOUND

I have just read the letter of S. Frost of Edinburgh, who replies to my earlier letter concerning my invention and operation of a hi-fi speaker system whose response is flat down to four Hz, suggesting that I should be careful. He quotes from the paperback "Supernature" by Dr Lyall Watson and suggests that my speaker could be harmful to certain people, due to its infrasound output.

I know that infrasound of very high intensity can give temporary effects which might be termed uncomfortable or disquieting by some people. However, the subject of infrasound in

general is now much better understood that it was in 1974 (the date quoted by Mr Frost which applies to the above publication) and it is now known that even prolonged exposures to infrasound of even very high intensities up to that experienced, say, in a rapidly moving railway carriage with the window open (which I believe in the order of 135-138dB?) do not cause lasting deleterious effects. My speakers at present have a maximum output on transients of around 15-20dB less than this, or around the level of v.l.f. caused in a house by a very strong wind blowing outside. There is no risk of permanent harm arising from their use as hi-fi speakers. Infrasound produced by helicopter blades, pneumatic drills, heavy trucks, etc. (from the driver's seat) can be louder than this and are still not harmful. It takes sound loud enough to physically shake one out of one's seat before even temporary damage is caused (note sound pressures, not structure-borne vibrations). Levels such as those of a full sized fog horn (marine, shore-based) at 3ft are at the danger area.

G. Holliman  
Watford  
Herts

## MICROCHIPS AND MEGADEATHS

Further to Mr P. C. Smethurst's letter in the December issue, may I suggest that the only way in which the technical society will become a reality is by a major evolutionary development of the human species.

The nearest approach the average homo-erectus makes to the technical society is to buy a digital wrist watch with alarm and graphic display, kidding himself that he will be able to tell the time with it. Such mistakes are inevitable with our present learning process.

Until our DNA reorganizes itself a little so that accumulated knowledge (only the facts, of course) can be passed directly to offspring, our ability will depend on Mr Smethurst's learning period of 15-20 years. Few people will reach his 'unusual' standard and buy watches with hands. R. G. Brown  
Watnall  
Notts

Tim Bierman (October Letters) and Roy C. Whitehead (January Letters) are wrong to imagine that refusal to fighting wars will avert their occurrence. Modern technological warfare, involving nuclear and space-based weapons, does not depend upon the recruitment of willing and gullible warriors. A small, minority elite now possesses the power to destroy the earth and, if competition over markets, trade routes and natural resources necessitates it, will sacrifice millions of human lives to the god of profit. If the threat of war is to be removed, political action must be taken to transfer power away from the possessing minority into the hands of the democratically organized world community. If the weapons are used, there will be no hiding places for conscientious objectors; the time for objecting is now.

Instead of listing names of *Wireless World* readers who would refuse to fight in the event of a future war, may I suggest that a better course would be to list the names of readers who have taken the step of extending their scientific interest in technology into a scientific analysis of society?

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## THE NEW ELECTRONICS

The article by Hugh Jaques in your January edition prompts me to add my own comments on the subject of "The new electronics".

It is all very well to decry falling standards, but I find the tone of that article rather counter-productive. The standard in Germany, if we wish to draw comparisons, is far lower — yet the number of "Diplomingenieure" (dipl — Ing) and Doctors of Science is far greater. Previous *Wireless World* editorials have covered the question of status — and one gets the clear impression that British engineers are developing an inferiority complex with regard to the Germans.

Yet, years ago, I attended a conference in Frankfurt when Cosmos and l.c.ds were introduced. The meeting began with German engineers pounding the table Kruschev-style; everyone was quite unruly. When I pointed out that l.c.ds, with a quoted life-expectancy of fifty thousand hours, could not complete for longevity with l.e.ds (up to one million hours), everyone was on his feet screaming "l.c.ds no good." The meeting broke up in chaos and I never did find out if one could prolong the life of l.c.ds by interposing ceramic capacitors in the leads to block the d.c. components of the signal, which causes electrolysis of the liquid crystals.

Dipl-Ing colleagues were forever asking me such questions as "What is the difference between a p-n-p and an n-p-n transistor", and a doctor of physics never answered any question without his "schlaue Buch" (clever book) which was his real brains.

No — the Germans are dishing out high-level qualifications in every branch of science almost like the free-gifts with chewing-gum. Yet the television programme "Bilder aus der Wissenschaft" (pictures from science) complained that Germany was not winning any Nobel Prizes.

To improve standards one must set an example through excellent work — rather than trying to catch people out. Indeed, there is nothing very wrong in a newly-qualified engineer being a little "green". The real education is the work itself, and if the British withhold their qualifications whilst the Germans mass-produce them, Britain will not be well represented at future international congresses, will lose presence in the world and cease to sell goods.

It would appear that Mr Jaques was not so "word-perfect" as he claims. In his Fig. 2, the gain is only  $-R_2/R_1$  if the source — impedance at point X is zero, which is what one would infer from the "gain between X and Z", because any generator impedance would be added to  $R_1$ . Secondly, the input-impedance at Y is  $R_2/(1+A)$  only if the source — impedance at X is infinite. Otherwise  $R_1$  and the source impedance form a series-string in parallel with  $R_2/(1+A)$ . What source impedance does Mr Jaques have in mind?

Perhaps you can see how destructive such a style of cross-examination can be. We all make mistakes which are not mistakes at all unless we want them to be. "What is the input impedance at Y with X open-circuit" would have been a better question, which would have saved Mr Jaques face. But I am just picking him up on words — as he was doing.

In the final analysis, engineers are paid for engineering — not for passing tests. Given the chance, many will succeed and many will fail. Be over selective and all will fail.

C. Wehner  
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# RECEIVERS FOR OPTICAL FIBRE COMMUNICATION

*During the next few years optical fibre systems will be used increasingly for long-distance telecommunications with emphasis on achieving greater bandwidth and greater spans between repeaters. In this rapidly developing subject it is essential to be aware not only of the latest published results but also of the underlying principles to fully appreciate the potential of optical communication. With this in mind, Dr Garrett reviews both the best reported performance in detectors and receivers and the areas where there is still room for improvement.*

Optical fibre communication systems are beginning to be used extensively for data links and for long-haul systems. The first "generation" of systems operates in the near infrared — a wavelength of about 0.85  $\mu\text{m}$  — where light sources may be made from gallium arsenide and detectors from silicon. At slightly longer wavelengths, 1.3 to 1.6  $\mu\text{m}$ , glass fibre is a better transmission medium, having enormous bandwidth and extremely low attenuation — 0.5 dB/km or even lower. Fibre systems are being used to carry telephone traffic at 140 Mbit/s over unrepeated spans of 10 to 12 km in the UK. Within the next few years it will be possible to operate at ten times that rate over at least five times that distance. As the market for fibre grows and the cost comes down, it will become economic to use fibre systems at lower data-rates as well, and also to transmit video either for entertainment or for teleconferencing.

The three basic functions of an optical receiver are to convert the signal from an optical to an electrical form, to amplify the signal, and to regenerate the transmitted message. The first of these is performed by an optical detector. Amplification is not specific to optical systems except for the special design of the front-end of the receiver, which is inseparable from the detector in determining the sensitivity. Estimation and regeneration of the message involves dealing with the noise and various system impairments; only the

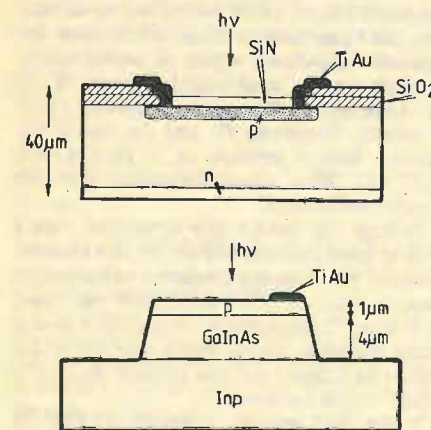


Fig. 1. Silicon p-i-n photodiode is suitable for wavelengths from 0.8 to 1  $\mu\text{m}$  (top), while InGaAs/InP p-i-n diode covers wavelengths from 1 to 1.6  $\mu\text{m}$  (bottom).

by I. Garrett

more basic ideas are covered; for more depth refer to the bibliography. In these functions, an optical receiver seems similar to a radio receiver. However, current optical receivers are quite different in the way in which they perform. Heterodyne detection, universal in radio practice because of its excellent sensitivity and rejection of adjacent channels, is at present impractical in optical receivers. It requires a local oscillator which matches the arriving signal in frequency, phase, and polarization. Today's semiconductor lasers have spectral line-widths of 25 MHz to 1000 GHz, and current fibres do not preserve a predictable polarization at the output end. Although the possible advantages of increased sensitivity and use of frequency and phase-shift keying have stimulated research into overcoming these and other problems, today's systems use incoherent (direct) detection, in which only the variations in optical power are sensed.

## Unity-gain detectors

The device which converts the optical signal to an electrical form must be efficient at the operating wavelength and must respond at a speed appropriate to the message data rate or frequency band. One may also require a linear response, operation at ambient temperature from a convenient voltage supply, and a preference for a small, light, cheap and reliable device. Semiconductor photodiodes fit all these requirements remarkably well, and there is little interest in other types of detector for optical telecommunication, at least in normal terrestrial environments. Photoconductive detectors have inferior noise performance except when the incident optical power level is high; pyro-electric detectors can only be made fast at the expense of sensitivity, and photomultipliers offer no advantage in sensitivity when, as is normally the case in fibre optic systems, the optical power level on zero bits is not zero. Phototransistors are convenient devices for low-speed data links, but are

generally not sufficiently fast and sensitive for telecommunication.

A photodiode is a reverse-biased p-n junction formed in a semiconductor material. Photons are absorbed in the semiconductor and create electron-hole pairs. These carriers can be separated by an electric field, such as exists in the depletion region of a p-n junction, and then give rise to a current in the external circuit. To convert light efficiently, the semiconductor material must have a high absorption coefficient at the wavelength of the light so that different materials are appropriate for different wavelength ranges.

The speed of response is governed by the time taken for the photogenerated electrons and holes to reach the terminals of the device, and by the RC time constant of the measuring circuit, which may be affected or even dominated by the junction capacitance. Photo-generated carriers travel across the device to the terminals from the points at which they are generated by diffusion and by drift in any internal field. The rate of diffusion is generally so slow that except in very thin layers most carriers are lost by recombination and do not contribute to the photocurrent. The device is made fast and efficient by ensuring that the incident photons are absorbed in the high-field depletion region of the junction.

Figure 1 illustrates a photodiode structure used in practice. It is a silicon device designed for the wavelength range 0.8 to 0.9  $\mu\text{m}$ , and has a thick depletion region 30 to 100  $\mu\text{m}$  thick formed in low-doped material. The absorption coefficient of silicon in this wavelength range is 950 to 350  $\text{cm}^{-1}$ , so that several tens of microns of material are needed for almost complete absorption. Very little of the incident radiation is absorbed in the undepleted  $n^+$  layer at the surface, which is only about 1  $\mu\text{m}$  thick. The device is designed so that the field required to deplete it fully is well below the breakdown field strength, but sufficiently high to accelerate the carriers to their scattering-limited velocity (around  $10^7 \text{ cm s}^{-1}$  in many semi-conductors at room temperature) resulting in a response time of about 10 ps per micron of depletion region. Depletion region doping is very low so that fast response is obtained with a moderate applied voltage. Such a device is known as a p-i-n photodiode, the i-region

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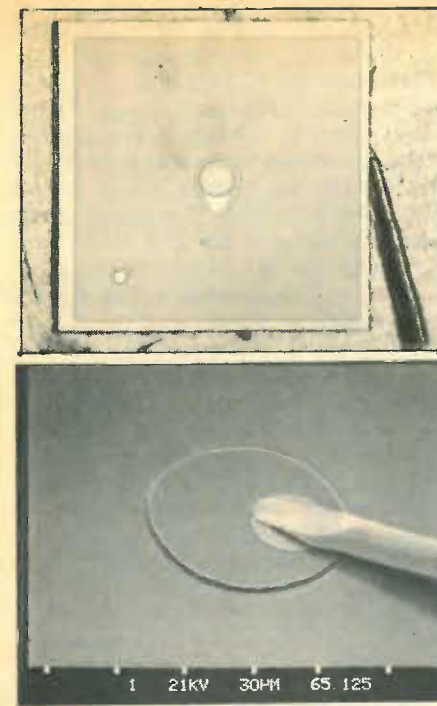


Fig. 2. Silicon p-i-n diode chip, top, is 1mm square with circle 100  $\mu\text{m}$  diameter and bonding pad beside it. Chip capacitance is below 0.1 pF, and reverse bias leakage current is around 50 pA at -10 V bias. Quantum efficiency at 0.85  $\mu\text{m}$  wavelength, corresponding to gallium arsenide injection lasers, is 0.95. Active area of InGaAs/InP photodiode isolated by mesa etching is 100  $\mu\text{m}$  in diameter in scanned electron micrograph (bottom). A small bonding pad is formed on the top surface as the device was intended for front illumination. Capacitance is 0.3 pF and reverse bias leakage current below 10 nA at -10 V bias. Quantum efficiency is only about 0.4 because many carriers recombine in the undepleted surface layer but this can be overcome by illuminating through the substrate; anti-reflective coatings also increase efficiency.

being nearly intrinsic. The wide depletion layer reduces the junction capacitance too. The device illustrated is 100  $\mu\text{m}$  in diameter and has a capacitance of less than 0.1 pF.

At wavelengths beyond 1  $\mu\text{m}$ , silicon becomes increasingly transparent and a different material is required for photodiodes intended for communication systems. An obvious choice is germanium which has a bandgap of 0.66 eV and so should be sensitive out to 1.8  $\mu\text{m}$  or so, well beyond the optimum transmission wavelengths of 1.3 and 1.55  $\mu\text{m}$ . The small bandgap of germanium is something of a disadvantage: coupled with the high density of states in the conduction band it means that the reverse bias dark current is large, which degrades the performance of an optical receiver. The other possible materials are the so-called group III-V compounds, binary compounds of elements from groups IIIb and Vb such as gallium arsenide and indium phosphide. To detect light at 1.55  $\mu\text{m}$ , a material with a bandgap near 0.8 eV is needed. None of the binary III-V compounds has such a bandgap, but many of the III-V compounds form extensive solid solutions with each other, and the mixed

compounds have properties intermediate between those of the binaries. So it looks as if there ought to be a wide choice of materials. In practice the choice is limited by the techniques available for preparing these materials in sufficiently pure and perfect form. The most usual materials for detectors in this range are the ternary compound (Ga,In)As and the quaternary (Ga,In)(As,P). In either material, the bandgap can be adjusted over a wide range by selecting a suitable composition. Reverse-bias dark current is smaller than in germanium by one or two orders of magnitude typically because of the much smaller density of states in the conduction band. Recently, the II-VI compounds such as (Cd,Hg)Te have also been studied for use as fast photodiodes in communication systems.

The second device illustrated has an absorbing layer of InGaAs deposited on an InP substrate, with the p-n junction formed by diffusing a dopant such as zinc into the absorbing layer. This device is designed for the wavelength range 1 to 1.6  $\mu\text{m}$ , in which the InGaAs layer has a high absorption coefficient, around  $10^4 \text{ cm}^{-1}$ , so only a thin absorbing layer is needed, about 3 to 10  $\mu\text{m}$ . This makes the response fast, but an important fraction of the incident radiation is absorbed in the undepleted  $p^+$  region at the surface even if it is only 1  $\mu\text{m}$  thick. Many of the carrier pairs formed in this region are lost by surface recombination or by recombination within this layer, so that the efficiency is reduced considerably. It is not easy to control the thickness of this layer much below 1  $\mu\text{m}$ , but the problem can be surmounted by arranging for the light to be incident through the back of the device, i.e. through the InP substrate, which is transparent at wavelengths beyond 0.95  $\mu\text{m}$ .

The quantum efficiency of a photodiode is the number of carrier pairs formed on average for each incident photon. It is less than unity in practical devices for three main reasons: some of the incident light is reflected; some carrier pairs are formed in undepleted material and so do not contribute to the photocurrent at high frequencies; and some carrier pairs recombine before reaching the terminals of the device. To improve the quantum efficiency, the surface of the device is often

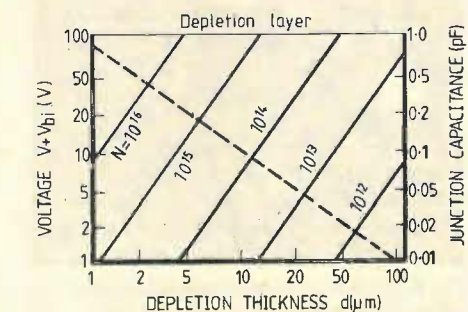


Fig. 3. Depletion voltage and junction capacitance as functions of the depletion layer thickness for a 100  $\mu\text{m}$  diameter diode, taking the relative dielectric constant to be 10, typical of many semiconductors.

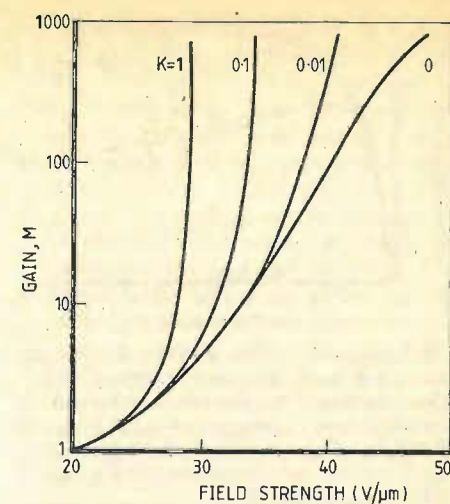


Fig. 4. Avalanche gain as a function of field strength — the breakdown characteristic. Parameter  $k$  is the ratio of ionization rates for electrons and holes.

given an anti-reflecting dielectric coating like the blooming of a camera lens; the surface reflection coefficient may be reduced from around 30% to almost zero. If the light has to pass through undepleted material, as in the lower diagram, this is kept as thin as possible or made of a semiconductor which is transparent at the wavelength of interest. Recombination of carriers within the depletion region is generally minimized by reducing deep-level impurities and crystal defects as far as possible.

The depletion layer thickness  $d$  is determined by the applied voltage  $V$  and the doping level  $N_D$ :

$$V + V_{bi} = qN_D d^2 / 2\epsilon\epsilon_0$$

where  $q$  is the electron charge and  $\epsilon$  is the relative dielectric constant, typically 10 to 15. Junction capacitance is

$$C_d = A\epsilon\epsilon_0/d$$

where  $A$  is the area of the junction. These relationships are plotted in Fig. 3, assuming a device diameter of 100  $\mu\text{m}$ . Doping levels of  $10^{12}$  to  $10^{13} \text{ cm}^{-3}$  are available in silicon, so that a few tens of microns can be depleted at 5 to 10 volts. In the mixed III-V compounds levels of  $10^{15} \text{ cm}^{-3}$  are the best normally available, so that 15 to 20 volts are required to deplete a few microns. Junction capacitance is typically 0.1 to 0.5 pF for a high-speed device so that the capacitance of a packaged device is usually dominated by the package.

The reverse-bias leakage current (dark current) of a photodiode is important because the shot noise on this current can be the dominant receiver noise in some situations. The dark current is caused by current leakage over the surface of the device as well as through the depletion region (bulk leakage). Surface leakage is minimised by careful processing and by coating the device with a passivating layer: methods vary from one material to another. Bulk leakage is due to diffusion of minority carriers from the undepleted



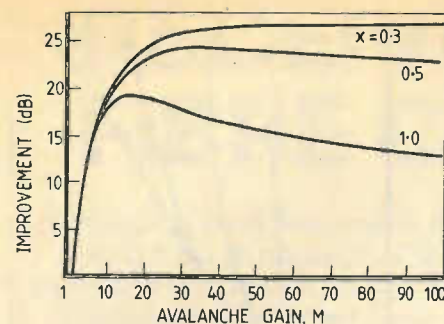


Fig. 5. Signal-to-noise ratio is improved as a result of avalanche gain. Parameter  $x$  is the exponent in the empirical expression for the excess noise factor  $F = M^x$ . Value of 0.3 to 0.5 relates to silicon reach-through diodes while germanium and III-V a.p.ds have a value close to 1.

regions and by generation and recombination of carrier pairs in the depletion region. The diffusion term usually dominates in materials with a large intrinsic carrier concentration, such as germanium. The generation-recombination term is the most important in silicon and in most III-V compounds of interest.

#### Detection in the presence of noise

The most important parameter of any receiver is its sensitivity, and there are several factors which prevent arbitrarily weak signals from being handled. The signal will have suffered various impairments during transmission, because of the dispersion and attenuation of the fibre. In addition to being distorted, the signal leaving the optical receiver has wideband random fluctuations produced by the components of the amplifier. Lastly, even with an infinite fibre bandwidth and a noiseless amplifier, the optical signal itself is statistical because of the quantum nature of light. Radio waves are also quantized, of course, but the quantum energy  $h\nu$  is much less than the thermal energy  $kT$  of electrons in the amplifier components so that quantum effects do not show up at radio frequencies. At room temperature  $kT/h$  is about 6000 GHz, well above the highest frequencies used in radio transmission, and well below the frequency corresponding to a wavelength of  $1 \mu\text{m}$ , which is 300 THz. Photons arrive at the detector at random instants with a Poisson probability distribution so that the variance in arrival rate is equal to the mean. If the expected number of photons in some time interval in  $m$ , then the probability that the number detected will be  $n$  is

$$p(n) = \text{Pos}[n, m] = m^n e^{-m} / n!$$

Consider a binary digital system in which one needs to decide whether or not a pulse was received during each bit period. The number of detected photons  $n$  is counted for each bit period, and if that number exceeds some threshold number  $d$  a one-pulse is recorded, otherwise a zero is recorded. Errors occur if  $n$  is less than  $d$  when a one-pulse was transmitted. It is easy to see that fewest errors are made

when the threshold  $d$  is set between 0 and 1 photons. The error probability is then  $P_e = e^{-m}$ , and one cannot have zero error probability with finite  $m$ . For  $P_e = 10^{-5}$ ,  $m = 11.5$  and for  $P_e = 10^{-9}$ ,  $m = 20.7$ .

In an analogue system, we are interested in the signal-to-noise ratio (snr) at the receiver output with a post-detection bandwidth  $B$  which smooths fluctuations over an integration time  $t = 1/2B$ . If the mean photon arrival rate is  $r$ , then the number  $m$  which arrives, on average, during the time  $t$  is  $m = r/2B$ . At the output of the receiver, the signal power is proportional to  $m^2$ , while the noise power is proportional to the variance of  $m$ , which is just  $m$ . Thus signal-to-noise ratio is

$$m^2/m = r/2B$$

For example, a 50dB signal-to-noise ratio and a 1MHz bandwidth requires, average,  $2 \times 10^{11}$  photon/s or 40 nW at a wavelength of  $1 \mu\text{m}$ .

That is the best performance one could expect, even with a perfect detector and a noiseless amplifier, limited only by the quantum fluctuations in the incoming optical signal. In real life, amplifiers are not noiseless because electrons in the conductors move with randomized velocities with energy  $\sim kT$ , and the amplifier has to have non-zero input conductance. Using conventional components, an amplifier with input

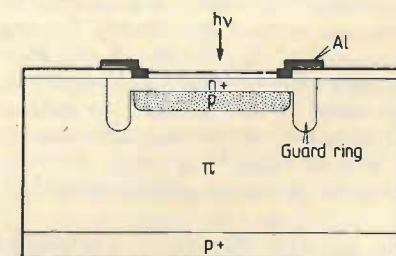


Fig. 6. Silicon reach-through avalanche photodiode is made by diffusion and implantation of dopants into a low-doped silicon substrate. Guard ring lowers electric field at the perimeter of the junction, preventing premature breakdown. Commercial silicon reach-through avalanche photodiode in a T0-18 can is the RCA 3090ZE.

capacitance of 10pF and a bandwidth of 10MHz would need to have an input resistance of about 10kohm or less loading the photodiode. The mean square thermal noise voltage in a bandwidth  $B$  due to a resistance  $R$  is  $\langle V^2 \rangle = 4kTRB = 8.3 \times 10^{-10} \text{ V}^2$  at room temperature for an  $R$  of 5 kohm and  $B$  10 MHz. The signal voltage generated across  $R$  due to  $m$  photons at a wavelength of  $1 \mu\text{m}$  detected in time  $t$  is  $V_s = mqr/t = 1.6 \times 10^{-8} m$  volts. The signal-to-noise ratio is

$$(1.6 \times 10^{-8} m)^2 / 8.3 \times 10^{-10} = 3 \times 10^{-7} m^2$$

so that in a digital system of 22 dB ratio,  $m$  is about 20,000 photons in a bit period  $t$  (taken as  $1/2B$  here). This is 1000 times or 30dB greater than the quantum noise limit, which justifies ignoring quantum noise in this calculation. As 30dB can be translated into perhaps 100 km of extra fibre at  $1.55 \mu\text{m}$  — by no means a small benefit — one would like to improve this situation. There are four ways of increasing the receiver sensitivity to consider. Reducing amplifier noise is one way, obviously — discussed see later — another way is discussed in the next section, and in the last section of this article two other ways are considered: optical amplifiers and coherent detection.

#### Avalanche photodiodes

An electron or hole accelerated by an electric field may gain sufficient energy so that when it is scattered by the lattice a lattice atom is ionized, creating an electron-hole pair. The newly created carriers can then cause impact ionization and so lead to an avalanche process with current gain.

If only one type of carrier were capable of causing impact ionization the avalanche process would advance across the high field region, the number of carriers increasing exponentially with distance but remaining finite: avalanche breakdown would be impossible. In real materials, however, both carrier types can cause impact ionization, usually with different efficiencies, providing a regenerative or positive feedback mechanism which can lead to a (theoretically) unbounded number of carriers in the breakdown. The avalanche current gain  $M$  is plotted as a function of electric field in Fig. 4;  $k$  is the ratio of ionization rates for electron and holes. The gradient of all the curves in Fig. 4 becomes infinite for some finite field, except for  $k = 0$ . The implication is as follows: to get useful current gain from the diode it must be biased close to breakdown — very close if  $k$  is near to unity. But any variation in field due to the diode not being perfectly uniform or the supply voltage being imperfectly regulated causes a change in the current gain, and this change can be large if  $k$  is near unity. The current gain becomes variable and also noisy. In silicon  $k$  can be as low as 0.01, and silicon diodes can be operated at gains of a few hundred or even thousands in some cases. In germanium and many III-V compounds,  $k$  is 0.3 — 1 and it is hard to fabricate and control a device for a gain

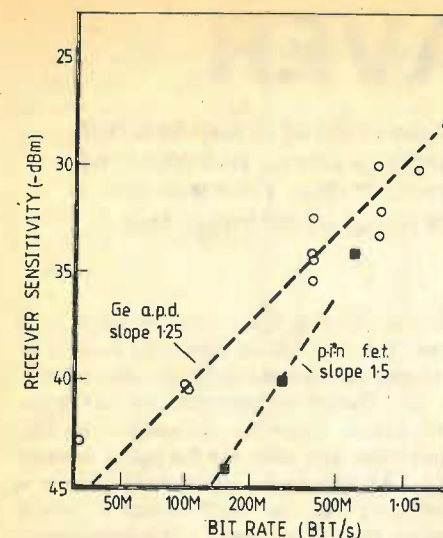


Fig. 7. Some published results on receiver sensitivity in experimental optical fibre transmission systems. Circles represent germanium diodes, and the slope of approximately 1.25 is expected for an excess noise factor exponent  $x$  close to unity. Filled squares are for p-i-n-f-e-t receivers discussed in part 2.

above 10 to 15. There are also noise problems associated with a value of  $k$  close to unity.

How is this current gain used to improve the sensitivity of an optical receiver? Current gain arising from avalanche gain increases the signal voltage across the amplifier input and so improves the signal-to-noise ratio as the amplifier noise is unaffected. However, the current gain also increases the quantum noise by the same amount as the signal, so that one cannot get beyond the quantum noise limit. In practice one cannot even get near to it because of extra noise introduced by the random impact ionization process. Consider a steady optical power  $P$  incident on the detector. The resulting multiplied photocurrent is  $\langle i_p \rangle = 2P\eta qM/h\nu$ . The mean square shot noise current on the photocurrent in a bandwidth  $B$  is  $2q \langle i_p \rangle BM^x$ , where  $M^x$  is the excess noise factor from the avalanche gain process ( $0 < x < 1$ ). The mean square thermal noise current is  $4kTB/R$ . So the output power signal-to-noise ratio is

$$\frac{(2P\eta qM/h\nu)^2}{2P\eta q^2 R M^{2+x} / h\nu + 4kTB/R}$$

With  $M = 1$  the thermal noise term dominates. As  $M$  is increased from unity the signal power increases as  $M^2$ , but so long as the thermal noise term dominates the total noise power is little affected and the signal-to-noise ratio increases. When  $M$  is large, thermal noise is insignificant and the signal-to-noise ratio decreases with increasing  $M$  as  $M^x$ . There is an optimum avalanche gain:

$$M^{2+x} = (4kT/R)(h\nu/xP\eta q^2)$$

so that

$$\frac{\text{Shot noise}}{\text{Thermal noise power}} = \frac{2}{x}$$

The empirical parameter  $x$  is related to  $k$ , the ratio of ionization rates for holes and electrons. Both depend on the material, and also on the electric field strength and direction. In silicon,  $k$  is about 0.02 and  $x$  is 0.3 typically. In germanium,  $k$  is between 0.7 and 1 and  $x$  is close to 1. In III-V alloys,  $k$  ranges from 0.2 to 1 and  $x$  is 0.7 to 1. The equation is plotted in Fig. 5 with different values of  $x$ . If  $x$  is small, as with a silicon diode, the optimum gain is large and the maximum in signal-to-noise ratio is broad. The diode can, in fact, be used to vary the gain of the receiver and so provide a.g.c. When  $x$  is near unity, less improvement is possible, the optimum gain is lower and the maximum much sharper. Such diodes may be difficult to control for optimum performance.

The theory of the avalanche process and the statistics of excess avalanche noise are important in the study of optical receivers, but they are beyond the scope of this article — consult the papers by McIntyre and co-workers in the bibliography for further details (part 2).

To make an avalanche photodiode in silicon with a fast response a simple p-n junction will not do because most photons will be absorbed in undepleted material where the field is negligible. It is necessary to use the "reach-through" structure shown in Fig. 6 in which the depletion region consists of a high-doped, high field-gain region followed by a lower field, low-doped absorbing region. The problem is to ensure that the absorbing region is fully depleted well before the gain region breaks down, and this demands great control over the fabrication of the device. Nevertheless, good commercial silicon reach-through diodes have been on the market for several years.

Most system work at longer wavelengths has been carried out using germanium avalanche photodiodes. Germanium seems an obvious material, as the photodiodes can be made sensitive out to  $1.6 \mu\text{m}$  and beyond by reducing the thickness of undepleted material near the surface. Germanium is not ideal because the ratio of ionization coefficients  $k$  is close to unity (i.e.  $x = 1$ ) so that the excess noise factor is high. More importantly, the reverse bias leakage current density is high because the high intrinsic carrier concentration results in a large diffusion contribution to the leakage current. The unmultiplied leakage current density is typically  $3 \times 10^{-4} \text{ A cm}^{-2}$  at room temperature, sufficient to cause a

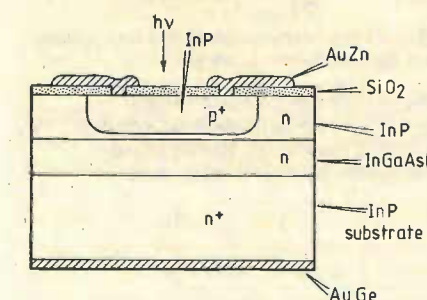


Fig. 8. Group III-V heterostructure a.p.d. has the high-field (gain) region within the large band-gap InP layer.

system penalty of a few decibels at a data-rate of a few hundred Mbit/s. The leakage current depends on temperature and at  $50^\circ\text{C}$  is about an order of magnitude greater than at  $20^\circ\text{C}$ , resulting in a large system penalty and reducing the optimum gain to about 3 to 5 as the dominant noise source may be multiplied bulk leakage. At room temperature, receiver sensitivities of  $-34 \text{ dBm}$  at 400Mbaud and  $-30 \text{ dBm}$  at 800 Mbaud have been reported using germanium photodiodes. These figures would be several dB worse at  $50^\circ\text{C}$ . Published receiver sensitivities at 1.3 and  $1.55 \mu\text{m}$  are shown in Fig. 7 for the available range of bitrates, and it can be seen that the bit-rate dependence is approximately the  $5/4$  power, as one would expect from an a.p.d. with an  $x$ -factor near unity. Also shown are the results for p-i-n receivers with a  $3/2$  power dependence, as discussed in part 2 of this article.

In pursuit of the excellent performance achieved with silicon a.p.ds considerable effort has been expended in research on diodes made in III-V compounds. To date no system results have been reported although there is much published material on the devices themselves. As with semiconductor lasers for wavelengths beyond  $1 \mu\text{m}$ . The main work has been carried out on the GaInAsP/InP system, and until recently avalanche gains in the region 10 to 20 were typical, limited probably by non-uniformity of the material of the high-field region leading to micro-plasma breakdown. More recently, a structure with the high-field region in InP has been described as shown in Fig. 8, and gains of up to several thousand reported. A different reverse-bias leakage current mechanism becomes important in the high-field region of III-V diodes: tunnelling of electrons from the valence band to the conduction band. This leakage is very sensitive to field and to band-gap. The implication is that the dark current can be reduced to an acceptable level only by keeping the high field region to low-doped, large band-gap material such as InP. The excess avalanche noise properties of the device then depend on this material. □

#### Correction

Phase-shifting oscillator, By Roger Roosen.

A number of misprints crept into this article published in the February issue, for which we must apologise. Many of the mathematical formulae were affected and we would be happy to provide interested readers with a corrected copy if they send us a stamped-addressed envelope.

The author has asked us to point out that distortion was measured using fixed 1% resistors for the tuning elements. Such figures could not be achieved with a two-gang potentiometer.

A numerical analysis of the thermistor distortion was made with a computer and the results were compatible with calculated ones. The only significant distortion generated in the n.t.c. is third harmonic.

The measured distortion figures show that the second-harmonic distortion of the circuit increases at low frequencies. This is due to second-order effects in the i.c.s due to temperature variation with the oscillator signal. This distortion sets the performance limit of the circuit at low frequencies.



# HEATING-FUEL SAVER

Over the season some saving can be made in heating fuel bills by switching on later when the weather is less cold. This feature is usually incorporated in large systems but the unit described, which may be built at low cost, is intended for domestic use. There is an outdoor temperature sensor which is not essential but may be used to monitor the heating system.

The outdoor sensor is a thermistor, of which the resistance ( $R_t$ ) must be known, or measured, at three relevant temperatures, for example 0°, 10°, and 20°C, which is connected in series with a fixed resistance  $R_s$ , across a stabilised voltage. By appropriate choice of  $R_s$  (see appendix), the relationship of the mid-point voltage ( $V_t$  to temperature can be quite well linearised, as shown in the table. The timing circuit uses a slowly-rising voltage  $V_p$ , and a comparator to close the switching relay when  $V_p$  reaches  $V_t$ . The ramp voltage  $V_p$  is generated digitally using a data-a converter in the prototype the popular Ferranti ZN425E, clocked at v.l.f. to give for example a delay of one hour per 10°C.

The power supply section shown in Fig 2 is suitable for a standard 24V d.c. octal-based relay, of which the coil resistance is typically 470 ohms. If a different voltage is used,  $R_d$  should be adjusted to give 8-12V input to the regulator.

## Counting-up

In Fig. 3, the 425 internal counter is brought into use by tying pin 2 high. The internal resistance ladder is connected to the internal reference source ( $V_{ref}$ ) by joining pins 15 and 16, and the analogue output  $V_p$  at pin 14 is then given by:

$$V_p = V_{ref} \times N / 256$$

where  $N$  is the count reached. The counter has eight stages, and the maximum count is (1 + 2 + 4 + 8 + 16 + 32 + 64 + 128) or 255. The nominal reference is 2.56V, giving 10mV per count, but its exact value is unimportant, since the thermistor  $R_t$  is also supplied from  $V_{ref}$ , and:

$$V_t = V_{ref} \times R_s / (R_s + R_t)$$

Thus the count required to make  $V_p$  exceed  $V_t$ , and so turn on the relay via comparator IC2a is given by:

$$N = \text{nearest whole number above}$$

$$\left( 256 \frac{V_t}{V_{ref}} = 256 \frac{R_s}{R_s + R_t} \right)$$

The table shows  $N$  values for various temperatures, relating to RS code 151-237 thermistor, which is a close-tolerance device ( $\pm 0.2^\circ\text{C}$ ). Resistance  $R_s$  should be made up to within 1% from metal-film

$^\circ\text{C}$	-5	0	5	10	15	20	25
$R_t \Omega$	42,295	32,650	25,377	19,900	15,701	12,490	10,000
$V_t/V_{ref}$	0.2680	0.3217	0.3790	0.4376	0.4965	0.5535	0.6076
Error $^\circ\text{C}$	+0.4	nil	-0.1	nil	+0.1	nil	-0.3
N (counts)	69	83	98	113	128	142	156

by David Ryder, Ph.D.

resistors. Other thermistors can be used by measuring them and calculating the appropriate  $R_s$  (see appendix). Setting-up is easier if test-resistances are made up to substitute for the thermistor at say 0°, 10°, and 20°C, and in the prototype these were built in using a four-way switch.

## Circuit operation

The 425 is clocked, pin 4, from a conventional 555 oscillator divided by a c.m.o.s. 4040B. The division ratio to 4040 pin 1 is 4096, and to pin 3, 64, the latter output being used via  $Tr_3$  to flash an l.e.d. and via  $S_1$  to give fast clocking of the 425 for test purposes. From the table the number of counts between 0°C and 20°C is 59, and if this is to occupy 59 minutes, one count per min, the 555 period must be 60/4096  $\approx$  0.0146 sec, or 14.6 ms.  $V_{r1}$  gives a range of about 1 to 3 hours per 20°C.

The comparator IC2a has an open-collector output, which is pulled up by the 1k resistor, and the relay is switched via  $Tr_2$ . The positive feedback from the output C to the non-inverting input is needed to latch the comparator, since  $V_t$  may subsequently rise above  $V_p$ , but diode  $D_4$  avoids loading on the input, and so on the 425

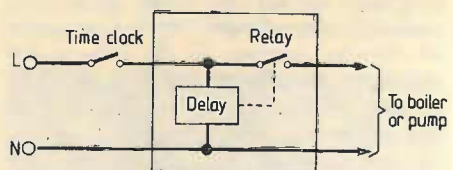


Fig. 1. In-line connection of delay unit between time-clock and load.

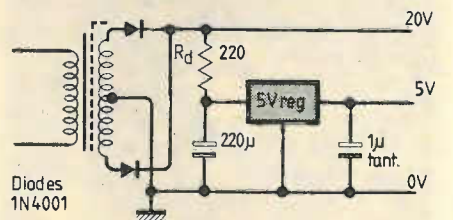


Fig. 2. Power-supply section. The regulator may be 100 mA or 1 A type.

Linearisation of RS code 151-237 thermistor, using calibration points 0°, 10°, and 20°C, resistor  $R_s$  15,485 ohms. Thermistor tolerance is ignored.

output, during the count-up, when C is low. The 'Set' button allows the relay to be closed without waiting for the time delay.

The 'Reset' button resets the 425 counter, pin 3, resets the comparator via  $D_5$ , and resets the 4040 via the p-n-p inverter  $Tr_1$ . At switch-on, the same function is performed by the 10μF capacitor, which delays the rise of point B. The 4040 (alone) is also reset via  $D_6$  when C eventually goes high, stopping the count at this point, and causing the l.e.d. to glow continuously.

The op.amp section of IC2 is used to drive a millimeter from  $V_t$  to indicate outdoor temperature, and almost any f.s.d. can be used up to say 5mA. In the prototype an existing 0-100 scale was used for degrees Fahrenheit, and the biasing shown,  $R_b$  and  $R_f$ , gives a reading of approx 32 at 0°C, which can be trimmed by the mechanical zero adjustment. The resistance of  $R_m$  was made up to give a swing of 36 divisions between 0°C and 20°C (32°F and 68°F). The meter may of course be remotely mounted, perhaps alongside your barometer.

## Checks

The eight counter outputs of the 425 are available at pins 5-7 and 9-13, and in that order have weights 1, 2, 4 . . . 128. A count of 83 for example, or 64 + 16 + 2 + 1, corresponds to pins 12, 10, 6, 5 high (and the rest low), and this allows the counting to be checked using the test resistances, and the 'fast' setting of  $S_1$ . An error of one count is not important. The 555 timing can be checked by a frequency meter, or from the l.e.d., which flashes 64 times per 'normal' 425 count.

## Variations

The basic circuit still has a long delay in cold weather, for example 69 counts at -5°C, and though this can be compensated by advancing the time-clock, it is more elegant to suppress it by jumping, clocking the 425 directly from the oscillator, point F, until an appropriate count is reached. Figure 4 shows two possible circuits, 4(a) being that used in the prototype. The logic shown may be realised in various ways, but diodes and transistors are cheap, and easy to lay out on Veroboard.

If it is required to use the thermometer when the time-clock is off, the delay unit must be continuously-powered, and reset may then be modified to Fig. 5, in which the time-clock signal is detected by a transistor-type optoisolator. Reverse voltage is limited by  $D_7$ . Resistor  $R_r$  should pass 5-10mA rms, and may be replaced by a capacitor, say 0.1μF, provided it is a type suitable for continuous mains working. The intermittent output allows the 1μF capaci-

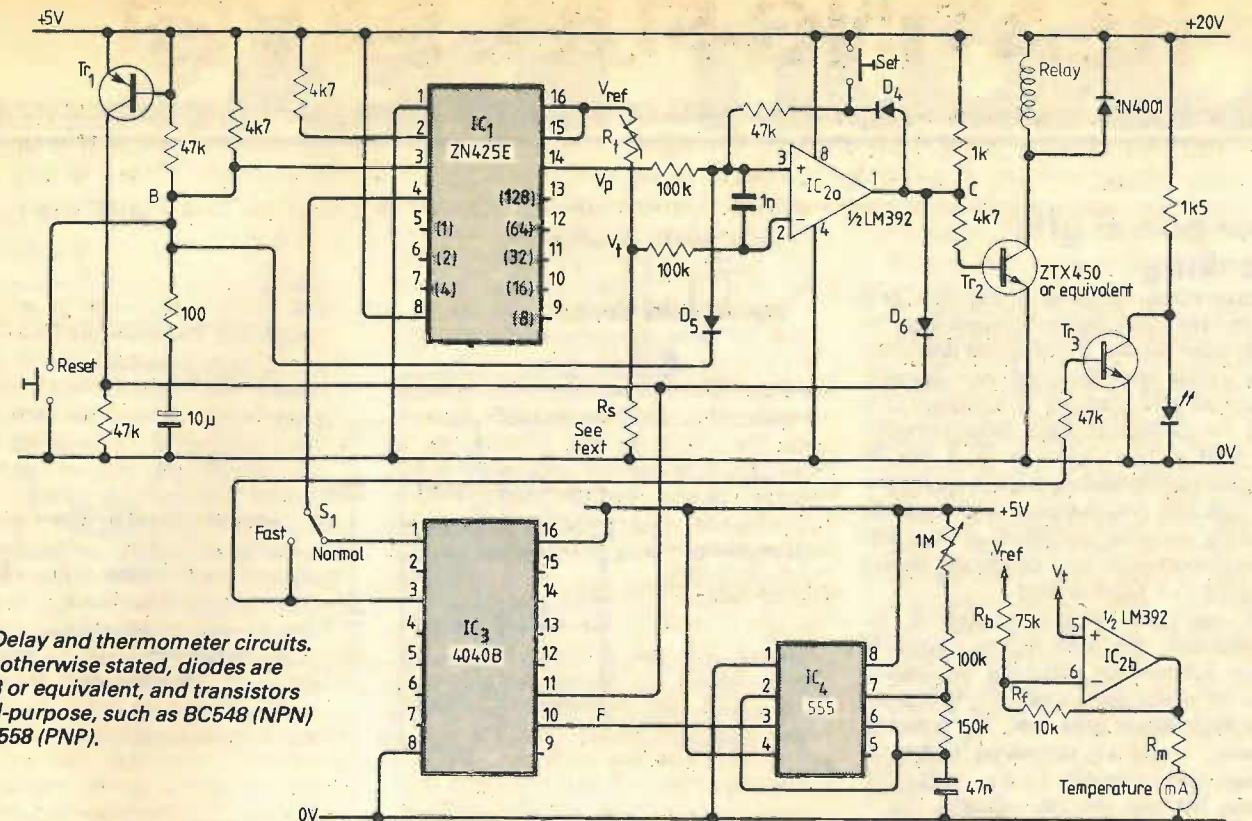


Fig. 3. Delay and thermometer circuits. Unless otherwise stated, diodes are 1N4148 or equivalent, and transistors general-purpose, such as BC548 (NPN) and BC558 (PNP).

tor to provide initial reset even if the delay unit is powered up with the time-clock on.

Since the 425 count stops at switch-on, it stores the switch-on temperature, which may be read out later in the day by switching IC2b input to  $V_p$  (425 pin 14) rather than  $V_t$ . However it is necessary at the same time to break the normal pin 14 connection, because the feedback with C high raises  $V_p$  above its actual switch-on value.

## Thermistor mounting

The RS device is a small bead, about 1.5mm dia. For the prototype, a 1.6mm hole was drilled nearly through a 12mm cube of aluminium, then enlarged part-way to a push-fit for a 4mm tube about 10cm long, which in turn fits through a 4mm hole drilled in the frame of a north-facing window. The thermistor leads were extended by 7/02 wires, and the assembly pushed down the tube, so that the thermistor bead entered fully into the 1.6mm hole. A blob of heat-conductive grease was used to improve thermal contact, and the block and tube were painted dull black. Thin twisted wire was used for connection. If a long run is needed, it would be advisable to decouple  $V_t$  to ground via 10μF to suppress any hum pick-up.

## Appendix

The usual thermistor formula is  $R_t = A \exp(B/T)$ , where  $T$  is absolute temperature in degrees Kelvin ( $C + 273$ ), and  $A$  (ohms) and  $B$  (K) are nominally constant.  $B$  is often around 3,000, and  $A$  is a small fraction of an ohm. Values can be deduced from measurement at any two temperatures, but since they are only approximately constant, calculations are best restricted to interpolation only.

The method of calculating  $R_s$  does not

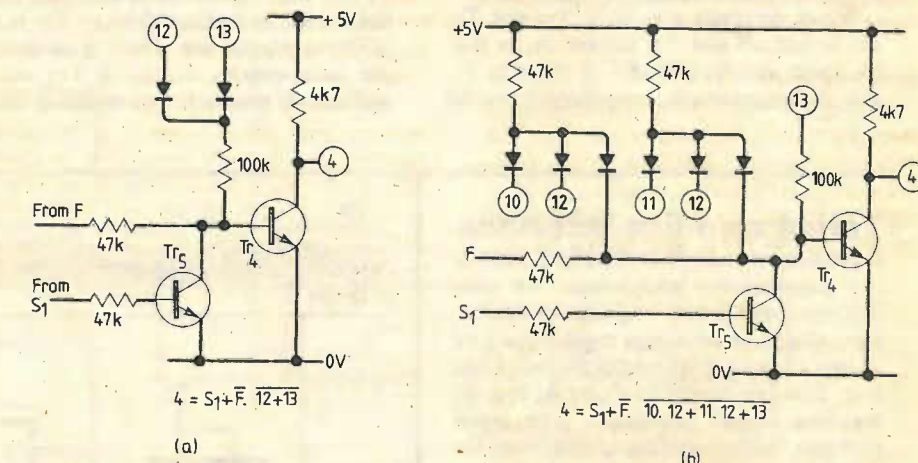


Fig. 4. Count-jumping; 4(a), jump to 64; 4(b), jump to 80. Numbers in circles are 425 pins, and the circuits replace the direct connection of  $S_1$  to pin 4.

however depend on  $A$  and  $B$ , but merely makes three calibration points lie on a straight line. The arithmetic is simplest if the calibration temperatures are equally spaced,  $T_3 - T_2 = T_2 - T_1$ . Suppose the value of  $R_t$  at  $T_1$  is  $R_1$ , at  $T_2$   $a.R_1$ , and at  $T_3$   $b.R_1$ . Then  $R_s$  is given by:

$$R_s = R_1 \times \frac{a-2b+a \cdot b}{1-2a+b}$$

As the table shows, the linearity between calibration points is good, and it is acceptable over a larger range. It may be noted that, from Thévenin's theorem, the same value of  $R_s$  applies in a circuit using a constant current through  $R_s$  and  $R_t$  in parallel. Maximum thermistor power occurs when  $R_t = R_s$ , and for Fig. 3 is 1.28²/15,485, about 0.1 mW, which for the device used, in free air, would produce about 0.1°C self-heating. When using lower-resistance thermistors, the possibility of self-heating error should be borne in mind.

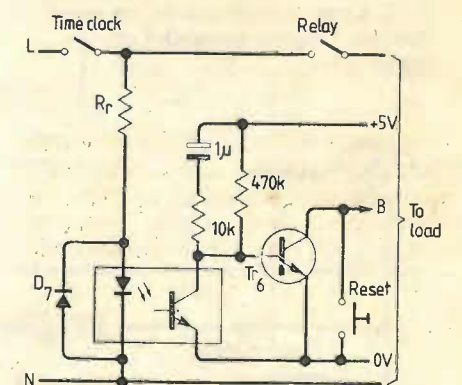


Fig. 5. Opto-isolator reset, for use when delay unit is independently powered. With this circuit omit components 100R, 10μF from Fig. 3.



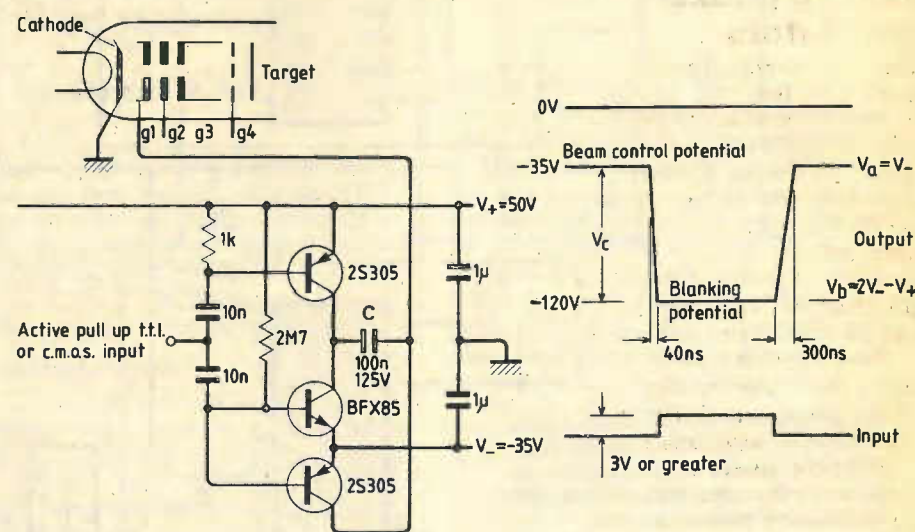
# CIRCUIT IDEAS

## Low-power grid blanking

Electron-beam blanking at the first grid can involve much higher voltages than cathode blanking but is sometimes desirable. This circuit was designed for digitally-controlled grid blanking of a camera tube used for quantitative light measurements. The grid voltage (equal to  $V_-$ ) can be accurately controlled during the active picture line and transitions to and from the blanking potential are short, at 40ns and 300ns respectively, with no ringing when a Schottky t.t.l. input is used.

Because grid-leakage current is extremely low, the high voltages required can be achieved by switching the connections of a charged capacitor. When the input logic signal goes low,  $Tr_2$  is turned off and  $Tr_1$  and  $Tr_3$  turned on so that the voltage over capacitor C,  $V_C$ , is the difference between the rail voltages,  $V_+ - V_-$ . The output to g1 is held at the negative rail, which controls the beam current.

When the input goes high,  $Tr_1$  and  $Tr_3$  are turned off and  $Tr_2$  turned on, so that the more positive side of C is taken to  $V_-$  and the negative side consequently to the



blanking potential,  $V_- - V_C$  which is also  $2V_- - V_+$ . The droop in blanking potential caused by leakage through  $Tr_3$  is negligible in normal use. There is no droop in the beam-control voltage as  $Tr_3$  remains sufficiently conductive throughout the ac-

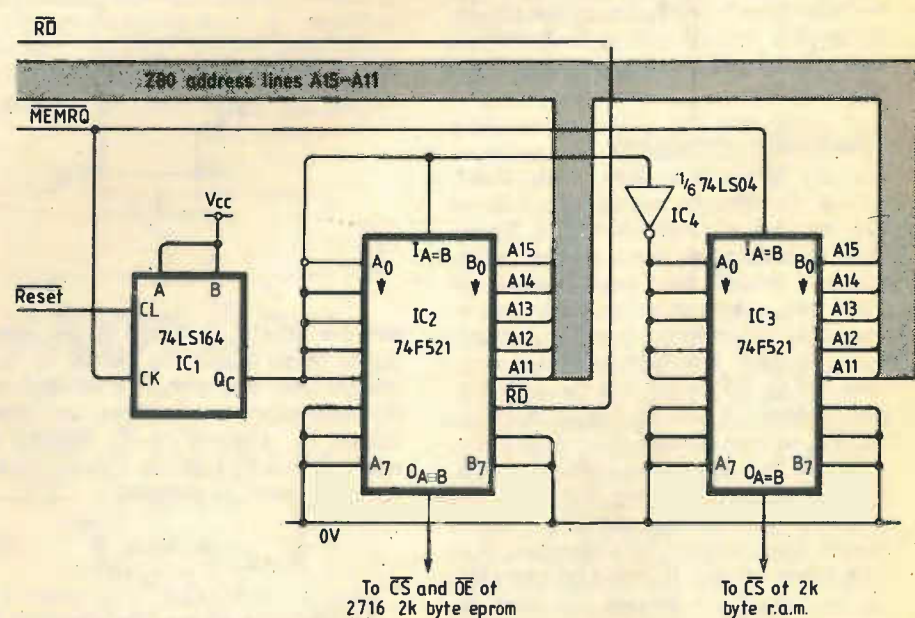
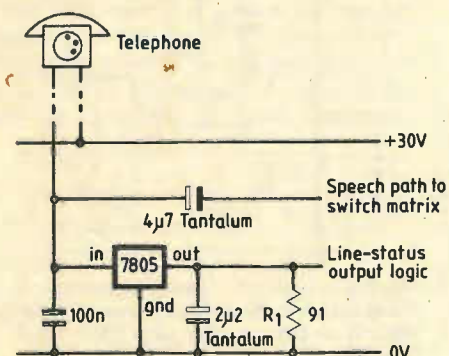
D. J. Thomas  
MRC  
Cambridge

## Telephone-line interface

Conventional telephone-interface circuits use relays and/or transformers for loop detection and speech coupling. In this circuit, a 5V positive-voltage regulator is used to feed a constant current to the telephone line. The line current is set by  $R_1$  and the regulator output provides a logic signal that will 'follow' dialling pulses from the telephone.

As this circuit provides unbalanced transmission to the telephone, it is only suitable for internal (intercom type) exchanges. A ring circuit could be provided by a third wire to the telephone. Acknowledgement to the Director of Research\* for permission to publish this information.

**F. T. Lyne**  
\*British Telecom Research Labs  
Ipswich



## Z80 memory mapping

**R.a.m. area for interrupt restart vectors and e.p.r.o.m. write protection** are provided by this automatic memory map and switch for a Z80 microprocessor system. On power-up, or after a reset, a 2K-byte e.p.r.o.m. (2716) occupies addresses 0000 to 07FF and a 2K-byte r.a.m. is address mapped to F800-FFFF. After a reset, the Z80 will perform an op-code fetch from location 0000. The e.p.r.o.m. will be selected after MREQ is activated. The instruction at locations 0000 to 0002 is IP F803

and the circuit will automatically switch r.a.m. and e.p.r.o.m. locations after the third memory access. The next op-code fetch will occur at location F803, causing execution to continue from the next contiguous location in e.p.r.o.m. Locations 0000 to 07FF are now occupied by the 2K r.a.m. so it is possible to initialize and modify the interrupt restart vectors, hence providing a greater degree of flexibility.

C. Jay  
Fairchild Camera and Instrument Ltd  
Bristol

## Testing p.r.b.s. generators

Readers experimenting with p.r.b.s generators may find this circuit useful for evaluating possible feedback configurations. Driven by an external clock at any speed up to a few hundred kHz, it gates clock-pulses to an external counter for exactly the duration of one complete sequence, maximal or otherwise, so that the final counts shows the number of steps in the sequence. The generator is preset so that the count begins almost immediately.

The shift-register shown has  $n$  effective stages and is negative-edge triggered (e.g. 4006's); for a positive-edge triggered shift-register the inverted clock-signal is used.

When the system is at rest, both flip-flops are in the reset state and no clock-pulses appear at the output. Point A is low,

so the auxiliary counter is held at zero and the input to the shift-register is held high. After a maximum of  $n$  clock-cycles all the stages of the shift-register will be in the high state, and the system ready to start.

The start button sets the start flip-flop on the next negative-going transition of the incoming clock-signal; contact-bounce has no effect. Point A goes high. This allows the generator to run normally, with its output (from stage  $n$  of the shift-register) controlling the auxiliary counter. When the generator output is high, the counter advances one count on each positive-going transition of the incoming clock-signal; when the generator output is low the counter is held at zero.

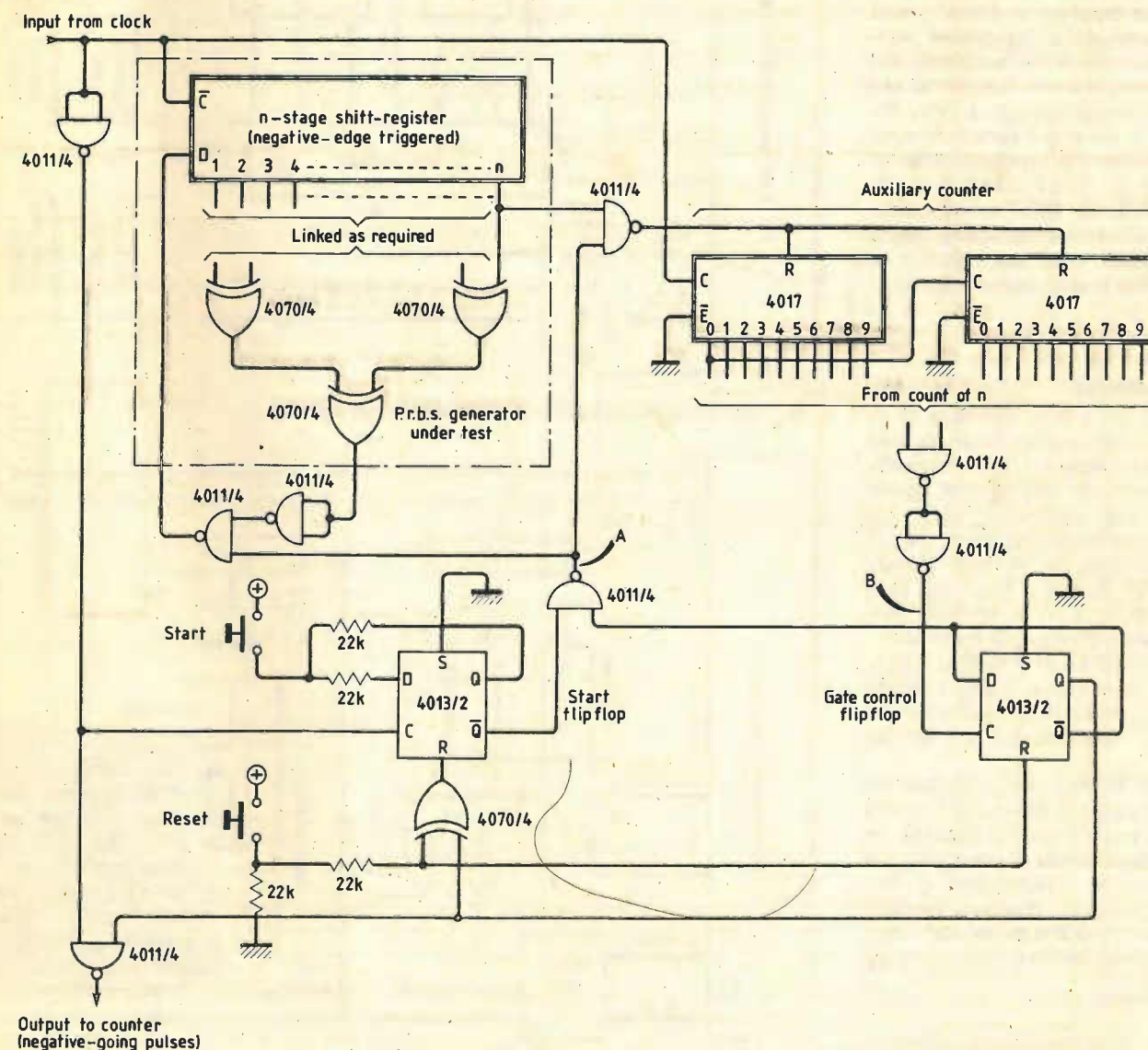
Once per complete sequence the generator output remains high for  $n$  consecutive clock-cycles; the counter then reaches the count of  $n$  causing point B to go high until

the counter is reset (nominally a half clock-cycle later).

Because all stages of the shift-register were initially preset to the high state, the first signal at B occurs during the  $n$ 'th clock-cycle from the start. This signal sets the gate flip-flop. This in turn allows clock-pulses to appear at the output, and also resets the start flip-flop while maintaining point A high so that the system continues to run. These conditions continue until the next signal appears at B exactly one sequence later, and resets the gate flip-flop; then the clock-pulses cease to appear at the output, point A goes low, the generator ceases to run, and, after a maximum of  $n$  clock-cycles, the system is back in the ready state.

Pressing the reset button will return the system to the ready state at any time.

E. L. Jones  
Bucknell  
Shropshire





# DESIGNING WITH MICROPROCESSORS 13

Clear-cut step-by-step procedures for the design and implementation of d.m.a. interfaces are described. Specifically, it is proved that in the case of action/status peripherals the interface reduces to two wires.

The block diagram of a d.m.a. system is shown in Fig. 1. The function and operation of the address decoder, the d.m.a. controller and the cycle-steal logic has been explained in the previous article (February, 1982). Briefly what happens is this. The programmer sends to the d.m.a. controller (by means of i/o instructions) three items of information specifying (i) the starting memory address, (ii) the size of the block, and (iii) the direction of transfer, followed by the 'go' command. On receipt of the 'go' command, the d.m.a. controller activates the peripheral interface by pulling enable signal E in Fig. 1 high ( $E = 1$ ). When activated, the interface monitors the status signals of the peripheral, and requests a cycle steal when the peripheral is ready. When the microprocessor responds, the interface and the d.m.a. controller generate the appropriate command signals needed by the peripheral and the memory chip for the transfer of one item of information (usually a byte) between them. At the end of each cycle steal, the memory address is incremented/decremented, and the word count is decremented ( $n := n - 1$ ). This process continues until the word count reduces to zero ( $n = 0$ ), at which time the interface is disabled and the end-of-transfer signal,  $\epsilon$ , is generated.

## D.m.a. interfaces

The function of d.m.a. interfaces is to request the microprocessor to go on hold when the main memory is to be accessed, and to generate the appropriate signals needed by the peripheral when the memory becomes accessible. In the case of cycle-steal systems, as we have already seen, the hold request is generated each time the memory is to be accessed, and removed after a memory cycle is granted.

The block diagram of a suitable d.m.a. interface, assuming logic signals throughout, is shown in the shaded section of Fig. 2. It operates in the following manner.

When logic block 1 recognizes that the peripheral is ready to be accessed, it sets flip-flop 3 by pulsing its clock terminal. Its output is Anded with the enable signal E to produce the cycle request signal c. (Assume  $\bar{e} = 1$ ). When the requested memory cycle is granted, line h is pulled high and a pulse is generated on line k. Signal h being

by D. Zissos\* assisted  
by Glen Stone\*

high, and  $E = 1$ , activates logic block 2, which responds by generating the appropriate command signals needed by the peripheral for accepting or receiving an item of information. Similarly, pulse k activates the d.m.a. controller, which ini-

tiates either a memory read or a memory write cycle. At the end of the memory cycle the microprocessor resumes normal activity, until the peripheral becomes ready, which causes logic block 1 to pulse the clock terminal of FF3. This pulls the cycle-steal line c high and sometime later a link between memory and logic block 1 is established for a memory cycle. The process repeats itself until the last item has

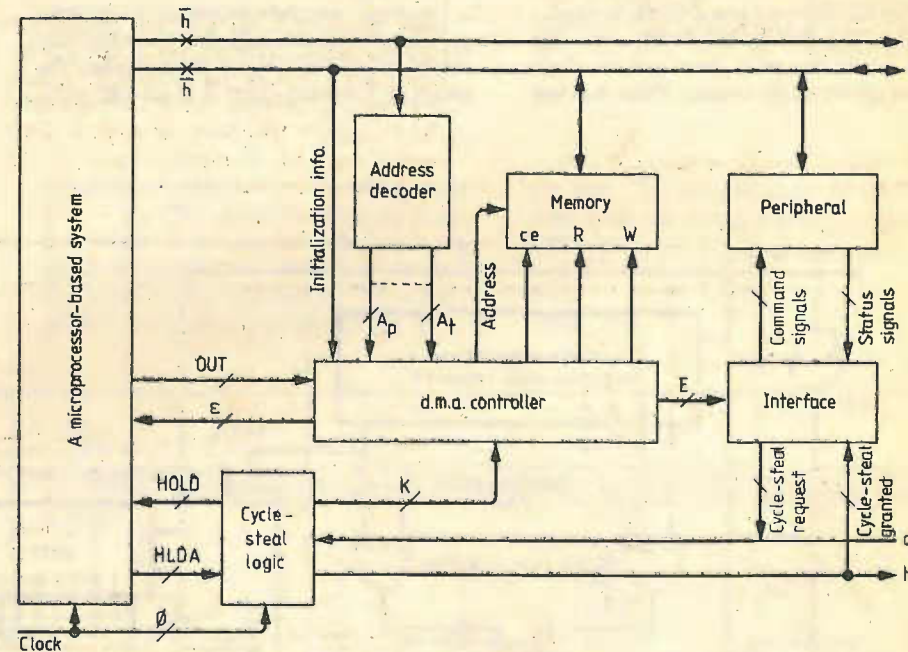


Fig. 1. Block diagram of a d.m.a. system using cycle stealing.

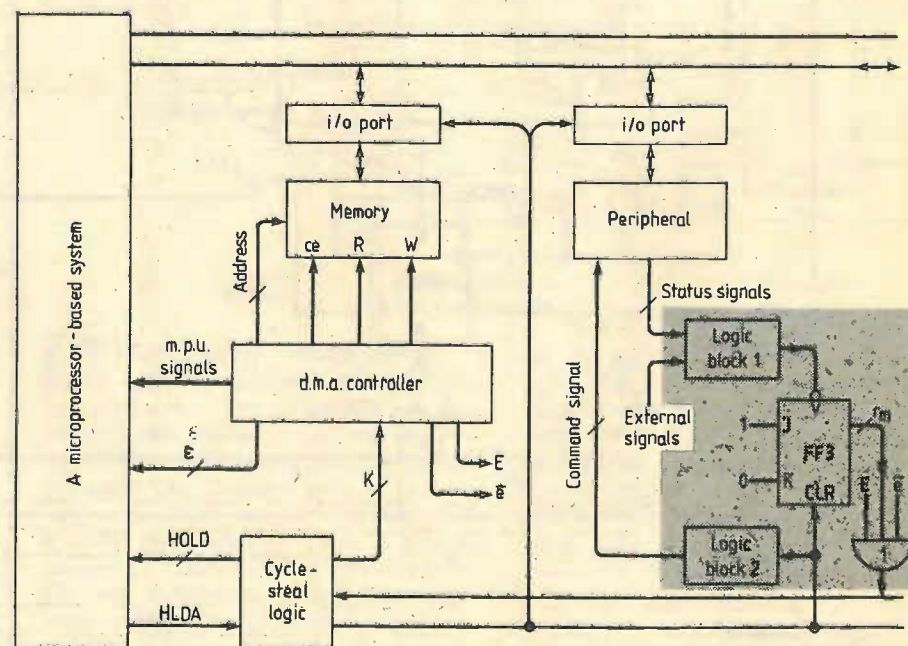


Fig. 2. Block diagram of peripheral interfaces in d.m.a. systems (shaded section).

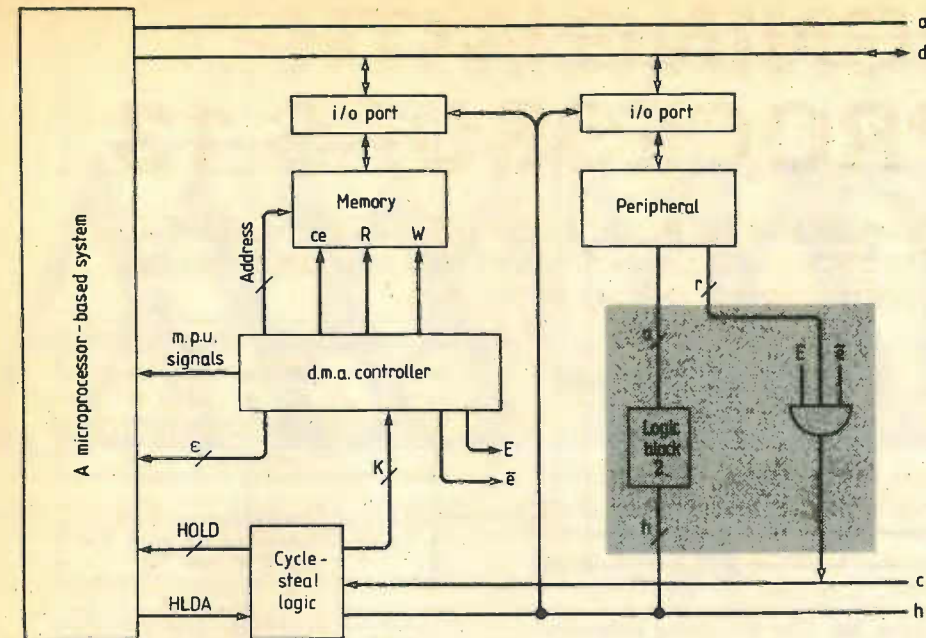
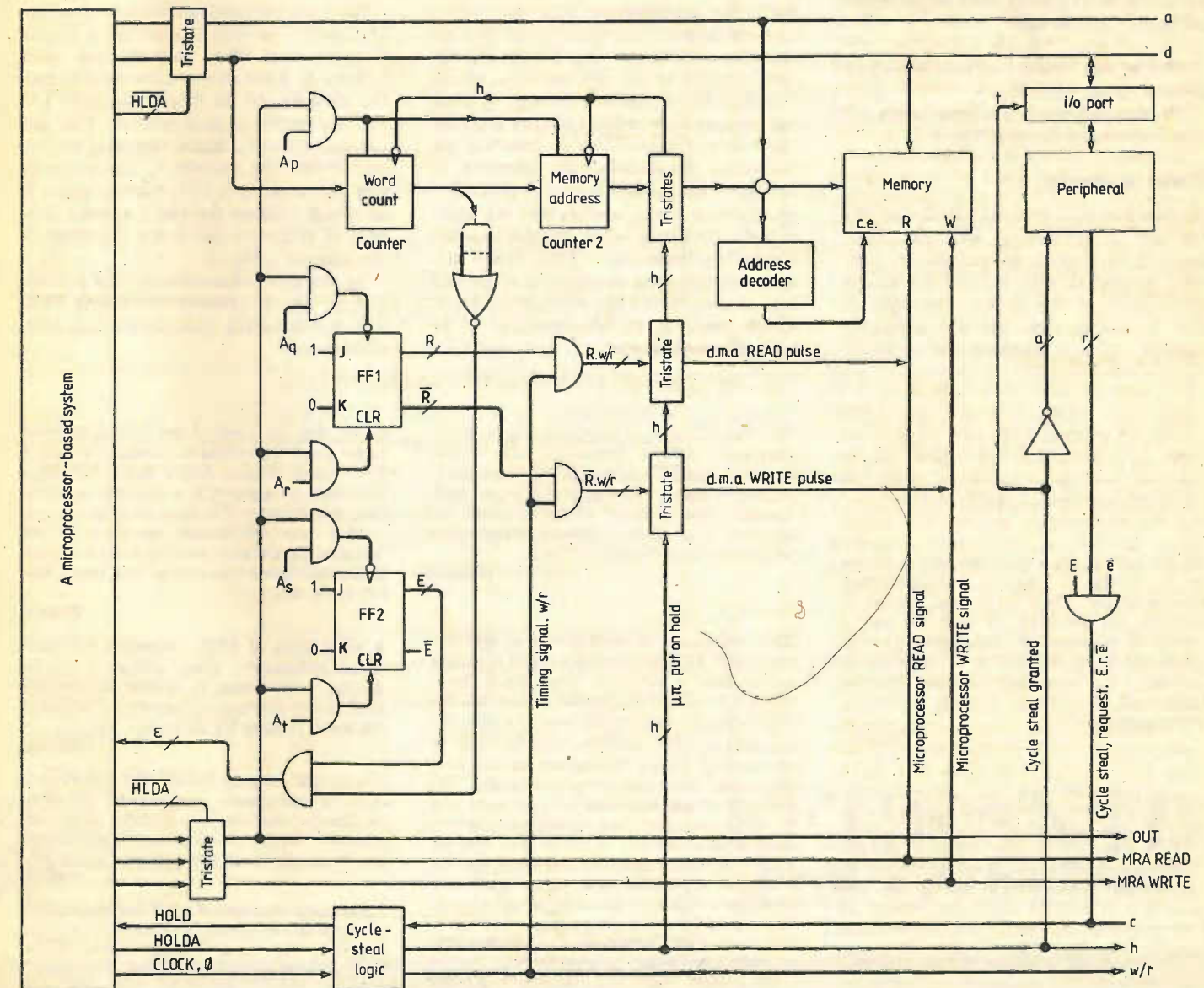


Fig. 3. D.m.a. interface for action/status peripherals.

Fig. 5. Circuit implementation of d.m.a. systems.



To prevent the word count from wrapping round, that is changing from all 0s to all 1s, after the last piece of information in our block has been transferred in or out of the main memory, it is necessary to disable the interface before the peripheral becomes ready. Because software responses invariably involve a time lag, depending on system activity at the time and on the level of priority assigned to the  $\epsilon$  flag, it cannot be used for this purpose. The most straightforward method in such a case is to use signal  $\epsilon$  in Fig. 3 of the previous article to disable the interface. Signal  $\epsilon$ , the reader will recall, changes to 1 at the end of the block transfer, that is when the word count becomes zero. Otherwise, the design and implementation of peripheral interfaces in d.m.a. systems, as indeed in all digital systems, is uncomplicated and is carried out using well-defined step-by-step procedures.

## The two-wire interface

In the case of action/status devices and no external signals, signal  $r_n$  is generated directly by the peripheral, thus eliminating the need for logic block 1 and FF3 in Fig. 2. This reduces the peripheral interface to logic block 2, as shown in Fig. 3.

\* Department of Computer Science, University of Calgary, Canada



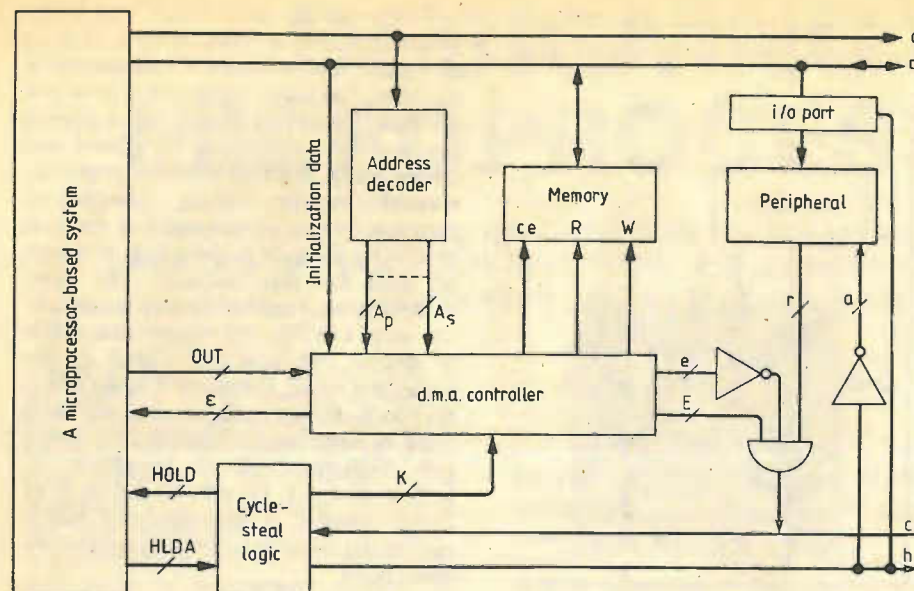


Fig. 4. The two-wire interface.

Now, to avoid possible problems resulting from peripherals being activated while data transfers take place, a peripheral will be activated when a cycle steal is terminated; that is, when the value of h changes from 1 to 0. Since action/status peripherals are activated by pulling their action terminal high, it follows that

$$a = \bar{h}$$

That is, logic block 2 reduces to a single inverter, as shown in Fig. 4.

The detailed circuit implementation of a d.m.a. system is shown in Fig. 5.

#### D.m.a. software

Because in d.m.a. systems transfers of data between a peripheral and the main memory take place autonomously, software is needed only to send initializing information to the d.m.a. controller in Fig. 1, and to clear the end-of-transfer signal,  $\epsilon$ , if it is implemented as an in-

terrupt flag. The initializing information, as we have already explained, consists of the following items

- the starting address,
- the block length,
- the direction of transfer, and
- the 'go' command.

It is transferred into the d.m.a. controller in the following manner. The programmer loads the accumulator with the initial memory address and executes an Out instruction with address  $A_p$ . This pulses the load terminal of the two counters, which transfers the accumulator contents (the initial memory address) into counter 1. At the same time, because the two counters are connected in cascade, the contents of counter 1 are pushed into counter 2. The programmer then transfers into the accumulator the block length and executes the same Out instruction. This causes the memory address in counter 1 to be pushed into counter 2, and the value of the block length (held in the accumulator) to be loaded into counter 1.

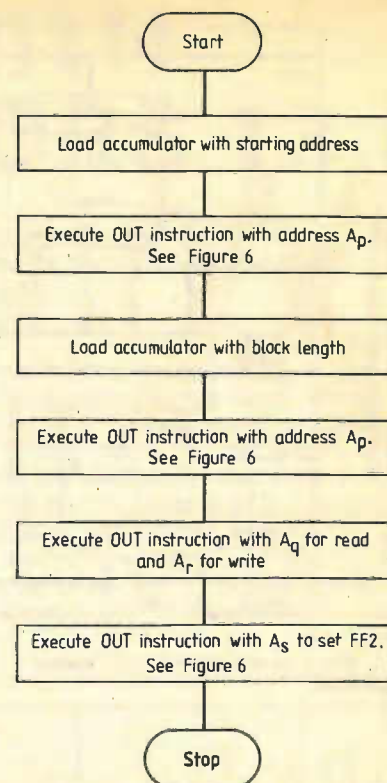


Fig. 6. D.m.a. software.

Next the programmer executes another Out instruction with  $A_q$  if the block of data is to be read from memory, and with address  $A_r$  if the data is to be written into the memory. In the first instance FF1 is set, and in the second is reset. The 'go' command consists also of executing an Out instruction with address  $A_s$ . Execution of this instruction sets FF2, turning signal E on which initiates the block transfer. For ease of reference the d.m.a. software is flowcharted in Fig. 6.

In our case acknowledging the end-of-transfer flag ( $\epsilon$ ) consists of resetting FF2, that is of executing an out instruction with address  $A_r$ . □

## LITERATURE RECEIVED

SE labs have issued a new shortform catalogue on the company's range of **instrumentation tape recorders**. There are a large number of recorders for laboratory or field use with a variety of numbers of track and recording speeds up to the SE9000, a 42 track digital recorder. Data Recording Division, SE Labs (EMI) Ltd, Spur Road, Feltham, Middlesex TW14 0TD.

WW401

The Micro Focus Newsletter has been produced to keep readers up to date with the latest COBOL computer language products and developments. COBOL is in increasing use in microcomputers and Micro Focus have announced a COBOL II which may be used on both mainframes and micros. The Newsletter is available free from Micro Focus, 58 Acacia Road, London NW8.

WW402

The 1981/82 Colorado Video short form catalog describes a series of specialised **video instruments** designed for slow scan tv telecommunications, computer/video input and output, measurement and analysis. The UK agents are Anaspec Ltd, Pearl House, Bartholomew Street, Newbury, Berks RG14 5LL.

WW403

**RS Catalogue.** The latest edition of the catalogue from RS Components Ltd has 344 pages and includes a newsheet called Rapid Scan, which is running a competition to find out who is RS's longest standing customer. Anyone who can find an old catalogue, delivery note or invoice from RS (or Radiospares as they were then) could win a magnum of champagne. The catalogue lists as additions to its contents over 75 items including data transmission cables, splashproof connectors, a bubble etch tank for p.c.b.s, a front panel with keyboard and the p.c.bs for a programmable timer, many new displays, a wide selection of tools and accessories and additions to the engineers bookshelf. Details from RS Components Ltd, PO Box 427, 13-17 Epworth Street, London EC2P 2HA.

WW404

**Racks.** The full range of Series 80 **instrument racks** from Imhof-Bedco Standard Products Ltd, Ashley Works, Ashley Road, Uxbridge, Middlesex, is detailed in a catalogue available from the company. The range includes the new S80/600 racks which meet the latest IEC297 specification. Detailed with the racks is a range of standard accessories such as tops, doors, mobile bases, etc.

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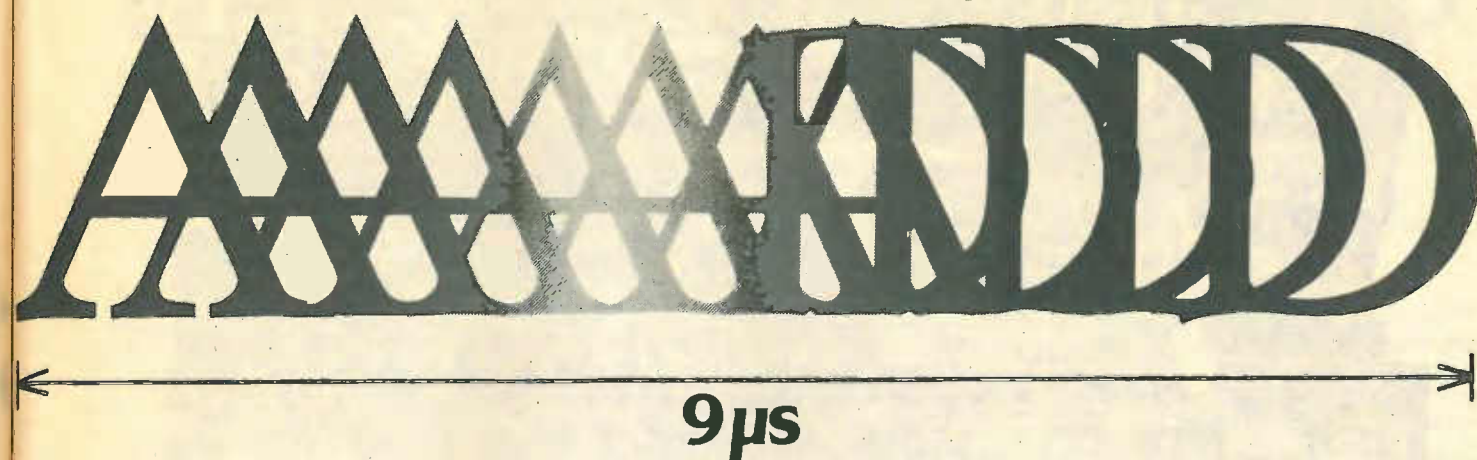
WW406

The French company Radiall offer a short catalogue of **microwave components**, including transitions, couplers, attenuators, relays and isolators. Write to Microwave Components, Lts, Invincible Road, Farnborough, Hants.

WW407

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# ELECTRONIC ORGAN WITH PIPE ORGAN SOUND

Observation of the waveforms emitted from a pipe organ show that many of them are triangular or closely related in shape. This design uses triangle-wave generators in a simplified organ system to reproduce them, and offers more accurate sound than those organs using sine, pulse, saw-tooth or square wave generators.

by J. H. Asbery,  
Ph.D., M.I.E.R.E.

The signals from the waveform generators can be fed by way of an appropriate stop, directly to the output amplifier without any filter. This simplifies the design and the use of high-level signals reduces noise problems.

If a triangle wave is rectified, an open diapason sound is produced. Full-wave rectification produces a triangle wave of twice the frequency which can be used as a 'four-foot flute' stop.

To reduce the cost and complexity of the organ, a multiphonic system<sup>1</sup> has been used which required only six generators, however many alternatives are possible.

An on/off detector to drive the attack/delay modulators has been developed which provides an improved performance.

The detector can also be used with other synthesizer circuits to eliminate one pole of the switching system. An ultrasonic signal is superimposed on the d.c. voltage of the resistor chain of the keyboard. When a key is pressed, this signal appears at the input of IC<sub>2</sub> which switches on the modulators at a steady rate and switches them off at a steady rate when the key is released. Collector resistors R<sub>54</sub> and R<sub>58</sub> of Tr<sub>3</sub> and Tr<sub>4</sub> can be common to all generators and

should be positioned close to the amplifier to avoid pick-up from the common earth wiring.

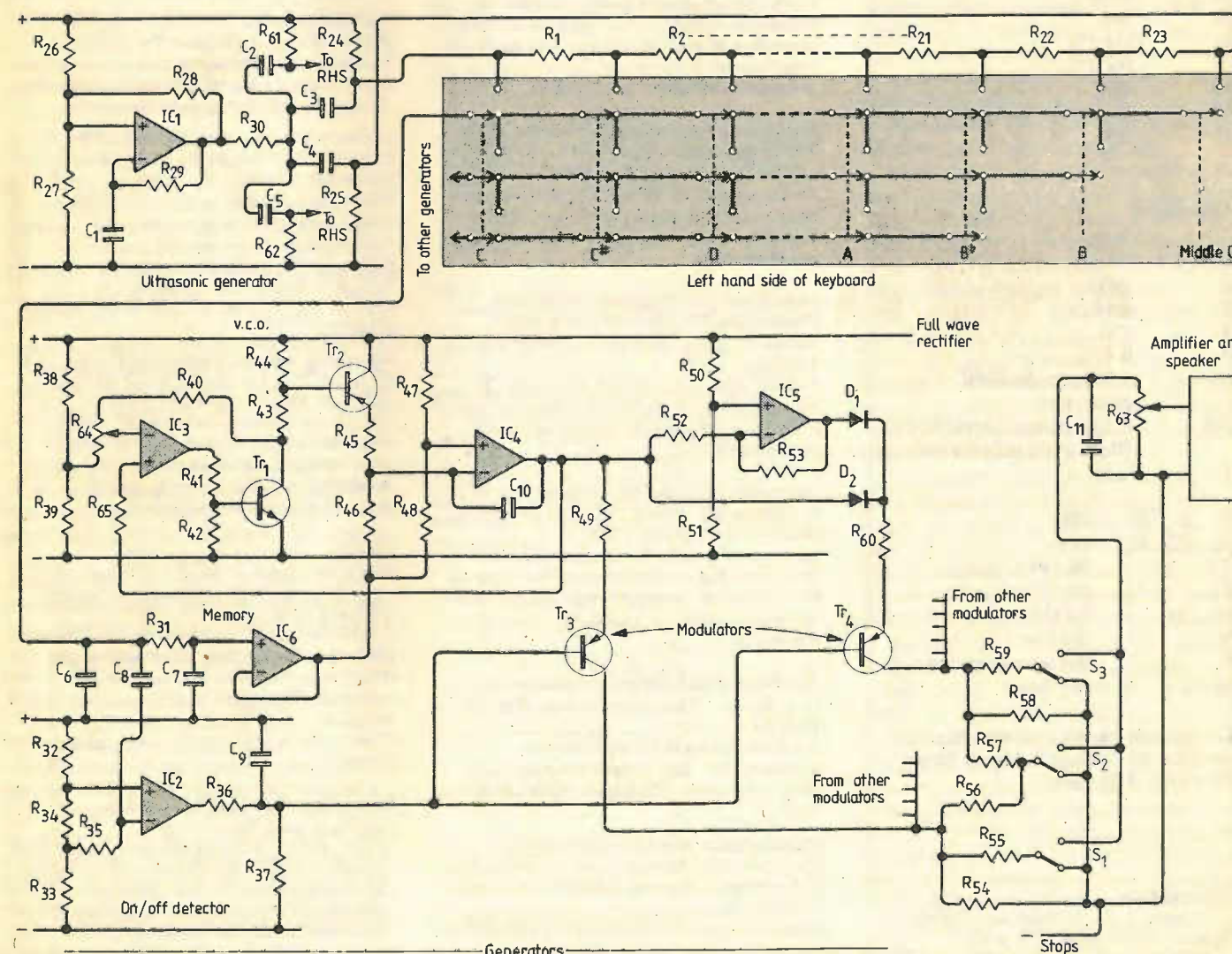
To produce an 'eight-foot diapason' signal it is not necessary to rectify the original triangle wave. By resistively mixing the original wave with one at half the amplitude of the full-wave rectified signal, the required tone is formed (at R<sub>56</sub>, R<sub>57</sub>).

Switching transistor Tr<sub>2</sub> is used in the reverse mode to reduce the voltage drop and improve the v.c.o. linearity.

The capacitor across the volume control (R<sub>63</sub>) compensates for a loss of sensitivity at low frequencies.

The complete organ is powered by a single +15V supply. The choice of a power amplifier has been left to the constructor.

Complete circuit showing one generator.





## Components

### Resistors

1 to 23 a set of music scale resistors from 10Ω upwards

24	165 1%
25	162 1%
26, 27, 28	33k
29, 30, 31	10k
32, 33	33k
34	68
35, 36	100k
37	220k
38	20k 5%
39, 40	20k 5%
41	10k
42	1k
43	1.2k 5%
44	470
45	11.5k
46	23k 1%
47	20k 5%
48	20k 5%
49	47k
50	15k
51	15k
52	15k
53	15k
54	10k
55, 56	100k
57	220k
58	10k
59	100k
60	33k
61	165 1%
62	162 1%
63	10k
64	3k preset (tuning)
65	10k

### Capacitors

1	2.2n
2, 3, 4, 5	0.1μ
6	220μ
7	0.18μ
8	15n
9	0.47μ
10R	0.025μ (right-hand generators)
10L	0.1μ (left-hand generators)
11	0.1μ (Both 2½% polystyrene)

IC <sub>1</sub> , IC <sub>2</sub> , IC <sub>3</sub>	709
IC <sub>4</sub> , IC <sub>5</sub> , IC <sub>6</sub>	741
Tr <sub>1</sub>	BC149 or similar
Tr <sub>2</sub> , Tr <sub>3</sub> , Tr <sub>4</sub>	BC307 or similar
D <sub>1</sub> , D <sub>2</sub>	1N4148
S <sub>1</sub>	(8ft flute)
S <sub>2</sub>	(8ft open diapason)
S <sub>3</sub>	(4ft flute)

Component kits are available from the author at 87 Oakington Manor Drive, Wembley, Middlesex.

### Reference

1. Asbery, J. H. Multiphonic Organ, *Wireless World*, June 1973, p.303.

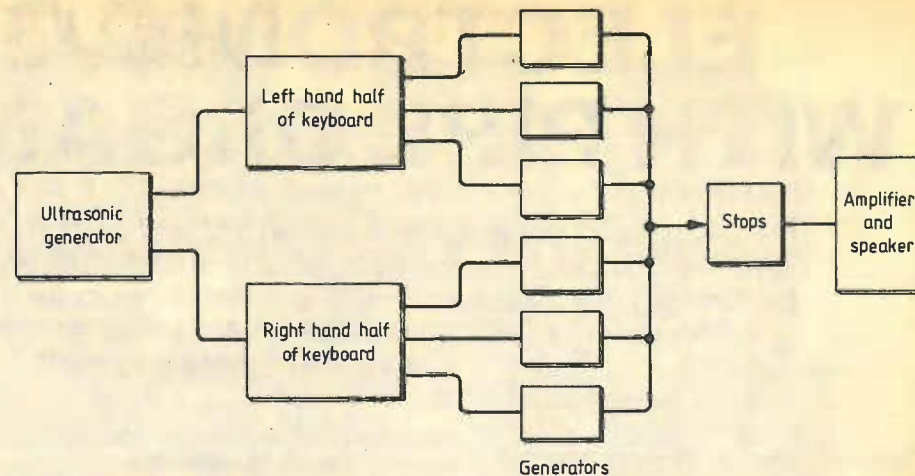


Fig. 1. Multiphonic organ system based on six triangle-wave generators.

## BOOKS

### COMPUTING

**Practical Trouble-shooting Techniques for Microprocessor Systems**, by J. W. Coffron. 246 pages, hardback. Prentice-Hall, £13.95. Fault-finding techniques for the hardware of 8-bit systems using 8080, 8085, Z80 and 6800 microprocessors. Final chapter devoted to TRS-80 microcomputer.

**The S-100 and other Micro Buses**, by E. C. Poe and J. C. Goodwin, 206 pages, paperback. Prentice-Hall, £6.95.

The S-100 and 20 other buses, as applied to most of the popular microcomputers. Includes a description of methods of converting signals on other buses to S-100 signals. Provides pin designations of various bus systems.

**Microprocessor and Microcomputer Technology**, by Noel M. Morris. 255 pages, hardback/paperback. Macmillan £15.00/£5.95. An introduction to the use of logic devices and microcomputers, starting from very simple description and progressing to programming and application.

**Learn Computer Programming with the Commodore VIC**, by L. R. Carter and E. Huzan. 100 pages, paperback. Hodder and Stoughton, £1.95.

A short course in the use of Basic on the VIC microcomputer. A number of applications and programs are given, and there are problems (with answers).

**Microelectronics and Microcomputers**, by L. R. Carter and E. Huzan. 232 pages, paperback. Hodder and Stoughton, £1.95.

Rather more general than the previous book, this is intended as an introduction to computing for the business or scientific user, and for those working on industrial control and measurement.

**The 68000: Principles and Programming**, by L. J. Scanlon. 238 pages, paperback. Prentice Hall, £10.45.

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**Microprocessors and Microcomputers, Hardware and Software**, by R. J. Tocci and L. P. Laskowski, 404 pages, hardback. Prentice-Hall, £15.70.

Micros introduced in a practical manner. First section is on basics of logic and number

systems; second section deals with computer architecture; last part is on programming in machine code and assembly language.

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**Wave Propagation Theory**, by J. R. Wait. 348 pages, paperback. Pergamon Press, £22.50. Primarily on electromagnetic wave propagation in, on or about the earth, but methods described can also be applied to acoustic waveguides.

**Aperture Antennas and Diffraction Theory**, by E. V. Jull. 173 pages, hardback. Peter Peregrinus, £27.00.

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**Microstrip Antenna Theory and Design**, by J. R. James, P. S. Hall and C. Wood. 290 pages, hardback. Peter Peregrinus, £31.00.

Design and fabrication of flat plate, 'printed' microwave aerials, with a resumé of recent advances and a chapter on trends and possible developments in the future. An appendix compares microstrip materials.

### VIDEO

**Video Handbook**, by R. V. Van Wezel, edited by G. J. King. 403 pages, hardback. Newnes Technical Books, £19.90.

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**Home Video Yearbook 1982**. 323 pages, paperback. Link House, £7.50.

In three parts. Firstly, hardware concerned with television reception and video recording, prices and suppliers; secondly short descriptions of commercially available video tapes; thirdly, lists of addresses of manufacturers and tape suppliers.

# DISC DRIVES

*Read/write head assemblies involve aerodynamic, mechanical and electro-mechanical techniques and are the most critical aspect of disc-drive design. But an equally important aspect of the system is how serial data is stored and recalled on a magnetic medium moving at high speed using a single low-mass head. These subjects form this chapter.*

by J. R. Watkinson

As previously stated, hard discs have a thin coating of magnetizable material and rotate at high speeds. Readers familiar with other magnetic recording systems will realize that ideally, the read/write head will be forced against, or at least touch, the recording medium. But because of the speed at which the disc rotates and the fragility of the medium, a gap is essential. Therefore, the head is designed to float, or 'fly', on the layer of air rotating with the disc. Consequently, the head is of low mass, so the gap between head and disc can be kept constant over the whole surface of the disc and a small degree of warping can be compensated for. Figure 1 outlines the read/write head's structure.

The magnetic head is carried by the slipper and consists of a permeable core with a coil wound round it. A paramagnetic barrier on the head core forces the flux out of the head onto the medium. Reluctance of the magnetic circuit depends mainly on the air gap between the head and the disc so the write flux is a function of the flying height. The air gap limits the recording wavelength to about ten times that of the flying height.

**Slippers.** Current 'state-of-the-art' slippers fly at less than 20 micro-inches (0.5 micron) above the disc. It is obvious that the lower the flying height, the more efficient reading and writing becomes, but what isn't perhaps so obvious is that the major design problem is making the slipper fly low enough. Lift rises rapidly as the separation reduces so to get the head closer to the disc, some of the lift has to be dumped. Early slippers had two small bleed holes, as shown in Fig. 2(a) to dump lift. These slippers had a flying height of around 100 micro-inches. Figure 2(b) shows a second generation slipper, with a large longitudinal bleed groove, designed for flying heights of about 50 micro-inches. The third example, Fig. 2(c), is designed for use below 20 micro-inches and has substantial bleed grooves and vestigial working surfaces. Although the surface of this slipper appears flat to the naked eye, it is actually formed to a high degree of accuracy in a compound curve.

**Suspension.** The slipper is mounted at the end of a rigid cantilever sprung toward the medium. The force with which the head is pushed toward the disc by the spring is equal to the lift at the flying height for which the head is designed. Because of the spring, the head may rise and fall over small warps in the disc; it would be virtually impossible to manufacture discs flat enough to allow this feature to be

dispensed with. As the slipper negotiates a warp it will pitch and roll, in addition to rising and falling, but it must be prevented from yawing. Downthrust is applied to the slipper at its aerodynamic centre by a spherical thrust button and the required degrees of freedom are provided by a flexural gimbal.

The mass of the head/cantilever and the spring compliance have a natural reso-

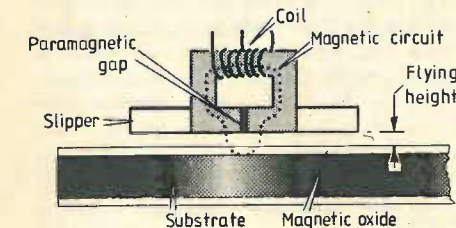


Fig. 1. An outline of the read/write head in relation to the disc. The slipper carries the head and is aerodynamically designed so that it flies on the air rotating with the disc.

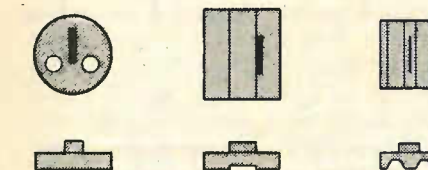


Fig. 2. Three generations of slipper design. The first generation, shown at (a), had two bleed holes to reduce lift and flew at around 100 micro-inches above the disc. A subsequent design, (b), had a longitudinal bleed groove and flew at around 50 micro-inches. This was superseded by the current head, (c), with substantial bleed grooves for flying heights of less than 20 micro-inches. The head shown in (c) has a compound curve on its working surface which aids aerodynamics but is invisible to the naked eye.

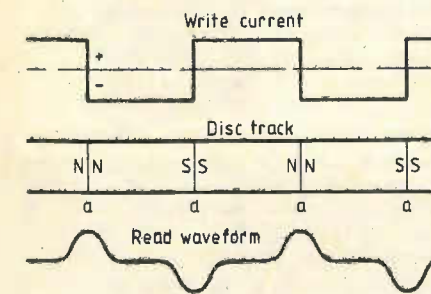


Fig. 3. In digital recording the polarity of the medium, either N-S or S-N, is controlled by the direction of the write current. Flux reversal, at points marked a, are referred to as transitions and determine the read waveform.

nance which must be set away from expected warp frequencies. Some cantilevers are fitted with synthetic-rubber dampers to control unwanted resonances.

Other essentials of the cantilever are the head separating ramp, which lifts the head clear of the disc as the positioner retracts, and some receptacle for an adjusting tool to align all of the heads to the same distance from the spindle at a given cylinder.

Handling and setting head assemblies requires care and skill; in some cases skin acid from a fingerprint is sufficient to etch the slipper surface and destroy its aerodynamic contour.

### Encoding techniques

With the exception of some non-interchangeable disc drives, only one head is active at any one time. A production tolerance exists between the actual lateral position of the head gap and the ideal, and this dimension may be several wavelengths at the densities used. As a result it is not generally possible to use parallel encoding in disc drives. This constraint largely defines the encoding techniques used.

As in all modern digital recording, the medium has only two states of magnetization, N-S and S-N. Devices have been made using the unmagnetized state, but these must be considered obsolete. The write process consists of supplying sufficient current to almost saturate the medium first in one direction, then the other. No erase process is necessary, as writing to saturation will erase a previous recording. Some heads do, however, have erase poles, the use of which will be detailed.

The output voltage from a read head is proportional to the rate of change of flux, hence an output pulse will only be obtained at the point where the write current changes direction, i.e. at a transition. Figure 3 shows that the pulses alternate in polarity. The pulse amplitude is a function of the cylinder address, as the relative speed of the outer cylinders is higher.

Data to be written enters the write circuitry as serial binary with a separate clock. Encoding consists of merging these two signals into one channel in such a way that they can be subsequently separated. Perhaps the simplest form of encoding is to reverse the write current every time the data is a binary one. It can be seen from Fig. 4(a) that this approach is of no use in a single channel, as when successive zeros occur, it is not possible to reconstitute the clock.



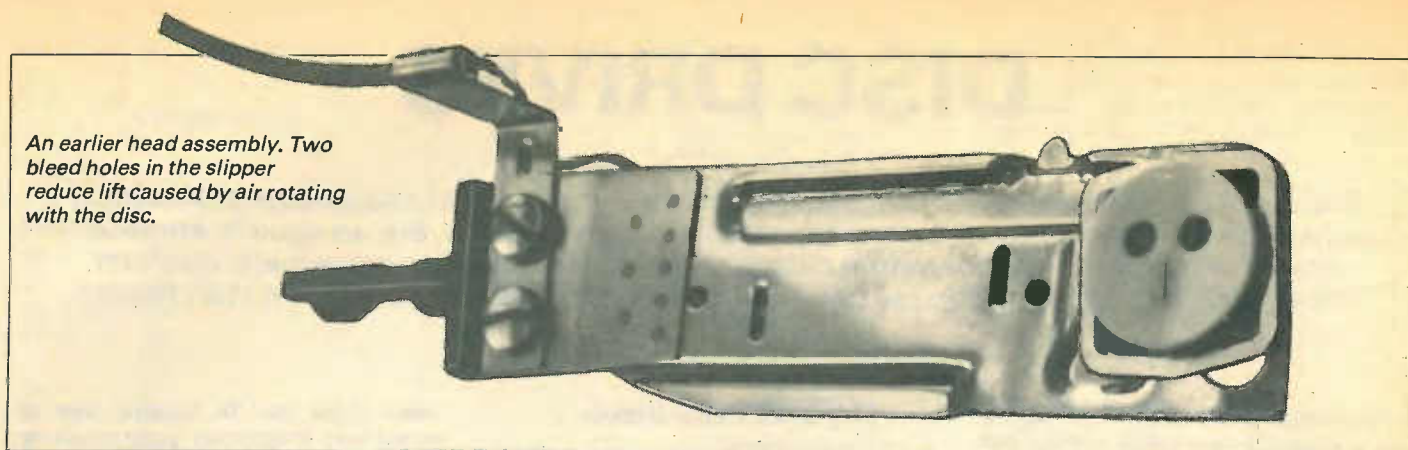
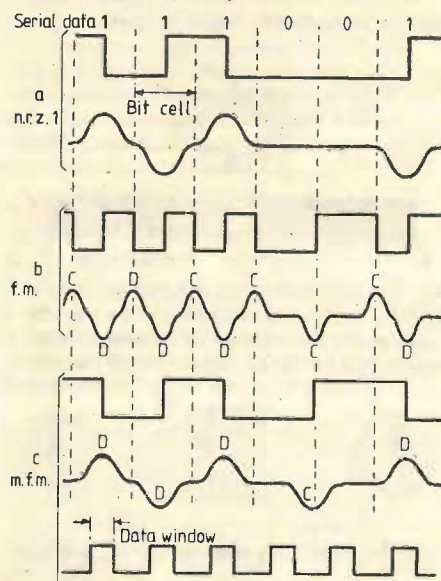


Figure 4 also introduces the concept of the 'bit cell', i.e. the time taken to record one bit. In a simple encoding system, there must be at least one transition per bit cell to carry the clock. Figure 4(b) shows a popular encoding technique, where each bit cell begins with a clock transition, and may or may not contain a further transition, depending on whether the data bit is a one or a zero. As the presence of the second transition doubles the recording frequency, the technique is known variously as f.m. or double-frequency recording. Data separation can be very simple, provided the signal-to-noise ratio is adequately high. The signal-to-noise ratio is determined not only by intrinsic medium noise and the electromagnetic environment, but also by the accuracy of the positioner. Consider the example in Fig. 5(a). Originally, data is written along path A, but positioner inaccuracy means that new data is being written along path B. Subsequently a read may take place along path C, where it will be seen that the read signal is degraded by the previous recording. The solution to this problem is to incorporate two erase gaps in the head, which erase a small area either side of the new data after writing. In Fig. 5(b) it can be seen that this process protects the data with a margin of unidirectionally magnetized oxide. The process is called 'tunnel erase' or 'side trim', and is generally employed on drives with relatively simple positioners. Such devices usually have low recording densities and accordingly a generous flying height, giving them the advantage that they can be used reliably in environments that would normally be considered unsuitable.

F.m. is easy to decode, but it is also fairly extravagant with transitions. Any encoding method in which the number of transitions per data bit can be reduced has to be an improvement, because for a given flying height, and hence a given minimum wavelength, a greater data density is possible.

In the next generation of read electronics, it is possible to relax constraints on the clock information through phase-locked-loop techniques. With this approach, it is acceptable for a bit cell to contain either clock information or data but both are not necessary. The read clock comes from a p.l.o. which continues in the absence of a transition at clock time, and which corrects its own frequency by continuously comparing its own phase with that of data

or clock transitions. In Fig. 4(c) it can be seen that the write current is reversed at the bit-cell centre for a one, and that the problem of successive zeros is handled by reversing the write current at the bit-cell boundary. It is interesting to compare the number of transitions required with the example of Fig. 4(b). On reading the data,

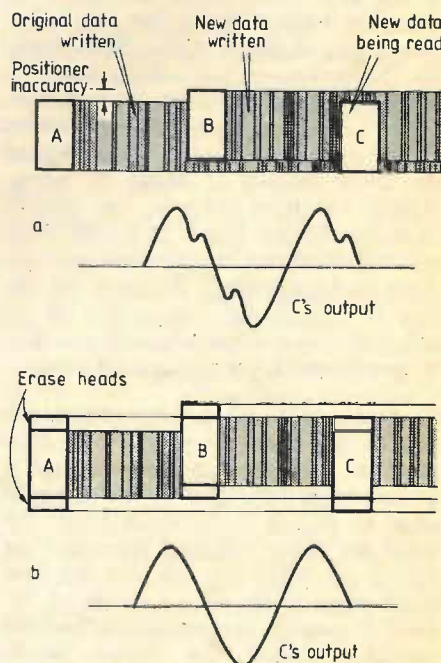


**Fig. 4.** Three data-recording methods compared. At (a), n.r.z.1 (modified non-return-to-zero) information is of little use on single-track recording apparatus as clock information cannot be carried. In 'f.m.' recording, (b), a clock transition is always present at the bit-cell boundary. The presence of a data '1' causes an extra transition at the bit-cell centre. In m.f.m. recording, shown at (c), a data '1' causes a transition at the bit-cell centre but the only other transitions are at the bit-cell boundaries between successive zeros. Both types of transition are used to synchronize a p.l.i. which opens a 'data window' at the bit-cell centre through which only data '1' pulses are read.

**Fig. 5.** In (a), track B has been written over track A, but through wide tolerances on the positioner repeatability, some of the original data remains at the edge of path B. If the new data is read while the head travels the same path it did when the original data was written, remaining original data will be read together with the new data, hence the signal-to-noise ratio will be degraded. At (b), the problem is solved by including two erase heads, one at either side of the write head, so that wherever data is written, any original data at either side of the track will be erased.

the p.l.o. can be used to open a 'time window' at the centre of the bit cell, so that only transitions corresponding to a binary one can pass through. Obviously, the system only works if the p.l.i. is synchronized, so a series of zeros, or preamble, is used before each block to allow the loop to lock. A unique synchronizing pattern delineates where actual data begins. This phase-locked data-recovery technique is used with modified-frequency modulation encoding (or Miller encoding) and allows the arrival time of read pulses to be predicted, and therefore noise pulses to be rejected. This means that a smaller s-to-n ratio can be tolerated than with f.m. encoding, allowing tunnel erase to be dispensed with. In any case, drives employing the m.f.m. technique are likely to have more accurate positioners.

Where f.m. requires signal-to-noise ratio, m.f.m. requires minimum phase errors, if the phase-locked data recovery is not to be upset. In Fig. 6, a head is depicted reading closely packed transitions. Owing to the airgap between the head and the medium, pulses generated tend to run into one another such that the waveform peak positions do not correspond to the actual position of the transitions. The phenomenon is referred to as peak-shift distortion, and is overcome by introducing opposing timing changes during the write process. This technique, precompensation,



tion, artificially advances transitions subject to delay on reading, and delays advanced transitions by taking a running sample of (usually) four data bits, and decoding the patterns to generate different clock times in a tapped delay line. M.f.m. requires a running sample, so the two processes are sometimes combined in one circuit.

Recently, a different approach to high density recording has been developed. Central to this approach is that transitions are not permitted at successive active edges of the write clock. Figure 7(a) shows that the four combinations of any two data bits may be expressed as three-bit codes which do not contain successive 'ones'. There are, however, four combinations of adjacent pairs of bits to violate the rule, Fig. 7(b). In these cases, the six bits are substituted by alternative bit patterns which must follow certain conditions; firstly, that the substitution contains no adjacent ones, secondly that the substitution ends in a zero so that no subsequent data can violate the rule, and thirdly the position of the ones is chosen to generate transitions at sequential integer multiples of the write-clock period. Fig. 7(c) shows that the highest recorded density results from a data stream of 0011's, and that this requires only six transitions for eight data bits. At maximum density, m.f.m. requires one transition per bit, so the relative efficiency is 8/6 or 33% greater. Fig. 7(c) also shows that much of the time the recorded density is below the maximum, and that seven even steps exist in the periods between any two transitions. This evenness allows effective phase-locked noise rejection to be employed, as the arrival time of readback pulses can be accurately predicted. In addition, precompensation is only required when changing to and from the highest density, as at all lower densities the transitions are far enough apart to make peak-shift distortion insignificant. This recording technique is known as 2/3 (pronounced 'two three') for obvious reasons. It is difficult to imagine a method

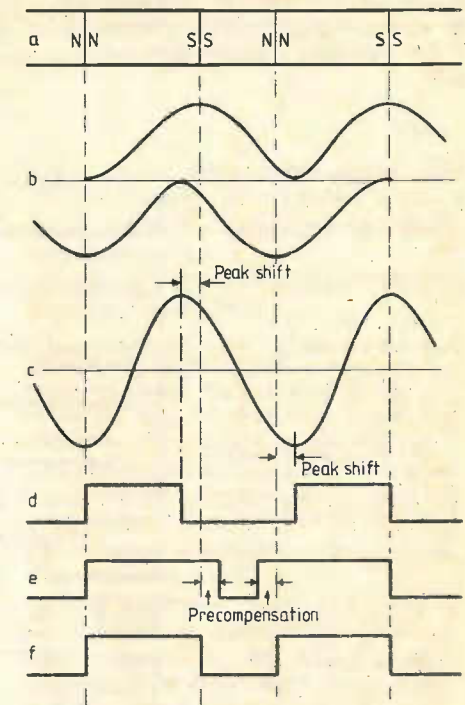
which would achieve a significant improvement in efficiency over it. Encoding is performed by a p.r.o.m. which takes in a running sample of data in the same way as m.f.m. Similarly, reading requires phase-locked circuitry, with a further p.r.o.m. containing the reverse truth table to the encoding p.r.o.m.

## Circuits

The same head is used for both reading and writing, and as stated, usually only one head is active at one time. The circuits involved in reading, writing and head selection come together at the read/write matrix where the flexible head cables plug in. It can be seen from Fig. 8 that the centre-tapped heads are isolated by connecting the centre tap to a negative voltage, which reverse-biases the matrix diodes. The centre tap of the selected head is made positive. When reading, a small current flows through both halves of the head coil, as the diodes are forward biased. Opposing currents in the head cancel, but read signals resulting from flux transitions on the disc can pass through the forward-biased diodes to become differential waveforms on the matrix bus. During a write, the current from the write generator passes alternately through the two halves of the head coil. Further isolation is necessary to prevent write-current voltages destroying the read amplifier inputs.

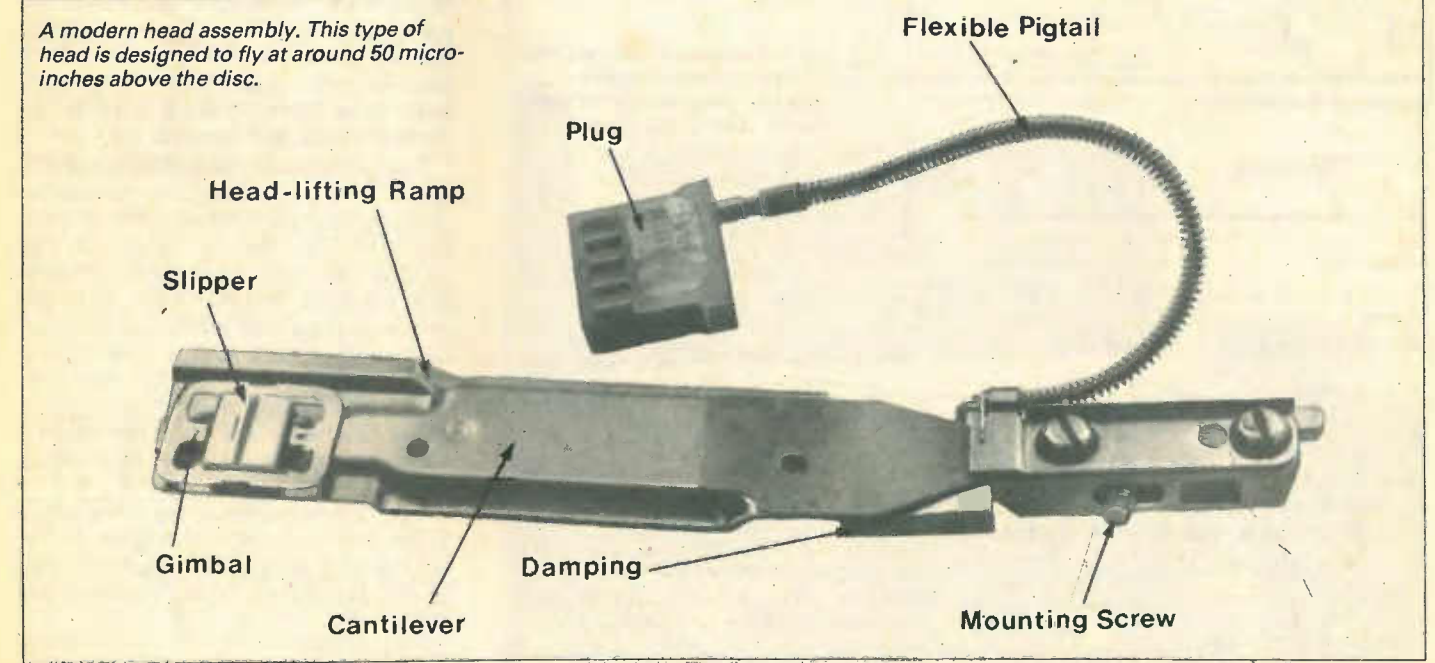
**Write-current programming.** The flying height changes as a function of relative velocity which is governed by the track radius. It is possible to program the write current from the current cylinder-address register such that the write flux remains essentially constant, despite changes in flying height. The number of write-current steps is usually between two and eight across the working surface of the disc, although some drives dispense with write current programming altogether. In Fig. 9, the write current is generated by holding the base of a transistor at a temperature-compensated reference voltage, and by selecting different emitter resistors

with transistor switches. As the current source is usually at about -40V, the switches are fed from the drive logic through level shifters. The write current is directed through the head by a pair of transistors in series with the current generator, which are driven in a complementary fashion by a bistable. The purpose of write



**Fig. 6.** Timing diagram showing peak distortion and precompensation. (a) shows the flux pattern of an ideal m.f.m. data track, and (b) shows individual read pulses from each transition, which are spread out because the head is not in contact with the medium. Peaks of the closely packed transitions are moved apart as shown in the summation of the waveforms of (b) at (c). Phase errors in the binary signal from the peak detector are shown at (d). To compensate for these errors, the write waveform is as shown in (e) and the adjusted peak detector output is shown in (f).

A modern head assembly. This type of head is designed to fly at around 50 micro-inches above the disc.



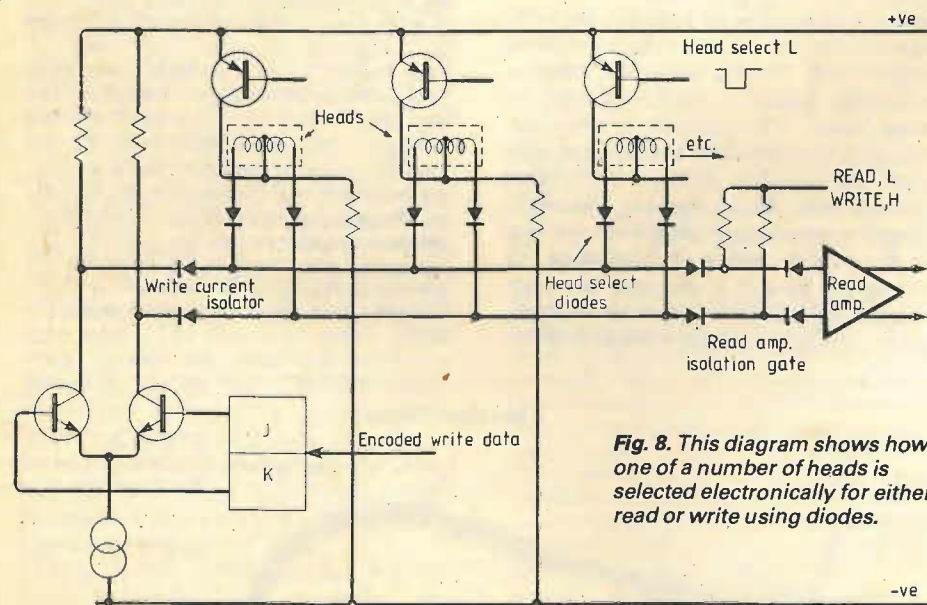
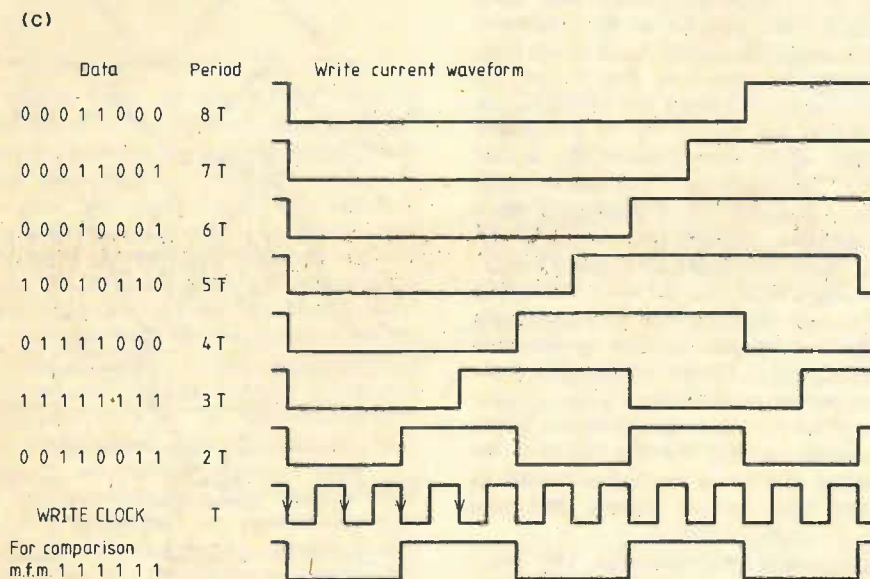


**Fig. 7 (a).** Two bits can be expressed as three code bits without successive transitions. In (b), adjacent pairs can break the encoding rule and in these cases, substitutions are made. Write current waveforms for seven different data streams using 2/3 encoding are shown at (c). The time steps between transitions are uniform, allowing phase-locked data recovery in the presence of noise. A maximum of six transitions are required for eight data bits; when compared with m.f.m. encoding, this gives a saving of 33%.

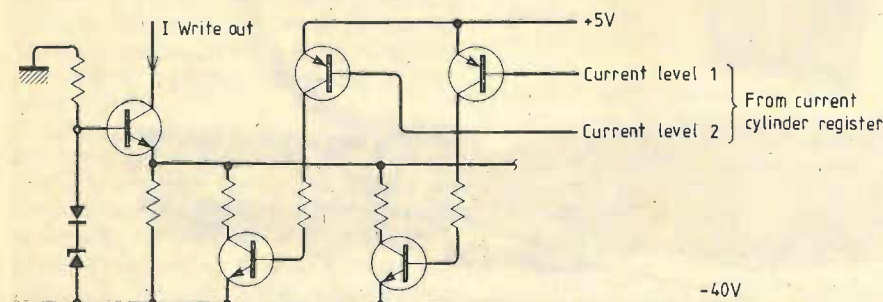
Data	Code
00	101
01	100
10	001
11	010

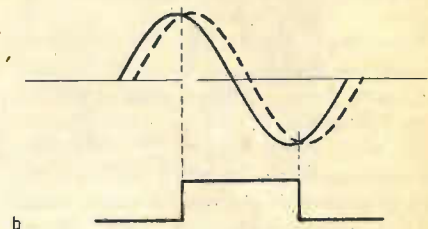
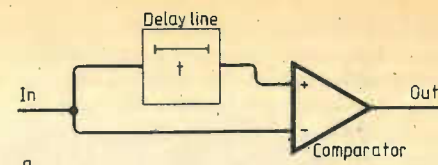
Data	Illegal code	Substitution
0000	101101	101000
0001	101100	100000
1000	001101	001000
1001	001100	010000



**Fig. 8.** This diagram shows how one of a number of heads is selected electronically for either read or write using diodes.



**Fig. 9.** A programmable write-current generator. Write current is generated by holding the base of a transistor at a temperature-compensated reference voltage, and by selecting different emitter resistors using transistor switches.



**Fig. 10 (a).** A simplified delay-line peak detector, and associated waveforms (b). A differential phase-lead peak detector is shown at (c).

encoding is to decide at what time to clock the bistable so that a transition is written by the current reversal.

**Reading.** When not actually writing, the write-current generator is turned off and the write-isolation diodes are reverse biased. The read isolation gate is enabled, allowing the differential read signal into the read linear amplifier. This amplifier raises the amplitude of the read signal to a constant level suitable for data recovery, and filters out unwanted signals. To this end the linear amplifier often contains both bandpass filters and an a.g.c. loop. In some cases, the linear amplifier's input and the a.g.c. capacitor are shorted during the address mark to stabilize the gain in the shortest possible time after entering a block. The address mark is a short section of the track preceding a data block and contains no transitions. A.g.c. squelch is released as the block is entered, and the linear-amplifier gain reduces from maximum using the fast attack slope of the forward-biased signal rectifier.

The constant-amplitude read signal now passes to the peak detector, as the position of the signal peaks corresponds to the position of the transitions on the disc. In Fig. 10(a) an analogue waveform is compared with a delayed version of itself. The comparator changes state at the signal peak. A differential version of this type of peak detector is shown in Fig. 10(c). The principle holds equally well if one signal is phase advanced, and thus the delay is sometimes substituted by the RC network shown.

The detected signal is fed to an appropriate data separator, which splits the signal into data and clock information to pass to the deserializer, which recreates data words.

To be continued

# 16-CHANNEL DATA ACQUISITION SYSTEM

A 4½-digit, 16-channel data acquisition system (d.a.s.) is described which functions as a talker-listener on the IEEE-488 bus (GPIB). It uses a 4½/5½-digit a-to-d subsystem, AD7555, with  $\pm 1.9999V$  full scale, as an easy interface with the Fairchild 96LS488 GPIB circuit.

by Pat Hickey

Figure 1 shows a block diagram of the GPIB 16-channel data acquisition system. The 96LS488 connects directly to the IEEE bus and controls all the other sections. (For clarity, a number of the control signals have been omitted.) A set of eight transceivers determines the flow of information (talking or listening) and the 'listen decode' circuitry sends the appropriate address to the 16-channel multiplexer. On selection of a channel, a start conversion signal is sent to the AD7555 a-to-d converter.

When conversion is complete, a service request is transmitted to the 96LS488, which in turn interrupts the IEEE bus: the bus can then interrogate the device for status or data information. Status information includes the last channel selected and the conversion status, while data information consists of a 4½-digit b.c.d.-encoded representation of the analogue voltage.

## The IEEE bus in brief

A full description and specification of the GPIB system is published in the IEEE document "IEEE Standard Interface for Programmable Instrumentation", IEE Std 488(1978), which should be referred to for a fuller explanation.

GPIB communication lines consist of eight data lines, three hand-shake lines, five control lines and eight ground lines, as shown in Fig. 2 (the IEEE connector). Data lines (D1-D8) contain the bidirectional data or information and are true low signals.

**Handshakes.** NRFD, DAV and NDAC are the three bidirectional handshake signals. DAV (Data Valid) is pulled low by a talker when the data has been placed on the bus, which tells the listener that the data is valid. NRFD (Not ready for data) is brought high (or released) by each instrument on the bus: when all the instruments have released it, it acts as an indication to the talker that a data transfer can begin. NDAC (Not Data accepted) is controlled by the device receiving the data, a low indicating that the data has not been captured and a high that this has been done. A simplified data transfer sequence is shown in Fig. 3.

A timing sequence starts when the listener brings NRFD high (1), saying it is ready to receive the data. The talker places the data on the bus (2), allows it to settle and brings DAV low (3), telling the listener that the data is valid. The listener brings NRFD low (4), indicating that it is not ready for another data transfer until

this transfer is completed. When the data has been processed, the listener brings NDAC high (5), saying that it has received the data. The listener responds by taking DAV high (6) (data is no longer valid) and removing the data from the bus (7). The listener brings NDAC low (8), acknowledging this, and NRFD high (9), indicating that it is ready for the next data byte. The timing of this sequence is not discussed here, since the 96LS488 IEEE-interface circuit takes complete control of the procedure.

**Control.** The five control lines are ATN, IFC, REN, SRQ, and EOI. The ATN (Attention) is asserted only by the controller and, when low, indicates that information on the line is address or control information: it is high when data is being transferred. The IFC (Interface Clear) line is asserted low by the controller to reset all GPIB devices.

REN (Remote Enable) allows local (i.e. front panel) control of devices if it is allowed to become high. When low it ensures that the controller is in command. SRQ (Service Request) is forced low by a talker/listener when it wishes to indicate to

the controller that it needs service. EOI (End or Identify) can be pulled low by a talker to signify the last byte in a multi-byte transfer.

All the aforementioned signals are taken care of by the 96LS488.

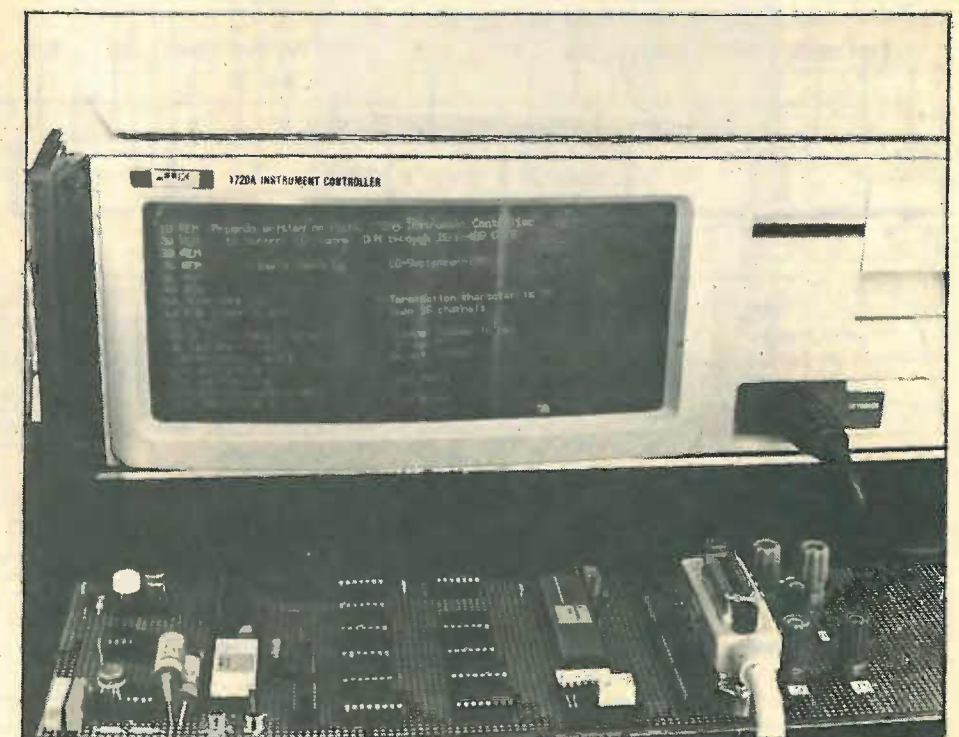
## 96LS488 GPIB circuit

Figure 4 shows a block diagram for the 96LS488, and the following description should be referred both to that and Fig. 7 (full circuit diagram). CP is a 10MHz clock which controls all internal timing, and can be generated using a 150Ω resistor and 150pF capacitor connected to an internal Schmitt trigger.

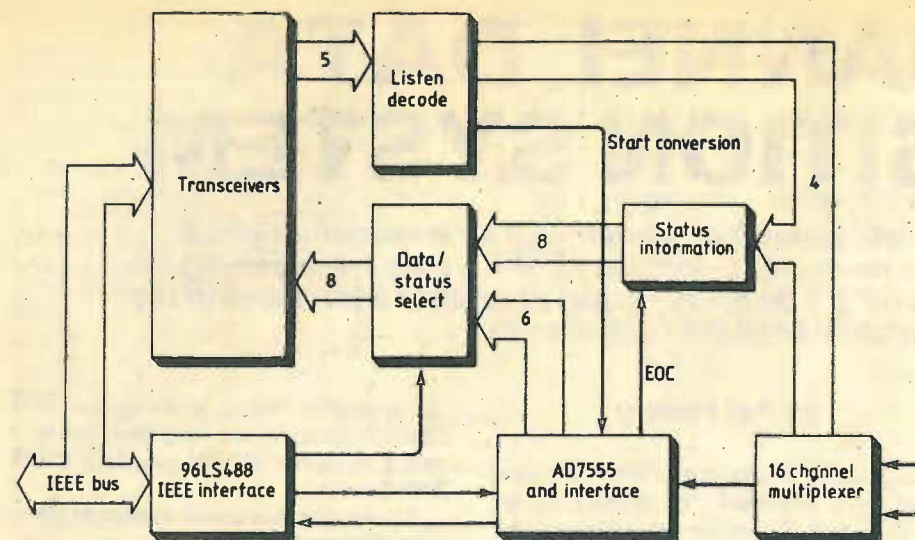
TXST (Transmit Status) and TXRDY (Transmit Ready) signals are used in transferring data from the AD7555 a-to-d converter to the 96LS488, as shown in Fig. 5. When the d.a.s. is requested to transfer information to the IEEE bus controller, the 96LS488 checks that TXRDY is high (meaning a byte is waiting). If it is high, the 96LS488 will read the data and bring TXST high (1), indicating that it has the information. TXRDY is then brought low (2), acknowledging this fact and TXST is brought low (3) again. When the next byte is ready (4), the AD7555 brings TXRDY high (5) and the sequence is repeated.

RXST (Receive Status) and RXRDY

Analog Devices, Limerick, Ireland





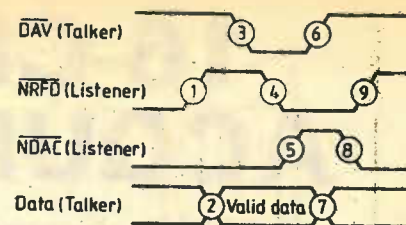
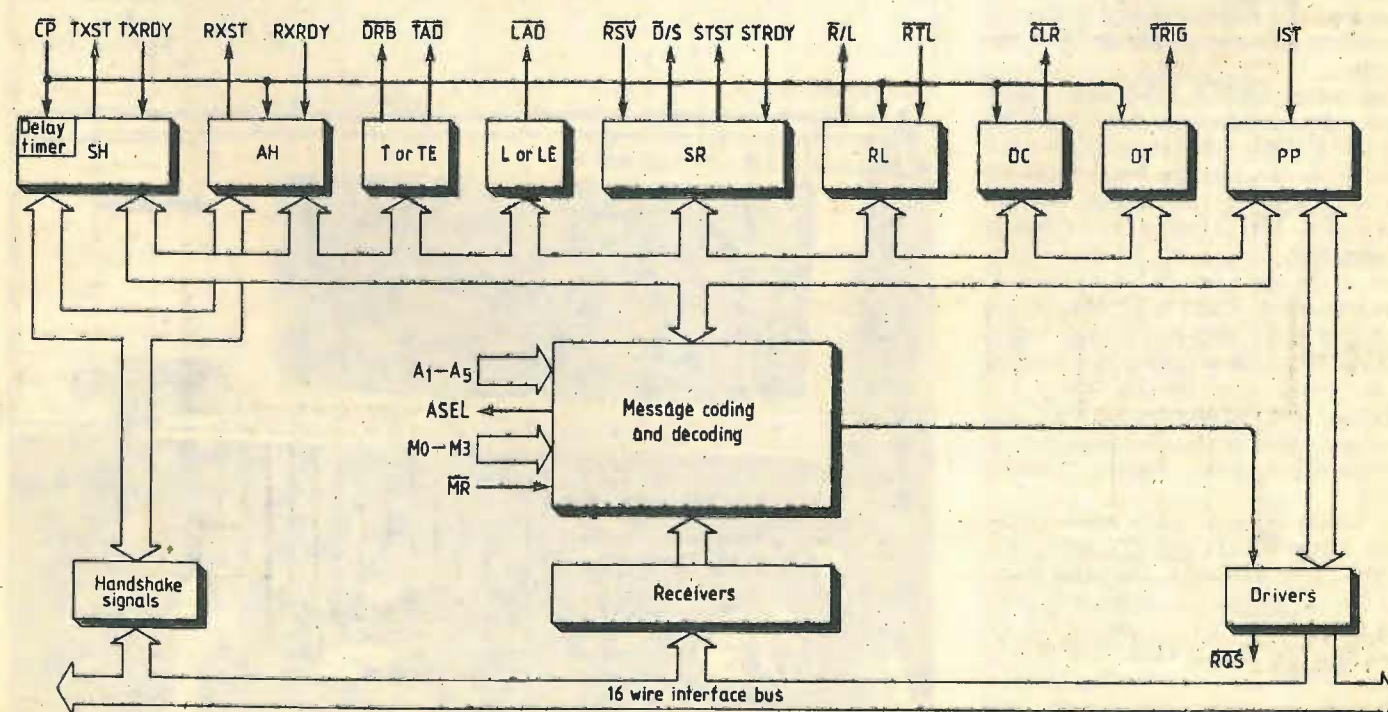


**Fig. 1. Block diagram of complete system. 96LS488 interfaces and controls rest of unit.**

(Receive Ready) are used as seen in Fig. 6 for transferring data from the 96LS488 to the 16-channel d.a.s. When valid data has been placed on the bus (1), RXST is taken high (2), indicating that the data is valid. When the data has been accepted, RXRDY is taken low (3), indicating that the data has been accepted, RXST is taken low (4), acknowledging this fact, and the data becomes non-valid (5). RXRDY is brought high (5), signalling that it is ready for the next byte of information. In Fig. 7, RXST is inverted and connected to RXRDY, in which case data is transferred at a data rate determined by the bus handshake.

The Drive Bus Output (DRB) signal is low when data is being transferred from the AD7555 a-to-d converter to the IEEE bus, and high when information is being

**Fig. 4. Functional block diagram of 96LS488.**



**Fig. 3. Simplified data transfer sequence.**

interrupt. This occurs when a conversion is completed.

D/S (Data/Status) is held low when data is being transferred to the IEEE bus, or high if status information is being transferred during a serial poll. In this application, it is used to select either data or status information via a data selector (2×74C157).

The STST (Status Status) and STRDY (Status Ready) signals operate similar to the TXST and TXRDY signals when sending status information during a serial poll. STRDY can be formed from an inversion of STST.

RTL (Return to Local Input) is tied high in this application, since the device is operating only in remote control.

CLR issues a negative pulse when the device receives a Device Clear command. This will reset all functions within the device.

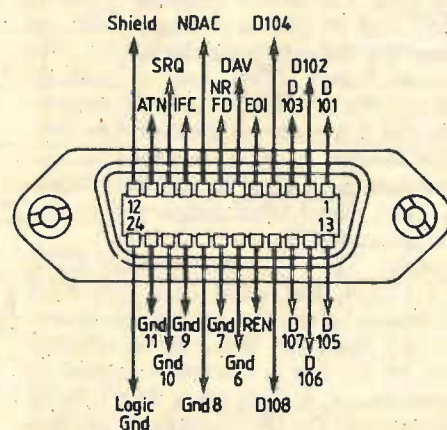
**TRIG** (Trigger output) issues a negative pulse when the device receives a DT (Device Trigger) command. It is not used in this application. The **IST** (Instrument Status Input) is used in parallel poll enable.

For more information on the above signals see the Fairchild 96LS488 data sheet.

### Data acquisition system

Figure 7 shows the complete circuit diagram of the data acquisition system. A brief review of each i.c. should help to understand its operation before the more complex timing of the system is discussed.

Circuits IC<sub>1,2</sub> are quad interface

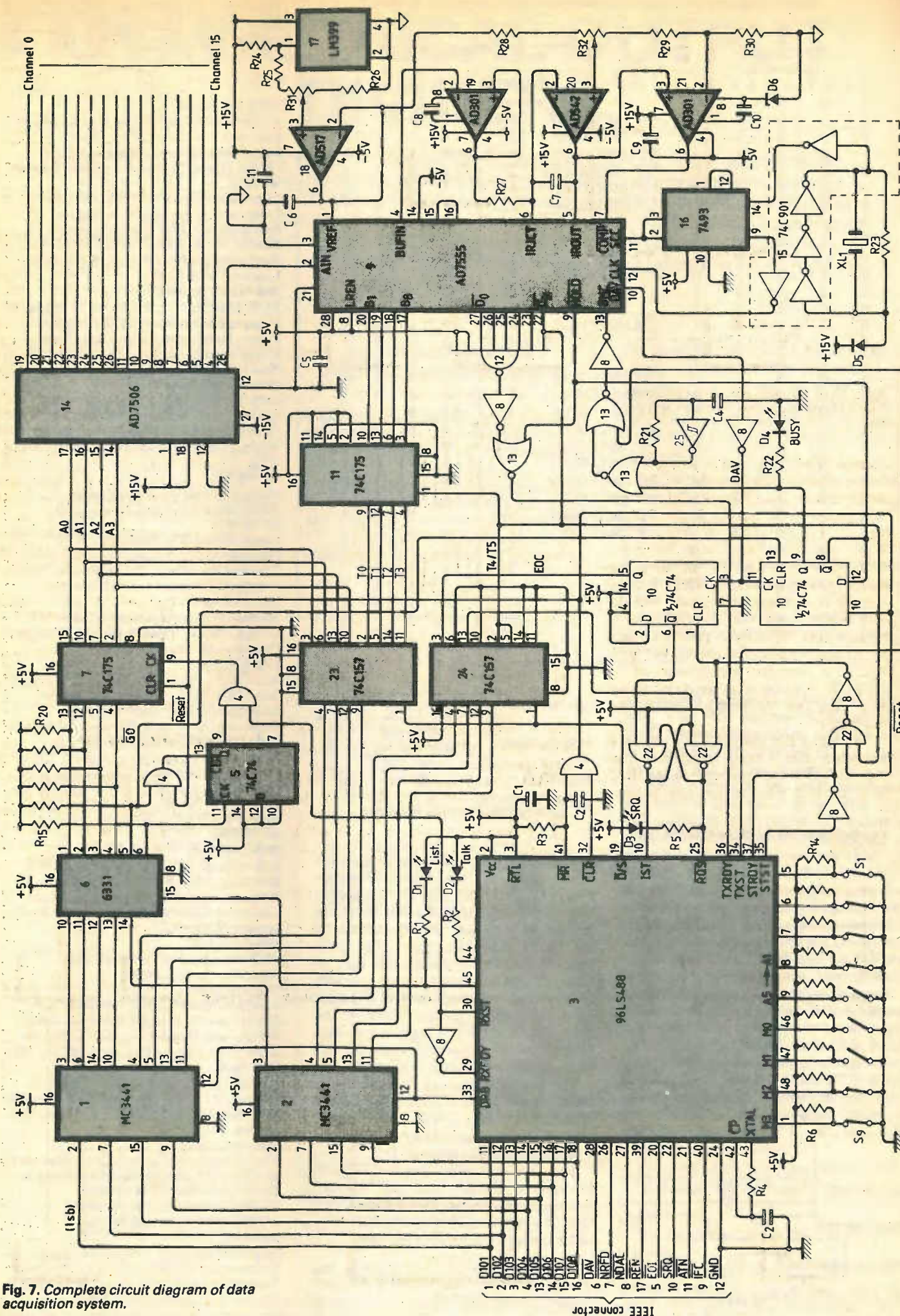


**Fig. 2.** GPIB communication lines shown in relevant positions on IEEE connector.

sent to the data acquisition system. In Fig. 6, the signal is used to enable (or disable) a set of transceivers.

**TAD** (Talk-Addressed) and **LAD** (Listen-Addressed) are active low when the device is addressed to talk or listen.

RSV (Request Service) is brought low by the AD7555 to initiate a service request



**Fig. 7. Complete circuit diagram of data acquisition system.**



Table 1. Contents of IC<sub>6</sub> r.o.m. for decoding ASCII information to binary.

R.o.m. inputs (addresses)					R.o.m. outputs (data)						
A 4	A 3	A 2	A 1	A 0		0 6	0 5	0 4	0 3	0 2	0 1
0	0	0	0	0		1	1	1	1	1	1
0	0	0	0	1	"A" (0100 0001)	1	1	1	0	1	0
0	0	0	1	0	"B" (0100 0010)	1	1	1	0	1	1
0	0	0	1	1	"C" (0100 0011)	1	1	1	1	0	0
0	0	1	0	0	"D" (0100 0100)	1	1	1	1	0	1
0	0	1	0	1	"E" (0100 0101)	1	1	1	1	1	0
0	0	1	1	0	"F" (0100 0110)	1	1	1	1	1	1
0	0	1	1	1		1	1	1	1	1	1
0	1	0	0	0		1	1	1	1	1	1
0	1	0	0	1		1	1	1	1	1	1
0	1	0	1	0	"*" (0010 1010)	0	1	1	1	1	1
0	1	0	1	1		1	1	1	1	1	1
0	1	1	0	0		1	1	1	1	1	1
0	1	1	0	1	CR (0000 1101)	1	0	1	1	1	1
0	1	1	1	0		1	1	1	1	1	1
0	1	1	1	1		1	1	1	1	1	1
1	0	0	0	0	"0" (0011 0000)	1	1	0	0	0	0
1	0	0	0	1	"1" (0011 0001)	1	1	0	0	0	1
1	0	0	1	0	"2" (0011 0010)	1	1	0	0	1	0
1	0	0	1	1	"3" (0011 0011)	1	1	0	0	1	1
1	0	1	0	0	"4" (0011 0100)	1	1	0	1	0	0
1	0	1	0	1	"5" (0011 0101)	1	1	0	1	0	1
1	0	1	1	0	"6" (0011 0110)	1	1	0	1	1	0
1	0	1	1	1	"7" (0011 0111)	1	1	0	1	1	1
1	1	0	0	0	"8" (0011 1000)	1	1	1	0	0	0
1	1	0	0	1	"9" (0011 1001)	1	1	1	0	0	1
1	1	0	1	0		1	1	1	1	1	1
1	1	0	1	1		1	1	1	1	1	1
1	1	1	0	0		1	1	1	1	1	1
1	1	1	0	1		1	1	1	1	1	1
1	1	1	1	0		1	1	1	1	1	1
1	1	1	1	1		1	1	1	1	1	1

transceivers (MC3441) and are designed to meet the IEEE standard 488-1975. The data direction is controlled by the DRB output of the 96LS488 (IC<sub>3</sub>). When it is low, data is transferred to the bus, and transferred from the bus when DRB is high. Switches S<sub>1</sub>-S<sub>5</sub> are used to select the address of the device. As an example:- For an address of 16, S<sub>5</sub> is open, while S<sub>4</sub>, S<sub>3</sub>, S<sub>2</sub> and S<sub>1</sub> are closed. (Address is 10000 = 16). Switches S<sub>6</sub>-S<sub>9</sub> are used to select the operating mode of the 96LS488 (the Fairchild data sheet gives more information on this). For a talker/listener on low speed, M0 and M1 are high, and M2 and M3 are low (ie, S<sub>6</sub> and S<sub>7</sub> are open, while S<sub>8</sub> and S<sub>9</sub> are closed).

Since all information is transmitted in parallel ASCII code, it is necessary to decode this to binary. The 6331 (IC<sub>6</sub>) is a 32x8 bit r.o.m. which is used for this purpose, whose contents are outlined in Table 1. The address latch, IC<sub>7</sub> (74C175), holds the address of the selected channel, its output being connected to the input of IC<sub>14</sub> (AD7506), a 16 channel multiplexer,

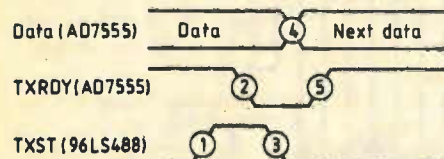


Fig. 5. Simplified talking sequence.

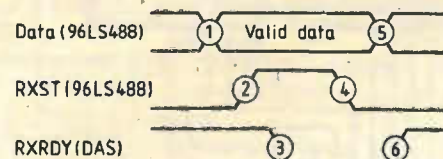


Fig. 6. Simplified listening sequence.

which in turn selects the appropriate analogue signal to the a-to-d converter subsystem (AD7555) IC<sub>9</sub>. On completion of a conversion, the b.c.d. data is held in internal latches, and can be accessed by control of the DMC pin. The IEEE transmit handshake signals are used to access this information during a readback cycle. A data selector IC<sub>11</sub> (756157) send 4½ digits and a carriage return to the 96LS488: When D5 is high, b.c.d. data from the a-to-d converter is selected, and when D5 is low a CR is selected.

The hex. c.m.o.s.-t.t.l. inverter (IC<sub>15</sub>) generates the 4.096 MHz clock with the crystal, whilst IC<sub>16</sub> (7493), a 4-bit binary counter, divides this by four, producing a 1.024 MHz clock for the AD7555.

The two multiplexer/selectors (IC<sub>23,24</sub>) are used to transfer either data or status information to the 96LS488. When D/S is low, data information is selected (T0-T5), and when high the status byte is sent.

The concluding article will continue this circuit description and include a program for scanning 16 channels.

## EVENTS

### 23/24/25th March

Electro-optics/Laser International '82 UK, at Metropole Convention Centre, Brighton. Details from: Cahners Exposition Group, Cavridy House, Ladymead, Guildford, Surrey GU1 1BZ.

### 25th March

Computational Techniques in Image Processing, at Queen Elizabeth College, London. Details from: The Meetings Officer, The Institute of Physics, 47 Belgrave Square, London, SW1X 8QX.

### 30th March/1st April

ETM '82 and Sensors & Systems '82 (Electronic testing and measurement) at Wythenshawe Forum, Manchester. Details from: Trident International Exhibitions Ltd, 21 Plymouth Road, Tavistock, Devon PL9 8AU.

### 30th March/1st April

CAD '82, (Computer-aided design conference and Exhibition) at Brighton Metropole, Sussex. Details from: IPC Exhibitions Ltd, Surrey House, 1 Throwley Way, Sutton, Surrey SM1 4QQ.

### 4th-7th April

National Association of Broadcasters, Exhibition, at Las Vegas, Nevada USA.

### 6th April

Current Research in Magnetism, at the Institute of Physics, London. Details from: The Meetings Officer, The Institute of Physics, 47 Belgrave Square, London SW1X 8QX.

### 12th-15th April

Electrostatics Conference, at St Catherine's College, Oxford. Details from: The Meetings Officer, Institute of Physics.

### 13th-16th April

Basic Electronics for Teachers, at University of Salford. Details from: The Administrative Assistance (Short Courses) Room 110, Registrar's Department, University of Salford, Salford M5 4WT.

### 20th April

Satellite Development in Broadcasting: M. W. Harman, at Room SG27, University of Aston, Gosta Green, Birmingham at 6.30pm. Details from: The IETTE, 2 Savoy Hill, London WC2R 0BS.

### 20th-22nd April

International Conference on Video and Data Recording (I.E.R.E.) University of Southampton, Southampton. Details from: Conference Registrar, IERE, 99 Gower Street, London WC1E 6AZ.

### 20th-22nd April

All Electronics Show, at the Barbican Exhibition Centre, London.

### 20th-23rd April

Communications '82, IEE Conference and Exhibition at the National Exhibition Centre, Birmingham. Details from: IEE Conference Department, Savoy Hill, London WC2R 0BL.

### 22nd April

Microprocessor in Building Services: M. W. Harman, at University of Strathclyde, Glasgow at 6.30pm. Details from: IETTE, 2 Savoy Hill, London WC2R 0BS.

### 23rd-25th April

The Computer Fair, at Earls Court. (Sponsored by Practical Computing and Your Computer) Details from: Exhibition Manager, IPC Exhibitions Ltd, Surrey House, 1 Throwley Way, Sutton, Surrey.

### 28th April

Propulsion Research - Impact on Fuel/Energy Conservation, at Hawthorns Hotel, Woodland Road, Bristol at 7.30pm. Details from: IETTE, 2 Savoy Hill, London WC2R 0BS.

# SYMMETRICAL-OUTPUT DIVIDERS

Expanding on February's article, the author first shows how further hexadecades may be added to the previously described binary-programmable counter. A basic b.c.d.-programmable counter follows and to conclude, details of how to add further decades. These circuits are designed to accept and provide equal mark-to-space ratio digital signals, and are programmable in integer steps. As frequency-dependent components are not used, the speed of each circuit is only limited by the speeds of the logic devices used.

by Gerard Girolami and Philippe Bamberger

For dividing in the range  $16 \leq N \leq 256$ , whether or not  $N$  is a prime number is important. If  $N$  is not prime then  $N = N_1 N_2$  and the divider can be made using two programmable divide-by-1-to-16 circuits described in the previous article. These may be connected either asynchronously or synchronously, the latter method being the fastest. To divide synchronously it is necessary to enable the 74C163 inputs as shown in Fig. 9. To divide asynchronously, the output of the divide-by- $N_1$  circuit has to be connected to the input of the

divide-by- $N_2$  circuit. The latter solution is not much simpler than connecting the dividers synchronously so the sacrifice in speed is usually unwarranted.

On the other hand, if  $N$  is prime, this solution no longer applies and it is necessary to design a programmable divide-by-1-to-256 counter using a slightly different approach. The procedure is identical to

that used for the 1-to-16 programmable counter except that the relationships in equations (1), (2) and (3) given in the previous article must be changed to force the counter to 'oscillate' around the transition between counts 127 and 128. The new equations are:

$$L + D = 255 = 2^8 - 1 \quad (4)$$

$$D - I/2 = 127 \text{ if } I \text{ is even} \quad (5)$$

and

$$D - (I + 1)/2 = 127 \text{ if } I \text{ is odd.} \quad (6)$$

These relationships can again be implemented using two binary adders as shown in Fig. 10.

As shown previously, it is possible to find the logic relationships between input and load data as follows,

$$L_0 = I_0 \oplus I_1$$

$$L_1 = (I_0 + I_1) \oplus I_2$$

and so forth up to

$$L_6 = (I_0 + I_1 + I_2 + I_3 + I_4 + I_5 + I_6) \oplus I_7$$

$$L_7 = 0$$

$$D = \bar{L}$$

### B.c.d. programmable counters

If division ratios from one to nine only are required, the previously described binary-programmable circuit may be used. If, however, a similar circuit is designed using a decade counter, and the maximum divisor range of one to ten is required, the counter will have to 'oscillate' at the 4-5 transition, rather than at the 7-8 transition as was the case with the binary-programmable circuit. This means that as  $Q_D$  is used as the output, the signal obtained will not be square. In fact, if the dividing ratio is from 1 to 6, there will be no output at all. It is easy to get round this problem by producing a logic 0 for states zero to four and logic 1 for the remainder, but this creates new problems;

- more circuits are required
- even with a synchronous counter, it is difficult to avoid spikes on the output, so the clock will have to latch the output signal
- the maximum operating frequency is lowered.

So, for division ratios from one to nine, it is more practical to use a binary-counter circuit. But the decade counter can be used to advantage if division ratios up to 100, or

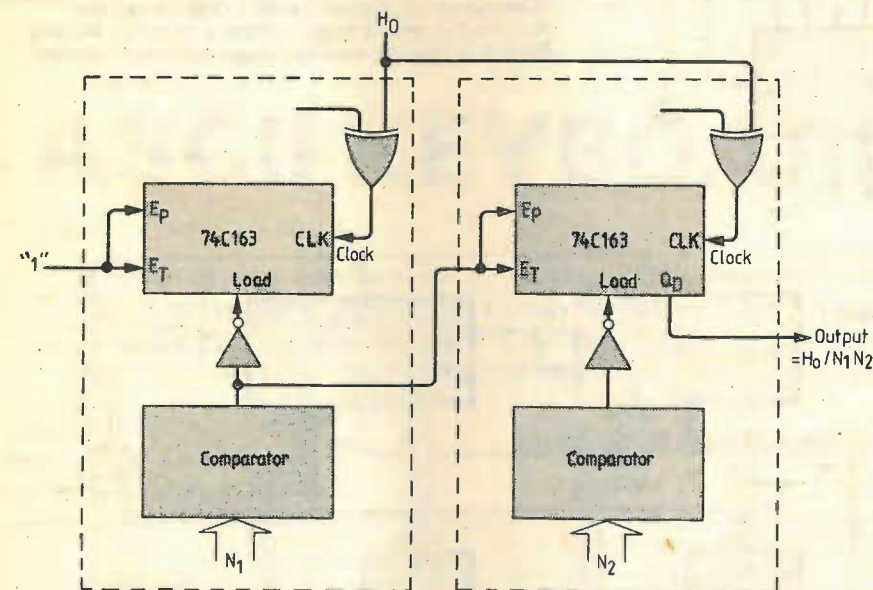


Fig. 9. Synchronous cascading of programmable divide-by-1-to-16 circuits.  $H_0$  is the input and  $N_1 N_2$  is the divisor,  $N$ . This method only applies where  $N$  is not prime.

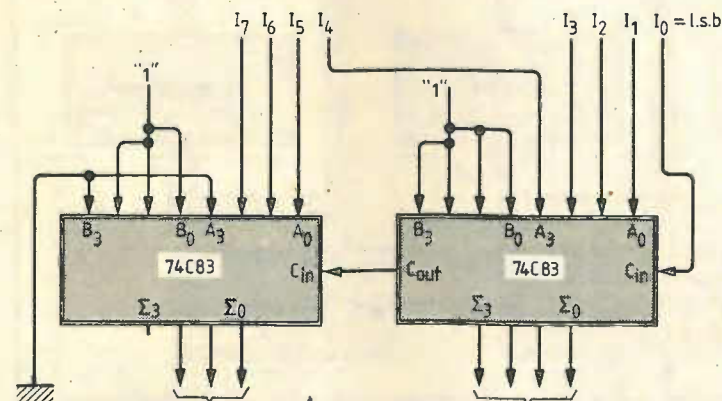


Fig. 10. Connecting binary adders for a programmable 1-to-256 divider, applying equations (5) and (6).  $S_3$  of the most significant decade is not used.



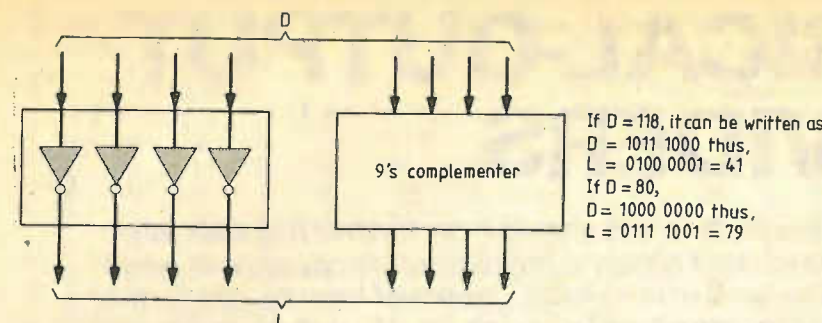


Fig. 11. Inverters and 9's complementers are used to apply equation (7) when cascading b.c.d.-input programmable dividers. Each additional decade will require the use of another 9's complementer.

Fig. 12. Two b.c.d. adders connected to give an output of  $I/2$  when the divisor ( $I$ ) is even, or  $(I + 2)/2$  when the divisor is odd. The b.c.d. input value is shifted one position toward the least significant bit and a correction made through the adders when the l.s.b. of each decade is logic 1. In the original circuit, MC14560 natural b.c.d. adders were used.

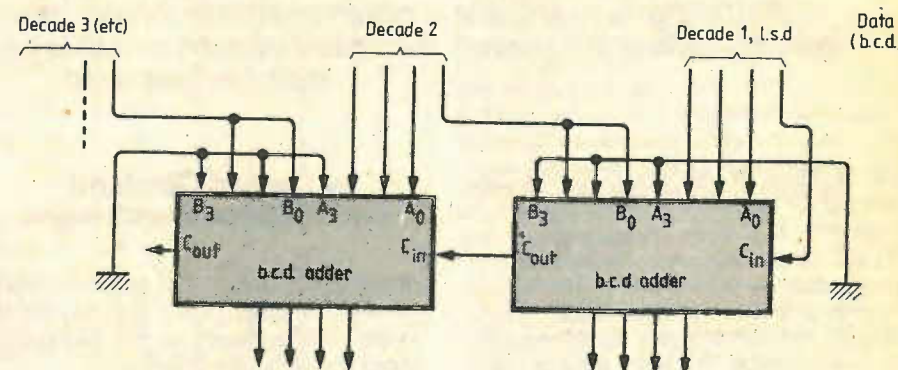
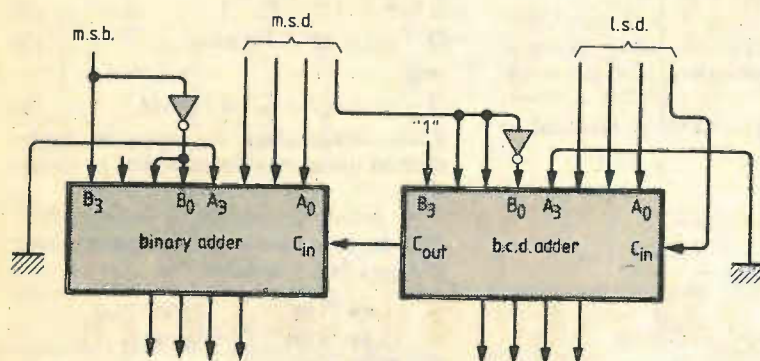


Fig. 13. The two adders, as shown, perform a similar function to those shown in Fig. 12, but by replacing the most significant decade i.c. by a binary type, the maximum possible division ratio is raised to 160 and the m.s.b. may be used to change the input function.

Fig. 14. Sections shown in Figs 11 and 13 combined with comparator and division circuits to form the b.c.d. input programmable divider for ratios  $1 \leq I \leq 100$ . Divisors up to 160 may be used with this circuit and further decades may be added.

even greater, are required. The following describes such dividers for ratios  $1 \leq I \leq 100$ , and further expansion.

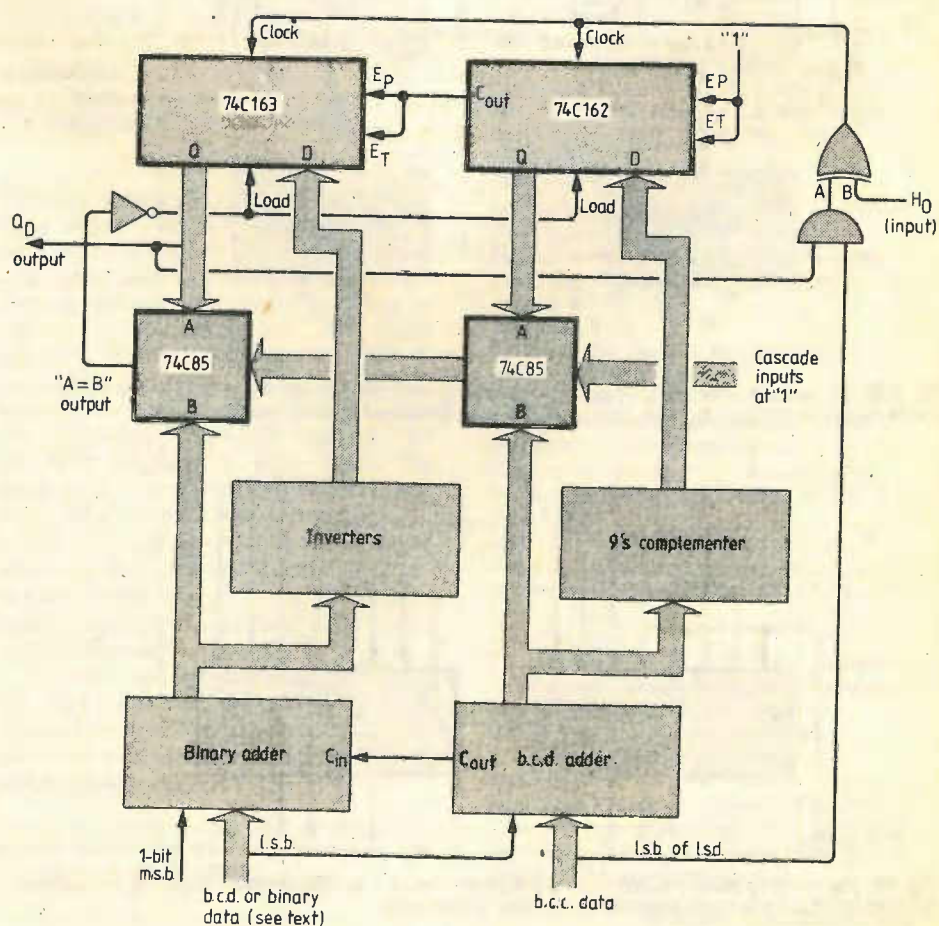
For ratios  $1 \leq I \leq 100$ , two dividers are connected synchronously and are made to 'oscillate' around a given transition (at  $p$  to  $p+1$ ). It should be obvious from the previous paragraph that a binary counter will still have to be used for the most-significant decade (m.s.d.).

If the output obtained is to be square, and one is to be free to choose a division ratio from 1 to 100, it is necessary to use the transition between counts 79 and 80 (or 799 and 800 if three decades are used) as the starting point.

Table 4 gives values for the following relationships;

- $L + D = 159$  (7)
- $D - I/2 = 79$  if  $I$  is even (8)
- $D - (I + 1)/2 = 79$  if  $I$  is odd (9)

To apply the value 159, a 9's complementer must be used in the least-significant decade, and four inverters for the next decade, Fig. 11. In the original circuit an



MC14561 9's complementer was used. To implement relationships (8) and (9),  $I/2$  or  $(I + 1)/2$  must be in b.c.d., see Fig. 12. The b.c.d. value is shifted one position to the least significant bit, and a correction is made through the b.c.d. adders when the l.s.b. of each decade is 1.

This method works well, but it is possible to make more use of the MC14560 adders because their design is such that arithmetical operations like  $14 + P$  ( $0 \leq P \leq 5$ ), which are not supposed to be valid in b.c.d., are possible and provide the correct result. Consequently, relationships (8) and (9) can be applied using a binary adder (for the m.s.d.), and a b.c.d. adder, as shown in Fig. 13. This circuit may be expanded to suit the desired number of decades. Figure 14 shows the complete circuit, which consists of the previously mentioned sections with two comparators and the dividers added. As can be seen in Fig. 14, the b.c.d.-input divider differs from the binary-input divider mainly through the inclusion of a b.c.d. adder for processing program-input data and the 9's complementer for the counter-load data.

Two other interesting features are inherent in the circuit;

- if the data m.s.b. is held high, the maximum programmable ratio is 199, whereas the maximum-possible division

Table 4: Divisor, load and detect ( $I$ ,  $L$  and  $D$ ) values for the b.c.d. programmable counter. This table is not given in full as it is obvious how omitted values are derived from the values given.

Divisor	Load	Detect
1	79	80
2	79	80
3	78	81
4	78	81
11	74	85
12	74	85
19	70	89
20	70	89
39	60	99
40	60	99
79	40	119
80	40	119
99	30	129
100	30	129

ratio is 160. Consequently, if a number higher than 160 is programmed, the actual ratio will be  $N - 160$ . For example if  $N = 173$ , the division ratio will be 13.

- if the data m.s.b. is held low, it is possible to use the full potential of the most significant digit, i.e., the input may be programmed to give ratios from 1 to 15. This means that the total division range will be from 1 to 160, the ratio 160 occurring when the value of the two input decades is zero.

If three decades are required, the fol-

lowing additional components are needed;

- a decade counter between the binary and b.c.d. counter (take care with the carry and enable-output connections)

- a comparator
- a 9's complementer
- a b.c.d. adder for input data (B inputs of this adder are connected as those of the l.s.d. adder).

C.m.o.s. i.c.s were originally used for the design and worked well up to 1MHz, depending on the division ratio. Changing the counters, comparators and gates to 74LS series i.c.s will bring the maximum usable frequency up to around 10MHz.

#### Bibliography

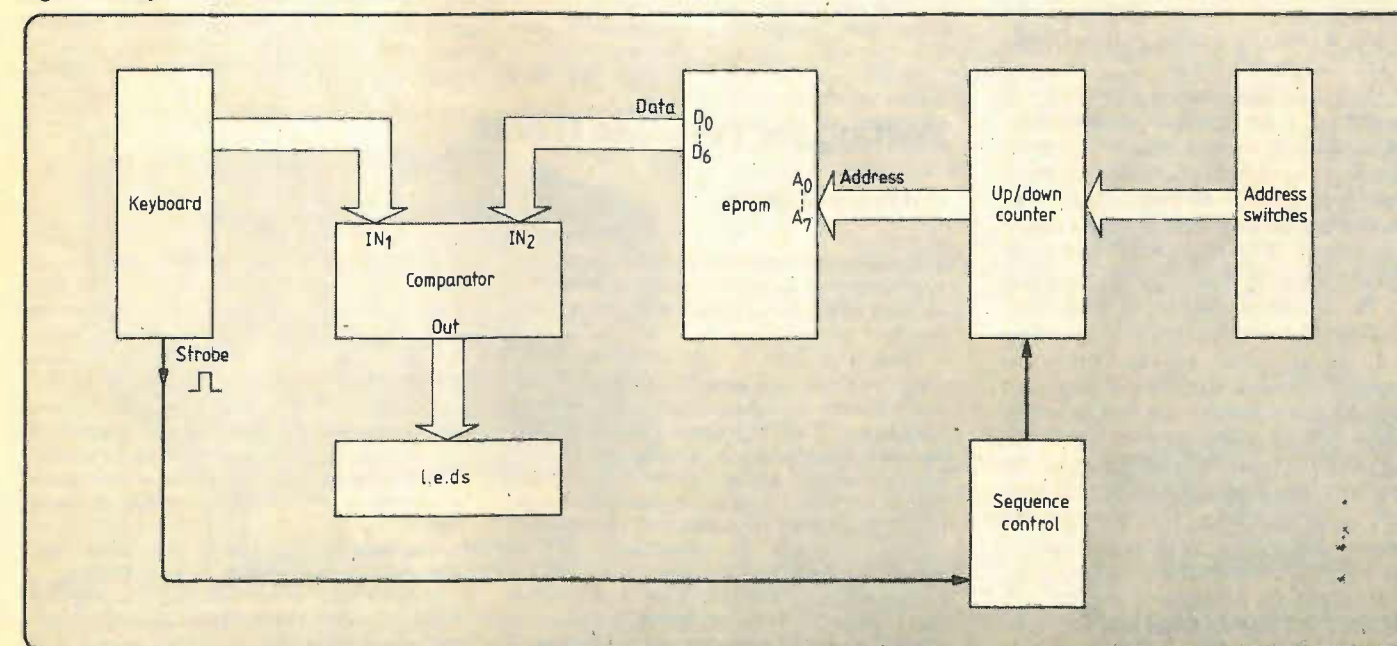
- C. F. Chen, Design of a divide-by-N asynchronous odd number counter with 50/50 duty cycle. *IEEE Proceedings*, September 1974, pp.1278-1279.
- J. L. Huertas, Square-wave frequency divider provides symmetrical output for odd divisors. *Electronic Design*, 21 September 1975, p100.
- P. Bamberger, G. Girolami, Méthodes simples pour la division de fréquence symétrique. *Electronique et applications industrielles*, No 258, 15 October 1978, pp.59-61.
- A. M. Madni and R. R. Orton, Cross-coupled one shots divide by odd numbers and give a symmetrical output. *Electronic Design*, 25 October 1979, p.114.
- L. E. Geigen, Divide symmetrical clock pulses by odd numbers, get a symmetrical output. *Electronic Design*, 1 March 1980, p.110.

## ASCII KEYBOARD TESTER

A time-saving method for detecting faulty keys or data lines. Traditionally keyboards have been tested by using a voltmeter or an oscilloscope in conjunction with a table of ASCII codes. This takes a long time and can be prone to error. The tester described here can detect faults quickly and easily.

by Waleed Habib Abdulla

Fig. 1. The keyboard tester in outline.





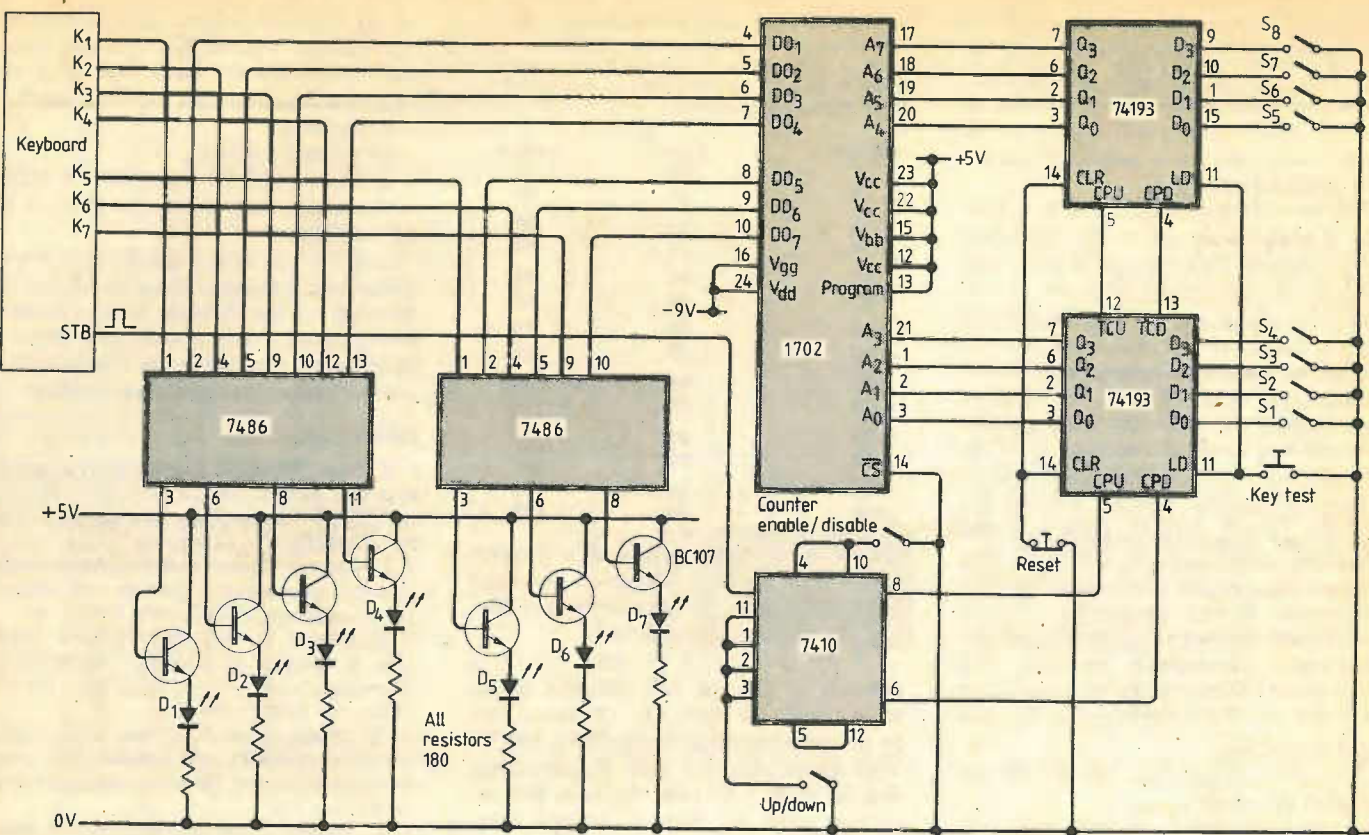


Fig. 2. The full circuit of the keyboard tester.

Figure 1 shows a block diagram of the tester. The ASCII code of each key is stored in an e.p.r.o.m. which holds an 'image' of the keyboard. When a key is pressed, the coded output may be compared with the stored code from the memory. Any mismatch will cause l.e.d. indicators to light. A counter is used to address the memory and is incremented by the keystroke strobe from the keyboard. Each time a key is pressed, the counter increments to the next address. Thus the keys must be tested in a set sequence governed by the order that they are programmed into the e.p.r.o.m. The full circuit is shown in Fig. 2. There is an up/down switch to reverse the counter, switches to set a specific address in the memory, a counter disable switch, 'reset' and 'key test' pushbuttons.

With the counter enabled and reset and switched to the 'up' mode, it is possible to press all the keys in sequence to check for errors. If no l.e.d. is lit, then the keyboard has no fault. If a l.e.d. should light then the corresponding bit can be tested inside the keyboard. It is possible to back-track and retest a key by reversing the sequence with the up/down switch. A fault may come from an individual key or from a data line. In the latter case, the same l.e.d. will remain lit when a number of keys are tested. To test a specific key the counter is disabled and the address of the key is entered on the switches. Pushing the key-test button will effect the comparison. Alternatively, one location in the memory (for example address 00) could be left vacant. Then with the counter set to that address, and disabled, the pressing of any key will cause the code coming from that key to be displayed on the l.e.d.s.

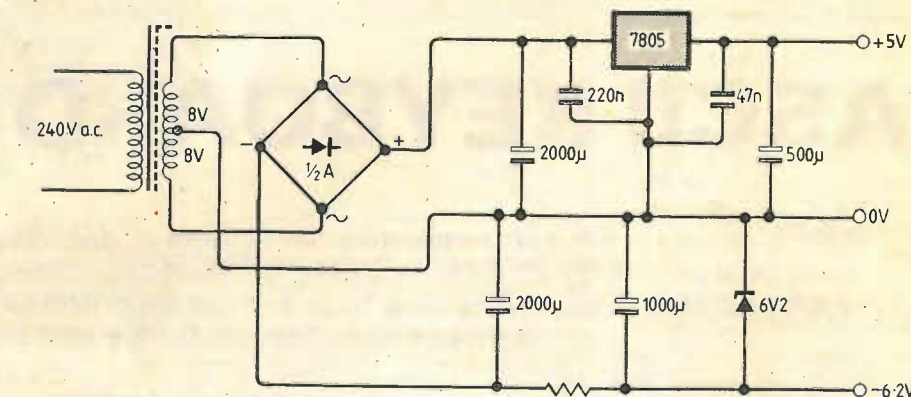


Fig. 3. A suitable power supply. The voltage needed is +5V at 220mA. The e.p.r.o.m. requires a negative voltage between -5.5 and -9V at 20mA. Resistor value selected to suit current rating of the zener diode.

## Writing for Wireless World

### Notes for authors

Potential authors often ask us for advice on how to present their material; fearing perhaps, that anything of less than a certain grammatical standard may be rejected.

There is no basis for this belief: any article which we think contains information of interest to our readers, or which will add to their store of knowledge, or which presents the design of interesting equipment, is acceptable. We are happy to correct any awkwardnesses of grammar or spelling. All we ask is that articles contain all the relevant information and include any relevant diagrams or illustrations. Articles should be original contributions and not just rehashed chapters from text books or application notes. There is no need to use a formal tone — a simple direct style makes for pleasant reading.

Diagrams need only be clear sketches, as we re-draw them all to our own style, but they must be clear so that the people in our drawing office can follow and reproduce them. Photographs can be sent as slides, negatives or glossy prints and will be returned, if this is requested.

We like to include brief biographical pieces on our authors, preferably with a photograph. If you have no objection to this, please let us have the information with the article, as well as any qualifications or honorifics that you may possess.

We pay for the articles that have been accepted immediately after their publication.

If you would like to talk about a proposed article, you may like to ring us on 01-661 3500, extension 3590 or 3128.

# NEW PRODUCTS

## WAVEFORM RECORDER

Digital waveform recorders are a new venture for Hewlett Packard but with their past experience in test and measuring instruments they have been able to jump in at the deep end. The HP5180 is a so called 'universal' waveform recorder, that is, it can be used on its own or under the control of a computer. A 10-bit a.-to-d. converter providing sampling rates up to 20MHz, and a 16K-by-10-bit memory that can be divided into a maximum of 32 segments form part of the system. Digital triggering is used so trigger times before or after the event, and trigger voltages, may be set and read accurately. One of the functions of two adjustable cursors is to pin-point a section of a waveform for vertical and/or horizontal zoom; these cursors may also be used to set trigger points. The front panel is, of course, designed ergonomically but nevertheless holds some 50 push buttons and one multi-purpose knob. With this in mind, up to four front-panel settings may be stored and recalled at will. All the front panel controls, and data i/o, are accessible through the HP-interface bus and 16-bit parallel d.m.a. (direct memory access) at transfer rates of up to 1M-word/s is possible. Hewlett-Packard Ltd, 308-314 Kings Road, Reading, Berks RG1 4ES.

WW301

## ELECTROMETER

Voltage, current, resistance and charge functions are included on Keithley's model 614 electrometer. On the three measuring ranges for up to 20V direct, the 4 1/2-digit meter's input impedance is  $5 \times 10^{13} \Omega$  and 20pF; resolution on the lowest range is 10µV. The most sensitive of nine direct-current

ranges has a resolution of 10fA and the maximum possible current reading is 2mA. Less than 200µV is present over the terminals on all current ranges. Resistances up to 200GΩ may be measured, also in nine ranges and resolution on the lowest range is 1Ω. Three other ranges are used for charge measurements down to around 10fC on the lowest range and up to 20nC on the highest. Outputs are provided for a chart recorder and for guarding when making voltage and current measurements. A rechargeable lead-acid battery is included. Keithley Instruments Ltd, 1 Boulton Road, Reading, Berks RG2 0NL.

WW302

## TOOLS

This company has a wide range of tools and has recently introduced two kits, in wallets with zips, for



routine servicing. The more elaborate of these contains 25 tools, including a miniature soldering iron, de-solder braid, solder, pliers, cutters, tweezers, a knife, an i.c. extraction tool, scissors, a wire stripper and a range of screwdrivers and adjusting tools. Seven tools are contained in the smaller kit, pliers, side-cutters, tweezers and four screwdrivers. The former, the 'computer-service wallet', sells at £39.50 including v.a.t. and postage, and the latter, the 'micro wallet', at £13.50, also inclusive. Toolmail Ltd, Parkwood Industrial Estate, Sutton Road, Maidstone, Kent ME15 9LZ.

WW303

## BEAD THERMISTORS

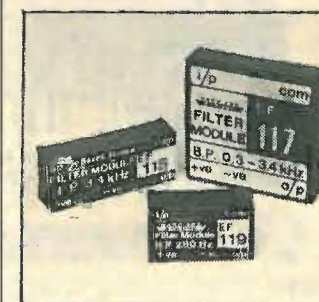
Often, thermal and voltage/current overloads in transformers, chokes, motors, generators, etc., are sensed by means of a p.t.c. thermistor. For this application, the response speed of a protection circuit is mainly de-

termined by the size of the thermistor and the thickness of its protective coating. Compstock have a range of general-purpose bead thermistors which all have a nominal resistance of 1kΩ at one of 13 temperatures from 80°C to 180°C, and each can be obtained as a bare pellet, resin dipped, sleeved or both resin dipped and sleeved. For all 13 reference temperatures, -5°C reduces the resistance to 550Ω and +5°C increases the resistance to 1.3kΩ. Compstock Electronics Ltd, Compstock House, London Road, Stanford-le-Hope, Essex SS17 0JU.

WW304

## VOICE FILTERS

Active voice-frequency filters for use in telecommunications are available from Barr and Stroud as small p.c.b.-mounting modules. There are currently four modules, the EF117, 118, 118A and 119, all with elliptic-type transfer functions providing a minimum attenuation rate of 40dB. The 117 is a band-pass filter for the range 300Hz to 3.4kHz; attenuation variation between 350Hz and 3.0kHz is less than ±0.5dB. Both versions of the



WW302

WIRELESS WORLD APRIL 1982

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# NEW PRODUCTS

118 are low-pass filters, the first with a cut-off frequency of 3.4kHz and the second (suffix A) with a cut-off frequency of 1.8kHz. Using the latter version, the upper part of the voice-frequency channel is left free to carry data. Lastly is the 119 high-pass filter with a cut-off frequency of 300Hz and an upper limit of 50kHz. Supply rails between  $\pm 5V$  and  $\pm 18V$  are required for these modules. Barr and Stroud, Melrose House, 4-6 Savile Row, London W1X 1AF. WW305

## ANTENNAE FOR MOBILE RADIO

A Swedish company, Allgon Antenn AB, has produced two antennae, one for the aeronautical and land-mobile distress frequencies of 121.5 and 243MHz, and the other an omnidirectional broadband type for transmit and receive in the range 225 to 400MHz. The first, called simply type 4104 (shown in



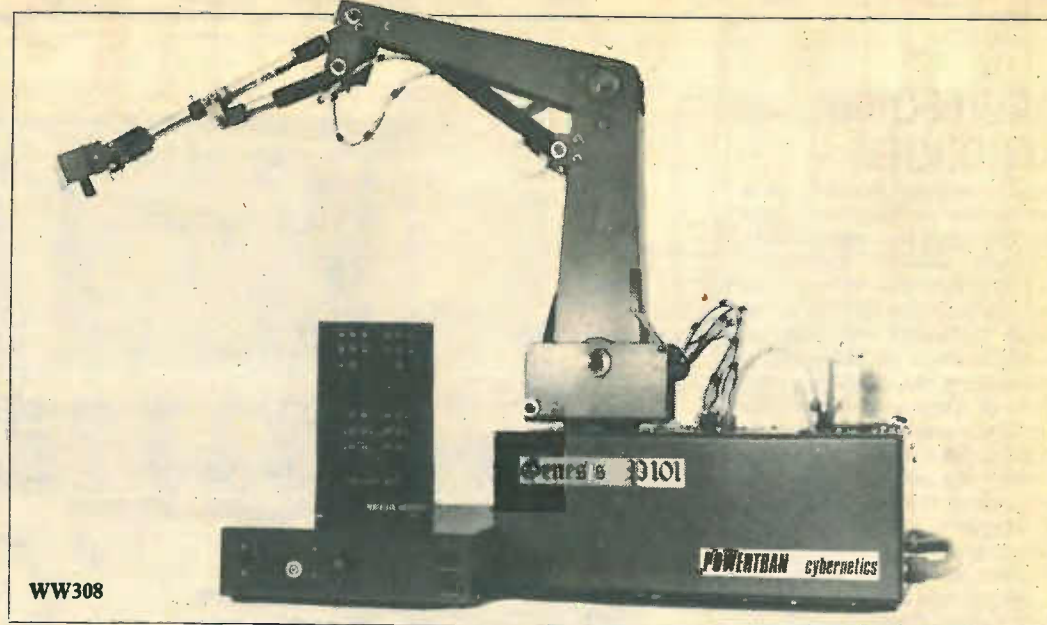
photo) operates on both distress frequencies simultaneously and can be used in base stations, on mobile-radio units and ships, or on helicopters and aircraft travelling at less than 200mile/h. The second, type 477, is a base-station antenna covering the 225 to 400MHz frequency range without tuning. In the middle of this range, the antenna's gain is 6dB. The maximum average transmitting power is 1.5kW. Allgon Antenn AB, Box 500, S-184 00 Åkersberga, Sweden. WW306

## MODULAR ORGAN KIT

A "budget-priced" electronic organ with features only previously available on more expensive instruments is claimed for the Wersi Comet. Imported non-exclusively to the UK from Germany by Aura Sounds in kit form as well as in transport-



WW307



WW308

able and spinet versions, it can be bought in stages, the basic organ comprising four packs totalling £1293. Further packs include auto-accompaniment, registration memory/piano, and string/guitar facilities, bringing price to about £1900 against a factory built price of £3,600. Satellite keyboards - up to four can be connected with sections of the organ assigned to them - cost £138 in kit form. The makers claim numerous "realistic" and interesting tonal colours including synthesizer effects and guitar voices as well as the more traditional drawbar and orchestral sounds. How far the claim to realism is justified is obviously open to question, especially with auto accompaniment, but it seems much the best at simulating pipe organs. In addition to features now common to electronic organs and synthesizers that rely on voltage-controlled filters and amplifiers, this microprocessor design also has a program memory for 20 registrations; and a key memory can play background chords after notes are released. A digital transposer can pitch the organ in any key so that tuning is not required. Aura Sounds Ltd, 17 Upper Charter Avenue, Barnsley, Yorks. WW307

## GENERAL PURPOSE ROBOTS

Hydraulically driven robot arms that can be controlled either manually or by computer are manufactured by Powertran Cybernetics for industrial, educational or home use. Complete systems range in price from around £600 to £800. Each unit has its own 6802-microprocessor control and hydraulic system, and is capable of handling several pounds. One of these units, the M101, has either four or five axes of arm movement and can be fitted with wheels capable of carrying over 50kg. Communication with a computer is through an optional RS232 interface. Powertran Cybernetics, Portway Industrial Estate, Andover, Hants SP10 3NN. WW308

## CABLE SIMULATOR

Cable transmission characteristics are important in digital communication systems, especially where p.c.m. regenerators are concerned. To reduce the amount of floor space often required for testing such designs, Wandel and Goltermann have introduced the PKN-1 for simulating cables with conductors between 0.6 and 1.4mm diameter. Cable attenuation is displayed on a digital readout and adjusted by means of two push buttons in steps of 1dB at a frequency of 1MHz. Both balanced and co-axial inputs and outputs are provided and a version of the PKN-1 with a 772kHz reference frequency can be supplied. A portable 200Hz to 620kHz level meter for measurements on voice channels in

local and remote networks has also been recently introduced by the same company. This meter has an analogue dB readout, a digital frequency display and a built-in generator. Wandel & Goltermann GmbH & Co., Postbox 45, Mühleweg 5, D-7412 Enningen, F. R. Germany. WW309

Professional readers are invited to request further details on items featured here by entering the appropriate WW reference number(s) on the mauve reply-paid card.

## CW AND RTTY TERMINAL

A communication terminal for encoding/decoding Morse or Baudot is manufactured by Polemark Ltd with inbuilt display, keyboard and real-time clock. The Microdot is a portable unit, microprocessor controlled, and has a 2Kbyte r.a.m. and 4K r.o.m., part of which contains some frequently used abbreviations and test-text which may be called using single key commands. Both modulator and demodulator are incorporated for c.w., f.s.k. and a.f.s.k. (audio-frequency shift keying). On receive, speed tracking is automatic; three fixed speeds may be set when transmitting and both transmit and receive speeds are displayed on the screen. Receive and transmit may be carried out simultaneously. The terminal's input may be connected directly to the output of a receiver or tape recorder, and the output directly to a transmitter. Self test is carried out by connecting the output to the input and supply requirements are 13.8V, direct at 2.4A. A price of £395 including v.a.t. and carriage is quoted. Polemark Ltd, 148-150 High Street, Barkway, Royston, Herts SG8 8EG. WW310

WIRELESS WORLD APRIL 1982

# First time on Earth.



SHARP MZ-80B

Sharp bring you the MZ80B. A machine that offers you functions previously only associated with more powerful, more expensive computers; that gives you versatility to handle a huge range of software and hardware applications in scientific, business and personal use.

The MZ80B opens up a new world of graphic display potential, more flexible data storage and retrieval, and ease of operation. Here is the computer from the future. Available today.

### Stunning Graphic Display.

Seeing is believing. The large-screen, high-focus, green-face display incorporated in the MZ80B gives you high-resolution graphics of 320 x 200 dots.

An additional graphic RAM can be added which allows another 320 x 200 dot resolution pattern to be displayed.

This dual high-resolution graphic ability is especially useful for simulating and displaying a dynamic picture. It can display 40 characters x 25 lines or 80 characters x 25 lines via software switching.

In addition there are facilities for full, on-screen editing, reverse video, partial scrolling and a full range of graphic symbols.

### Character and Graphic Printer.

This fast, quiet printer will reproduce your graphic displays and, of course, print-out upper and lower case letters and symbols. A tractor/friction feed version is also available.

### Data Storage/retrieval.

The MZ80B has a remarkable memory. 64K of RAM. And that constitutes all the memory area, giving flexible storage of any computer language and its software. The cassette deck is electromagnetically-controlled, with a data transfer speed of 1800 bits/sec combined with a unique

programme search facility to make data storage and retrieval super-fast.



A typewriter-style keyboard incorporates characters and symbols plus a numeric key-pad and ten user-definable keys for fast and simple operation.

BASIC is, of course, provided with Z-80 Assembler Packages, PASCAL and a BASIC compiler.

### Floppy Disk Drive.

A twin Floppy Disk Drive unit can be added which will give you 560 bytes of storage on double-sided, double-density disks.



### Comprehensive Documentation.

Each MZ80B comes complete with a full set of documentation including an owners' manual giving full circuit diagrams, a monitor reference manual and programming manuals.

### Interfaces

RS-232C and IEEE Interfaces are available from January 1982 allowing the MZ80B to communicate with scientific instruments and other peripherals.

### CP/M\*22

CP/M\* is also available making a wide range of packages immediately available including wordprocessing, financial modelling, data base management to mention but a few. CP/M\* also increases the disk capacity to 680K.

(CP/M\* is a Trade Mark of Digital Research Ltd.)

**SHARP**  
First, and foremost

SHARP ELECTRONICS (UK) LTD., COMPUTER DIVISION,  
SHARP HOUSE THORP RD., NEWTON HEATH,  
MANCHESTER M10 9BE. TELEPHONE: 061-205 2333.

Why on Earth don't you find out more?



Please send me full information on the Sharp MZ80B computer.

Name

Address

Tel:

To: Sharp Electronics (UK) Ltd., Computer Division,  
Sharp House, Thorp Road, Newton Heath,  
Manchester M10 9BE. Telephone 061-205 2333.  
(WW4/82)

WW - 011 FOR FURTHER DETAILS



Item No.	Description	Price
238	TEKTRONIX PORTABLE OSCILLOSCOPE type 422. Dual Trace	£350
239	TEKTRONIX STORAGE MONITOR type 613	£485
240	TEXAS SILENT TUBE type 1000 baud	£165
241	MARCONI UNIVERSAL BRIDGE type TF200	£275
242	CALCOMP DRUM PLOTTER Model 564	£425
243	MARCONI AM/FM SIGNAL GENERATOR TF95A/3/S (CT402) 1.5-220MHZ	£245
244	MARCONI AC MILLIVOLTMETER TF2800 10HZ-5MHZ; 1mV-300V	£35
245	MOSELEY WAVEFORM TRANSLATOR type 101	£25
246	MARCONI HETERODYNE FREQUENCY METER TF1067/1 5 to 1000MHZ	£25
218	R&S CAPACITANCE METER BN5201	£55
219	S.E. LABS OSCILLOSCOPE type SM111. Dual Trace Solid State DC-20MHZ	£245
220	BAOR OISH AERIAL 27" Waveguide size 0.5x1/8" complete with couplers	£55
221	EX-MINISTRY MASTS. Approx. 27ft. Heavy duty with guys etc.	£25
222	PHILIPS VIDEO COLOUR TEST GENERATOR type PM522	£225
223	AS above but NO CASE	£195
192	BELUX POWER UNIT type CMT3001. +/-0-1000Volts in 3 ranges	£40
193	PHILIPS AUTOMATIC ELECTRONIC VOLT OHM METER type PM2405	£20
194	KAY ELECTRIC VARI-SWEEP Model 402 15-40MHZ	£175
195	TAYLOR VALVE VOLT METER type 172A	£15
196	ROGERS DISTORTION FACTOR METER Model 344A 100HZ-10KHZ 0.01%	£75
197	STP MULTIMETER type 8810CA	£45
198	LOW FREQ. SPECTRUM ANALYSER 0.36-1KHz C.W. CHART RECORDER	£50
199	PLESSEY TELEGRAPH SIGNAL GENERATOR TS610 with TDM system 70. Speed 50-75-100 (2 units)	£300
198	AS above but SPEED 45.5-90-175	£100
199	HEWLETT PACKARD DIGITAL VOLT METER type 3300A	£75
200	HEWLETT PACKARD WAVE ANALYSER Model 302A (No case)	£20
201	KNIGHTS SWEEP/MARKER GENERATOR & RF GENERATOR (2 units)	£75
168	PRESTON DIGITAL MULTIMETER X-MOD 723	£25
169	R&S LIMIT BRIDGE K25 BN5500	£45
170	MARCONI VARIABLE ATTENUATOR TF104/2S (CT421)	£25
171	SOLARTRON/SCHUMBERGER JMI381 Programmable Pseudo Random Signal Generator	£120
172	WAYNE KERR VHF ADMITTANCE BRIDGE 8801	£100
173	WAYNE KERR BRIDGE DETECTOR R101	£75
174	LABSEAR COLOURMATCH 825 Pattern Generator CM6004-PG	£20
175	GAUMONT-KALEE FLUOR METER type 1740	£60
176	R&S SELEKTOMAT USVW BN521/2	£80
177	BRITISH PHYSICAL LABORATORIES MEGOHMMETER RM 1603	£25
183	R&S RESONANCE FREQUENCY METER 30-500MHZ WAM BN4312/2	£50
184	ADVANCE SIGNAL GENERATOR type E2 100KHZ-100M	£45
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152	BELL 110A GAUSSMETER with probe	£50
153	MARCONI SIGNAL GENERATOR TF801D/1S 10-485 MHZ	£25
154	AVO VALVE TESTER CT180	£20
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88	TEKTRONIX VECTORSCOPE type 526	£375

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89	BELL & HOWELL TV CAMERA with SHIBAN FUJINON TV ZOOM LENS F1.8-20-100mm	£125
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73	RHODE & SCHWARTZ UHF TEST RECEIVER BN1523 280-940MHZ	£95
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75	R & S ATTENUATOR type OPR BN1040/20	£20
76	HEWLETT PACKARD WIDE RANGE OSCILLATOR type 200C0	£50
77	HEWLETT PACKARD AUDIO OSCILLATOR type 200A1	£50
78	FERRORGRAPH RECORDER TEST SET type RT52	£275
51	BENKX CROSSHATCH DOT GENERATOR UHF/VHF	£25
52	MARCONI AF Output Meter type T835	£20
53	RHODE & SCHWARTZ NOISE GENERATOR SKTU BN4151/2/60 3-1000MHZ	£100
54	TAYLOR RF SIGNAL GENERATOR type 68A/M Mk2	£75
55	ADVANCE O METER type T2 100KHZ-100MHZ	£20
56	ADVANCE PULSE GENERATOR type P650020	£25
57	AIRMEC MILLIVOLTMETER type 301	£20
58	TAYLOR VALVE TESTER type 450	£50
59	B & K LEVEL RECORDER type 2305, 50dB Potentiometer. Brand new with acces.	£285
60	B & K LEVEL RECORDER type 2305	£225
61	MARCONI AM SIGNAL GENERATOR type TF801D/BS 10-485MHZ	£125
62	MARCONI UHF SIGNAL GENERATOR type TF1060	£125
63	AVO TRANSISTOR ANALYSER type TA	£30
64	ADVANCE STABILISED POWER UNIT type PP1 0-500V; 300MA	£25
65	PYE SCALAMP 40KV RMS Max ELECTROSTATIC VOLT METER	£80
66	PYE SCALAMP 20KV RMS Max ELECTROSTATIC VOLT METER	£80
4	TEKTRONIX 561A with 3A1 & 3B3 Plug-ins. Dual Trace Oscilloscope	£200
5	EQUIPMENT OSCILLOSCOPE type 061A. Dual Trace 10MHZ	£200
6	SCOPEX OSCILLOSCOPE type 4D10. Dual Trace 10MHZ	£160
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10	B & K MICROPHONE AMPLIFIER type 2605	£40
11	B & K BEAT FREQUENCY OSCILLATOR type 1013	£105
12	B & K BEAT FREQUENCY OSCILLATOR type 1018	£50
13	B & K AUDIO FREQUENCY SPECTROMETER type 2112	£295
14	B & K FREQUENCY RESPONSE TRACER type 4707	£225
15	HEWLETT PACKARD LCR BRIDGE type 4251A	£350
16	HEWLETT PACKARD LCR METER type 4342A	£1350
17	AUTO TRANSFORMER 1KVVA Ph Volts 30/240 Set 115	£18
18	POLARAD FIELD STRENGTH METER type FM-82 with RF Tuning Unit FM-X2 7360-1000MHZ	£190
20	POLARAD MICROWAVE SIGNAL GENERATOR Model MSG-1 950-2400MHZ	£250
21	POLARAD MICROWAVE SIGNAL GENERATOR Model MSG-2 2150-4600MHZ	£250
22	TEKTRONIX CURVE TRACER type 578	£150
24	KROHN-HITE BAND PASS FILTER Model 330A	£25
25	KROHN-HITE REJECTION FILTER Model 350A	£25
26	MARCONI IN SITU UNIVERSAL BRIDGE type TF2701. Battery Operated	£35
27	BRANDENBURG HIGH VOLTAGE GENERATOR type MR50	£75
154	AVO VALVE TESTER CT180	£20
155	ADVANCE AUDIO SIGNAL GENERATOR type J1A	£30
64	ADVANCE UHF SIGNAL GENERATOR SG68 370-1040 MHZ	£150
87	HEWLETT PACKARD DC POWER SUPPLY type 6448B 0-600Volts 0-1.5Amps	£295
88	GRUNDIG UNIVERSAL VHF CONVERTER type V52	£25
89	DECADE CAPACITOR 1413 with ANALOGUE LIMIT COMPARTMENT 1782 and IMPEDANCE COMPARTMENT 1854 by GENERAL RAD	£175
88	TEKTRONIX VECTORSCOPE type 526	£375

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	<b>AVO SIGNAL GENERATOR</b>	
	No. 2 AM/FM	
	AM 0.45-225MHZ	
	FM 20-100MHZ	
	£75 each	
	<b>IKEMAMI MONITOR</b>	
	20" Black & White	
	Solid state. Video in, int. ext. Sync.	
	£75 each	
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	TYPE TE-22, 20HZ-200KHZ. Portable, as new	
	ONLY £35 each. P&P £4	
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	Russian Type 4324	
	AC/DC volts; AC/DC current; ohms, etc. Brand new, boxed.	
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	<b>GENERAL PURPOSE OSCILLOSCOPE</b>	
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	Single beam. Size approx. 6x7x9 1/2". Weight 7lbs. Ideal for the beginner or school user.	
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	TYPE TU-2 Testing	
	TYPE W Differential Comparator	
	TYPE Z Differential Comparator	
	TYPE IA5 DC-50 MHZ Differential	
	TYPE IS1 Sampling	

Item No.	Description	Price
AA119	AS215	1.38
AA130	AS216	1.27
AA131	AS217	1.15
AA213	AS220	2.64
AA215	AS221	2.88
AA217	AU113	2.88
AC107	AU110	3.45
AC125	BA156	0.12
AC126	BA157	0.12
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AC128	BA159	0.12
AC141	BA160	0.12
AC142	BA161	0.12
AC143	BA162	0.12
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AC206	BA225	0.12
AC207	BA226	0.12
AC208	BA227	0.12
AC209	BA228	0.12
AC210	BA229	0.12
AC211	BA230	0.12
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Item No.	238	TEKTRONIX PORTABLE OSCILLOSCOPE type 422. Dual Trace.	£280
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	240	TEXAS SILENT 700 PRINTER Model KSR233-300 baud	£275
	241	MARCONI UNIVERSAL BRIDGE type 17200	£185
	242	CALCOMP DRUM PLOTTER Model 564	£425
	243	MARCONI AM/FM SIGNAL GENERATOR TP995A/3 (CT402) 1.5-220MHz	£245
	244	MARCONI AC MILLIVOLTMETER T2500 10KHz-5MHz; 1mV-300V	£35
	245	MOSELEY WAVEFORM TRANSLATOR type 101	£35
	246	MARCONI HETERODYNE FREQUENCY METER T1067/15 to 1000MHz	£225
	218	R&S CAPACITANCE METER BN501	£35
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	220	RADAR DISH AERIAL 27". Waveguide size 0.5x1.5" complete with couplers.	£35
	221	EX-MINISTRY MASTS. Approx. 27ft. Heavy duty with gussets	£25
	222	PHILIPS VIDEO COLOUR TEST GENERATOR type PM5522	£195
	223	As above but NO CASE	£195
	192	BELK POWER UNIT type CMT3001. +/-0-1000V in 3 ranges	£40
	193	PHILIPS AUTOMATIC ELECTRONIC VOLT OHM METER type PM2405	£220
	194	KAY ELECTRIC VARI-SWEEP Model 4001 15-470MHz	£175
	195	TAYLOR VALVE VOLTMETER type 172A	£15
	196	ROGERS DISTORTION FACTOR METER DM344A 100Hz-10KHz 0.01%	£75
	197	STP MULTITEMP type 6810CA	£45
	198	PYE PH METER Model 725	£20
	199	FENLOW L.F. SPECTRUM ANALYSER 0.35-1Kc/s. CW CHART RECORDER	£285
	197	PLESSEY TELEGRAPH SIGNAL GENERATOR TSG10 with TDS type 70. Speed 50-75 100 (2 units)	£280
	198	As above but SPEED 45.5-50-75	£180
	199	HEWLETT PACKARD DIGITAL VOLTMETER type 3480A	£155
	200	HEWLETT PACKARD WAVE ANALYSER Model 302A (No case)	£155
	201	KNIGHTS SWEEP/MARKER GENERATOR & RF GENERATOR (2 units)	£75
	198	PRESTON DIGITAL MULTIMETER X-Mod 723	£25
	199	R&S LIMIT BRIDGE KZS BN5500	£45
	170	MARCONI VARIABLE ATTENUATOR T1073A/25 (CT421)	£25
	171	SOLARTRON/SCHLUMBERGER JM1881 Programmable Pseudo Random Signal Generator	£120
	172	WAYNE KERR VHF ADMITTANCE BRIDGE 8801	£100
	173	WAYNE KERR BRIDGE DETECTOR R161	£75
	174	LASGEAR COLOURMATCH 625 Pattern Generator CM6004-PG	£120
	175	GAUMONT-KALEE FLUTTER METER type 1740	£40
	176	R&S SELEKTOMAT USVW BN521/2	£25
	177	BRITISH PHYSICAL LABORATORIES MEGOHMMETER RM 180/3	£135
	178	R&S RESONANCE FREQUENCY METER 30-500MHz WAM BN431/2	£120
	179	ADVANCE SIGNAL GENERATOR type E2 100KHz-100m	£120
	180	VIDEO CIRCUITS TUBE TESTER V33	£25
	158	HEWLETT PACKARD OSCILLOSCOPE 182A with 1808A and 1825A. 75MHz P Delayed Sweep	£280
	159	MARCONI UNIVERSAL BRIDGE T1313 1/4"	£120
	160	ADVANCE DIGITAL MULTIMETER DM12	£120
	161	TAYLOR AM-FM SIGNAL GENERATOR Model 62A MK2	£25
	162	WAYNE KERR BRIDGE type B221	£25
	163	BELL 116A GAUSSMETER with probe	£25
	164	MARCONI SIGNAL GENERATOR T80D/15 10-405 MHz	£25
	165	AVO VALVE TESTER CT100	£25
	64	ADVANCE AUDIO SIGNAL GENERATOR type J1A	£25
	65	ADVANCE UHF SIGNAL GENERATOR SG65 370-1040 MHz	£25
	66	HEWLETT PACKARD DC POWER SUPPLY type 6448B	£25
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	68	DECADE CAPACITOR 1415 with ANALOG LIMIT	£25
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ISOLATION TRANSFORMERS	
Pri tapped 220-240V sec 240V 500 watts. Open frame type, top panel connections. Ex-equipment, but in perfect condition. £18, corr £3, V.A.T. £2.70.	
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Type 42A panel mounting, input 240V, output 0-270V 2A with control knob. Ex-equipment, but in perfect condition. £10, P&P £2.50, V.A.T. £1.88.	
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on frame clamped type, split bin. All primaries 240V No. 1 tapped 12-15-20-24-30V 750 £4. No. 2 sec 0-0.3V 1A and 200 MVA £2.50. No. 3 15-0-15V 4A and 6.3V 200 MVA. No. 4 10-12V 750 MVA and 6.3V 200 £4. No. 5 sec 13V 1/2A £1.50. ec8V 1/2A 6.3V 600 MVA. 6.3V 1/2A 0.001 £1.75. No. 7 1/2A 0.001 £1.75. No. 8 sec 1A x 2 £1.75. No. 9 sec 18V 10 sec 24V 2A £4.50. No. 10 2A £3.50. All prices in-tage and V.A.T.	
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Brushed Aluminium Arm with stereo ceramic cartridge and Diamond Stylus, 3-speeds. Manual and Auto Stop/Start. Large Metal Turntable. Cueing Device. £22 Post £2

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Stabilised output, 9 volt 400 m.a. U.K. made in plastic case with screw terminals. Safety overload cut out. Size 5 x 3 1/4 x 2 1/2 in. Transformer Rectifier Unit. Suitable Radios, Cassettes, models, £4.50. Post 65p.

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25 volt, 50 volt, VU Meter.  
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Kit of parts to build a 3 channel sound to light unit. 1,000 watts per channel. Suitable for home or disco. Easy to build. Full instructions supplied. Post 95p

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**MAINS TRANSFORMERS**

250-0-250V 70mA, 6.3V, 2A	£4.50
250-0-250V 50mA, 6.3V, 3A, 6.3V 1A	£5.00
350-0-350V 250mA, 6.3V, 6A CT.	£12.00
300-0-300V 120mA, 2x6.3V 2A C.T.; 6V 2A	£12.00
220V 45mA, 6.3V 2A	£2.50

**AUTO 115V to 240V 150W £9. 250W £10. 400W £11. 500W £12. £2.**

**GENERAL PURPOSE LOW VOLTAGE**

Tapped outputs available:	
2 amp. 0, 4, 6, 8, 9, 10, 12, 15, 18, 25 and 30V	£6.00
1 amp. 0, 6, 8, 10, 12, 15, 18, 20, 24, 30, 36, 40, 48, 60	£6.00
2 amp. 0, 6, 8, 10, 12, 15, 18, 20, 24, 30, 36, 40, 48, 60	£10.50
3 amp. 0, 6, 8, 10, 12, 15, 18, 20, 24, 30, 36, 40, 48, 60	£12.50
5 amp. 0, 6, 8, 10, 12, 15, 18, 20, 24, 30, 36, 40, 48, 60	£16.00
8-9-10-15V, 12 amp.	£3.50
6V, 1/2 amp.	£2.00
6-0-6V, 1/2 amp.	£3.50
9V, 3 amp.	£1.50
9-0-9V, 50mA	£1.50
10-0-10V, 2 amp.	£3.00
10-30-40V, 2 amp.	£3.50
12V, 100ma	£1.50
12V, 750 ma	£2.00
12V 3 amps	£3.50

**RECTIFIERS**

6-12 volt 3a	£4.00
6-12 volt 4a	£6.50
6-12 volt 2a	£1.10
6-12 volt 4a	£2.00

**OPUS COMPACT SPEAKERS £22 pair Post £2**

**TEAK VENEERED CABINET**

11x8 1/2 x 7 in. 15 watts  
50 to 14,000 cps. 4 ohm or 8 ohm

**OPUS TWO 15x10 1/2 x 7 3/4 in 25 watt**  
2-way system £39 pair. Post £3.

**LOW VOLTAGE ELECTROLYTICS**

1mf, 2mf, 4mf, 8mf, 10mf, 16mf, 25mf, 30mf, 50mf, 100mf, 250mf, All 15 volts. 22 mf/6v/10v; 25 mf/6v/10v; 47 mf/10v; 50 mf/6v; 68 mf/6v/10v/16v/25v; 100 mf/10v; 150 mf/6v/10v; 200 mf/10v/16v; 220 mf/4v/10v/16v; 330 mf/4v/10v; 500 mf/6v; 680 mf/6v/10v/16v; 1000 mf/2.5v/4v/10v; 1500 mf/6v/10v/16v; 2200 mf/6v/10v; 3300 mf/4v/10v; 4700 mf/4v. 500mf 12V 15p; 25V 20p; 50V 30p; 100mf 76V 80p. 1000mf 12V 20p; 25V 35p; 50V 50p; 100V 70p. 2000mf 6V 25p; 25V 42p; 40V 60p; 2000mf 100V £1.20. 2200mf 63V 90p; 2500mf 50V 70p; 3000mf 50V 65p. 4500mf 64V £2. 4700mf 63V £1.20. 2700mf 76V £1.
--

**HIGH VOLTAGE ELECTROLYTICS**

8/450V	45p	8-8/450V	75p
8/800V	£1.20	8-16/450V	75p
16/350V	45p	20-20/450V	75p
32/500V	75p	32-32/350V	50p
32/350V	50p	32-32/500V	£1.80
50/500V	£1.20	50-50/300V	50p

**CAPACITORS** Various 10pf to 100,000pf 5p.  
PAPER 350V-0.1 7p; 0.5 20p; 1mf 150V 20p; 500V-0.001 to 0.05 12p; 0.1 15p; 0.25 25p; 0.47 35p.

**VALVE OUTPUT TRANSFORMERS (small) 90p.**

**TRIMMERS 10pf, 30pf, 50pf, 5p. 10pf, 150pf, 15p.**

**MICROSWITCH SINGLE POLE CHANGEOVER 30p.**

**SUB-MIN MICRO SWITCH. 30p.** Single pole changeover.

**TWIN GANG, 120pf 50p; 500 + 200 pf £1.**

**GEARED 365 + 365 + 25 + 25pf £1.**

**TRANSISTOR TWIN GANG. Japanese Replacement 50p.**

**HEATING ELEMENTS, WAFER THIN**

Size 11x9x3/8 in. Operating voltage 240V, 250W approx. Suitable for Heating Pads, Food Warmers, Convector Heaters, Propagation, etc. Must be clamped between two sheets of metal ceramic, etc. ONLY 60p EACH (FOUR FOR £2) ALL POST PAID.

**NEW baker Star sound**

high power full range quality loudspeakers produced to give exceptional reproduction. Ideal for Hi-Fi, music P.A. or discotheques. These loudspeakers are recommended where high power handling is required with quality results. The high flux ceramic magnet ensures clear response.

MODEL	INCHES	OHMS	WATTS	TYPE	PRICE	POST
MAJOR	12	8-16	30	HI-FI	£14	£2
DELUXE MK II	12	8	15	HI-FI	£14	£2
SUPERB	12	8-16	30	HI-FI	£24	£2
AUDITORIUM	12	8-16	45	HI-FI	£22	£2
AUDITORIUM	15	8-16	80	HI-FI	£34	£2
GROUP 45	12	4-8-16	45	PA	£14	£2
GROUP 75	12	4-8-16	75	PA	£18	£2

**BAKER 150 WATT MIXER/POWER AMPLIFIER £89 Post £2**

**SLAVE VERSION £75**</



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PLEASE ADD 15% TO ALL ORDERS INC. CARR.  
CURRENT RANGE OF NEW L.T. TRANSFORMERS  
OPEN TYPE TAG CONNECTIONS  
ALL PRIMARIES 220-240V

Type	Sec. Taps	Amps	Price	Carr.
1	24-30-40-48-60v	12	£36.50	£2.00
2	24-30-40-48-60v	10	£31.50	£2.00
3	24-30-40-48-60v	8	£27.50	£1.75
4	24-30-40-48-60v	5	£16.75	£1.75
5	24-30-40-48-60v	3	£11.50	£1.25
6	24-30-40-48-60v	2	£7.50	£1.25

6-8-10-12-15-18-20-24-36-40-48-60V  
CAN BE OBTAINED FROM THE ABOVE RANGE

7	19-25-33-40-50V	10	£27.50	£2.00
8	19-25-33-40-50V	8	£19.50	£1.75
9	19-25-33-40-50V	6	£9.25	£1.25
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OR 25-0-25V OR 20-0-20V CAN BE  
OBTAINED FROM THE ABOVE RANGE

11	12-15-20-25-30V	10	£18.50	£1.75
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13	12-15-20-25-30V	2	£6.25	£1.25

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OR 12-0-12V OR 15-0-15V CAN BE  
OBTAINED FROM THE ABOVE RANGE

14	12-24V	12v 60A, 24v 30A	£39.50	£3.50
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18	12-24V	12v 4A, 24v 2A	£4.25	£1.25

**HEAVY DUTY OF TRANSFORMERS**  
Type OT28EL 100 watts, 3.75A, 15V, 1.75K CT, 4 EL34 2x25  
m/s dc max. £15.50; P&P £1.25. Type OT29EL 500 watts, 3.75A, 15V,  
3.5K CT, rated 2x125 m/s dc max. £8.95; P&P £1.

**AUTO STEP-DOWN TRANSFORMERS**  
FOR AMERICAN EQUIPMENT  
240/110 Volts, 80-2250 watts. Regular stock line. Types 80-1500  
watts are fully shrouded. Fitted with American two or three pin  
socket outlets and 3 core 240V mains lead. Types 1750 and 2250  
watts are steel cased with two American socket outlets. Neon  
indicator, three core mains lead and carrying handle. Send SAE for  
price list and further details. American sockets, plugs, adaptors  
also available.

**SPECIAL OFFER, HINCHLEY MAINS**  
ISOLATION TRANSFORMERS  
Prim 240V, Sec 240V 250 watts. Open frame type. Tag connections.  
Fused input, £10, p.p. £2. V.A.T. £1.80. Parmeko pri tapped 115-  
220-240V, Sec 240V 6 amps. Fully shrouded top panel connections.  
Sec can be wired to give 120-0-120V. £25, carr. £5, V.A.T. £4.50.

**HEAVY DUTY ISOLATION**  
TRANSFORMERS  
240-240V ex-computer equipment.  
Large selection available 10-15 amps.  
Fraction of maker price. Telephone for  
further details.

**SPECIAL OFFER HEAVY DUTY**  
TRANSFORMERS  
Pri 240V sec 50V 15 Amps. Twice  
will give 100V CT or 50V 30A. Open  
frame type. Terminal block primary.  
Sec heavy wire leads. Frame size  
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TRANSFORMERS  
Pri 110-220-240V Secs 14V, 3V,  
12V, 1V. Separate windings. All at  
40 Amps. 14-15-15-17-18-18-19-  
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**IMPORTANT NEWS!!**  
We would like to announce the opening of our new branch at 21-23 BELL ST., NW1. It  
will, of course, stock our fantastic range of products that have made us famous over the  
past 25 years, PLUS!! many new ranges, i.e. computer and electronic components, test  
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plugs and sockets.

**SPECIAL OFFER, HIGH POWER AMPLIFIER**  
TRANSFORMERS  
Pri tapped 120-240V sec tapped 34-29-0-29-  
34V 6 amps and 46V 1A. Open frame type. Tag  
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4.5H 280 M/A £3.95, P&P £1.75, V.A.T.  
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V.A.T. 70p. 10H 120 M/A £3.95, P&P £1.  
V.A.T. 60p. 5H 180 M/A £3.95, P&P £1, V.A.T.  
60p. 15H 300 M/A £5, P&P £2, V.A.T.  
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40p. 50H 25 M/A £2, P&P £1, V.A.T. 45p.  
10H 75 M/A £2, P&P £1, V.A.T. 45p. Open  
types top panel connections 5H 350 M/A  
£3.50, P&P £2, V.A.T. 82p. 1H 1 amp  
£3.95, P&P £2, V.A.T. 90p. 25H 60 M/A  
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**LT CHOKES**  
'C' core 8 M/H 15A £6.75, P&P £2, V.A.T.  
£1.32. 4.8 M/H 10A open frame type  
£3.95, P&P £1.50, V.A.T. 81p. Potted  
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V.A.T. 90p. 130 M/H 1.5A £2.50, P&P £1,  
V.A.T. 52p. 2 M/H 12A x 2, 2 M/H 24A or 4  
M/H 12A £4.50, P&P £1.50, V.A.T. 90p.  
Open type 20 M/H 3A open type £1.95,  
P&P 75p, V.A.T. 40p. 'C' core 10 M/H 25A  
£10, Carr £3, V.A.T. £1.95. 'C' core 200  
M/H 8A size 9 x 8 x 7 in, £20, Carr £5,  
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**INDUSTRIAL AUTOMATIC 24 VOLT**  
20 AMP BATTERY CHARGERS  
AC input 240V 50Hz. DC output 24V  
20A. Built in steel case size 15 x 12 x  
9 1/2 ins. 6 x 4 3/4 x 4 in. £4.50. P&P £1.50  
plus VAT. Total £6.90.

**HIGH GRADE TRICKLE CHARGERS**  
Input 240V AC. Output 12v DC 2 Amps.  
With mains lead, red and black battery  
leads with attached clips. Completely  
fused. Housed in wall mounting, steel  
case size 6 x 4 3/4 x 4 in. £4.50. P&P £1.50  
plus VAT. Total £6.90.

**HEAVY DUTY LT TRANSFORMERS BY**  
FAMOUS MAKERS  
Pri 220-240V sec 70-0-70V 10A C core  
type top panel connections £20, carr £4,  
V.A.T. £3.60. Pri 115-220-240V sec 22.8V  
10.5A and 10V 14A open frame tag panel  
connections £10, P&P £2, V.A.T. £1.80.  
Pri 220-240V sec 40V 8A, 38V 4A, 20V  
4A, 30V 2.6A, 26V 2.6A 28V 2.5A, 51V 200  
M/A £15, carr £3. Pri 110-220-240V sec  
14V 5A, 14V 2/2A, 12V 10A, 8V 10A, 24V  
1A. Separate windings £10, carr £3,  
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**MONITORS**  
12 inch monochrome chassis, suit-  
able for use with home computer.  
Video input. Overall size of chassis  
16 x 14 x 14 ins, £45, V.A.T. £6.75.  
Callers only.

**SPECIAL OFFER OF ERIE**  
ELECTROLYTIC CAPACITORS  
22,000 MFD 63V DC WKG £4.50 inc.  
postage and V.A.T. 6800 MFD 100V DC  
WKG £2.50 inc. postage and V.A.T. 3600  
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and V.A.T. 10,000 MFD 16V DC WKG five  
for £2.50 inc. postage and V.A.T. 100  
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**AC WKG BLOCK CAPACITORS**  
15 MFD 250V AC WKG £2, 8-4 MFD 250V  
AC WKG £1.50, 5 MFD 440V AC WKG £2,  
2.5 MFD 360V AC WKG £1.7, MFD 0-1  
MFD 700V AC WKG £2, 1.5 MFD 370V AC  
WKG £1, 0.75 MFD 440V AC WKG 75p.  
0.25 MFD 1500V AC WKG £1, 4 MFD 250V  
AC WKG £1.

**SPECIAL OFFER VARIABLE**  
TRANSFORMERS  
Brand new, boxed, input 240V, out-  
put 0-265 volts, 5 amps. Base and top  
panel mounting with calibrated dial  
0-265 volts and control knob. Price  
£26 inc. carr. and VAT.

**ISOLATION TRANSFORMERS**  
Pri tapped 220-240V sec 240V 500 watts.  
Open frame type, top panel connections.  
Ex-equipment, but in perfect condition,  
£15, carr £3, V.A.T. £2.70.

**BENCO VARIABLE TRANSFORMERS**  
Type 42A panel mounting, input 240V,  
output 0-270V 2A with control knob. Ex-  
equipment, but in perfect condition, £10,  
P&P £2.50, V.A.T. £1.88.

**DC WKG BLOCK CAPACITORS**  
8 MFD 100V DC WKG £3, P&P £1, V.A.T.  
60p. 8 MFD 350V DC WKG £1.25, P&P  
50p, V.A.T. 25p. 6 MFD 350V DC WKG £1,  
P&P 50p, V.A.T. 22p. 4 MFD 500V DC  
WKG £1, P&P 50p, V.A.T. 22p. 2 MFD  
600V WKG 60p, P&P 20p, V.A.T. 12p. 1  
MFD 1000V DC WKG 60p, P&P 20p,  
V.A.T. 12p. 1 MFD 600V DC WKG 5 for  
£1.50, P&P 50p, V.A.T. 30p. 0.25 MFD  
500V DC WKG 5 for £1.25, P&P 50p,  
V.A.T. 15p. 0.1 MFD 1500V DC WKG 5 for  
£1.25, P&P 50p, V.A.T. 15p. 2 MFD 100V  
DC WKG ten for £1.50, P&P 75p, V.A.T.  
33p. Tubular metallised paper caps 20  
MFD 350V DC WKG with clip £3, P&P  
50p, V.A.T. 52p.

**LOW CURRENT LT**  
TRANSFORMERS  
Open frame clamp type, split  
bobbin. All primaries 240V No. 1  
sec tapped 12-15-20-24-30V 750  
M/A £4. No. 2 sec 9-0-9V 1A and  
6.3V 200 M/A £2.50. No. 3 15-0-15V  
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sec 12-0-12V 750 M/A and 6.3V 200  
M/A £4. No. 5 sec 13V 1/2A £1.50.  
No. 6 sec 8V 1/2A 6.3V 600 M/A, 6.3V  
300 M/A, 50V 40 M/A £2.50. No. 7  
sec 17V 1/2A (DC) £1.75. No. 8 sec  
16.5V 1/2A x 2 £1.75. No. 9 sec 18V  
2A £4. No. 10 sec 24V 2A £4.50. All prices  
include postage and V.A.T.

**SPECIAL OFFER LT**  
TRANSFORMERS  
Computer grade Pri 115-230V sec  
27V 10A, 9V 3A, 15V 0.5A, 15V 0.5A.  
175V 100 M/A. Separate windings,  
open frame type, top panel connec-  
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84p.

**LATEST PURCHASE, COMPUTER**  
GRADE T TRANSFORMERS. All Prima-  
ries 240V. No. 1 sec. 26V 6A £6.50, car-  
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P&P £2.  
No. 3 sec. 36V 6A £6.50, P&P £1.50.  
No. 4 sec. 43V 3A £4.75, P&P £1.50.  
No. 5 sec. 24V 2A £2.75, P&P £1.25.  
No. 6 sec. 27.5-0-27.5V 1.2A and 70-7V  
0.75A £3.50, P&P £1.25.  
No. 7 17V 1A £2 P&P £1.  
No. 8 13V 3A and 15V 1A £3.50, P&P  
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PLEASE ADD V.A.T.

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Plays 12", 10" or 7" records.  
Auto or Manual. A high  
quality unit backed by BSR  
reliability. Stereo Ceramic  
Cartridge, AC 200/250V. Size  
13 1/2 x 11 1/4 in. 3 speeds.  
Above motor board 3 1/4 in.  
Below motor board 2 1/4 in.  
Post £2 Board £1 extra



### HEAVY METAL PLINTHS Post £2

Cut out for most BSR or Garrard decks.  
Silver grey finish, black trim. Size 16x13 1/4 in.

£4

### DECCA TEAK VENEERED PLINTH. Post £1.50

Superior finish with space and panel for  
small amplifier. Board is cut for B.S.R.  
18 1/4 in. x 14 1/4 in. Black/silver face trim. Also with  
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13 1/4 x 12 x 2 1/4 in. £5 16 1/2 x 13 x 4 in. £6  
15 1/4 x 13 1/2 x 4 in. £6 14 1/2 x 13 1/2 x 2 1/4 in. £5  
17 x 12 1/2 x 3/4 in. £6 17 1/4 x 13 1/4 x 4 1/4 in. £6

Callers Only (not suitable for post)  
21 1/2 x 14 1/4 x 2 1/4 in. £6 21 x 13 1/2 x 4 1/4 in. £6  
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### BSR SINGLE

### PLAYER DECKS

### BSR P232 BELT DRIVE

### QUALITY DECK

Manual or automatic play.  
Precision ultra slim arm.  
Cueing device. Bargain price  
With stereo ceramic cartridge

£24

BSR P204 SPECIAL SINGLE PLAYER ideal for portable  
two-speed Hi-Fi system with ADC QLM30 stereo  
magnetic cartridge and cueing device. £24 Post £2

BSR ready cut mounting board. Only £1 extra.

### GARRARD 6-200 SINGLE PLAYER DECK

Brushed Aluminium Arm with stereo ceramic cartridge  
and Diamond Stylus, 3-speeds. Manual and Auto  
Stop/Start. Large Metal Turntable.  
Cueing Device.  
Ready cut mounting board £1 extra. £22 Post £2

### BATTERY ELIMINATOR MAINS to 9 VOLT D.C.

Stabilised output, 9 volt 400 m.a. U.K. made in plastic  
case with screw terminals. Safety overload cut out. Size  
5 x 3 1/4 x 2 1/2 in. Transformer Rectifier Unit. Suitable  
Radios, Cassettes, models, £4.50. Post 65p.

**DE LUXE SWITCHED MODEL STABILISED. £7.50. Post £1.**  
1, 3-6-7 1/2 volt 400ma DC max. Universal output  
plug and lead. Pilot light, mains switch, polarity switch.

**DRILL SPEED CONTROLLER/LIGHT DIMMER KIT. Easy to build kit.**  
Controls up to 480 watts AC mains, £3. Post 65p.  
**DE LUXE MODEL READY-BUILT 800 watts. Front plate fits**  
standard box, £5. Post 65p.

### EMI 1 1/2" x 8in. LOUSPEAKERS

Model 450, 10 watts R.M.S. with  
moving coil tweeter and two-way  
crossover; 3 ohm or 8 ohm.  
£9.50 Post £1.50. "Final Clearance".  
SUITABLE BOOKSHELF CABINET £6.50.



RELAYS. 12V DC £1.25. 6V DC 95p. 18V £1.25.  
BLANK ALUMINIUM CHASSIS. 6x4-£1.45; 8x6-£1.80;  
10x7-£2.30; 12x8-£2.60; 14x9-£3; 16x6-£2.90;  
16x10-£3.20. All 2 1/2 in. deep. 18 swg  
ANGLE ALI 6x3 1/4 x 3 1/4 in. 18 swg. 25p.

ALUMINIUM PANELS. 18swg. 6x4-45p; 8x6-75p;  
14x3-75p; 10x7-95p; 12x8-£1.10; 12x5-75p;  
16x6-£1.10; 14x9-£1.45; 12x12-£1.50; 16x10-£1.75.  
PLASTIC AND AL BOXES IN STOCK. MANY SIZES  
ALUMINIUM BOXES. 4x4x1 1/2 in. 4x2 1/2 x 2 1/2 in. 3x2x1 1/2 in.  
6x4x2 £1.60. 7x5x3 £2.40. 8x6x3 £2.50. 10x7x3 £3.  
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BRIDGE RECTIFIER 200V PIV 2a £1.4a £1.50. 8a £2.50.  
TOGGLE SWITCHES SP 30p. DPST 40p. DPDT 50p.  
RESISTORS. 100 to 10M. 1/4W, 1/2W, 1W, 1p. 2W 10p.  
HIGH STABILITY. 1/2w 2% 10 ohms to 1 meg. 8p.  
Ditto 5%. Preferred values, 10 ohms to 10 meg. 3p.  
WIRE-WOUND RESISTORS 5 watt, 10 watt, 15 watt 20p  
PICK-UP CARTRIDGES SONATONE 9TA £2.50. 9TAC £3.80  
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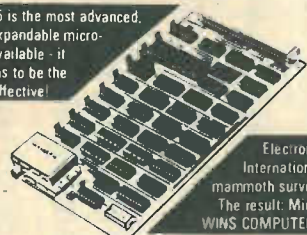
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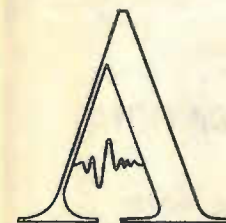
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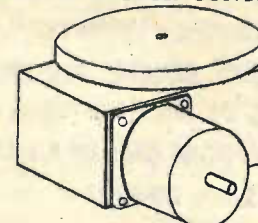
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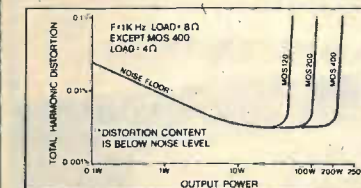
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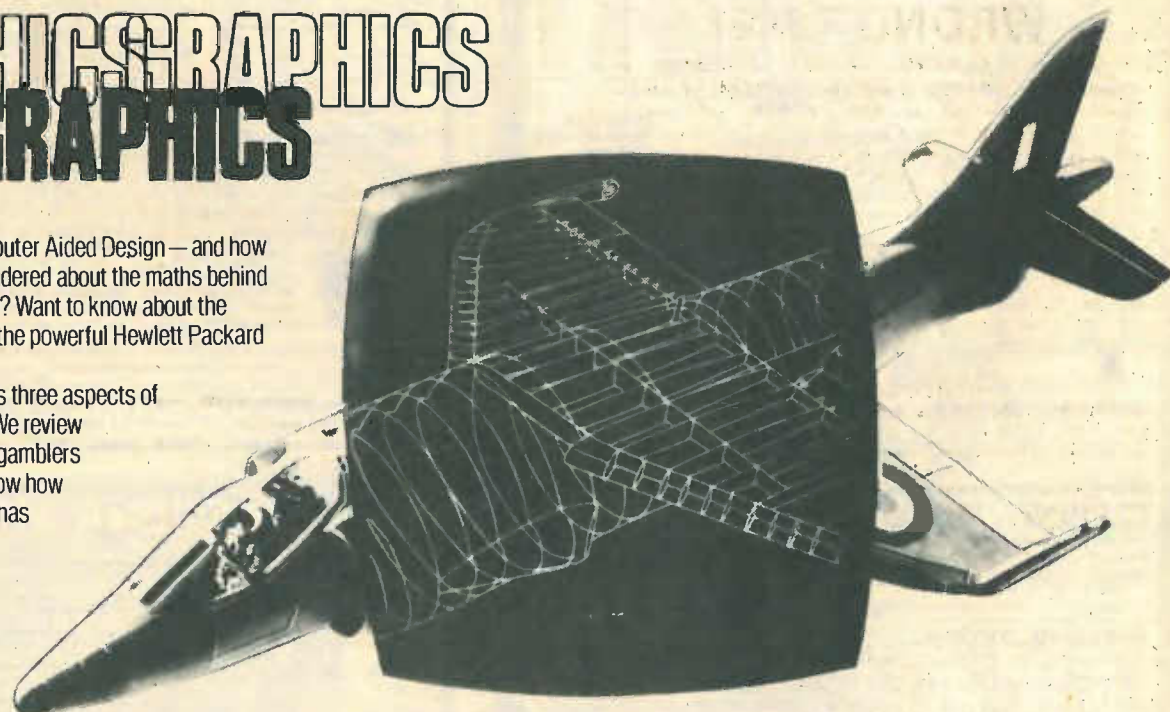
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What's going on in Computer Aided Design — and how will it develop? Ever wondered about the maths behind interactive 3-D graphics? Want to know about the graphics capabilities of the powerful Hewlett Packard HP-83?

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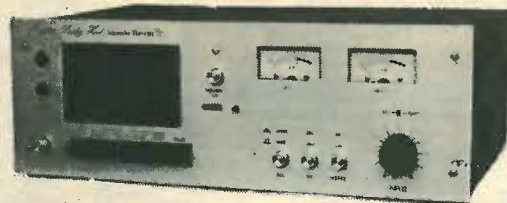
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# HART

## LINSLEY HOOD CASSETTE RECORDER 2



Our new improved performance model of the Linsley Hood Cassette Recorder incorporates our VFL 910 vertical front mechanism and circuit modifications to increase dynamic range. Board layouts have been altered and improved but retain the outstandingly successful mother-and-daughter arrangement used on our Linsley-Hood Cassette Recorder 1.

This latest version has the following extra features: Ultra low wow-and-flutter of .09% — easily meets DIN Hi-Fi spec. Deck controls latch in rewind modes and do not have to be held. Full Auto-stop on all modes. Tape counter with memory rewind. Oil damped cassette door. Latching record button for level setting. Dual concentric input level controls. Phone output. Microphone input facility if required. Record interlock prevents rerecording on valued cassettes. Frequency generating feedback servo drive motor with built-in speed control for thermal stability. All these desirable and useful features added to the excellent design of the Linsley-Hood circuits and the quality of the components used makes this new kit comparable with built-up units of much higher cost than the modest £24.90 + V.A.T. we ask for the complete kit.

## LINSLEY HOOD CASSETTE RECORDER 1



We are the Designer Approved suppliers of kits for this excellent design. The Author's reputation tells all you need to know about the circuitry and Hart expertise and experience guarantees the engineering design of the kit. Advanced features include: High-quality separate VU meters with excellent ballistics. Controls, switches and sockets mounted on PCB to eliminate difficult wiring. Proper moulded escutcheon for cassette aperture improves appearance and removes the need for the cassette transport to be set back behind a narrow finger trapping slot. Easy to use, robust Lenco mechanism. Switched bias and equalisation for different tape formulations. All wiring is terminated with plugs and sockets for easy assembly and test. Sophisticated modular PCB system gives a spacious, easily-built and tested layout. All these features added to the high-quality metalwork make this a most satisfying kit to build. Also included at no extra cost is our latest HS 16 Sendus Alloy super head, available separately at £8.20 but included free with the complete kit at £75 plus VAT.

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These latest designs from the drawing board of John Linsley-Hood, engineered to the very highest standard, represent the very best that is available on the kit market today. The delicacy and transparency of the tone quality enable these amplifiers to outperform, on a side-by-side comparison, the bulk of amplifiers in the commercial market-place and even exceed the high standard set by his earlier 75-watt design.

Three versions are offered, a 30-watt with Darlington output transistors, and a 35- and 45-watt, both with Mosfet output devices. All are of identical outside appearance which is designed to match and stack with our Linsley-Hood cassette recorder 2.

As with all Hart kits the constructors interests have been looked after in a unique way by reducing the conventional (and boring) wiring almost to the point of extinction.

Any of these kits represents a most cost-effective route to the very highest sound quality with the extra bonus of the enjoyment of building a sophisticated piece of equipment.

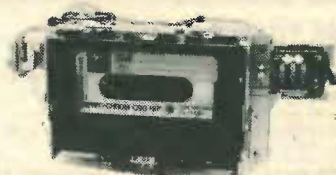
30-watt Darlington amplifier, fully integrated with tone controls and magnetic pick-up facility. Total cost of all parts is £81.12. Special offer price for complete kits £72.

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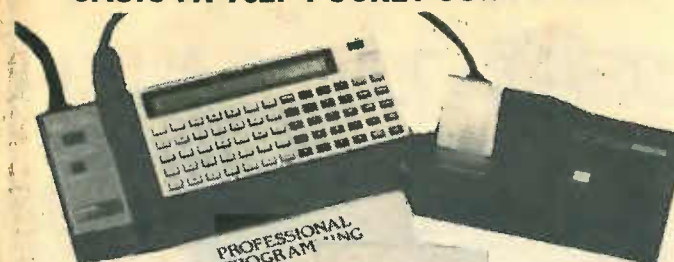
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Model No.	Module	What it does	Current required	Price inc. VAT	Price ex. VAT
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HY 9	Stereo pre-amp	Two channels, mag. cartridge, mic + volume control.	10 mA	£7.71	£6.70
HY 12	Mono pre-amp	Mixes two signals into one, with bass/mid-range/treble controls.	10 mA	£7.71	£6.70
HY 66	Stereo pre-amp	Two channels, with inputs for mic/mag. cartridge/tape/tuner/auxiliary, with volume/bass/treble/balance.	20 mA	£14.02	£12.19
HY 69	Mono pre-amp	Two input channels, mag. cartridge mic, with mixing and volume/treble/bass controls.	20 mA	£12.02	£10.45
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For easy mounting we recommend: B 6 mounting board for modules HY6-HY13 £0.90 inc. VAT. (0.78 ex. VAT.) B 66 mounting board for modules HY66-HY77 £1.12 inc. VAT. (0.99 ex. VAT.) All modules are encapsulated and include clip-on edge connectors. All operate from +15V minimum to +30V maximum, needing dropper resistors for higher voltages. Modules HY6 to HY13 measure 45 x 20 x 40mm. HY66 to HY77 measure 90 x 20 x 40mm.

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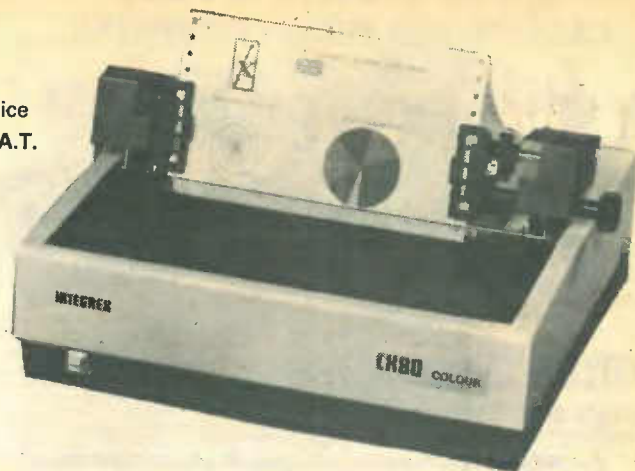


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HY 68	Stereo mixer	Two channels, each mixing ten signals into one.	20 mA	£9.14	£7.95
HY 74	Stereo mixer	Two channels, each mixing five signals into one — with treble and bass controls.	20 mA	£13.17	£11.45

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HY 73	Guitar pre-amp	Handles two guitars (bass and lead) and mic with separate volume/bass/treble and mix.	20 mA	£14.09	£12.25
HY 76	Stereo switch matrix	Provides two channels, each switching one of four signals into one.	20 mA	To be announced	
HY 77	Stereo VU meter driver	Programmable gain/LED overload driver.	20 mA	£10.64	£9.25

For easy mounting we recommend:  
B 6 mounting board for modules HY6, HY13 £0.90 inc. VAT. (0.78 ex. VAT.)  
B 66 mounting board for modules HY66, HY77 £1.12 inc. VAT. (0.99 ex. VAT.)  
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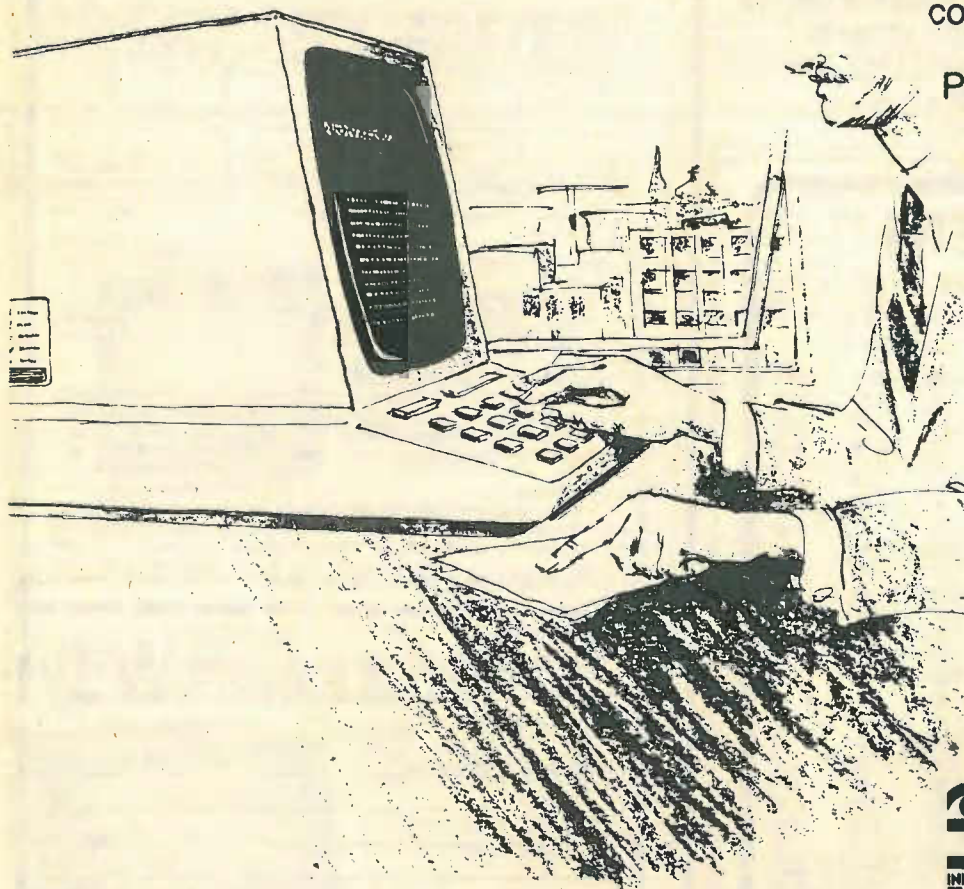
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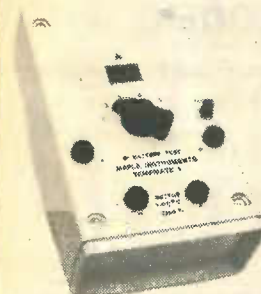
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7484 25p	74LS108 50p	4095 500p	LM339 85p	TA7310 160p
7485 25p	74LS109 50p	4096 500p	LM339 85p	TA7310 160p
7486 25p	74LS110 50p	4097 500p	LM339 85p	TA7310 160p
7487 25p	74LS111 50p	4098 500p	LM339 85p	TA7310 160p
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88	3 1.5	3.46	1.57	
85	5 2.5	6.06	1.57	
70	6 3	6.67	1.57	
108	8 4	8.03	1.57	
116	12 6	9.31	2.10	
17	16 8	11.46	2.25	
115	20 10	13.69	2.25	
187	30 15	19.23	2.55	
232	40 20	27.61	5.00	
226	60 30	35.35	4.50	

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126	2 1	6.36	1.57	
127	4 2	7.86	1.90	
125	6 3	11.78	2.10	
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40	10 5	17.10	2.40	
120	12 6	19.44	2.55	
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122	20 10	32.05	4.50	
189	24 12	37.02	5.50	

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TYPE	AMPS	PRICE	P/P	
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431	2 1	7.84	1.57	
432	4 2	12.94	2.25	
433	6 3	14.62	2.40	
434	8 4	20.04	2.70	
435	10 5	28.76	2.90	
436	12 6	36.16	4.50	
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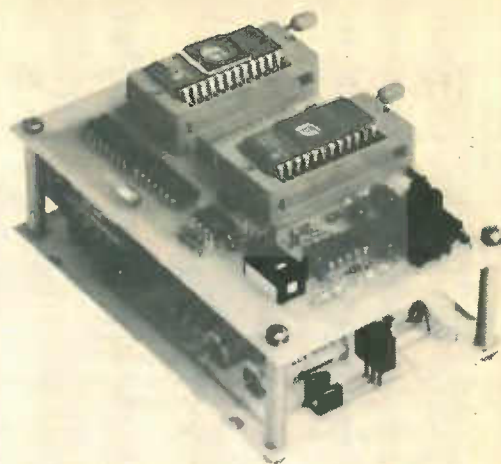
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417C	200	4.00	1.20	
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25/50V RANGE PRI 120/220/240V				
TYPE	AMPS	PRICE	P/P	
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79	2 1.0	3.29	1.20	
3	4 2	6.18	1.57	
20	6 3	7.19	1.90	
21	8 4	8.52	1.90	
51	10 5	10.57	2.10	
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6VA	
24V	1.50
12VA	
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18VA	
9-0-9	2.64p
24VA	
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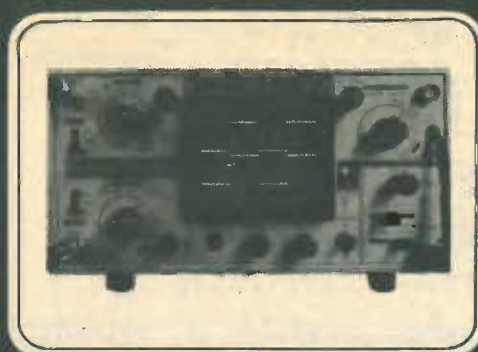
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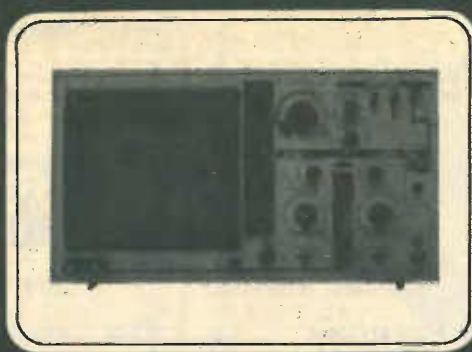
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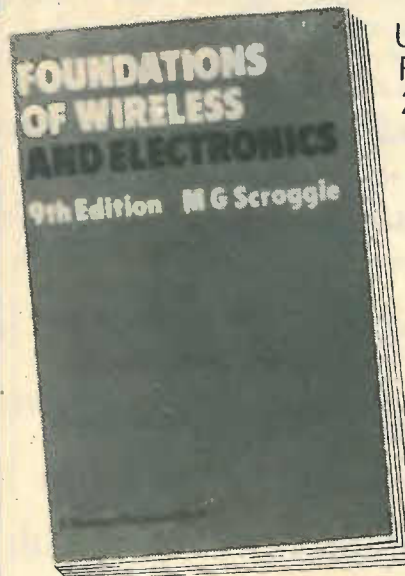
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A complete I/O terminal with integral 8 hole paper tape punch and reader, full ASCII keyboard, 120 column printer, and control unit. The printer is capable of 150 baud with a serial TTL or balanced input-output sold in good overall condition but untested. Complete with circuit unguaranteed. Connect direct to your micro at ONLY £99.00 + £11.50 carr + vat.

## MPU EXPERIMENTORS +5v, 12v, 12v, 24v POWER SUPPLY

Once again we are very pleased to offer this superb Power Supply Unit, and hope to satisfy most of our previous customers who were disappointed when we sold out due to demand last time they were advertised!! These units may just have well been made for your lab., they consist of a semi-enclosed chassis measuring 160mm x 120mm x 350mm containing all silicon electronics to give the following fully regulated and short circuit proof outputs of:

+5v @ 2 amps DC +12v @ 800 ma DC  
+12v @ 800 ma DC +24v @ 350 ma DC

and if that's not enough a fully floating 5v output @ 50 ma DC which may be seriesed to give a host of other voltages. All outputs are brought out to the front panel via miniature jack sockets and are also duplicated at the rear on short flying leads. Units accept standard 240v mains input. They are ex GPO and may have minor scratches on the front panels, they are sold untested but in good internal condition. £16.50 each + £2.50 p+p complete with circuit and component list. Transformer guaranteed. HURRY WHILE STOCKS LAST!!

## HIGH SPEED DATA MODEMS

A superb piece of engineering made by SE Labs Ltd. to a "no cost spared" spec for the GPO, the Modem 12 is a synchronous Modem for use on DATEL 2412 services, or other data links. Many features include switchable V28 modulation, 2400 baud full duplex 600/1200 standby, auto answer, 4 wire or 2 wire operation. Self test, LED status indication, CMOS technology, modular construction, original cost over £700 each. Believed brand new, supplied complete with PSU etc. £185.00 + £9.50 carriage + VAT.

\*Permit on may be required for connection to PO lines

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"Dial our Database!" Get information on 1000's of stock items and order via your computer. 300 baud on 01-689 6800 18.30 to 0900 6 days a week and all day Sundays. IT'S FREE!

## DIABLO S30 DISK DRIVES

Another shipment allows us to offer you even greater savings on this superb 2.5 MB (formatted) hard disk drive. Two types are available both fully refurbished and electronically identical, the only difference is the convenience of changing the disk packs.

S30 front loader, pack change via front door £550 + vat

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+ & - 15v PSU for 2 drives £125 + vat

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## MAINS FILTERS

Professional type mains filters as used by "Main Frame Manufacturers" ideal for curing those annoying hang ups and data glitches, fit one now and cure your problems! Success on Devices SD5 A10 5 amp £6.95 Success on Devices SD5 A10 5 amp £6.95

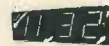
Comum Inc F1900 30 amp £13.95 + pp £1.00

## DC SYSTEM SUPPLY

Professional fully cased fan cooled system supply. Standard 240V ac input with the following DC outputs 5V @ 11 amps +15-17v @ 8amps, -15-17v @ 8amps and +24v @ 4 amps. All outputs are fully crowbar protected and the 5 volt output is fully regulated. Sold tested and in a new or little used condition complete with circuit £55.00 + car £8.50 + vat DIM 15.5" x 9" x 6"

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★ ALARM  
★ 50/60 HZ



The same module as used in most ALARM/CLOCK radios today, the only difference is our price! All electronics are mounted on a PCB measuring only 3" x 1 1/2" and by addition of a few switches and 5/16 volts AC you have a multi function alarm clock at a fraction of cost. Other features include snooze timer, am pm, alarm set, power fail indicator, flashing seconds cursor, modulated alarm output etc. Supplied brand new with full data only Suitable transformer £1.75. £5.25

## ELECTRONIC COMPONENTS & EQUIPMENT

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Due to our massive bulk purchasing programme which enables us to bring you the best possible bargains, we have thousands of I.C.'s, Transistors, Relays, Cap's, P.C.B.'s, Sub-assemblies, Switches, etc. etc. surplus to our requirements. Because we don't have sufficient stocks of any one item to include in our ads., we are packing all these items into the "BARGAIN PARCEL OF A LIFETIME" Thousands of components at giveaway prices! Guaranteed to be worth at least 3 times what you pay plus we always include something from our ads. for unbeatable value!! Sold by weight

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SAVE OVER £1400

## THE PRINTER SCOOP OF THE YEAR THE LOGABAX Z80 MICROPROCESSOR CONTROLLED LX180L MATRIX PRINTER



A massive bulk purchase enables us to offer you this superb professional printer at a fraction of its recent cost of over £2000. Utilising the very latest in microprocessor technology, it features a host of facilities with all electronics on one plug in P.C.B. Just study the specification and you will instantly realise it meets all the requirements of the most exacting professional or hobbyist user.

STANDARD FUNCTIONS ★ Full ASCII character set ★ Standard ink ribbon ★ RS232C/V24 serial interface - 7 x bit controlled baud rates up to 9600 ★ 194 characters per line ★ Parallel interface ★ Handshakes on serial and parallel ports ★ 4 Type fonts, italic script, double width, italic large, standard ★ Internal buffer ★ Internal self test ★ 170 CPS ★ Variable paper tractor up to 17.5" wide ★ Solid steel construction ★ All software in 2708 eeproms easily reconfigured for custom fonts etc. All this and more, not refurbished but BRAND NEW At Only £525 +VAT

OPTIONAL EXTRAS\* lower case option £25.00\* 16k buffer £30.00\* Second tractor for simultaneous dual forms £85.00\* Floor stand £45.00\* specialist carriage £19.00 All items plus VAT data sheet on request.

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Unbelievable value the DRE 7100 & 7200 8" disk drives utilise the finest technology to give you 100% bus compatibility with most drives available today, the only difference being our PRICE and the superb manufacturing quality. The 7100 single sided & 7200 double sided drive accept hard or soft sectoring IBM or ANSI standard giving a massive 0.8 MB (7100) & 1.6 MB (7200) of storage. Absolutely SHUGART, BASF, SIEMENS etc compatible. Supplied BRAND NEW with user manual and 90 day warranty.



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Unless otherwise stated all prices inclusive of V.A.T. Cash with order. Minimum order value £20.00 Prices and Postage quoted for UK only. Where post and packing not indicated please add 80p per order. Bona Fida account orders minimum £20.00. Export and trade enquiries welcome. Orders despatched same day where possible. 3% surcharge on Access and Barclaycard orders.

## SOFTY 1 & 2 EPROM BLOWER

Software development system invaluable tool for designers, hobbyists, etc. Enables open heart surgery on 2716, 2708 etc. Blows, copies, reads EPROMS or emulates EPROM/ROM/RAM in situ whilst displaying contents on domestic TV receiver. Many other features. £115 + carr. + VAT. Optional 2716, 2716 Function Card £40 + VAT. PSU £20 + £1.50 carr. + VAT.

Softy 2 for 2716/2732 £169 + VAT

Write of phone for more details.

## 9" VIDEO MONITORS

Ex-equipment 9" Motorola Video Monitors 75Ω composite input, tested but unguaranteed. £39.99 + £7.50 carriage + VAT. Complete with circuit.

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## RCA FULLY CASED ASCII CODED KEYBOARDS



IDEAL - TANGERINE, OHIO ETC.

Straight from the U.S.A. made by the world famous R.C.A. Co., the VP600 Series of cased freestanding keyboards meet all requirements of the most exacting user, right down to the price! Utilising the latest in switch technology. Guaranteed in excess of 5 million operations. The keyboard has a host of other features including full ASCII 128 character set, user definable keys, upper/lower case, rollover protection, single 5V rail, keyboard impervious to liquids and dust, TTL or CMOS outputs, even an on-board tone generator for keypress feedback, and a 1 year full R.C.A. backed guarantee.

VP601 7 bit fully coded output with delayed strobe, etc. £43.95  
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Plug for VP606, VP616 £2.10  
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ORDER NOW OR SEND FOR DETAILS.

## 5v D.C. POWER SUPPLIES

Following the recent "SELL OUT" demand for our 5v 3 amp P.S.U. we have managed to secure a large quantity of ex-computer systems P.S.U.'s with the following spec.: 240 or 110v A.C. input. Outputs of 5v @ 3-4 amps, 7.2v @ 3 amps and 6.5v @ 1 amp. The 5v and 7.2v outputs are fully regulated and adjustable with variable current limiting on the 5v supply. Unit is self contained on a P.C.B. measuring only 12" x 5" x 3". The 7.2v output is ideal for feeding "on board" regulators or a further 3 amp LM323K regulator to give an effective 5v @ 7 amp supply. Supplied complete with circuit at only £10.95 + £1.75pp. Believed working but untested, unguaranteed.

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(1471)

## BBC TRANSMITTER DEPARTMENT MOTSPUR PARK, SURREY

## ENGINEER, VACUUM DEVICES

£8,950-£10,924 p.a.  
(according to qualifications and experience)

We require an Electronic Engineer, with C.Eng/de-gree/HNC qualification, plus a minimum of three years' post-qualification experience in the design, manufacture or application of vacuum devices used in broadcast transmission.

Specific areas of involvement and responsibility include the application, acquisition, testing and distribution throughout the BBC of all types of vacuum devices and in particular the valves and klystrons, etc., used at transmitting stations, computerised stock control and in staff management.

Relocation expenses will be considered and benefits include 5 weeks' annual holiday. Men and women are equally eligible to apply.

Requests for application forms to The Engineering Recruitment Officer, BBC, Broadcasting House, London W1A 1AA, quoting reference 82.E.1140/WW and enclosing an addressed envelope at least 9" x 4".



(1544)



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(291)

Sound Attenuators Limited require an

## Electronics Engineer

to work on the active control of sound in ducts. We require a graduate in electronics with at least two years' practical experience and an interest in acoustics. The project involves the implementation of basic research already undertaken at the University of Essex. The successful candidate must demonstrate self-reliance, practical ability and a keen interest in seeing the project through to a successful conclusion.

Write in the first instance, enclosing a full c.v. to:

Mr. A. T. Fry  
Sound Attenuators Ltd.  
Eastgates, Colchester, Essex  
Tel: 0206 866911

(1566)



## Systems Design Team

### Satellite Communications Ground Terminals

Marconi Space and Defence Systems are Europe's acknowledged leaders in the development of advanced systems for aerospace and satellite communications.

To meet the growing interest in satellite communications we are strengthening the specialist teams working on sophisticated satellite ground terminals - offering total involvement from initial design and development through to implementation.

We need ambitious and enthusiastic men and women with several years' post-graduate experience in the design, development or operation of ground terminals or in other communications systems drawing on similar RF techniques. A knowledge of military satellite communications would be a distinct advantage.

### SYSTEMS MANAGER

Aged 30+ must be able to combine high level technical expertise with the man-management skills necessary to weld a group of systems professionals into a closely knit team. At least 4 years' experience in a similar role is essential.

### SENIOR SYSTEMS ENGINEERS

Applicants should be in their late 20's to early 30's and have had relevant in-depth experience.

### SYSTEMS ENGINEERING

We have a number of openings for graduates in their mid-20's with an Honours degree in Engineering, Physics or Mathematics and 1-2 years' post-graduate experience.

These are key career positions carrying salaries that fully reflect their importance, as well as an attractive range of benefits, including relocation assistance where necessary.

To discuss any of these posts with one of our senior specialists or project managers, telephone Bill Seton, Ext. 18, or Liz Kahn, Ext. 22, on (01) 954 2311 or write to them at Marconi Space and Defence Systems Ltd., The Grove, Warren Lane, Stanmore, Middlesex, HA7 4LY.

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Space & Defence Systems



1546

CAMBRIDGE HEALTH DISTRICT  
(TEACHING)  
PHYSICS DEPT.  
ADDENBROOKE'S HOSPITAL  
HILLS ROAD, CAMBRIDGE

### Medical Physics Technician Grade II (£6,668-£8,316 p.a.)

An electronics technician is required to provide maintenance and support services to the CT Head Scanner at Addenbrooke's Hospital and to electro-medical equipment in the thoracic surgical unit, Papworth Hospital. Applicants should hold an appropriate HNC or equivalent qualification and have several years' experience in the field of electronics. (Mini-computer experience advantageous).

For further details contact  
Mr P. E. Ward,  
Principal Physics Technician  
Addenbrooke's Hospital  
Hills Road, Cambridge CB2 2QQ  
Tel: (0223) 245151 ext. 471

Application form and job description from the Personnel Dept. Ext. 7350.

### INSTITUTE OF PSYCHIATRY AUDIO-VISUAL TECHNICIAN

A vacancy exists for an Audio-Visual Technician at this postgraduate medical school and associated teaching hospital. Applicants should be experienced in maintenance of television equipment and preferably hold relevant technical qualifications: eg City & Guilds Course 222 or 224.

Salary according to experience and qualifications on Whitley Council Medical Laboratory Scientific Officer scale currently £4,958 p.a. to £6,993 p.a. plus London Weighting £932 p.a.

For application form with job description please write to the Assistant Secretary, Institute of Psychiatry, De Crespigny Park, Denmark Hill, London SE5 8AF or telephone 703 5411 Ext 214 quoting reference MJC/WW.

(1547)

### LEADING LONDON ADVERTISING AGENCY Requires

### YOUNG ENGINEER

to take charge of all in-house audio and video equipment including Rank Cintel, Sony 1" and studio cameras. Applicants should send details of experience to Box 1552, c/o Quadrant House, The Quadrant, Sutton, Surrey SM2 5AS.

(1552)

### THE THOMSON FOUNDATION TELEVISION COLLEGE

### An ENGINEERING LECTURER and an ASSISTANT ENGINEERING LECTURER

are required at the College to join a team of staff training engineers from developing countries, in Television and Radio. The successful candidates will have had a minimum of 5 years' or 3 years' experience respectively and broadcasting technology, and will hold an appropriate degree, HND or equivalent.

Salaries: Lecturer - £9,251 by 5 increments to £11,504; Asst. Lecturer - £7,284 x 5 to £9,052.

The posts are pensionable, based at Glasgow where the residential training is conducted, but involve also short assignments abroad each year for in-country training.

Please write or phone for application form to Principal, Thomson Foundation TV College, Kirkhill House, Newton Mearns, Glasgow G77 5RH (041-639 1021).

(1545)

## Electronics - up to £7,500

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We are:

- \* a leading pharmaceutical company with world-wide interests.

You will:

- \* help to design, modify and where necessary repair advanced scientific instruments and computers in the Physical Chemistry Department.

Probably in your 20's, male or female, you should ideally have:

- \* formal training up to HNC or equivalent
- \* an interest in scientific measurement techniques
- \* sound practical experience of electronics.

We offer:

- \* a competitive salary dependent upon experience and ability
- \* day release opportunities for further study
- \* Flextime working
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Interested? For an application form please ring our automatic telephone answering service on 01-650 6541, giving your name, address and quoting reference no. WRL/176.

Alternatively write to A. G. Murdoch, Personnel Officer, The Wellcome Research Laboratories, Langley Court, Beckenham, Kent BR3 3BS.



**Wellcome**

(1543)



## THEATRE PROJECTS

Theatre Projects Special Projects Group is a manufacturing company specialising in audio and lighting control equipment for the broadcast, film and theatre industries. We are currently seeking the following additional staff to provide a firm foundation for expansion.

### PRODUCTION MANAGER

Must be capable of planning and controlling all aspects of a mixed batch production/custom manufacturing environments often working to tight schedules. Will be required to plan and set up own manufacturing facility when we shortly move to new premises. The applicant should have 2-3 years in a related area of responsibility and be educated to HNC or degree standard. He/She will be part of the core of management of this part of the company.

### PROJECT ENGINEER - BROADCAST AUDIO SYSTEMS

The candidate must have prior engineering design experience and a broad understanding of the design/manufacturing environment. He/She will be required to oversee projects from concept/quotation through to installation. Specialist skills in either circuit or mechanical design are required along with familiarity of the broadcast environment. Both posts command a salary from £7,500, which is negotiable according to the skills and experience of the applicants.

Theatre Projects, 11 Marshalsea Road, London SE1. Tel: 01-403 3838

(1562)

## REPORTER/STAFF WRITER MIDDLE EAST ELECTRONICS

An enthusiastic journalist, ideally with technical qualifications (Degree or HND) and experience, to work on Middle East Electronics.

This magazine, which is going monthly in May, is read by senior electronic engineers in the Middle East and the Editor is looking for a responsible number two to develop the journal's potential.

Writing and subbing skills essential plus knowledge of the industry and, preferably, experience of developing countries and their technology problems. Computer science background an advantage.

Our UK office is located in Morden, Surrey, but we offer opportunities for travel. Salary £7,613 p.a. (subject to NUJ consultation).

For an application, please write to, or telephone Ray Ashmore, Editor, Middle East Electronics, Crown House, London Road, Morden, Surrey. Tel: 01-543 3051.

Salary and conditions is accordance with the IPC/NUJ agreement.

We are an equal opportunity employer.

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(1563)

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**SENIOR DIGITAL DESIGNER.** Applied research in radar signal processing. Experience high speed real-time HW/SW. Knowledge of bitlice processors, array multipliers useful. To £11,000. South Coast.

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Please telephone or send c.v. to:

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Tel. 01-486 9250

"PROBABLY THE BEST KNOWN SUPPLIER OF ELECTRONIC ENGINEERS IN THE COUNTRY" Financial Times (1357)



## Research & Development Engineers

### YOUR OPPORTUNITY TO ADVANCE BROADCAST TELEVISION TECHNOLOGY

Tremendous growth and success has resulted in career opportunities at Sony Broadcast Ltd., a company established four years ago to specialise in the high technology field of broadcast television equipment. The Advanced Developments group is part of an international R&D team committed to pioneering new technology. Applications are invited from experienced engineers capable of contributing to one or more of the following activities:

- Digital Video Systems
- Digital Audio Systems
- Audio/Video Digital Recording
- Mathematical Modelling
- Computer Control Systems
- Microprocessor Applications
- Analogue Video Development

The successful candidates will join one of the following sections:

#### Research & Development

Established as a world leader pioneering digital recording, we are currently extending our range of activities. The R/D team is responsible for studying the development and application of digital video and audio processing techniques. In addition increasing support is required from theoretical studies and computer simulation.

#### Special Design Projects

Increasing use is being made of computer and microprocessor based equipment for signal routing and control in studio centres. Hardware/Software engineers are required for the development of automated broadcasting and remote control systems. This can include "one off" developments designed to customer requirements.

Appointments will be made at all levels and applicants should have an honours degree or equivalent qualification. Attractive salaries are offered together with first class conditions of employment and relocation assistance will be given where appropriate.

If you are interested contact:-

**Mike Jones,**  
Senior Personnel Officer



#### Sony Broadcast Ltd.

City Wall House  
Basing View, Basingstoke  
Hampshire RG21 2LA  
United Kingdom  
Telephone (0256) 55 0 11

(1540)

## An Electronics Engineer

Is needed to make original contributions within a lively internationally collaborative space sciences programme. The post will be concerned initially with a magnetospheric sounding satellite and will include some travel to Germany and the U.S.A.

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The appointment, at Professional and Technical Officer Grade II level, attracts a starting salary between £6,557 and £7,520, with increments to £8,697.

Some assistance with the expenses incurred in house sales/purchase may be available.

The Laboratory, situated 18 miles south of Oxford, offers excellent working conditions. Benefits include an extensive bus system, generous holidays and sickness leave and a non-contributory superannuation scheme.

Apply by phone or letter to Lorna Bird,  
Ext. 510, quoting VN0 17.  
Closing date: 8th April 1982.

Science and Engineering Research Council  
**Rutherford Appleton Laboratory**  
Chilton, Didcot, OXON. OX11 0QX. Telephone Abingdon 21900.

(1548)

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with PDP 11/34 and RSX experience to work on Software to 0521 standards. To £10,000 - Hants.

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To carry out field maintenance on Business Computer Systems. To £10,000 + car - London.

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To control the development of Industrial Process Control Systems. To £11,500 - Bucks.

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### R.F. DESIGN ENGINEER

to lead the development of a new Low Power Transmitter. To £10,000 - Yorks.

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with R.F. Micro Wave, Analogue, Digital or Software experience to work on new Instrumentation Systems. To £11,000 - Herts.

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Windsor (07535) 57818/58022  
24-hour service (1119)

## CLIVEDEN

## CHARING CROSS HOSPITAL MEDICAL SCHOOL (University of London) MEDICAL PHYSICS TECHNICIAN

An enthusiastic person is required in the Department of Anaesthesia in Charing Cross Hospital Medical School.

Work involves a full range of physiological measurements on patients in the operating theatres and intensive Care Unit, and maintenance of equipment.

Assistance will also be required in the development of instrumentation for measurements and techniques in the cardiovascular, respiratory and electrophysiological fields.

The successful candidate should be qualified in at least one of these fields and show an interest and willingness to learn about the others.

An aptitude for meeting the many demands that working in a small team places on the individual will also be sought.

Salary will be within the range of £4,958-£6,993 per annum plus £859 London Weighting Allowance, according to qualifications and experience.

Applications on forms obtainable from The Secretary, Charing Cross Hospital Medical School, The Reynolds Building, St. Dunstan's Road, London W6 8RP (tel: 01-748 2040 ext 2067) within three weeks of the appearance of this advertisement.

(1533)

## DIGITAL EXPERIENCE? FIELD SUPPORT R & D AND SALES VACANCIES IN COMPUTERS NC, COMMS., MEDICAL VIDEO, ETC.

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01-290 0267

## LOGEX

ELECTRONICS RECRUITMENT SERVICE  
LOGEX HOUSE, BURLEIGH, STROUD  
GLOUCESTERSHIRE GL5 2PW  
TEL. 0453 883264, 01-290 0267 (321)

WIRELESS WORLD APRIL 1982

## Electronics Engineers

Glaxo have the following opportunities at their Research Central Services Unit at Greenford, which is involved in the design and maintenance of electronic equipment needed for experimental work:

### ELECTRONICS DESIGN ENGINEER

£6705 pa to £9475 pa

to carry out design work on a wide range of laboratory equipment employing analogue, digital and microprocessor techniques. Candidates, aged 25+, should be qualified to degree level or equivalent with several years general design experience.

### SERVICE TECHNICAL OFFICER/ENGINEER

£5874 pa to £9210 pa

to be responsible for general servicing work. Candidates, qualified to Higher National Certificate or City & Guilds Full Technical standard should have several years experience of analogue and digital equipment, preferably in a laboratory environment.

Starting salaries will be between the figures quoted which include London Allowance and will reflect qualifications and experience.

In addition the Company operates a bonus scheme and non-contributory pension scheme. Assistance with relocation expenses will be available in appropriate cases.

Please write or telephone for an application form to: Miss E. M. Butler, Personnel Department, Glaxo Group Research Limited, Greenford Road, Greenford, Middlesex UB6 0HE. Tel: 01-422 3434, ext. 2707 quoting reference number ZH/418.

# Glaxo Group Research Ltd.

(1541)

## GWENT HEALTH AUTHORITY

## ELECTRONIC AND BIO-MEDICAL EQUIPMENT MAINTENANCE TECHNICIAN GRADE II

This is an established post offering wide scope and opportunity in the development of electronic and bio-medical services. The successful candidate will be responsible to the Area Engineer for the testing and maintenance of a variety of electronic and bio-medical equipment throughout the area, and will also be responsible for the development of policy regarding maintenance contracts.

The technician will be based at a purpose-built workshop at Allt-Yr-Yn Hospital, Newport, and will be responsible for an establishment of two junior grade technicians, but authority has been given for the further development of this service.

Applicants should be in possession of ONC/HNC (or equivalent qualifications) in Electrical/Electronic Engineering, and should have wide experience of Health Service electronic equipment and safety aspects involved. In addition to these requirements, the applicant should be capable of preparing reports and be able to develop and operate a planned preventive maintenance scheme.

Hours: Normally 38 per week.

Salary: £6,668-£8,316

Application form and job description are available from:

The Area Personnel Department  
Mamhilad, Pontypool, Gwent

Closing date: 31.3.82

(1559)

## TRAINEE BROADCAST ENGINEERS

ITN needs more engineers to support its expanding programme of news coverage - expansion which is expected to continue through the 80s with the development of the Channel Four news service.

We have a number of vacancies for Engineering Trainees, vacancies which could give you the opportunity to start a career in Broadcasting Television Engineering with ITV.

First, we need you to have a firm interest in pursuing a career in the technical branch of broadcasting.

Then you should have completed, or expect this year to complete, theoretical training in Electronic Engineering with a bias towards Television or Audio applications. Qualifications most suitable are T.E.C. Higher Technical Diploma, T.E.C. Higher Technical Certificate or the HND/HNC equivalent.

Initially, you would be involved in a 9-12 month familiarisation period by a rotational attachment to our four maintenance areas and the Projects Department.

After successful training you would be employed on the maintenance or operation of a wide range of broadcast equipment in our Central London Studios near Oxford Circus, from which the ITN national news programmes are networked.

Successful applicants will join ITN in early September, 1982. Starting salaries would lie within the range of £5,120 (at 18) rising to £6,472 at age 20.

If you have the qualifications and the drive to work with us in a busy, lively environment then call us on 01-637 8644 ext 275 or write to

The Manager, Technical Training  
ITN House  
48 Wells Street  
London W1P 4DE

for an application form quoting reference 476099

(1532)



## Appointments

# Electronics R&D

£8,589

Join us in the forefront  
of technology

## Senior Engineer — Broadcast Video Equipment

**A challenging role in high technology  
Quality Assurance**

Due to significant continued expansion, an excellent opportunity has arisen at the international headquarters of Sony Broadcast, a world leader in professional broadcast television equipment. The Company has an expanding range of high technology products which includes video cameras, VTRs, editing control systems, digital time base correctors and monitors.

An experienced engineer is required to join the Quality Assurance team and assume responsibility for the throughput of cameras and other products. Activities will include close liaison with other engineering departments and will necessitate working to stringent specifications. A knowledge of current camera measurement practices would be advantageous.

Age 25+ applicants should be educated to at least HNC Electronics and have several years engineering experience. The position would suit a self starter who also has the ability to lead and motivate a small team. Prospects for career development are considerable.

We offer a first class working environment in our new prestigious engineering complex, together with an attractive salary and excellent conditions of employment, which include Company pension/life assurance schemes, private medical cover and staff restaurant.

*If you are interested please write, giving details of experience and present salary, to Mike Jones, Senior Personnel Officer.*

**Sony Broadcast Ltd.**  
City Wall House  
Basing View, Basingstoke  
Hampshire RG21 2LA  
United Kingdom  
Telephone (0256) 55 0 11  
1529

# HF-VHF-UHF and Microwave

**A challenging and full career in  
Government Service**

Candidates, normally aged under 30, should have a good honours degree or equivalent in a relevant subject, but any candidates about to graduate may be considered.

Appointments as Higher Scientific Officer (£6,530-£8,589) or Scientific Officer (£5,176-£6,964) according to qualifications and experience. Promotion prospects.

Please apply for an application form to the Recruitment Officer (Dept WW 4.82), H M Government Communications Centre, Hanslope Park, Milton Keynes MK19 7BH.

1425

## Communications Proposals Engineer to £10,500

Join the UK's leading Communications System House specialising in oil field locations. Palmer EAE require a Proposals Engineer with a broad experience of Multi-Channel Microwave links, P.A. and entertainments systems, standby power supplies, SOLAS and telephone plant. Applicants should be educated to HNC/DEGREE standard and be familiar with recognised international standards, i.e., C.C.I.R., C.C.I.T.T., etc. Duties will include preparing technical proposals, procurement specifications and procedures relating to installation/commissioning.

This post is based in Great Yarmouth and occasional overseas travel will be required. Excellent terms and conditions are offered including pension scheme, BUPA, relocation expenses, etc.

There are also a number of vacancies for suitably qualified COMMUNICATIONS ENGINEERS and TECHNICIANS to work both in the UK and overseas.

For further information regarding these opportunities on an application form for the post of Communication Proposals Engineer, please telephone:

**Mike Futter on Great Yarmouth (0493) 58541**  
Palmer EAE Limited, Offshore House, Gt. Yarmouth, Norfolk

**PALMER EAE**  
(1560)

## TECHNICAL/SERVICE MANAGER

Due to the expansion of our business we are urgently seeking a person capable of setting up and running a pager service department, of maintaining transmitters and of evaluating and commissioning both paper and mobile systems. This is an exciting position in an established company and will appeal to the person who has technical experience and wishes to become involved also in the commercial side of a company with expansion plans for the future. A high salary, car and other benefits are available for the right person.

Send CV to: P. Sinnot, Managing Director  
Pageboy Services (UK) Ltd., Westley House  
Trinity Avenue, Bush Hill Park, Enfield  
EN1 1HP. Tel. 01-367 4545  
(1516)

## CAPE WARWICK LTD.

require

## Electronics, Control & Instrumentation Engineers

As an expanding independent testing laboratory we require suitably qualified/experienced engineers to design, arrange, manufacture, commission and maintain test equipments.

Send c.v. or telephone for application forms to:

**Mrs. E. Archer**  
Cape Warwick Ltd.  
Cape Road, Warwick  
Warks CV34 5DL  
Tel: Warwick (0926) 496421  
(A Thomas Tillings Company)

(1535)

UNIVERSITY COLLEGE CARDIFF  
FACULTY OF SCIENCE

## ASSISTANT EXPERIMENTAL OFFICER

Applications are invited for the post of Assistant Experimental Officer in the faculty of science electronics workshop. Duties will include the design, development and maintenance of electronic equipment, particularly microprocessors for both research and teaching.

Applicants should have a degree in electronics or related subject or an equivalent qualification. Experience in microprocessor interfacing techniques and electronic instrument design would be an advantage.

Salary scale O.R. 18 £5,285-£8,925

Applications to the Vice Principal (Administration) and Registrar, University College Cardiff, P.O. Box 78, Cardiff, from whom further particulars may be obtained.

Closing date 2nd April. Ref. No. 2348a.  
(1556)

## APPOINTMENTS IN ELECTRONICS to £15,000

**MICROPROCESSORS  
COMPUTERS — MEDICAL  
DATA COMMS — RADIO**

Design, test, field and support engineers — for immediate action on salary and career advancement, please contact

**Technomark**  
Engineering and Technical Recruitment

11, Westbourne Grove  
London W2. 01-229 9239 (9257)

**HARROW COLLEGE OF HIGHER EDUCATION.** Audio-visual Aids Technician. Salary to maximum of £6,009 p.a. To supervise the closed-circuit television studio in the Educational Resources Centre and to assist with other audio-visual services. Ability to provide first-line maintenance of video equipment essential. Applications are invited from men or women. Application form obtainable from the Administration Office, returnable by 15 April, Harrow College of Higher Education, Northwick Park, Harrow, Middlesex HA1 3TP. Telephone 01-864 5422, extn 232.  
(1554)

**R & D OPPORTUNITIES.** Senior level vacancies for Communications Hardware and Software Engineers, based in West Sussex. Competitive salaries offered. Please ring David Bird at Redifusion Radio Systems on 01-874 7281.  
(1162)

## Appointments

# Microwave Specialists Communications Satellite Payload Equipment

Marconi Space and Defence Systems, Europe's acknowledged No. 1 in the development of advanced satellite systems, are seeking the following specialists to play key roles in new communications satellite projects at their Stanmore location. We would like to hear from suitably qualified and experienced men or women who want the chance to work in a high technology environment that offers total involvement and lots of excitement.

## MICROWAVE EQUIPMENT MANAGERS

Will be responsible for an Equipment forming part of the Communication Payload programme. This will involve original design, manufacture and test of breadboards; engineering; qualification and flight model hardware; and will entail liaison with European prime contractors on all aspects of the programme. The programmes are usually of an international nature, requiring high technology designs, coupled with demanding timescales.

## MICROWAVE DEVELOPMENT ENGINEERS

Will report to the Equipment Manager and will be responsible for development work on the payload equipments. Tasks will include the design of microwave circuits with the emphasis being on lightweight, high reliability designs including extensive use of MIC technology.

Applicants for both positions should hold a degree or equivalent qualification and have had at least 2 years' relevant experience.

Salaries will be negotiable and accompanied by an excellent range of benefits.

To find out more details, write or telephone Bill Seton, Personnel Manager with brief details of your career to date.

Marconi Space and Defence Systems, The Grove, Warren Lane, Stanmore, Middx. HA7 4LY Tel. 01-954 2311 Extn. 18

**Marconi**  
Space & Defence Systems



1558

## WILTSHIRE COUNTY COUNCIL Department of Architectural Services

Appointment of

# CHIEF SERVICES ENGINEER

(Salary £11,220-£12,408)

Applications are invited for this post, the duties of which concern the design and provision of electrical and mechanical services for building projects and for the associated maintenance and energy conservation work in buildings throughout the county.

The successful candidate should be a Member of the Chartered Institution of Building Services with sound experience of Mechanical Services and should also be a Member of the Institution of Electrical Engineers.

Application forms and full details may be obtained from the County Architect, County Hall, Trowbridge (Tel. 3641 ext. 2115) quoting reference AR.82.35 and should be returned to him by 19th March, 1982.  
(1526)

ELECTRONICS DEVELOPMENT AND  
SERVICING:  
CANCER HOSPITAL AND RESEARCH  
INSTITUTE

## AN ELECTRONICS TECHNICIAN

is required to join an established group working on development and maintenance of medical and cancer research equipment. The job will entail a fair degree of responsibility and calls for someone able and willing to work as a member of a team. Interest and ability in computing and/or r.f. work would be an advantage. The work will be located at our Institute/Hospital site at Sutton, Surrey, which is well provided with staff amenities. Salary on either Research Officer (£5,600-£7,336 p.a.) or Technician (£4,958-£6,993 p.a.) scale plus London Allowance of £557 p.a. Starting point will depend on qualifications and experience, and opportunities for later promotion to higher scales. Candidates should hold City & Guilds Final Certificate, HNC, BSc or an equivalent qualification in electronics or telecommunications. Further information may be obtained from Mr. John Phelps (01-643 8901).

Applications in duplicate with the names and addresses of two referees should be sent to the Secretary, Institute of Cancer Research, 34 Summer Place, London SW7 3NU, quoting ref. 301/B/14.  
(1549)



## Appointments

# Develop your potential in our future



Founded in 1936, Marconi Instruments today employs some 2,000 people in the design, development, production and marketing of its advanced communications test equipment and A.T.E.

To meet the challenges of tomorrow's markets, we need more electronics designers and technicians. And to turn new ideas into fully operational equipment we need production and service personnel as well.

If you would like to develop your potential in the exciting future of Europe's leading test equipment specialist, complete the coupon and send it to us at the address below:-

## marconi instruments

Return this coupon to John Prodger, Marconi Instruments Limited, Freepost, St. Albans, Hertfordshire, AL4 0BR. Telephone: St. Albans 59292

A GEC-Marconi Electronics Company

Name	_____				Age	_____			
Address	_____								
Telephone Work/Home (if convenient) _____									
Years of experience	<input type="checkbox"/> 0-1	<input type="checkbox"/> 1-3	<input type="checkbox"/> 3-6	<input type="checkbox"/> Over 6					
Present salary	<input type="checkbox"/> £4000-5000	<input type="checkbox"/> 5000-6000	<input type="checkbox"/> 6000-7000	<input type="checkbox"/> Over 7000					
Qualifications	<input type="checkbox"/> None	<input type="checkbox"/> C&G	<input type="checkbox"/> HNC	<input type="checkbox"/> Degree					
Present Job	_____ (1234)								

## Technicians in Communications

**GCHQ** We are the Government Communications Headquarters, based at Cheltenham. Our interest is R & D in all types of modern radio communications - HF to satellite - and their security.

**THE JOB** All aspects of technician support to an unparalleled range of communications equipment, much of it at the forefront of current technology.

**LOCATION** Sites at Cheltenham in the very attractive Cotswolds and elsewhere in the UK; opportunities for service abroad.

**PAY** Competitive rates, reviewed regularly. Relevant experience may count towards increased starting pay. Promotion prospects.

**TRAINING** We encourage you to acquire new skills and experience.

**QUALIFICATIONS** You should have a TEC Certificate in Telecommunications, or acceptable equivalent, plus practical experience.

**HOW TO APPLY** For full details on this and information on our special scheme for those lacking practical experience, write now to

**Recruitment Office**  
GCHQ, Oakley, Priors Road, Cheltenham  
Glos. GL52 5AJ  
or ring  
0242 21491  
ext 2269

(1530)

# GCHQ

## ELECTRONICS RESEARCH AT THE UNIVERSITY OF ESSEX

Graduates who have (or final year students who expect to obtain) a first or upper second class honours degree are invited to apply for research leading to a higher degree (M.Sc., M.Phil. or Ph.D) in the following areas: **Acoustic Noise and Vibration Cancellation** (adaptive microprocessor-controlled systems); **Audio Engineering** (amplifier design, digital signal processing, stereo); **Circuit Design Studies** (circuit theory, sensitivity effects, CAD, filter realisations); **Digital Transmission for Telecommunications** (filters, line codes); **Interactive Systems** (handwriting analysis, computer graphics, personal databases); **Microcomputer Systems** (embedded microcomputer applications, microprogramming, architectures); **Microwave and Millimetre Wave Propagation** (scattering from precipitation particles, space frame radomes); **Optical Communications** (detectors, noise processes, signal design, switching); **Picture Coding and Processing** (data reduction, adaptive filtering and coding, feature extraction); **Satellite Communication Systems** (business systems, protocols, data and video services, intermodulation studies); **Telecommunication Switching Systems and Software** (computer control, software production, teletex and viewdata); **Visual Displays and Television Engineering** (computer graphic input systems, stereo and colour displays).

Further information and application form available from: Dr. J. K. Fidler, Chairman, Department of Electrical Engineering Science (Ref. Jan/2), University of Essex, Wivenhoe Park, Colchester CO4 3SQ. (1542)

## SITUATIONS VACANT

# Electronic Engineers - What you want, where you want!

TJB Electrotechnical Personnel Services is a specialised appointments service for electrical and electronic engineers. We have clients throughout the UK who urgently need technical staff at all levels from Junior Technician to Senior Management. Vacancies exist in all branches of electronics and allied disciplines - right through from design to marketing - at salary levels from around £4000 to £12000 p.a.

If you wish to make the most of your qualifications and experience and move another rung or two up the ladder we will be pleased to help you. All applications are treated in strict confidence and there is no danger of your present employer (or other companies you specify) being made aware of your application.

TJB ELECTROTECHNICAL PERSONNEL SERVICES,  
12 Mount Ephraim,  
Tunbridge Wells,  
Kent. TN4 8AS.

Tel: 0892 39388



Please send me a TJB Appointments Registration form:

Name \_\_\_\_\_

Address \_\_\_\_\_

(861)

## BROADCAST FIELD SERVICE ENGINEERS

### MIDDLE EAST

To join highly professional team based in Reading, Berkshire, responsible for installation and service of television studio equipment at customer sites throughout the Middle East.

Key requirements are:

- ★ Degree/HNC in Electronics or equivalent qualification demonstrating a sound theoretical knowledge.
- ★ Three years' experience in Broadcast Television servicing VTRs, Cameras, Vision Mixers, etc.
- ★ Ability to work on own initiative while travelling away from base.

Successful applicants will receive product training, excellent basic salary with generous overseas allowance as appropriate.

# AMPEX

Please contact Maureen Brake on: Reading (0734) 85200, Ampex Great Britain Limited, Acre Road, Reading, Berks.

(1555)

## ARTICLES FOR SALE

**WORLD RADIO TV HANDBOOK 1982**, write for details. "Broadcasts to Europe," quarterly frequency guide, £1.30, full year £4.50. Trade/club enquiries welcome. Pointsea, 25 Westgate, North Berwick, East Lothian. (1534)

**TRANSFORMERS**, line adjustment type, 2.5 KVA, tapped at 0V, 200V, 220V, 230V, 240V £8 each. Also mains transient suppressors, 11 amp, boxed, 4in x 4in x 3in £5 each. Both plus VAT and postage. Electrovernal Ltd, Luton 54309. (1524)

**EQUIPMENT FOR** coils, transformers, components, degassing silicone rubber, resin, epoxy. Lost wax casting for brass, bronze, silver, etc. Impregnating coils, transformers, components. Vacuum equipment low cost, used and new. Also for CRT regunning metallising. Research & Development. Barratts, Mayo Road, Croydon CR0 2QP. 01-684 9917. (9678)

## Southampton THE UNIVERSITY

INSTITUTE OF SOUND AND VIBRATION RESEARCH  
VOICE COMMUNICATIONS SYSTEMS

**SALARY TO £12,500**

Electronic and Electroacoustic Engineers are required to join a small team working on innovative R & D projects for industry. Work includes speech processing, noise cancellation systems and microprocessor-controlled adaptive filters. Duties may involve equipment development, production of prototypes, testing and field trials.

Further details from D. A. S. Copland, The University, Southampton SO9 5NH to whom applications (in duplicate) should be sent quoting reference No. 320/A/WW. 1557

## ARTICLES FOR SALE

AIRMEC 248A Wave Analyser.....	£75
PARAMETRON 477 Spectrum Analyser.....	£650
H-P 8551B Spectrum Analyser.....	£2500
R & S FNA Audio Spectrograph.....	£70
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Attenuators H-P, Marconi, Philco, from.....	£175
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RADIOMETER BKF6 Distortion Meter.....	£75
TEKTRONIX 1301 & C Meter.....	£125
TELEQUIPMENT D.43 Dual Beam Oscilloscope.....	£80
TELEQUIPMENT S54 Single Beam Oscilloscope.....	£150
TEKTRONIX 564 Storage Oscilloscope.....	£1550
MARCONI 2950/5 Mobile Test Set.....	£75
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MARCONI TF 144/4 A.M. Signal Generator.....	£100
MARCONI TF 1059 Sweep Generator.....	£500
PROSSER A100/A340 Wave Form Generator.....	£300
TEKTRONIX 109 Pulse Generator.....	£75
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MARCONI TF 801D/8 Signal Generator.....	£275
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RIKIDENKI B 2413 Pen Recorder.....	£50
RANK 12084/2 Insulation & Pulsed Flash Tester.....	£500
BARNET Dead Weight Tester c/w Weights.....	£2750 o.n.o.
I.C.I. Ultrasonic Cleaner.....	

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TEL: AVEBURY (067-23) 219

(1384)

**ELECTRONIC AND COMPUTER SERVICES**  
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## HARD DISC BARGAINS

- ★ Diablo series 30 25 megabyte exchangeable disc drive. Industry standard. Easily interfaced to most micros, etc. Complete with power supply unit.....
- ★ Teletype ASR 33.....
- ★ Deckwriter LA36.....
- ★ Fast papertape punch.....

All prices inclusive VAT - Carriage extra. (1557)

## FOR SALE

2 Pye Cambridge radio telephone mobile transceivers: Low band AM boot and dash mounted. Forms of tender which should be returned by 5th April, 1982, are available from the Divisional Manager, Sussex River and Water Division, Southern Water Authority, Falmer, Brighton BN1 9PY. Tel: (0273) 606766. (1551)

**PRINTED CIRCUITS.** Make your own simply, cheaply and quickly. Golden Fotolok Light Sensitive Laquer - now greatly improved and very much faster. Aerosol cans with full instructions, £2.25. Developer 35p. Ferric Chloride 55p. Clear Acetate sheet for master 14p. Copper-clad Fibreglass Board approx. 1mm thick £1.75 sq. ft. Post/Packing 60p. White House Electronics, Castle Drive, Praa Sands, Penzance, Cornwall. (714)

**£15,000 PLUS VAT** buys 100 TV rentals releasing £10,000 p.a. gross income. Scope for expansion. South Bristol area. Box No. 1527 (1527)



## 1565

## (1564)

## 11407

## (9101

## (1638)

## (1391)



## (1168)

## (9063

PLEASE WRITE IN BLOCK LETTERS. CLASSIFICATION \_\_\_\_\_ NUMBER OF INSERTIONS \_\_\_\_\_



## COMPUTER APPRECIATION

86 High Street, Bletchingley, Redhill, Surrey RH1 4PA. Tel: Godstone (0883) 843221

PDP 11/23 SYSTEM COMPRISING KDF11-AA processor with KEF11-AA floating point option and MMU; MSV11-DD 64K byte memory; MXV11-A multifunction card with dual serial interfaces, 32K byte memory, real time clock and MXV11-A2 bootstrap ROM; XYLOGICS disc controller, BA11-MF box and PSU with 3 slots; one fixed and one removable DIABLO Series 30 2.5 megabyte disc drives. All processor cards were purchased new by ourselves Sept., '81. Disc psu, all cabling and BA11-MF all unused prior to assembly Sept., '81. Price includes 2 cartridges and RT11. £3,900

PDP 11/03 with LSI11 processor, MSV11-DD 64K byte memory, DLV11-J quad serial interface, DICOLL Model AMD 8200 dual double density floppy disc drives. All contained in compact portable case. £2,250

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VENTEX Model 1100 MICROCOMPUTER with 16K byte memory incorporating 12 line x 80 ch. VDU, twin 8" floppy disc drives, QUME Model S45 daisy wheel printer, communications interface and disc operating system. £950

TEKTRONIX Model 4008/1 high resolution graphics display terminal. Compact storage tube based terminal with 800 x 1024 addressable points in graph mode or 35 lines x 74 ch. in alpha mode. RS232 interface up to 4800 Baud. Current (Jan., '81) new price £2,490. £850

IBM Model 1063 GOLF BALL PRINTER. Compact 15 cps high-reliability mechanical printer in fully refurbished condition. Exactly equivalent to 735 less keyboard. Can be driven from PIA (or similar) with addition of PSU and solenoid driver ICs (details available soon). Accepts standard office golfballs. £395

DIABLO Model 1620 HyTerm TERMINAL. Daisy wheel terminal/typewriter. 110, 300 and 1200 Baud, RS232 interface. In ex-demo condition. £250

FACT 4070 PAPER TAPE PUNCH. for 5-hole or 8-hole operation. Complete with feed and take up spools, chad box and perspex cover. Parallel interface. New cost in excess of £1,200. £175

MOSELY ANALOGUE X-Y PLOTTER. A4 size. Without pens. £75

AMP VERNER WIDE RANGE OSCILLATOR. Sine and square wave to 1 MHz. Battery powered. AS NEW. £50

TEKTRONIX Model 463 DUAL BEAM PORTABLE OSCILLOSCOPE. 75 MHz version of 465. Almost as new. £750

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HOUSTON INSTRUMENTS Model EDP-1 high resolution digital plotter using fan-fold paper. With Model ETC-5A intelligent microprocessor-based controller with character generation, etc. and interfacing to a serial (V24) line. Software to drive this plotter from PDP 11 machines is available from DECUS. Current list price in excess of £3,500. £740

FACT Model 4001 1000 cps capacitive paper tape reader. £150

TREND Model HSR 500 optical paper tape reader. £175

EMS SYNTHESIZER Model SYNTHI A complete with AKS keyboard and DK2 keyboard. Current (Jan., '81) new price £1,921. £350

EIKO 16mm sound projector with Q-H lamp. £50

MULTIDYNE synthesized communications receiver complete with FSK demodulator and EXTREL Model AF baudot coded printer. £350

Please note:  
 \* VAT and carriage extra all items  
 \* Visitors welcome, but by appointment please  
 \* We are keen to bid competitively for all good used equipment

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